

**BIOS MCSDK 2.0.8**

---

# Release Notes

Applies to Product Release: 02.00.08.20  
Publication Date: April 17, 2012

## Document License

---

This work is licensed under the Creative Commons Attribution-NoDerivs 3.0 Unported License. To view a copy of this license, visit <http://creativecommons.org/licenses/by-nd/3.0/> or send a letter to Creative Commons, 171 Second Street, Suite 300, San Francisco, California, 94105, USA.

Copyright (C) 2011-2012 Texas Instruments Incorporated - <http://www.ti.com>

## Contents

---

© Copyright 2011-2012 Texas Instruments, Inc.  
All Rights Reserved

1	Overview .....	1
2	Licensing .....	1
3	Documentation .....	2
4	Device Support.....	2
5	Technical Support and Product Updates .....	2
6	IS NOT .....	3
7	Installation .....	3
8	Issue List.....	3
9	Releases .....	3
9.1	Build 2.0.8.20.....	3
9.2	Previous Releases .....	7
9.2.1	Build 2.0.7.19.....	7
9.2.2	Build 2.0.6.18.....	10
9.2.3	Build 2.0.5.17.....	15
9.2.4	Build 2.0.4.16.....	18
9.2.5	Build 2.0.3.15 .....	22
9.2.6	Build 2.0.2.14 .....	24
9.2.7	Build 2.0.1.12 .....	26
9.2.8	Build 2.0.0.11 .....	28
9.2.9	Build 2.0.0.9 .....	30
9.2.10	Build 2.0.0.8 .....	32
9.2.11	Build 2.0.0.6 .....	34



## BIOS MCSDK 2.00.08.20

### 1 Overview

This document is the Release Notes for **Release 2.0.8** of the Multicore Software Development Kit (MCSDK) for the BIOS operating system. This BIOS MCSDK software release gives developers the ability to evaluate the hardware and software capabilities of the evaluation platform and provide core software to rapidly develop a multi-core application.

### 2 Licensing

The tables below describe the primary license for the packages that make up the BIOS MCSDK. As shown in the *Releases* section below, some of these packages are delivered with the Code Composer Studio installer and some are delivered with the BIOS MCSDK installer.

**IMPORTANT: There may be portions of these packages under other licenses and you should refer to the individual package Software Manifest (after installation) for complete and specific licensing.**

#### Target Content

This is software that runs on the device. For the location, replace `<ver>` with the version that is part of the component name (e.g., 2.0.0.11, etc.).

Package	Primary License	Manifest Location
MCSDK	BSD	[as_installed]/mcsdk_<ver>/bios_mcsdk_swmanifest_<ver>/pdf
PDK 6678	BSD	[as_installed]/pdk_c6678_<ver>/pdk-c6678_swmanifest.pdf
PDK 6670	BSD	[as_installed]/pdk_c6670_<ver>/pdk-c6670_swmanifest.pdf
BIOS 6.0	BSD	[as_installed]/bios_<ver>/sysbios_<ver>_manifest.html
IPC	BSD	[as_installed]/ipc_<ver>/ipc_<ver>_manifest.html
NDK	BSD	[as_installed]/ndk_<ver>/NDK_<ver>_Manifest.html
MATHLIB	BSD	[as_installed]/mathlib_c66x_<ver>/docs/MATHLIB_Software_Manifest.html
DSPLIB	BSD	[as_installed]/dsplib_c66x_<ver>/docs/DSPLIB_Software_Manifest.html
IMAGELIB	BSD	[as_installed]/imglib_c66x_<ver>/docs/IMGLIB_Software_Manifest.html
EDMA3	BSD	[as_installed]/edma3_ild_<ver>/software-manifest.pdf

MCSA Framework	BSD	[as_installed]/ccsv5/uia_<ver>/uia_<ver>_manifest.html
----------------	-----	--

## Host Tools

These are development tools that run primarily on the development host. For the location, replace <ver> with the version that is part of the component name (e.g., 2.0.0.11, etc.).

Package	Primary License	Manifest
MCSDK (flash and eeprom tools)	BSD	[as_installed]/mcsdk_<ver>/bios_mcsdk_swmanifest_<ver>.pdf
CG XML	BSD	[as_installed]/cg_xml/cg_xml_<ver>_Manifest.pdf
XDC	EPL 1.0	[as_installed]/xdctools_<ver>/docs/xdctools.chm (see License section)
Code Composer Studio 5	Click Wrap	N/A

## 3 Documentation

- **EVM Quick Start Guide:** Provides information on hardware setup and running the demonstration application that is loaded on flash. This document is provided as part of the EVM kit.
- [BIOS MCSDK Getting Started Guide:](#) Provides information on installing the software from the two software packages (MCSDK, CCS), connect to the target EVM using JTAG, load pre-compiled libraries to the EVM, and run the application.
- [BIOS MCSDK Users Guide:](#) Provides detailed information regarding software elements and infrastructure to allow developers to start creating applications.
- [Image Processing Demonstration Guide:](#) Provides information on the image processing demo including software design, performance results, development concerns.

## 4 Device Support

The device and platform supported with this release include:

- **C6678:** This release supports the Texas Instruments [TMS320C6678](#) high performance DSP. The EVM supported by the software is [TMDSEVM6678L](#).
- **C6670:** This release supports the Texas Instruments [TMS320C6670](#) high performance DSP. The EVM supported by the software is [TMDSEVM6670L](#).
- **C6618:** This release supports the Texas Instruments [TMS320TCI6618](#) high performance DSP. The EVM supported by the software is TMDSEVM6618LXE.

## 5 Technical Support and Product Updates

For technical discussions and issues, please visit

- **C66x Multicore forum:** [http://e2e.ti.com/support/dsp/c6000\\_multi-core\\_dsps/f/639.aspx](http://e2e.ti.com/support/dsp/c6000_multi-core_dsps/f/639.aspx)
- **BIOS Embedded Software forum:** <http://e2e.ti.com/support/embedded/f/355.aspx>

- **Embedded Processors wiki:** [http://processors.wiki.ti.com/index.php/Main\\_Page](http://processors.wiki.ti.com/index.php/Main_Page)

For product updates, please visit

- **Multicore Software Development Kits:**  
<http://focus.ti.com/docs/toolsw/folders/print/bioslinuxmcsdk.html>

The EVM comes with disks containing the MCSDK software and CCS. You can start with these or go to the MCSDK software download site listed above to check for the latest updates and version. The BIOS-MCSDK release download will also have pointers to applicable CCS and compiler release versions as well. Please review the release notes and software manifest before downloading and/or installing the software.

## 6 IS NOT

- Support for BIOS5 or older releases
- Support for CCS 4.x or older releases
- DSP image format other than ELF (e.g., COFF)
- Security Accelerator LLD is not included in this package, but has been tested against SA LLD 1.0.1.4

## 7 Installation

See the online *BIOS MCSDK Getting Started Guide* ([link](#)) for installation information.

## 8 Issue List

The list of issues can be found online using this [link](#).

## 9 Releases

### 9.1 Build 2.0.8.20

This is the post production release.

Component	Version	Installer
NDK	2.20.06.35	MCSDK
IPC	1.24.02.27	MCSDK
EDMA3 LLD	2.11.03.03	MCSDK
SYS/BIOS	6.33.02.31	MCSDK
DSPLIB	3.1.0.0	MCSDK
MATHLIB	3.0.1.1	MCSDK
IMGLIB	3.1.0.1	MCSDK
MCSDK Tools		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.14	MCSDK
MAD-UTILS	1.0.0.5	MCSDK
EEPROM Writer	1.0.0.4	MCSDK

NAND Writer	1.0.0.4	MCSDK
NOR Writer	1.0.0.3	MCSDK
MCSA	1.0	MCSDK
<b>POST</b>	<b>1.0.0.5</b>	<b>MCSDK</b>
<b>PDK-C6678</b>	<b>1.0.0.20</b>	<b>MCSDK</b>
Platform Library	2.0.0.12	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.1.2	MCSDK
PA LLD	1.2.1.2	MCSDK
<b>CPPI LLD</b>	<b>1.0.2.1</b>	<b>MCSDK</b>
QMSS LLD	1.0.3.6	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	1.0.1.7	MCSDK
RM LLD	1.0.0.4	MCSDK
TSIP LLD	1.0.0.4	MCSDK
HYPLINK LLD	1.0.1.3	MCSDK
<b>PDK-C6670</b>	<b>1.0.0.20</b>	<b>MCSDK</b>
<b>Platform Library</b>	<b>2.0.0.13</b>	<b>MCSDK</b>
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.1.3	MCSDK
PA LLD	1.2.1.2	MCSDK
<b>CPPI LLD</b>	<b>1.0.2.1</b>	<b>MCSDK</b>
QMSS LLD	1.0.3.6	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	1.0.1.7	MCSDK
RM LLD	1.0.0.4	MCSDK
<b>AIF2 LLD</b>	<b>1.0.0.2</b>	<b>MCSDK</b>
BCP LLD	1.0.0.8	MCSDK
FFTC LLD	1.0.0.15	MCSDK
TCP3D LLD	1.0.0.13	MCSDK
TCP3E LLD	1.0.0.10	MCSDK
<b>Demos</b>		
HUA Demo	2.0.0.4	MCSDK
Image Processing Demo	1.0.0.4	MCSDK
<b>Other Tools</b>		
CodeGen	7.3.1	<a href="#">download</a>
CCS	5.1.0.09000	<a href="#">download</a>



CCS	5.0.3.00028	<a href="#">download</a>
C6670/C6678 Simulator	1.0.1	CCS
C6618 Simulator	0.4.0	CCS
XDCTools	3.23.01.43	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	<a href="#">download</a>
minGW	mingw-get-inst-20110316	<a href="#">download</a>

**New features:**

- CPPI cache issue fixed

**Issues Fixed:**

id	Severity	Headline
SDOCM00090320	S1 – Critical / PS	Cache issue in Cppi_txChannelOpen and Cppi_rxChannelOpen (DSP_291)
SDOCM00090343	S2 - Major	Intermittent POST failures on C6670 EVMs are seen
SDOCM00090578	S3 - Minor	Load code sections from multiple memory regions into DSP via PCIE
SDOCM00090577	S3 - Minor	PCIE boot example for DSP local reset

**Known Issues:**

ID	Severity	Headline	Workaround (If applicable)
SDOCM00082115	S2 - Major	C6670 EVM LE & BE: TCP3E_dbIMgzTestProject LE and BE fails on EVM	<p>Make the following changes in the file Test_Main.c to get all tests working</p> <p>1) At line 838 you will see code</p> <pre>maskWord = 0xFFFFFFFF &gt;&gt; (32 - ((cbPtr-&gt;blockSize + cbSet.numTailBits) % 32));</pre> <p>following that line add the following code</p> <pre>maskWord = EndianessManager(maskWord, cbPtr-&gt;outOrder);</pre> <p>2) At line 1882 change the second argument to the function from “cbPtr-&gt;outOrder ^ orderMask” to “orderMask”.</p> <p>3) At line 1868, change the #ifdef to #if. This change is useful when user changes the value of FILL_INVALID_BITS.</p>

SDOCM00082173	S2 - Major	PDK TSIP_testProject - readme needs to indicate dependency on clock for running the sample code.	
SDOCM00085875	S2 - Major	Linux Host: Compilation fails for HyperLink, PA & PCIE projects	The compilation in Linux fails due because in Linux the file names are case sensitive. Please change the include file names in the LLD examples to appropriate cases to compile the example projects.
SDOCM00088276	S2 - Major	Program EVM Documentation: Missing Program EVM documentation for CCS 5.1 users	The default CCXML considered in the program evm script is for CCS 5.0.3 - so it would not work for CCS 5.1. This can be addressed by using an optional feature in the script to point to the customized ccxml path for flashing the evm.
SDOCM00079559	S3 - Minor	NIMU driver do not support JUMBO packets	None
SDOCM00081766	S3 - Minor	IMGLIB: IMGLIB package expanded in the graphical editor the information displayed is not meaningful	
SDOCM00081767	S3 - Minor	DSPLIB: DSPLIB package expanded in the graphical editor the information displayed is not meaningful	
SDOCM00082117	S3 - Minor	IPC example projects can not be re-built even after the projects get cleaned	In order to rebuild the project after import simply delete the Debug and/or Release directories. Afterwards the projects will clean and build without any issues.
SDOCM00085469	S3 - Minor	BIOS Cache API performance lags behind CSL Cache API Performance	
SDOCM00085566	S3 - Minor	BIOS MCSDK installer crashes while installing MCSA in CCS 5.1	Please see the link <a href="http://processors.wiki.ti.com/index.php/BIOS_MCSDK_2.0_User_Guide#Q:_Can_I_use_CCS_5.1_with_BIOS_MCSDK_2.0.3F">http://processors.wiki.ti.com/index.php/BIOS_MCSDK_2.0_User_Guide#Q:_Can_I_use_CCS_5.1_with_BIOS_MCSDK_2.0.3F</a> for the workaround.
SDOCM00085799	S3 - Minor	There is a assertion failure when shutting down a telnet connection to evm.	

SDOCM00086524	S3 - Minor	PA_emaExample_e exampleProject depends on race condition to function correctly	
---------------	------------	--	--

## 9.2 Previous Releases

### 9.2.1 Build 2.0.7.19

This is the post production release.

Component	Version	Installer
NDK	2.20.06.35	MCSDK
IPC	1.24.02.27	MCSDK
EDMA3 LLD	2.11.03.03	MCSDK
SYS/BIOS	6.33.02.31	MCSDK
DSPLIB	3.1.0.0	MCSDK
MATHLIB	3.0.1.1	MCSDK
IMGLIB	3.1.0.1	MCSDK
<b>MCSDK Tools</b>		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.13	MCSDK
MAD-UTILS	1.0.0.5	MCSDK
EEPROM Writer	1.0.0.4	MCSDK
NAND Writer	1.0.0.4	MCSDK
NOR Writer	1.0.0.3	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.4	MCSDK
<b>PDK-C6678</b>	<b>1.0.0.18</b>	<b>MCSDK</b>
Platform Library	2.0.0.12	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.1.2	MCSDK
<b>PA LLD</b>	<b>1.2.1.2</b>	<b>MCSDK</b>
<b>CPPI LLD</b>	<b>1.0.1.5</b>	<b>MCSDK</b>
<b>QMSS LLD</b>	<b>1.0.3.6</b>	<b>MCSDK</b>
PCIe LLD	1.0.0.3	MCSDK
<b>SRIO LLD</b>	<b>1.0.1.7</b>	<b>MCSDK</b>
<b>RM LLD</b>	<b>1.0.0.4</b>	<b>MCSDK</b>
TSIP LLD	1.0.0.4	MCSDK
HYPLINK LLD	1.0.1.3	MCSDK
<b>PDK-C6670</b>	<b>1.0.0.18</b>	<b>MCSDK</b>

Platform Library	2.0.0.12	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.1.3	MCSDK
PA LLD	<b>1.2.1.2</b>	MCSDK
CPPI LLD	<b>1.0.1.5</b>	MCSDK
QMSS LLD	<b>1.0.3.6</b>	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	<b>1.0.1.7</b>	MCSDK
RM LLD	<b>1.0.0.4</b>	MCSDK
AIF2 LLD	<b>1.0.0.0</b>	MCSDK
BCP LLD	<b>1.0.0.8</b>	MCSDK
FFTC LLD	<b>1.0.0.15</b>	MCSDK
TCP3D LLD	1.0.0.13	MCSDK
TCP3E LLD	1.0.0.10	MCSDK
<b>Demos</b>		
HUA Demo	2.0.0.4	MCSDK
Image Processing Demo	1.0.0.4	MCSDK
<b>Other Tools</b>		
CodeGen	7.3.1	<a href="#">download</a>
CCS	5.1.0.09000	<a href="#">download</a>
CCS	5.0.3.00028	<a href="#">download</a>
C6670/C6678 Simulator	1.0.1	CCS
C6618 Simulator	0.4.0	CCS
XDCTools	3.23.01.43	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	<a href="#">download</a>
minGW	mingw-get-inst-20110316	<a href="#">download</a>

**New features:**

- New LLDs: AIF2 (C6670 only) and RM

**Issues Fixed:**

id	Severity	Headline
SDOCM00087039	S2 - Major	CPPI_LLD: Cppi_close has cache coherence problem
SDOCM00089081	S2 - Major	Linking error while using CSL MPU Functionality
SDOCM00088086	S2 - Major	Possible issue with CSL_PLLC_PLLDIV4_16_RATIO_MASK in CSL

SDOCM00087978	S2 - Major	Inconsistency in qmss_qm.h and qmss_drv.c
SDOCM00086136	S2 - Major	CSL_tmrOpen() returns error while opening timer 8-15 in C6678
SDOCM00086668	S2 - Major	KeyStone CSL Update for chip-level interrupt controller and EDMA naming conventions

**Known Issues:**

ID	Severity	Headline	Workaround (If applicable)
SDOCM00082115	S2 - Major	C6670 EVM LE & BE: TCP3E_dbIMgzTestProject LE and BE fails on EVM	<p>Make the following changes in the file Test_Main.c to get all tests working</p> <p>1) At line 838 you will see code</p> <pre>maskWord = 0xFFFFFFFF &gt;&gt; (32 - ((cbPtr-&gt;blockSize + cbSet.numTailBits) % 32));</pre> <p>following that line add the following code</p> <pre>maskWord = EndianessManager(maskWord, cbPtr-&gt;outOrder);</pre> <p>2) At line 1882 change the second argument to the function from "cbPtr-&gt;outOrder ^ orderMask" to "orderMask".</p> <p>3) At line 1868, change the #ifdef to #if. This change is useful when user changes the value of FILL_INVALID_BITS.</p>
SDOCM00082173	S2 - Major	PDK TSIP_testProject - readme needs to indicate dependency on clock for running the sample code.	
SDOCM00085875	S2 - Major	Linux Host: Compilation fails for HyperLink, PA & PCIE projects	The compilation in Linux fails due because in Linux the file names are case sensitive. Please change the include file names in the LLD examples to appropriate cases to compile the example projects.
SDOCM00088276	S2 - Major	Program EVM Documentation: Missing Program EVM documentation for CCS 5.1 users	The default CCXML considered in the program evm script is for CCS 5.0.3 - so it would not work for CCS 5.1. This can be addressed by using an optional feature in the script to point to the customized ccxml path for flashing the evm.

SDOCM00079559	S3 - Minor	NIMU driver do not support JUMBO packets	None
SDOCM00081766	S3 - Minor	IMGLIB: IMGLIB package expanded in the graphical editor the information displayed is not meaningful	
SDOCM00081767	S3 - Minor	DSPLIB: DSPLIB package expanded in the graphical editor the information displayed is not meaningful	
SDOCM00082117	S3 - Minor	IPC example projects can not be re-built even after the projects get cleaned	In order to rebuild the project after import simply delete the Debug and/or Release directories. Afterwards the projects will clean and build without any issues.
SDOCM00085469	S3 - Minor	BIOS Cache API performance lags behind CSL Cache API Performance	
SDOCM00085566	S3 - Minor	BIOS MCSDK installer crashes while installing MCSA in CCS 5.1	Please see the link <a href="http://processors.wiki.ti.com/index.php/BIOS_MCSDK_2.0_User_Guide#Q:_Can_I_use_CCS_5.1_with_BIOS_MCSDK_2.0.3F">http://processors.wiki.ti.com/index.php/BIOS_MCSDK_2.0_User_Guide#Q:_Can_I_use_CCS_5.1_with_BIOS_MCSDK_2.0.3F</a> for the workaround.
SDOCM00085799	S3 - Minor	There is a assertion failure when shutting down a telnet connection to evm.	
SDOCM00086524	S3 - Minor	PA_emicExample_exampleProject depends on race condition to function correctly	

### 9.2.2 Build 2.0.6.18

This is the post production release.

Component	Version	Installer
NDK	<b>2.20.06.35</b>	MCSDK
IPC	<b>1.24.02.27</b>	MCSDK
EDMA3 LLD	<b>2.11.03.03</b>	MCSDK
SYS/BIOS	<b>6.33.02.31</b>	MCSDK
DSPLIB	3.1.0.0	MCSDK
MATHLIB	<b>3.0.1.1</b>	MCSDK
IMGLIB	<b>3.1.0.1</b>	MCSDK

<b>MCSDK Tools</b>		
BIN2CCS	1.0.0.0	MCSDK
IBL	<b>1.0.0.13</b>	MCSDK
MAD-UTILS	1.0.0.5	MCSDK
EEPROM Writer	1.0.0.4	MCSDK
NAND Writer	1.0.0.4	MCSDK
NOR Writer	1.0.0.3	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.4	MCSDK
<b>PDK-C6678</b>	<b>1.0.0.18</b>	<b>MCSDK</b>
Platform Library	<b>2.0.0.12</b>	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	<b>1.0.1.3</b>	MCSDK
PA LLD	1.2.0.1	<b>MCSDK</b>
CPPI LLD	<b>1.0.1.3</b>	MCSDK
QMSS LLD	<b>1.0.2.3</b>	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	1.0.1.3	MCSDK
TSIP LLD	1.0.0.4	MCSDK
HYPLINK LLD	1.0.1.3	MCSDK
<b>PDK-C6670</b>	<b>1.0.0.18</b>	<b>MCSDK</b>
Platform Library	<b>2.0.0.11</b>	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	<b>1.0.1.3</b>	MCSDK
PA LLD	1.2.0.1	<b>MCSDK</b>
CPPI LLD	<b>1.0.1.3</b>	MCSDK
QMSS LLD	<b>1.0.2.3</b>	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	1.0.1.3	MCSDK
BCP LLD	1.0.0.7	MCSDK
FFTC LLD	1.0.0.14	MCSDK
TCP3D LLD	1.0.0.13	MCSDK
TCP3E LLD	1.0.0.10	MCSDK
<b>Demos</b>		
HUA Demo	2.0.0.4	MCSDK
Image Processing Demo	1.0.0.4	MCSDK
<b>Other Tools</b>		

CodeGen	7.3.1	<a href="#">download</a>
CCS	5.1.0.09000	<a href="#">download</a>
C6670/C6678 Simulator	1.0.1	CCS
C6618 Simulator	0.4.0	CCS
XDCTools	<b>3.23.01.43</b>	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	<a href="#">download</a>
minGW	mingw-get-inst-20110316	<a href="#">download</a>

**New features:**

- Support for TMS320C6678 PG 2.0 devices

**Issues Fixed:**

id	Severity	Headline	FoundInProduct Version	Platform
SDOCM00084516	S3 - Minor	Support PCIE boot in big endian mode for 6670/6678	02.00.02	N/A
SDOCM00084518	S3 - Minor	Support PCIE boot with 64-bit addressing	02.00.02	N/A
SDOCM00087579	S3 - Minor	Program EVM user guide needs to update PROGRAM_EVM_TARGET_CONFIG_FILE variable usage	02.00.05	N/A
SDOCM00088275	S3 - Minor	Support of EDMA over PCIE	02.00.02	C6678
SDOCM00088382	S3 - Minor	PCle boot example documentation incorrectly talking about IBL	02.00.05	N/A



**Known Issues:**

ID	Severity	Headline	Workaround (If applicable)
SDOCM00082115	S2 - Major	C6670 EVM LE & BE: TCP3E_dbIMgzTestProject LE and BE fails on EVM	<p>Make the following changes in the file Test_Main.c to get all tests working</p> <p>1) At line 838 you will see code</p> <pre>maskWord = 0xFFFFFFFF &gt;&gt; (32 - ((cbPtr-&gt;blockSize + cbSet.numTailBits) % 32));</pre> <p>following that line add the following code</p> <pre>maskWord = EndiannessManager(maskWord, cbPtr-&gt;outOrder);</pre> <p>2) At line 1882 change the second argument to the function from "cbPtr-&gt;outOrder ^ orderMask" to "orderMask".</p> <p>3) At line 1868, change the #ifdef to #if. This change is useful when user changes the value of FILL_INVALID_BITS.</p>
SDOCM00082173	S2 - Major	PDK TSIP_testProject - readme needs to indicate dependency on clock for running the sample code.	
SDOCM00085875	S2 - Major	Linux Host: Compilation fails for HyperLink, PA & PCIE projects	The compilation in Linux fails due because in Linux the file names are case sensitive. Please change the include file names in the LLD examples to appropriate cases to compile the example projects.
SDOCM00086136	S2 - Major	CSL_tmrOpen() returns error while opening timer 8-15 in C6678	<p>Add following code in function: CSL_tmrGetBaseAddress ()</p> <pre>case CSL_TMR_8:     pBaseAddress-&gt;regs = (CSL_TmrRegsOvly)CSL_TIMER_8_REGS;     break;</pre> <p>Similar addition for all timers from CSL_TMR_9..15</p> <p>Would need to recompile CSL library after the change</p>

SDOCM00088086	S2 - Major	Possible issue with CSL_PLLC_PLLDIV4_16_RATIO_MASK in CSL	
SDOCM00088233	S2 - Major	Release Notes: List both CCS 5.0.3 and CCS 5.1 as supported under tools	
SDOCM00088276	S2 - Major	Program EVM Documentation: Missing Program EVM documentation for CCS 5.1 users	The default CCXML considered in the program evm script is for CCS 5.0.3 - so it would not work for CCS 5.1. This can be addressed by using an optional feature in the script to point to the customized ccxml path for flashing the evm.
SDOCM00079559	S3 - Minor	NIMU driver do not support JUMBO packets	None
SDOCM00081766	S3 - Minor	IMGLIB: IMGLIB package expanded in the graphical editor the information displayed is not meaningful	
SDOCM00081767	S3 - Minor	DSPLIB: DSPLIB package expanded in the graphical editor the information displayed is not meaningful	
SDOCM00082117	S3 - Minor	IPC example projects can not be re-built even after the projects get cleaned	In order to rebuild the project after import simply delete the Debug and/or Release directories. Afterwards the projects will clean and build without any issues.
SDOCM00085469	S3 - Minor	BIOS Cache API performance lags behind CSL Cache API Performance	
SDOCM00085566	S3 - Minor	BIOS MCSDK installer crashes while installing MCSA in CCS 5.1	Please see the link <a href="http://processors.wiki.ti.com/index.php/BIOS_MCSDK_2.0_User_Guide#Q:_Can_I_use_CCS_5.1_with_BIOS_MCSDK_2.0.3F">http://processors.wiki.ti.com/index.php/BIOS_MCSDK_2.0_User_Guide#Q:_Can_I_use_CCS_5.1_with_BIOS_MCSDK_2.0.3F</a> for the workaround.
SDOCM00085799	S3 - Minor	There is a assertion failure when shutting down a telnet connection to evm.	
SDOCM00086524	S3 - Minor	PA_emaExample_exampleProject depends on race condition to function correctly	

### 9.2.3 Build 2.0.5.17

This is the post production release.

Component	Version	Installer
NDK	2.20.04.26	MCSDK
IPC	1.24.00.16	MCSDK
EDMA3 LLD	<b>2.11.03.02</b>	MCSDK
SYS/BIOS	<b>6.32.05.54</b>	MCSDK
DSPLIB	3.1.0.0	MCSDK
MATHLIB	3.0.0.0	MCSDK
IMGLIB	3.0.2.1	MCSDK
<b>MCSDK Tools</b>		
BIN2CCS	1.0.0.0	MCSDK
IBL	<b>1.0.0.12</b>	MCSDK
MAD-UTILS	<b>1.0.0.5</b>	MCSDK
EEPROM Writer	1.0.0.4	MCSDK
NAND Writer	1.0.0.4	MCSDK
NOR Writer	1.0.0.3	MCSDK
MCSA	1.0	MCSDK
POST	<b>1.0.0.4</b>	MCSDK
<b>PDK-C6678</b>	<b>1.0.0.17</b>	<b>MCSDK</b>
Platform Library	<b>2.0.0.11</b>	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.1.1	MCSDK
PA LLD	<b>1.2.0.1</b>	<b>MCSDK</b>
CPPI LLD	1.0.1.0	MCSDK
QMSS LLD	<b>1.0.2.0</b>	MCSDK
PCIe LLD	<b>1.0.0.3</b>	MCSDK
SRIO LLD	1.0.1.3	MCSDK
TSIP LLD	1.0.0.4	MCSDK
HYPLINK LLD	<b>1.0.1.3</b>	MCSDK
<b>PDK-C6670</b>	<b>1.0.0.17</b>	<b>MCSDK</b>
Platform Library	<b>2.0.0.10</b>	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.1.1	MCSDK
PA LLD	<b>1.2.0.1</b>	<b>MCSDK</b>
CPPI LLD	1.0.1.0	MCSDK
QMSS LLD	<b>1.0.2.0</b>	MCSDK

PCIe LLD	<b>1.0.0.3</b>	MCSDK
SRIO LLD	1.0.1.3	MCSDK
BCP LLD	1.0.0.7	MCSDK
FFTC LLD	1.0.0.14	MCSDK
TCP3D LLD	1.0.0.13	MCSDK
TCP3E LLD	1.0.0.10	MCSDK
<b>Demos</b>		
HUA Demo	2.0.0.4	MCSDK
Image Processing Demo	1.0.0.4	MCSDK
<b>Other Tools</b>		
CodeGen	7.2.4	<a href="#">download</a>
CCS	5.0.3.00028	<a href="#">download</a>
C6670/C6678 Simulator	1.0.1	CCS
C6618 Simulator	0.4.0	CCS
XDCTools	<b>3.22.04.46</b>	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	<a href="#">download</a>
minGW	mingw-get-inst-20110316	<a href="#">download</a>

**New features:**

- Updated PLL sequences in GEL, IBL and Platform Library
- Updated SYS-BIOS/XDC tools
- Updated PA, EDMA3, QMSS, Hyperlink and PCIe LLDs.
- MAD prelink bypass (MAD Lite support)

**Issues Fixed:**

id	Severity	Headline	FoundInProductVersion	Platform
SDOCM00079207	S3 - Minor	IBL PLL/DDR sequence needs to be updated to fix board boot failures	02.00.00	C6678
SDOCM00081068	S3 - Minor	(Child) PDK (C6670): qmlnframCExampleProject generates warning on compile	02.00.00	C6670
SDOCM00082942	S3 - Minor	building mad image causes many warnings.....	02.00.01	C6678
SDOCM00083981	S2 - Major	srioboot_example build fails on Linux host.	02.00.02	N/A
SDOCM00084194	S2 - Major	BIOS MCSDK needs to update to the XDC version which retains all the project templates	02.00.01	N/A
SDOCM00084289	S3 - Minor	There are TI GT specific paths in the resulting install	02.00.02	N/A
SDOCM00084626	S2 - Major	(Child) QMSS LLD: Add API to support Queue Diversion request from PASS	02.00.04	C6678

SDOCM00085146	S2 - Major	Platform Library PLL/DDR sequence needs to be updated to fix board boot failures	02.00.03	N/A
SDOCM00085246	S2 - Major	Ease of Use: Include Program EVM under BIOS MCSDK	02.00.04	
SDOCM00085522	S3 - Minor	Cannot create new RTSC project due to XDC issue introduced in BIOS MCSDK 2.0.1	02.00.01	N/A
SDOCM00085679	S3 - Minor	MAD: Insufficient information/instructions in readme file for MAD loader	02.00.04	N/A
SDOCM00085746	S2 - Major	IBL needs to have a command line tool to update configuration	02.00.04	N/A
SDOCM00085875	S2 - Major	Linux Host: Compilation fails for HyperLink, PA & PCIE projects	02.00.04	N/A
SDOCM00085913	S2 - Major	NDK/helloWorld_evmc6670.out does not build	02.00.04	C6670
SDOCM00085919	S3 - Minor	Linux Host: Ethernet boot example missing precompiled image for ppcsendpkt.c	02.00.04	N/A
SDOCM00086135	S3 - Minor	Updated embedded components to align with CCS 5.1 set	02.00.04	N/A
SDOCM00086176	S3 - Minor	IBL compiled in Windows has corrupted default configuration	02.00.04	N/A
SDOCM00086177	S3 - Minor	IBL always compiles with CGT v7.2.1 from CCS, even though MCSDK installs another version (Windows only)	02.00.04	N/A
SDOCM00086468	S2 - Major	CCS 5.1 imports non-RTSC projects as RTSC projects.	02.00.05	N/A
SDOCM00086572	S2 - Major	Link to MinGW in Release Notes is not correct	02.00.04	N/A
SDOCM00086609	S3 - Minor	POST: Print configuration information (hex dump of the registers)	02.00.04	N/A
SDOCM00086612	S3 - Minor	Integrate EDMA3 LLD 02.11.03.02	02.00.04	N/A

**Known Issues:**

id	Headline	Severity	Platform
SDOCM00079559	NIMU driver do not support JUMBO packets	S3 - Minor	C6678
SDOCM00081766	IMGLIB: IMGLIB package expanded in the graphical editor the information displayed is not meaningful	S3 - Minor	N/A
SDOCM00081767	DSPLIB: DSPLIB package expanded in the graphical editor the information displayed is not meaningful	S3 - Minor	N/A

SDOCM00082115	C6670 EVM LE & BE: TCP3E_dbIMgzTestProject LE and BE fails on EVM	S2 - Major	C6670
SDOCM00082117	IPC example projects can not be re-built even after the projects get cleaned	S3 - Minor	C6678
SDOCM00082173	PDK TSIP_testProject -readme needs to indicate dependency on clock for running the sample code.	S2 - Major	C6678
SDOCM00085799	There is a assertion failure when shutting down a telnet connection to evm.	S3 - Minor	N/A
SDOCM00086851	Eclipse Update Manager does not work in Linux environment	S2 - Major	N/A
SDOCM00085469	BIOS Cache API performance lags behind CSL Cache API Performance	S3 - Minor	N/A
SDOCM00086136	CSL_tmrOpen() returns error while opening timer 8-15 in C6678	S2 - Major	C6678
SDOCM00085769	SRIO turn-around time is not consistent	S2 - Major	
SDOCM00086524	PA_emacExample_exampleProject depends on race condition to function correctly	S3 - Minor	N/A
SDOCM00087039	CPPI_LLD: Cppi_close has cache coherence problem	S2 - Major	N/A
SDOCM00085566	BIOS MCSDK installer crashes while installing MCSA in CCS 5.1	S3 - Minor	N/A

#### 9.2.4 Build 2.0.4.16

This is the post production release.

Component	Version	Installer
NDK	2.20.04.26	MCSDK
IPC	<b>1.24.00.16</b>	MCSDK
EDMA3 LLD	2.11.02.04	MCSDK
SYS/BIOS	6.32.04.49	MCSDK
DSPLIB	<b>3.1.0.0</b>	MCSDK
MATHLIB	3.0.0.0	MCSDK
IMGLIB	<b>3.0.2.1</b>	MCSDK
<b>MCSDK Tools</b>		
BIN2CCS	1.0.0.0	MCSDK
IBL	<b>1.0.0.11</b>	MCSDK
MAD-UTILS	1.0.0.3	MCSDK
EEPROM Writer	1.0.0.4	MCSDK
NAND Writer	1.0.0.4	MCSDK
NOR Writer	1.0.0.3	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.3	MCSDK

<b>PDK-C6678</b>	<b>1.0.0.16</b>	<b>MCSDK</b>
Platform Library	2.0.0.10	MCSDK
NIMU Transport	<b>1.0.0.8</b>	MCSDK
IPC SRIO Transport	<b>1.0.1.0</b>	MCSDK
IPC QMSS Transport	<b>1.0.1.0</b>	MCSDK
CSL	1.0.1.1	MCSDK
PA LLD	1.1.1.0	<b>MCSDK</b>
CPPI LLD	<b>1.0.1.0</b>	MCSDK
QMSS LLD	<b>1.0.1.0</b>	MCSDK
PCIe LLD	1.0.0.2	MCSDK
SRIO LLD	<b>1.0.1.3</b>	MCSDK
TSIP LLD	1.0.0.4	MCSDK
HYPLINK LLD	<b>1.0.1.2</b>	MCSDK
<b>PDK-C6670</b>	<b>1.0.0.16</b>	<b>MCSDK</b>
Platform Library	2.0.0.9	MCSDK
NIMU Transport	<b>1.0.0.8</b>	MCSDK
IPC SRIO Transport	<b>1.0.1.0</b>	MCSDK
IPC QMSS Transport	<b>1.0.1.0</b>	MCSDK
CSL	1.0.1.1	MCSDK
PA LLD	1.1.1.0	<b>MCSDK</b>
CPPI LLD	<b>1.0.1.0</b>	MCSDK
QMSS LLD	<b>1.0.1.0</b>	MCSDK
PCIe LLD	1.0.0.2	MCSDK
SRIO LLD	<b>1.0.1.3</b>	MCSDK
BCP LLD	1.0.0.7	MCSDK
FFTC LLD	1.0.0.14	MCSDK
TCP3D LLD	1.0.0.13	MCSDK
TCP3E LLD	1.0.0.10	MCSDK
<b>Demos</b>		
HUA Demo	2.0.0.4	MCSDK
Image Processing Demo	1.0.0.4	MCSDK
<b>Other Tools</b>		
CodeGen	7.2.4	<a href="#">download</a>
CCS	5.0.3.00028	<a href="#">download</a>
C6670/C6678 Simulator	1.0.1	CCS
C6618 Simulator	0.4.0	CCS
XDCTools	3.22.01.21	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	<a href="#">download</a>
mingW	mingw-get-inst-20110316	<a href="#">download</a>

**New features:**

- Single IBL binary image support. (deprecated the .dat file support)
- POST bin file (deprecated the .dat file support)
- IPC Transport Optimization over SRIO and QMSS
- New kernels and platforms support in DSPLIB and IMGLIB

**Issues Fixed:**

id	Severity	Headline	FoundInProductVersion	Platform
SDOCM00080936	S3 - Minor	IBL make needs to build one image including boot loader + config table	02.00.00	N/A
SDOCM00081268	S2 - Major	IPC: Need New Version of NameServer to support Off-Chip Communication	02.00.00	N/A
SDOCM00082455	S3 - Minor	Document how to connect a PC to two EVMs with the same emulator type in the UG section of 'other useful ref'	02.00.00	N/A
SDOCM00082687	S2 - Major	SRIO IPC Transport Optimizations	02.00.00	N/A
SDOCM00082688	S2 - Major	QMSS IPC Transport Optimizations	02.00.00	N/A
SDOCM00083018	S3 - Minor	SRIO Multicore loopback example uses illegal Serdes PLL setting for Multiply	02.00.00	C6678
SDOCM00083475	S3 - Minor	QOS_TEST option in QMSS LLD unit test fails	02.00.01	N/A
SDOCM00083489	S3 - Minor	(Child) QMSS LLD update to include new QoS firmware	02.00.01	N/A
SDOCM00083631	S2 - Major	BootLoader: Ethernet boot examples does not provide any application for Linux to verify the examples	02.00.02	N/A
SDOCM00083901	S2 - Major	There is a race condition in the TX path and PA command config path that cause TX queue description corruption	02.00.02	N/A
SDOCM00083981	S2 - Major	srioboot_example build fails on Linux host.	02.00.02	N/A
SDOCM00084150	S2 - Major	The EEPROM writer should not provide/use byte swap option in the eepromwriter_input.txt.	02.00.01	N/A



SDOCM00084158	S2 - Major	Prefix listlib APIs in CPPI LLD to avoid conflicts with other listlib APIs used in the system	02.00.01	C6670
SDOCM00084287	S2 - Major	Linux installer does not work in commandline mode	02.00.02	N/A
SDOCM00084288	S3 - Minor	Default installation directory in Linux installer is not correct	02.00.02	N/A
SDOCM00084290	S3 - Minor	Add more text to the installer window and cleanup	02.00.02	N/A
SDOCM00084421	S3 - Minor	%PDK_INSTALL_PATH % set incorrectly by pdksetupenv.bat	02.00.02	N/A
SDOCM00084481	S2 - Major	Forum Issue: Failed to open HeapBufMP_open in multicore application	02.00.02	N/A
SDOCM00084482	S3 - Minor	EVM_init is called too early in all of the demo/unit test applications	02.00.03	N/A
SDOCM00085741	S2 - Major	Prototype of DSPF_sp_fir_cplx does not match function name in DSPLIB	02.00.04	C6678
SDOCM00085562	S2 - Major	NIMU RX DESC value non-power of 2	02.00.04	C6670

**Known Issues:**

id	Severity	Headline	Platform
SDOCM00084194	S2 - Major	BIOS MCSDK needs to update to the XDC version which retains all the project templates	N/A
SDOCM00085875	S2 - Major	Linux Host: Compilation fails for HyperLink, PA & PCIE projects	N/A
SDOCM00085913	S2 - Major	NDK/helloWorld_evmc6670.out does not build	C6670
SDOCM00082173	S2 - Major	PDK TSIP_testProject -readme needs to indicate dependency on clock for running the sample code.	C6678
SDOCM00082115	S2 - Major	C6670 EVM LE & BE: TCP3E_dbIMgzTestProject LE and BE fails on EVM	C6670
SDOCM00083981	S2 - Major	srioboot_example build fails on Linux host.	N/A
SDOCM00085469	S3 - Minor	BIOS Cache API performance lags behind CSL Cache API Performance	N/A
SDOCM00085566	S3 - Minor	BIOS MCSDK installer crashes while installing MCSA in CCS 5.1	N/A

SDOCM00085919	S3 - Minor	Linux Host: Ethernet boot example missing precompiled image for ppcsendpkt.c	N/A
SDOCM00079559	S3 - Minor	NIMU driver do not support JUMBO packets	C6678
SDOCM00081196	S3 - Minor	IBL i2cConfig.gel doesn't indicate completion	C6678
SDOCM00081766	S3 - Minor	IMGLIB: IMGLIB package expanded in the graphical editor the information displayed is not meaningful	N/A
SDOCM00081767	S3 - Minor	DSPLIB: DSPLIB package expanded in the graphical editor the information displayed is not meaningful	N/A
SDOCM00082117	S3 - Minor	IPC example projects can not be re-built even after the projects get cleaned	C6678
SDOCM00085799	S3 - Minor	There is a assertion failure when shutting down a telnet connection to evm.	N/A

### 9.2.5 Build 2.0.3.15

This is the post production release.

Component	Version	Installer
NDK	2.20.04.26	MCSDK
IPC	1.23.01.26	MCSDK
EDMA3 LLD	2.11.02.04	MCSDK
SYS/BIOS	6.32.04.49	MCSDK
DSPLIB	3.0.0.8	MCSDK
MATHLIB	3.0.0.0	MCSDK
IMGLIB	3.0.1.0	MCSDK
<b>MCSDK Tools</b>		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.10	MCSDK
MAD-UTILS	1.0.0.3	MCSDK
EEPROM Writer	1.0.0.4	MCSDK
NAND Writer	1.0.0.4	MCSDK
NOR Writer	1.0.0.3	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.3	MCSDK
<b>PDK-C6678</b>	<b>1.0.0.15</b>	<b>MCSDK</b>
Platform Library	2.0.0.10	MCSDK
NIMU Transport	1.0.0.7	MCSDK
IPC SRIO Transport	1.0.0.0	MCSDK
IPC QMSS Transport	1.0.0.0	MCSDK
CSL	1.0.1.1	MCSDK
<b>PA LLD</b>	<b>1.1.1.0</b>	<b>MCSDK</b>
CPPI LLD	1.0.0.16	MCSDK

QMSS LLD	1.0.0.17	MCSDK
PCIe LLD	1.0.0.2	MCSDK
SRIO LLD	1.0.1.1	MCSDK
TSIP LLD	1.0.0.4	MCSDK
HYPLINK LLD	1.0.1.1	MCSDK
<b>PDK-C6670</b>	<b>1.0.0.15</b>	<b>MCSDK</b>
Platform Library	2.0.0.9	MCSDK
NIMU Transport	1.0.0.7	MCSDK
IPC SRIO Transport	1.0.0.0	MCSDK
IPC QMSS Transport	1.0.0.0	MCSDK
CSL	1.0.1.1	MCSDK
<b>PA LLD</b>	<b>1.1.1.0</b>	<b>MCSDK</b>
CPPI LLD	1.0.0.16	MCSDK
QMSS LLD	1.0.0.17	MCSDK
PCIe LLD	1.0.0.2	MCSDK
SRIO LLD	1.0.1.1	MCSDK
BCP LLD	1.0.0.7	MCSDK
FFTC LLD	1.0.0.14	MCSDK
TCP3D LLD	1.0.0.13	MCSDK
TCP3E LLD	1.0.0.10	MCSDK
<b>Demos</b>		
HUA Demo	2.0.0.4	MCSDK
Image Processing Demo	1.0.0.4	MCSDK
<b>Other Tools</b>		
CodeGen	7.2.4	<a href="#">download</a>
CCS	5.0.3.00028	<a href="#">download</a>
C6670/C6678 Simulator	1.0.1	CCS
C6618 Simulator	0.4.0	CCS
XDCTools	3.22.01.21	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	<a href="#">download</a>
minGW	mingw-get-inst-20110316	<a href="#">download</a>

New features:

- None

Externally Tracked Issues Fixed:

- None

Other Issues Fixed:

- PA LLD: Mismatch between the OSAL begin and end tags for C6678 and C6670.

## 9.2.6 Build 2.0.2.14

This is the post production release.

Component	Version	Installer
NDK	2.20.04.26	MCSDK
IPC	1.23.01.26	MCSDK
EDMA3 LLD	2.11.02.04	MCSDK
SYS/BIOS	<b>6.32.04.49</b>	MCSDK
DSPLIB	3.0.0.8	MCSDK
MATHLIB	3.0.0.0	MCSDK
IMGLIB	3.0.1.0	MCSDK
<b>MCSDK Tools</b>		
BIN2CCS	1.0.0.0	MCSDK
<b>IBL</b>	<b>1.0.0.10</b>	<b>MCSDK</b>
MAD-UTILS	1.0.0.3	MCSDK
<b>EEPROM Writer</b>	<b>1.0.0.4</b>	<b>MCSDK</b>
<b>NAND Writer</b>	<b>1.0.0.4</b>	<b>MCSDK</b>
<b>NOR Writer</b>	<b>1.0.0.3</b>	<b>MCSDK</b>
MCSA	1.0	MCSDK
POST	1.0.0.3	MCSDK
<b>PDK-C6678</b>	<b>1.0.0.14</b>	<b>MCSDK</b>
<b>Platform Library</b>	<b>2.0.0.10</b>	<b>MCSDK</b>
<b>NIMU Transport</b>	<b>1.0.0.7</b>	<b>MCSDK</b>
<b>IPC SRIO Transport</b>	<b>1.0.0.0</b>	<b>MCSDK</b>
<b>IPC QMSS Transport</b>	<b>1.0.0.0</b>	<b>MCSDK</b>
<b>CSL</b>	<b>1.0.1.1</b>	<b>MCSDK</b>
PA LLD	1.1.0.10	MCSDK
CPPI LLD	1.0.0.16	MCSDK
QMSS LLD	1.0.0.17	MCSDK
PCIe LLD	1.0.0.2	MCSDK
<b>SRIO LLD</b>	<b>1.0.1.1</b>	<b>MCSDK</b>
TSIP LLD	1.0.0.4	MCSDK
<b>HYPLINK LLD</b>	<b>1.0.1.1</b>	<b>MCSDK</b>
<b>PDK-C6670</b>	<b>1.0.0.14</b>	<b>MCSDK</b>
<b>Platform Library</b>	<b>2.0.0.9</b>	<b>MCSDK</b>
<b>NIMU Transport</b>	<b>1.0.0.7</b>	<b>MCSDK</b>
<b>IPC SRIO Transport</b>	<b>1.0.0.0</b>	<b>MCSDK</b>
<b>IPC QMSS Transport</b>	<b>1.0.0.0</b>	<b>MCSDK</b>
<b>CSL</b>	<b>1.0.1.1</b>	<b>MCSDK</b>
PA LLD	1.1.0.10	MCSDK
CPPI LLD	1.0.0.16	MCSDK
QMSS LLD	1.0.0.17	MCSDK

PCIe LLD	1.0.0.2	MCSDK
<b>SRIO LLD</b>	<b>1.0.1.1</b>	<b>MCSDK</b>
BCP LLD	1.0.0.7	MCSDK
FFTC LLD	1.0.0.14	MCSDK
TCP3D LLD	1.0.0.13	MCSDK
TCP3E LLD	1.0.0.10	MCSDK
<b>Demos</b>		
<b>HUA Demo</b>	<b>2.0.0.4</b>	<b>MCSDK</b>
<b>Image Processing Demo</b>	<b>1.0.0.4</b>	<b>MCSDK</b>
<b>Other Tools</b>		
CodeGen	7.2.4	<a href="#">download</a>
CCS	5.0.3.00028	<a href="#">download</a>
C6670/C6678 Simulator	1.0.1	CCS
C6618 Simulator	0.4.0	CCS
XDCTools	3.22.01.21	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	<a href="#">download</a>
minGW	mingw-get-inst-20110316	<a href="#">download</a>

New features:

- Linux Installer and Build Support
- Added PCIe example for C6670
- Updated the CSL, SRIO LLD for C6670 and C6678
- Updated Hyperlink LLD for C6678

Externally Tracked Issues Fixed:

id	Severity	State	Headline	Found In Product	Found In Release Version	Affected Targets
SDOCM00082731	Major	Planned	C6670 and C6678 CSL error for PLLM field	BIOS-MCSDK	02.00.00	N/A
SDOCM00082984	Major	Planned	The serial version of Image Processing demo does not build when copied to CCS workspace	BIOS-MCSDK	02.00.01	N/A

Other Issues Fixed:

- IPC SRIO examples use wrong datarate: clock is 250MHz, not 312.5MHz for C6670
- Simulator target for NDK is broken
- Added ability to do NDK loopback under HUA

- Reset Isolation needs to be disabled for SRIO sample initialization for C6678
- Update of the CSL-RL for EMIF-16 for C6678
- Writer utility should strip the carriage return character when parsing the input text file
- Platform Lib: C6678 needs to be aligned with the CSL EMIF16 updates for C6678

### 9.2.7 Build 2.0.1.12

This is the post production release of BIOS MCSDK 2.0.1

Component	Version	Installer
NDK	2.20.04.26	MCSDK
IPC	1.23.01.26	MCSDK
EDMA3 LLD	2.11.02.04	MCSDK
SYS/BIOS	6.32.01.38	MCSDK
DSPLIB	3.0.0.8	MCSDK
MATHLIB	3.0.0.0	MCSDK
IMGLIB	3.0.1.0	MCSDK
<b>MCSDK Tools</b>		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.8	MCSDK
MAD-UTILS	1.0.0.2	MCSDK
EEPROM Writer	1.0.0.3	MCSDK
NAND Writer	1.0.0.3	MCSDK
NOR Writer	1.0.0.2	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.3	MCSDK
<b>PDK-C6678</b>	<b>1.0.0.12</b>	<b>MCSDK</b>
Platform Library	2.0.0.8	MCSDK
NIMU Transport	1.0.0.5	MCSDK
CSL	1.0.1.0	MCSDK
PA LLD	1.1.0.10	MCSDK
CPPI LLD	1.0.0.16	MCSDK
QMSS LLD	1.0.0.17	MCSDK
PCIe LLD	1.0.0.2	MCSDK
SRIO LLD	1.0.0.14	MCSDK
TSIP LLD	1.0.0.4	MCSDK
HYPLINK LLD	1.0.0.4	MCSDK
<b>PDK-C6670</b>	<b>1.0.0.12</b>	<b>MCSDK</b>
Platform Library	2.0.0.8	MCSDK
NIMU Transport	1.0.0.5	MCSDK

CSL	1.0.1.0	MCSDK
PA LLD	1.1.0.10	MCSDK
CPPI LLD	1.0.0.16	MCSDK
QMSS LLD	1.0.0.17	MCSDK
PCIe LLD	1.0.0.2	MCSDK
SRIO LLD	1.0.0.14	MCSDK
BCP LLD	1.0.0.7	MCSDK
FFTC LLD	1.0.0.14	MCSDK
TCP3D LLD	1.0.0.13	MCSDK
TCP3E LLD	1.0.0.10	MCSDK
<b>Demos</b>		
HUA Demo	2.0.0.3	MCSDK
Image Processing Demo	1.0.0.3	MCSDK
<b>Other Tools</b>		
CodeGen	7.2.4	<a href="#">download</a>
CCS	5.0.3.00028	<a href="#">download</a>
C6670/C6678 Simulator	1.0.1	CCS
C6618 Simulator	0.4.0	CCS
XDCTools	3.22.01.21	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	<a href="#">download</a>
minGW	mingw-get-inst-20110316	<a href="#">download</a>

New features:

- Added Multicore Application Deployment Utilities (MAD-Utils)

Externally Tracked Issues Fixed:

id	Severity	State	Headline	Found In Product	Found In Release Version	Affected Targets
SDOCM00082091	Major	Planned	HeapMultiBufMP_alloc fails as it is missing a cache operation	BIOS-MCSDK	02.00.00	N/A
SDOCM00082426	Major	Planned	CSL makefile: needs to be updated to include assembly files to the library	BIOS-MCSDK	02.00.00	N/A
SDOCM00082085	Major	Planned	IPC initialization fails if run after power cycle and before CPU a reset	BIOS-MCSDK	02.00.00	N/A
SDOCM00079689	Minor	Planned	BIOS: whole_program does not build for C6678	BIOS-MCSDK	02.00.00	N/A

Other Issues Fixed:

- MAD tools integration with Image Processing Demo
- DSP Code generation tools update to 7.2.4 version
- PDK c6670 SRIO and QMSS IPC examples require a clean then rebuild in order to operate correctly

### 9.2.8 Build 2.0.0.11

This is the production release of BIOS MCSDK 2.0.0

Component	Version	Installer
NDK	2.20.04.26	MCSDK
IPC	1.22.05.27	MCSDK
EDMA3 LLD	2.11.02.04	MCSDK
SYS/BIOS	6.31.04.27	CCS
DSPLIB	3.0.0.8	MCSDK
MATHLIB	3.0.0.0	MCSDK
IMGLIB	3.0.1.0	MCSDK
<b>MCSDK Tools</b>		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.6	MCSDK
EEPROM Writer	1.0.0.3	MCSDK
NAND Writer	1.0.0.3	MCSDK
NOR Writer	1.0.0.2	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.3	MCSDK
<b>PDK-C6678</b>	<b>1.0.0.11</b>	<b>MCSDK</b>
Platform Library	2.0.0.7	MCSDK
NIMU Transport	1.0.0.4	MCSDK
CSL	1.0.0.12	MCSDK
PA LLD	1.1.0.10	MCSDK
CPPI LLD	1.0.0.16	MCSDK
QMSS LLD	1.0.0.17	MCSDK
PCIe LLD	1.0.0.2	MCSDK
SRIO LLD	1.0.0.14	MCSDK
TSIP LLD	1.0.0.4	MCSDK
HYPLINK LLD	1.0.0.4	MCSDK
<b>PDK-C6670</b>	<b>1.0.0.11</b>	<b>MCSDK</b>
Platform Library	2.0.0.7	MCSDK
NIMU Transport	1.0.0.3	MCSDK
CSL	1.0.0.6	MCSDK
PA LLD	1.1.0.10	MCSDK
CPPI LLD	1.0.0.16	MCSDK
QMSS LLD	1.0.0.17	MCSDK



PCIe LLD	1.0.0.2	MCSDK
SRIO LLD	1.0.0.14	MCSDK
BCP LLD	1.0.0.7	MCSDK
FFTC LLD	1.0.0.14	MCSDK
TCP3D LLD	1.0.0.13	MCSDK
TCP3E LLD	1.0.0.10	MCSDK
<b>Demos</b>		
HUA Demo	2.0.0.3	MCSDK
Image Processing Demo	1.0.0.2	MCSDK
<b>Other Tools</b>		
CodeGen	7.2.1	CCS
CCS	5.0.3.00028	<a href="#">download</a>
C6670/C6678 Simulator	1.0.1	CCS
C6618 Simulator	0.4.0	CCS
XDCTools	3.20.08.88	CCS
CG_XML	2.30	MCSDK
minGW	mingw-get-inst-20110316	<a href="#">download</a>

New features:

- PCIe Boot example added
- PCIe LLD added for C6670
- Image Processing Demo support for C6670
- IPC over SRIO & QMSS transports
- SRIO boot example

Externally Tracked Issues Fixed:

id	Severity	State	Headline	Found In Product	Found In Release Version	Affected Targets
SDOCCM00079225	Major	Planned	Simulator target does not work for NDK	BIOS-MCSDK	02.00.00	C6678
SDOCCM00079505	Major	Planned	EDMA3 LLD: Can not build EDMA3 LLD examples	BIOS-MCSDK	02.00.00	C6678
SDOCCM00079607	Major	Planned	HUA: EMAC switch statistics sometimes show inconsistent values	BIOS-MCSDK	02.00.00	C6678
SDOCCM00081144	Major	Planned	Image processing demo with MCSA integrated crashes after few runs	BIOS-MCSDK	02.00.00	C6678
SDOCCM00081396	Major	Planned	Platform Library: platform_lib and platform_lib_test projects	BIOS-MCSDK	02.00.00	N/A

			do not build for both 6670/6678			
--	--	--	------------------------------------	--	--	--

Other Issues Fixed:

- CSL PDK (C6670): PCIE project fails to compile due to undefined symbol CSL\_BootCfgGetPCIEPLLLock
- PDK (C6618 simulator): BCP example project for Big Endian and simulator hangs at second test
- PDK (C6670): BCP test and example projects for Big Endian fail all tests.
- C6670 EVM BE: TCP3E\_dbIMgzTestProject Big Endian fails on EVM
- C6670 EVM BE: TCP3E\_exampleProject Big Endian fails on EVM
- There is no Readme for shmlpcBenchmark\_c6678 – PDK
- Platform Library simulator target does not work for NDK
- getLibs() added for Platform Library and NIMU should be adopted by all MCSDK components
- Fixed issue preventing building EDMA3 LLD examples
- HUA: Occasionally Flash Page has nothing after the How It Works text
- makefile support for MCSDK components
- HUA Demo: Running the Transmit UDP benchmark multiple times crashes the system
- IBL need to support I2C boot from multiple device/images based on the dip switch setting
- NAND ecc bytes computation needs to be changed for BIOS MCSDK to align with Linux
- Provide makefiles for EEPROM/NAND/NOR writers
- ibl\src\make\ibl\_c661x\i2crom.map.pre defines clocks/plls incorrectly
- IBL PLL workaround is too slow for PCIe
- TCP3d/TCP3e LLD source files does not build using the external to PDK EDMA3LLD driver provided by TI-India
- NIMU Rx pDataBuffers are not cached aligned
- Image processing demo: The edge slices are not cache aligned
- PDK (C66x): hyplnk\_read\_ECCErrors\_reg() doesn't work. It accesses the wrong HW register.

**9.2.9 Build 2.0.0.9**

This is a Beta-2 release.

Component	Version	Installer
PDK-C6678	1.0.0.9	MCSDK
PDK-C6670	1.0.0.9	MCSDK
Platform Library (C6678)	2.0.0.6	MCSDK
Platform Library (C6670)	2.0.0.2	MCSDK
NDK	2.20.03.24	MCSDK
IPC	1.22.05.27	MCSDK
EDMA3 LLD	2.11.1.2	MCSDK
SYS/BIOS	6.31.04.27	CCS
DSPLIB	3.0.0.7	MCSDK
IMGLIB	3.0.1.0	MCSDK
MCSDK Tools		
BIN2CCS	1.0.0.0	MCSDK

IBL	1.0.0.4	MCSDK
MAD	1.0.0.0	MCSDK
EEPROM Writer	1.0.0.1	MCSDK
NAND Writer	1.0.0.1	MCSDK
NOR Writer	1.0.0.0	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.2	MCSDK
<b>Demos</b>		
HUA Demo	2.0.0.2	MCSDK
Image Processing Demo	1.0.0.0	MCSDK
<b>Other Tools</b>		
CodeGen	7.2.0	CCS
CCS	5.0.2.00006	<a href="#">download</a>
C66x Functional Simulator	1.0.0	CCS
XDCTools	3.20.08.88	CCS
CG_XML	2.2	<a href="#">download</a>
Python	2.7.1	<a href="#">download</a>

New features

- PCIe LLD added for C6678
- BCP, TCP3d, TCP3e and FFTC LLDs added for C6670
- Multicore System Analyzer (MCSA) support

Issues fixed in this Release:

id	Severity	State	Headline	Found In Product	Found In Release Version	Affected Targets
SDOCM00079595	Minor	Planned	HUA: Going back to the Main Flash Page after a NOR Read or Write shows the NAND selection	BIOS-MCSDK	02.00.00	C6678
SDOCM00079834	Minor	Planned	Import of LLD example projects gives warning and build fails unless a macros.ini is imported manually	BIOS-MCSDK	02.00.00	C6678

Other bug fixes:

- Going back to the Main Flash Page after a NOR Read or Write Shows the NAND selection
- HUA project name should be changed from "hpdspua" to "hua" to align w/ collateral
- Import of LLD example projects gives warning and build fails unless a macros.ini is imported manually
- Installer sometimes has an error at the end of the installation stage

- Browsing the MCSDK help content (UG and GSG) always crashes my Eclipse..
- On the information page the User switch 1/2 display "OFF" even though 1 is ON in static mode.
- Can not write HUA to NAND flash
- Text in browser after test is run appears chopped.
- RBL EMAC boot example and any necessary tools
- Eclipse plugin did not work after the new installer update
- DOC: Host boot utilities documentation in UG
- IBL build instruction/documentation confusion
- POST: Can not build Release build configuration for POST
- Platform Library: Need C6670-sim target support
- HUA : eeprom read causes application to crash on NY
- Boot example projects for 6670 are missing
- When written to NOR as bootable image on HUA failed to boot.
- HUA does not print debug messages on UART
- DOC: Need C6670 HW setup guide wiki page
- POST: Add ability to indicate SA-enabled device
- Nand writer on C6678 not working reliably...
- Many MCSDK examples don't work out of box
- Image processing demo should print messages in UART console
- HUA : Internal RAM test checks for valid core between  $1 \leq \text{core\_id} < 6$  instead of  $1 \leq \text{core\_id} < 8$ .

### 9.2.10 Build 2.0.0.8

This is an update to Beta-1.

Component	Version	Installer
PDK-C6678	1.0.0.8	MCSDK
PDK-C6670	1.0.0.8	MCSDK
Platform Library (C6678)	2.0.0.5	MCSDK
Platform Library (C6670)	2.0.0.1	MCSDK
NDK	2.20.03.24	MCSDK
IPC	1.22.03.23	CCS
EDMA3 LLD	2.11.1.2	MCSDK
SYS/BIOS	6.31.04.27	CCS

DSPLIB	3.0.0.5	MCSDK
IMGLIB	3.0.0.5	MCSDK
<b>MCSDK Tools</b>		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.2	MCSDK
MAD	1.0.0.0	MCSDK
EEPROM Writer	1.0.0.0	MCSDK
NAND Writer	1.0.0.0	MCSDK
NOR Writer	1.0.0.0	MCSDK
POST	1.0.0.2	MCSDK
<b>Demos</b>		
HUA Demo	2.0.0.1	MCSDK
Image Processing Demo	1.0.0.0	MCSDK
<b>Other Tools</b>		
CodeGen	7.2.0	CCS
CCS	5.0.2.00006	<a href="#">download</a>
C66x Functional Simulator	1.0.0	CCS
XDCTools	3.20.08.88	CCS
CG_XML	2.2	<a href="#">download</a>
Python	2.7.1	<a href="#">download</a>

New features

- C6670 EVM support added

Issues fixed in this Release

id	Severity	State	Headline	Found In Product	Found In Release Version	Affected Targets
SDOCM00079340	Major	Planned	On system reset, sometimes Ethernet packets are not received	BIOS-MCSDK	02.00.00	C6678
SDOCM00079839	Minor	Planned	Installer sometimes has an error at the end of the installation stage	BIOS-MCSDK	02.00.00	C6678
SDOCM00079937	Minor	Planned	DOC: BIOS MCSDK release notes link in Eclipse is broken	BIOS-MCSDK	02.00.00	C6678
SDOCM00080174	Critical	Planned	Automatic software update via Eclipse does not work after updating installer to beta1a	BIOS-MCSDK	02.00.00	C6678

## Other bug fixes:

- In Eclipse help the release note link for MCSDK is broken
- POST does not display FPGA version
- PLATFORM LIB: platform\_lib\_lite needs some documentation on intent/purpose
- PDK LLD UserGuide: Document the LLD user guide information in wiki Userguide

**9.2.11 Build 2.0.0.6**

This is the Beta-1 release of BIOS MCSDK 2.0.

Component	Version	Installer
PDK-C6678	1.0.0.6	MCSDK
PDK-C6670	1.0.0.6	MCSDK
Platform Library (C6678)	2.0.0.5	MCSDK
NDK	2.20.03.24	MCSDK
IPC	1.22.03.23	CCS
EDMA3 LLD	2.11.1.2	MCSDK
SYS/BIOS	6.31.04.27	CCS
DSPLIB	3.0.0.5	MCSDK
IMGLIB	3.0.0.5	MCSDK
<b>MCSDK Tools</b>		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.1	MCSDK
MAD	1.0.0.0	MCSDK
EEPROM Writer	1.0.0.0	MCSDK
NAND Writer	1.0.0.0	MCSDK
NOR Writer	1.0.0.0	MCSDK
POST	1.0.0.0	MCSDK
<b>Demos</b>		
HUA Demo	2.0.0.1	MCSDK
Image Processing Demo	1.0.0.0	MCSDK
<b>Other Tools</b>		
CodeGen	7.2.0	CCS
CCS	5.0.2.00006	<a href="#">download</a>
C66x Functional Simulator	1.0.0	CCS
XDCTools	3.20.08.88	CCS
CG_XML	2.2	<a href="#">download</a>
Python	2.7.1	<a href="#">download</a>

## New features

- Initial Multicore SDK for KeyStone devices
- C6678 EVM support
- C6678/C6670 simulator support

Issues fixed in this Release

- None (this is the first release)