




# **MSP430 DriverLib for MSP430F5xx\_6xx Devices**

## **User's Guide**

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# 1 Introduction

The Texas Instruments® MSP430® Peripheral Driver Library is a set of drivers for accessing the peripherals found on the MSP430 5xx/6xx family of microcontrollers. While they are not drivers in the pure operating system sense (that is, they do not have a common interface and do not connect into a global device driver infrastructure), they do provide a mechanism that makes it easy to use the device's peripherals.

The capabilities and organization of the drivers are governed by the following design goals:

- They are written entirely in C except where absolutely not possible.
- They demonstrate how to use the peripheral in its common mode of operation.
- They are easy to understand.
- They are reasonably efficient in terms of memory and processor usage.
- They are as self-contained as possible.
- Where possible, computations that can be performed at compile time are done there instead of at run time.
- They can be built with more than one tool chain.

Some consequences of these design goals are:

- The drivers are not necessarily as efficient as they could be (from a code size and/or execution speed point of view). While the most efficient piece of code for operating a peripheral would be written in assembly and custom tailored to the specific requirements of the application, further size optimizations of the drivers would make them more difficult to understand.
- The drivers do not support the full capabilities of the hardware. Some of the peripherals provide complex capabilities which cannot be utilized by the drivers in this library, though the existing code can be used as a reference upon which to add support for the additional capabilities.
- The APIs have a means of removing all error checking code. Because the error checking is usually only useful during initial program development, it can be removed to improve code size and speed.

For many applications, the drivers can be used as is. But in some cases, the drivers will have to be enhanced or rewritten in order to meet the functionality, memory, or processing requirements of the application. If so, the existing driver can be used as a reference on how to operate the peripheral.

Each MSP430ware driverlib API takes in the base address of the corresponding peripheral as the first parameter. This base address is obtained from the msp430 device specific header files (or from the device datasheet). The example code for the various peripherals show how base address is used. When using CCS, the eclipse shortcut "Ctrl + Space" helps. Type `_MSP430` and "Ctrl + Space", and the list of base addresses from the included device specific header files is listed.

The following tool chains are supported:

- IAR Embedded Workbench®
- Texas Instruments Code Composer Studio™

Using assert statements to debug

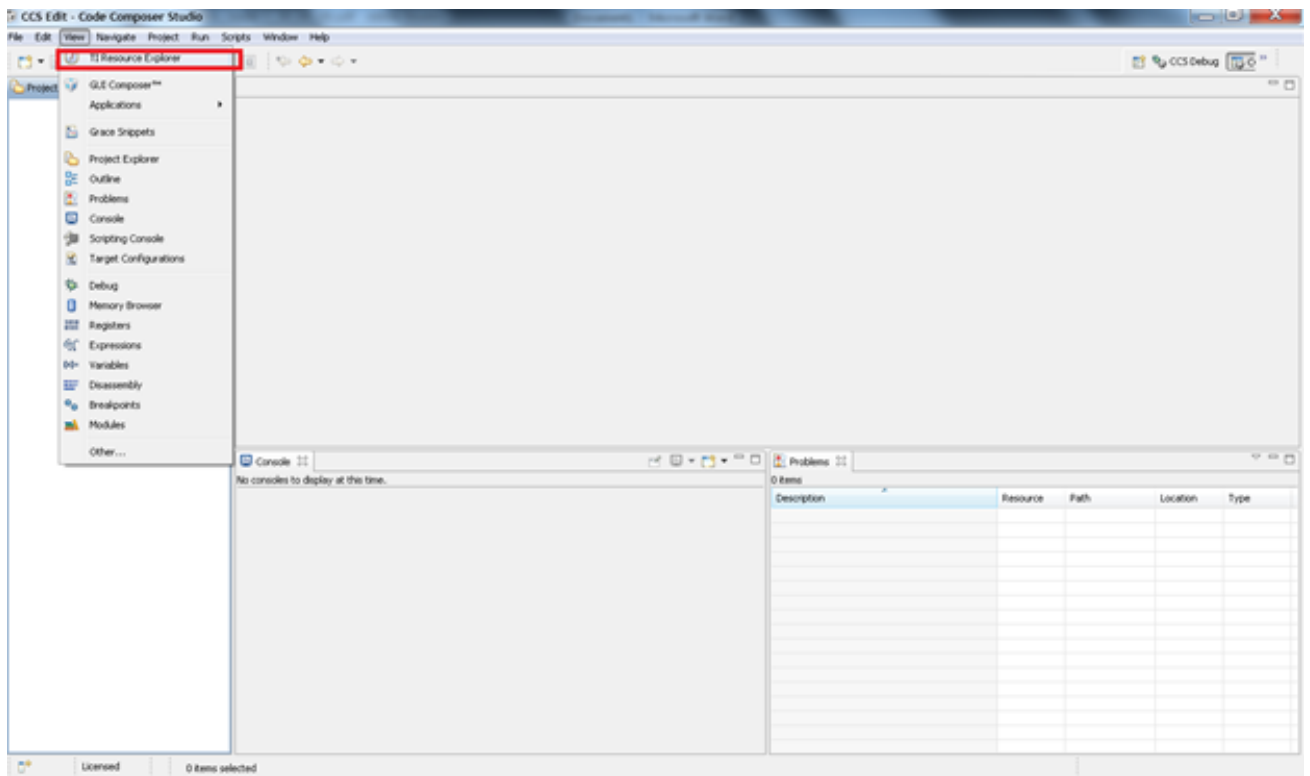


Assert statements are disabled by default. To enable the assert statement edit the `hw_regaccess.h` file in the `inc` folder. Comment out the statement `#define NDEBUG` -> `//#define NDEBUG` Asserts in CCS work only if the project is optimized for size.

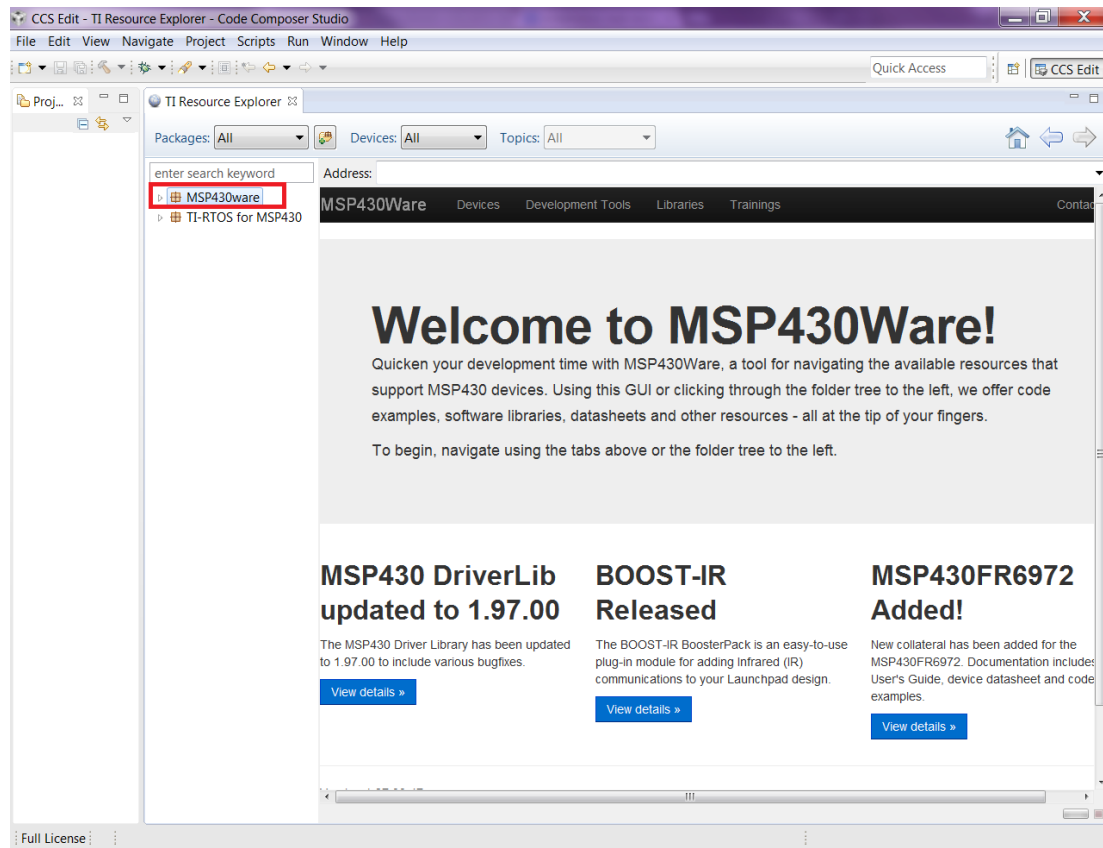
## 2 Navigating to driverlib through CCS Resource Explorer

### 2.1 Introduction

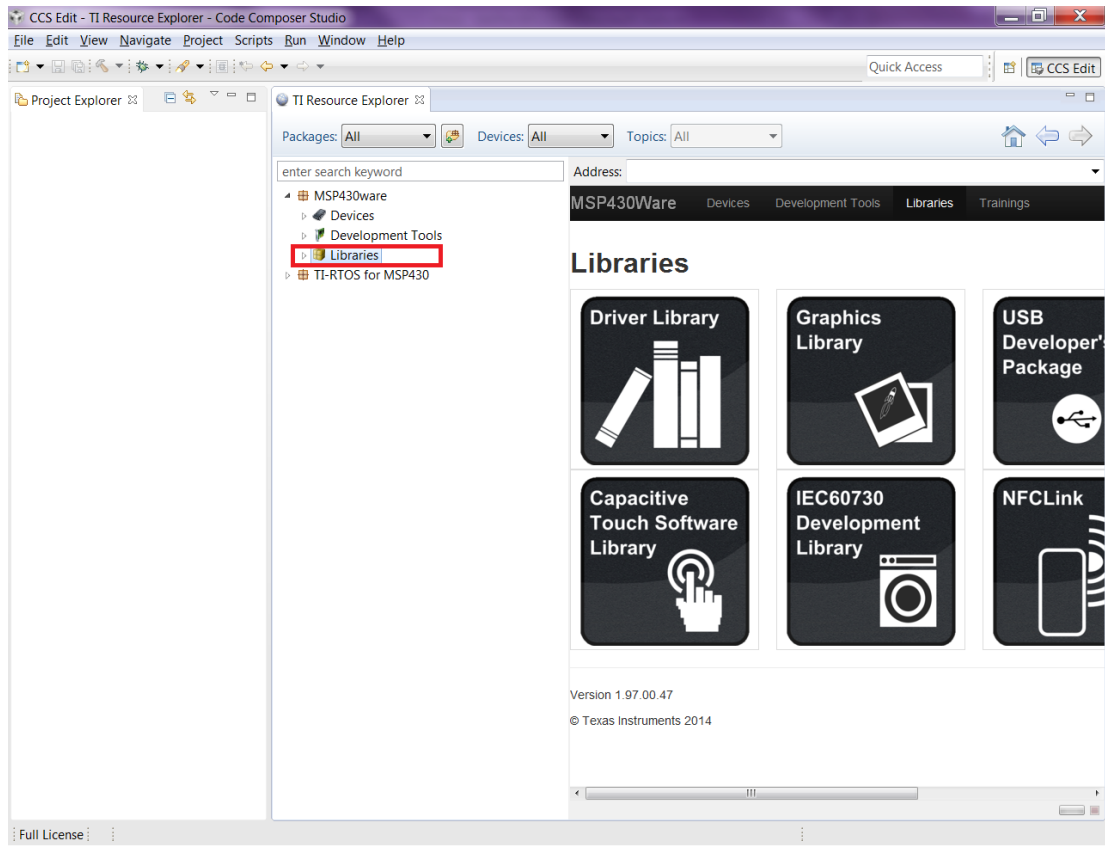
In CCS, click View->TI Resource Explorer

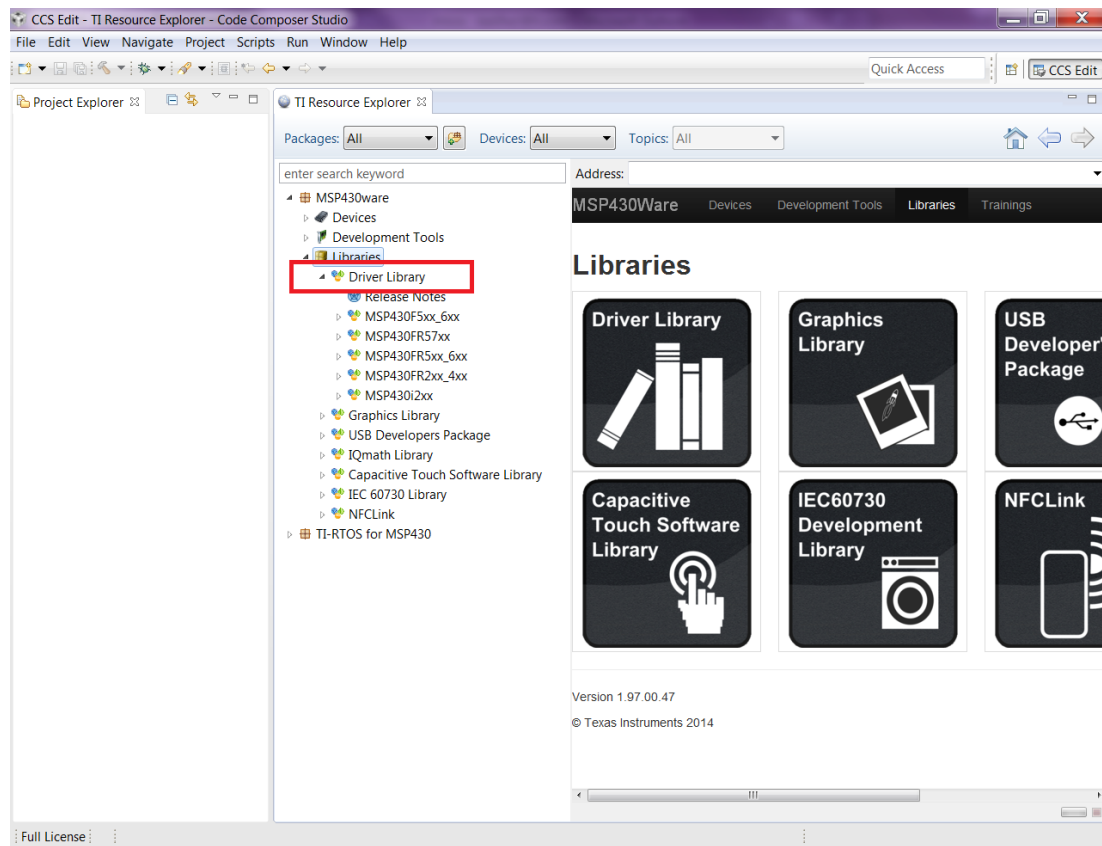


In Resource Explorer View, click on MSP430ware

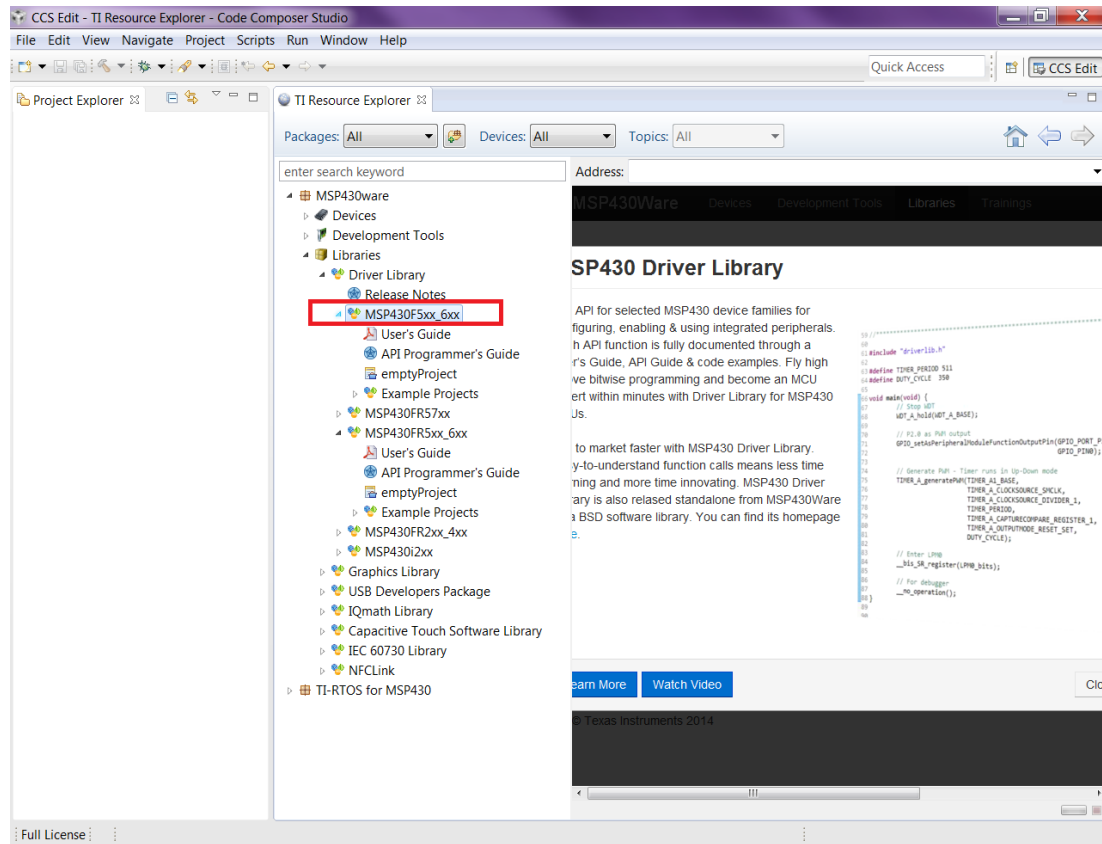


Clicking MSP430ware takes you to the introductory page. The version of the latest MSP430ware installed is available in this page. In this screenshot the version is 1.30.00.15 The various software, collateral, code examples, datasheets and user guides can be navigated by clicking the different topics under MSP430ware. To proceed to driverlib, click on Libraries->Driverlib as shown in the next two screenshots.

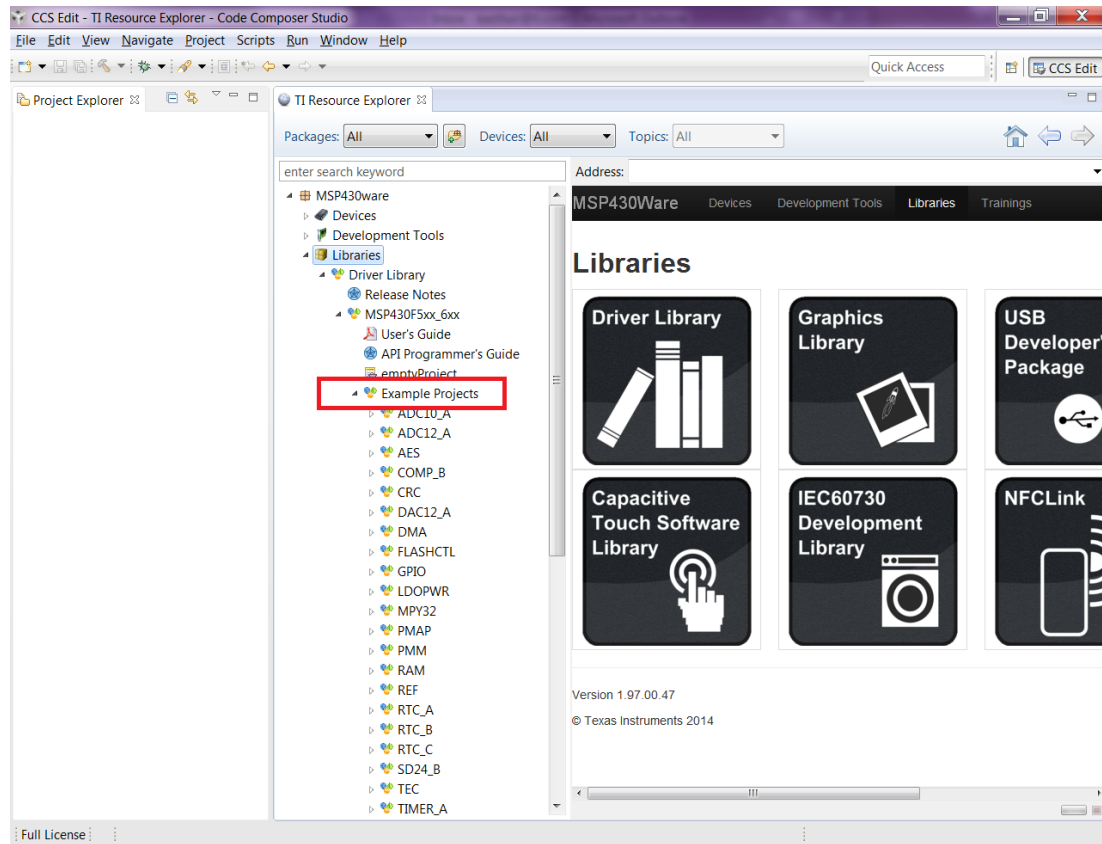




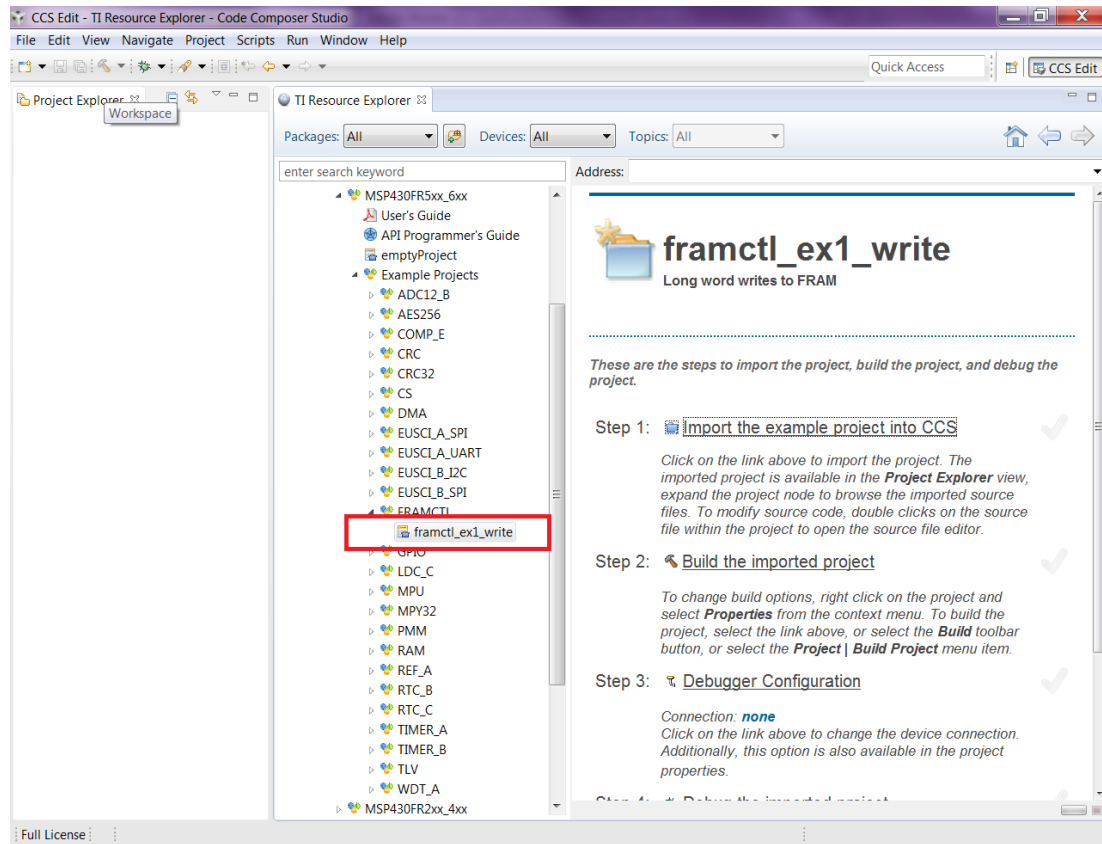
Driverlib is designed per Family. If a common device family user's guide exists for a group of devices, these devices belong to the same 'family'. Currently driverlib is available for the following family of devices. MSP430F5xx\_6xx MSP430FR57xx MSP430FR2xx\_4xx MSP430FR5xx\_6xx MSP430i2xx



Click on the MSP430F5xx\_6xx to navigate to the driverlib based example code for that family.



The various peripherals are listed in alphabetical order. The names of peripherals are as in device family user's guide. Clicking on a peripheral name lists the driverlib example code for that peripheral. The screenshot below shows an example when the user clicks on GPIO peripheral.



Now click on the specific example you are interested in. On the right side there are options to Import/Build/Download and Debug. Import the project by clicking on the "Import the example project into CCS"



The screenshot shows the CCS Resource Explorer interface. On the left, the Project Explorer tree is expanded to show the 'framctl\_ex1\_write' project under the 'FRAMCTL' folder. The main pane displays the following content:

**framctl\_ex1\_write**  
Long word writes to FRAM

These are the steps to import the project, build the project, and debug the project.

**Step 1:** [Import the example project into CCS](#)

Click on the link above to import the project. The imported project is available in the **Project Explorer** view, expand the project node to browse the imported source files. To modify source code, double clicks on the source file within the project to open the source file editor.

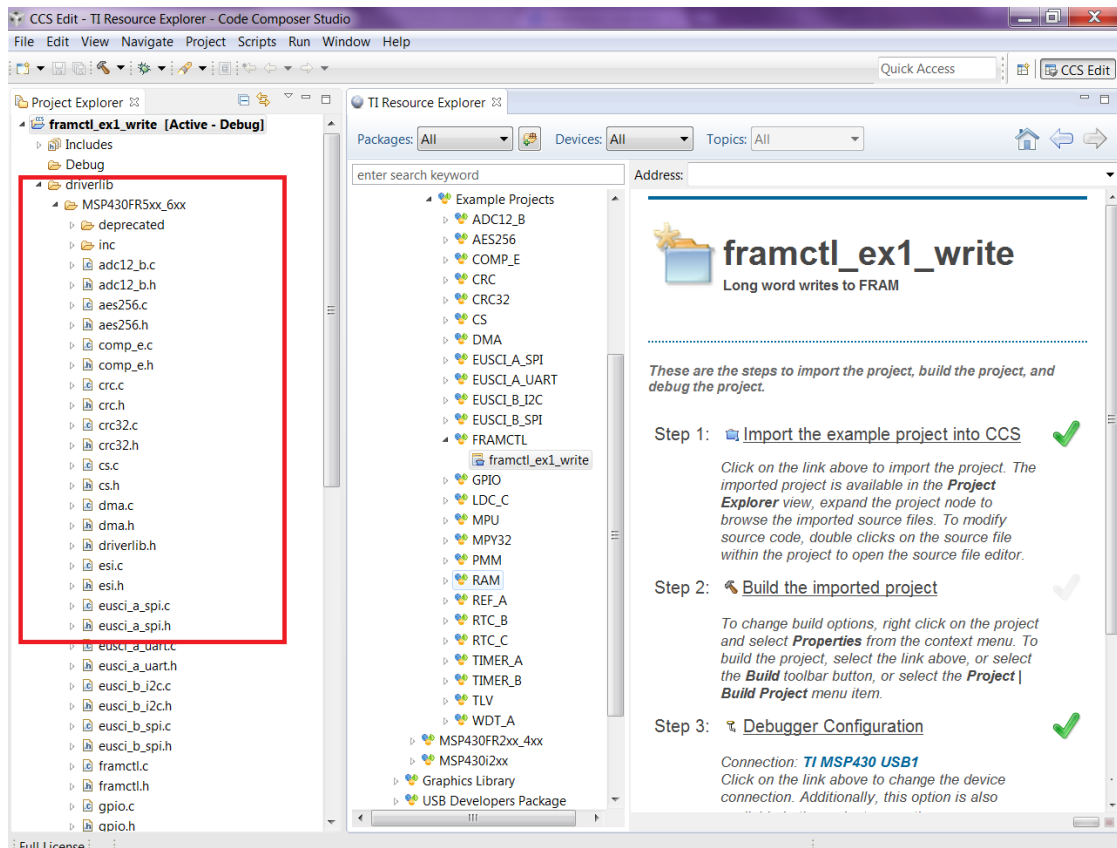
**Step 2:** [Build the imported project](#)

To change build options, right click on the project and select **Properties** from the context menu. To build the project, select the link above, or select the **Build** toolbar button, or select the **Project | Build Project** menu item.

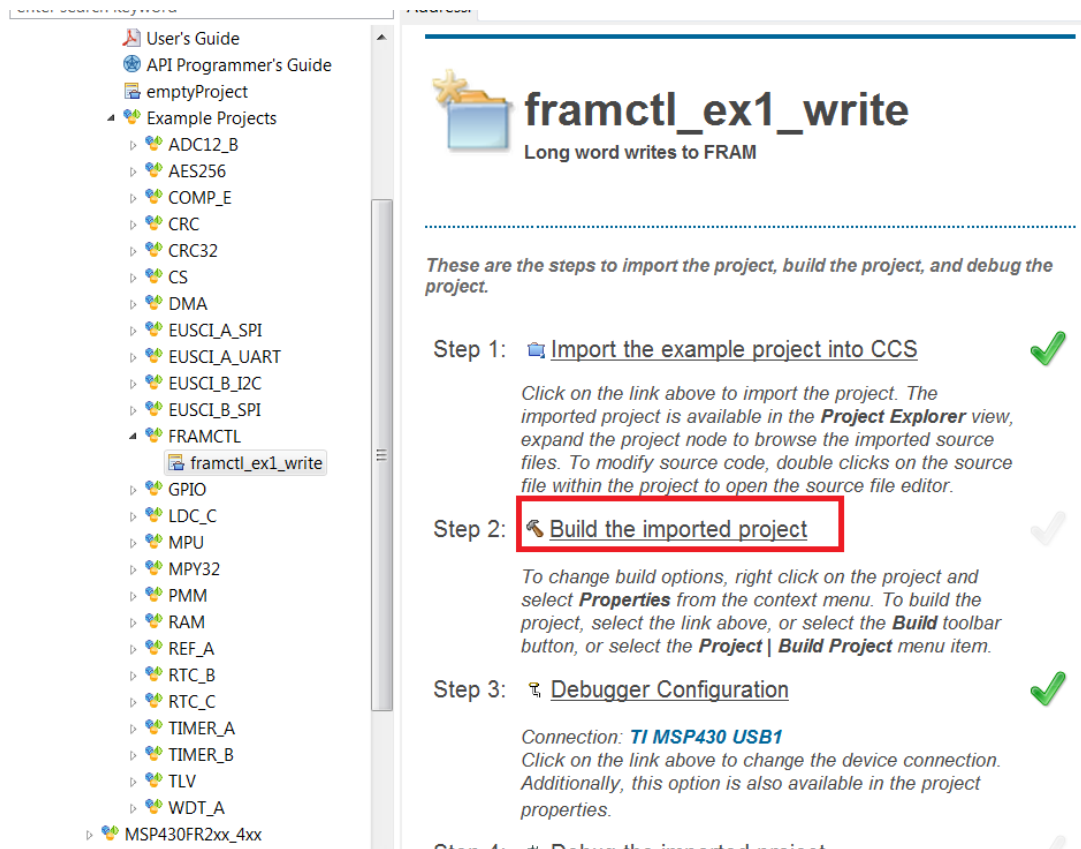
**Step 3:** [Debugger Configuration](#)

Connection: **TI MSP430 USB1**  
Click on the link above to change the device connection. Additionally, this option is also available in the project properties.

The imported project can be viewed on the left in the Project Explorer. All required driverlib source and header files are included inside the driverlib folder. All driverlib source and header files are linked to the example projects. So if the user modifies any of these source or header files, the original copy of the installed MSP430ware driverlib source and header files get modified.



Now click on Build the imported project on the right to build the example project.



The screenshot shows the CCS Resource Explorer on the left and a tutorial page for the project 'framctl\_ex1\_write' on the right. The project tree on the left includes 'User's Guide', 'API Programmer's Guide', 'emptyProject', 'Example Projects' (with sub-items like ADC12\_B, AES256, COMP\_E, CRC, CRC32, CS, DMA, EUSCLA\_SPI, EUSCLA\_UART, EUSCLB\_I2C, EUSCLB\_SPI, FRAMCTL, framctl\_ex1\_write, GPIO, LDC\_C, MPU, MPY32, PMM, RAM, REF\_A, RTC\_B, RTC\_C, TIMER\_A, TIMER\_B, TLV, WDT\_A), and 'MSP430FR2xx\_4xx'. The tutorial page on the right has a title 'framctl\_ex1\_write' with the subtitle 'Long word writes to FRAM'. Below the title, it says 'These are the steps to import the project, build the project, and debug the project.' The steps are:

- Step 1: [Import the example project into CCS](#) (checked)
- Step 2: [Build the imported project](#) (highlighted with a red box, unchecked)
- Step 3: [Debugger Configuration](#) (checked)

Step 2 description: 'To change build options, right click on the project and select **Properties** from the context menu. To build the project, select the link above, or select the **Build** toolbar button, or select the **Project | Build Project** menu item.'

Step 3 description: 'Connection: **TI MSP430 USB1**. Click on the link above to change the device connection. Additionally, this option is also available in the project properties.'

Now click on Build the imported project on the right to build the example project.

**Step 1:** [Import the example project into CCS](#)

Click on the link above to import the project. The imported project is available in the **Project Explorer** view, expand the project node to browse the imported source files. To modify source code, double clicks on the source file within the project to open the source file editor.

**Step 2:** [Build the imported project](#)

To change build options, right click on the project and select **Properties** from the context menu. To build the project, select the link above, or select the **Build** toolbar button, or select the **Project | Build Project** menu item.

**Step 3:** [Debugger Configuration](#)

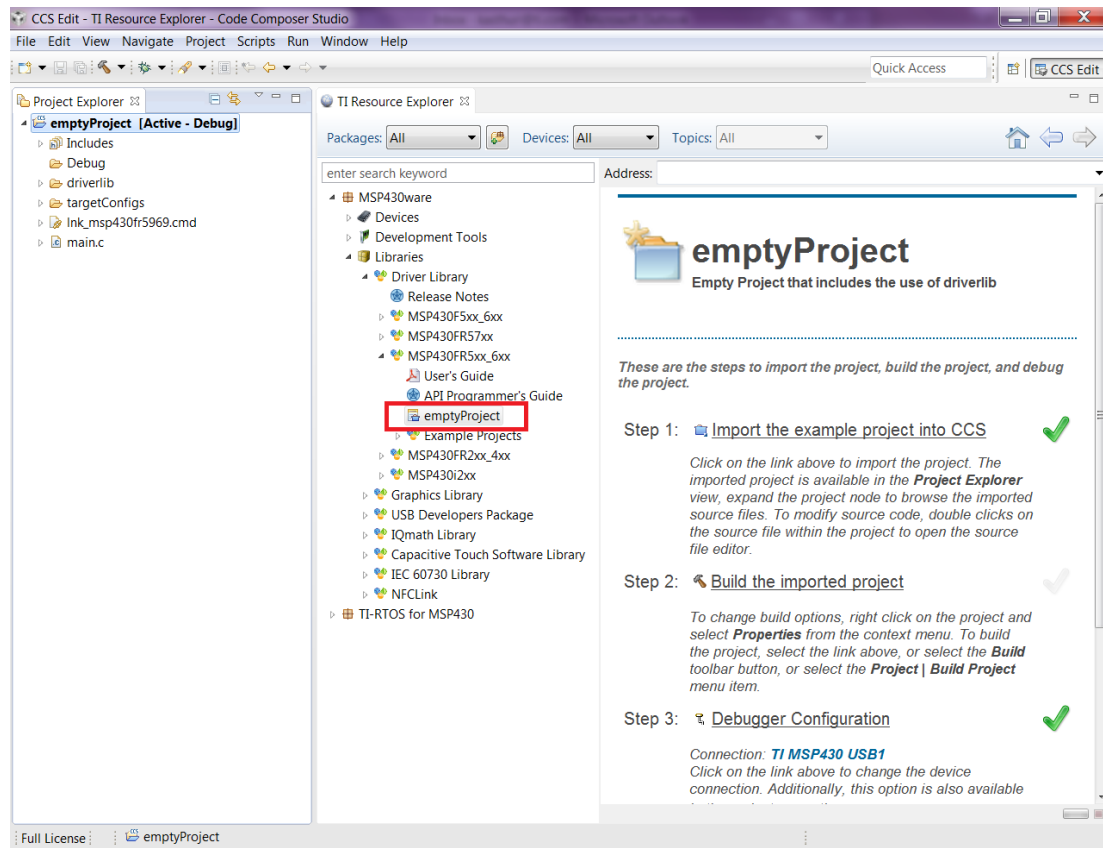
Connection: **TI MSP430 USB1**  
Click on the link above to change the device connection. Additionally, this option is also available in the project properties.

**Step 4:** [Debug the imported project](#)

Click on the link above to launch a debug session for the **framctl\_ex1\_write** project and switch to the **CCS Debug Perspective**. Additionally, these are other methods to start a project debug session. Select the project in the **Project Explorer** view and click on the bug toolbar button. To relaunch a previous debug session, click on the small arrow beside the bug toolbar button and select one of the debug session from the history.

The COM port to download to can be changed using the Debugger Configuration option on the right if required.

To get started on a new project we recommend getting started on an empty project we provide. This project has all the driverlib source files, header files, project paths are set by default.

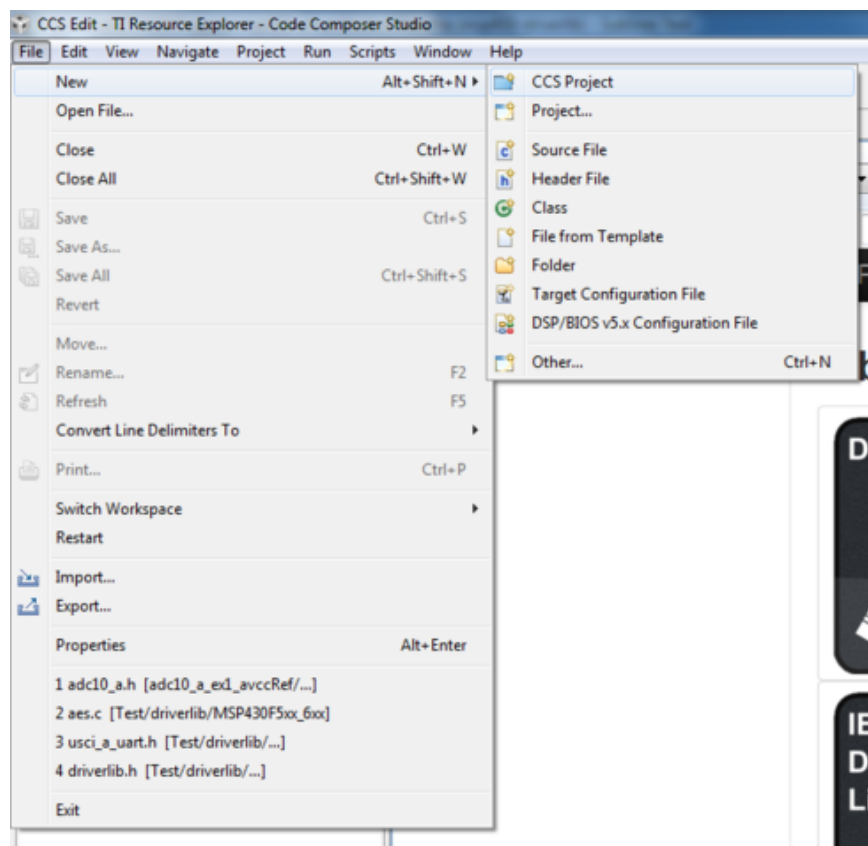


The main.c included with the empty project can be modified to include user code.

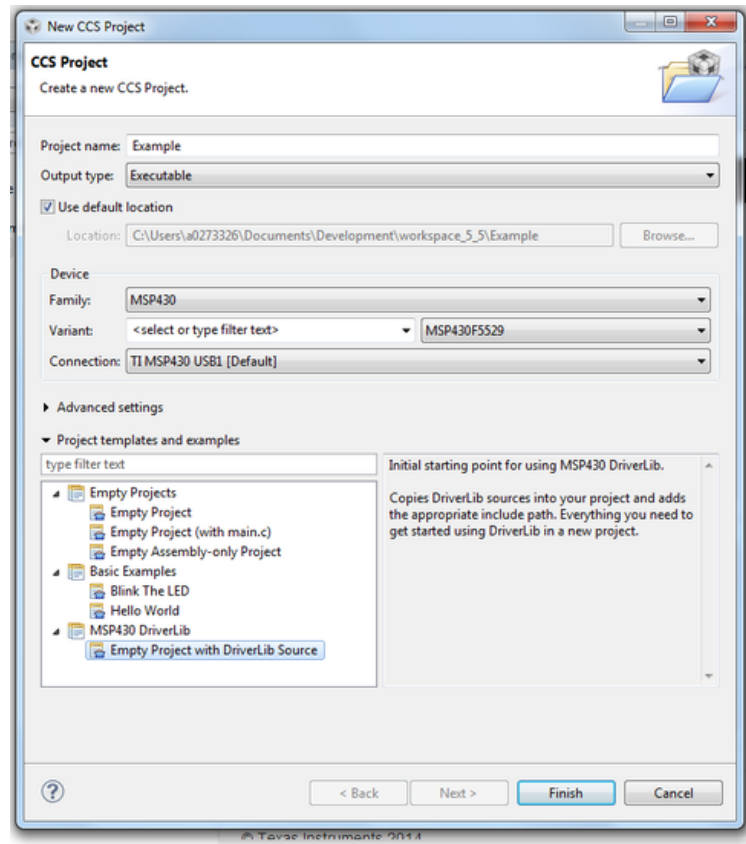
## 3 How to create a new CCS project that uses Driverlib

### 3.1 Introduction

To get started on a new project we recommend using the new project wizard. For driver library to work with the new project wizard CCS must have discovered the driver library RTSC product. For more information refer to the installation steps of the release notes. The new project wizard adds the needed driver library source files and adds the driver library include path. To open the new project wizard go to File -> New -> CCS Project as seen in the screenshot below.



Once the new project wizard has been opened name your project and choose the device you would like to create a Driver Library project for. The device must be supported by driver library. Then under "Project templates and examples" choose "Empty Project with DriverLib Source" as seen below.



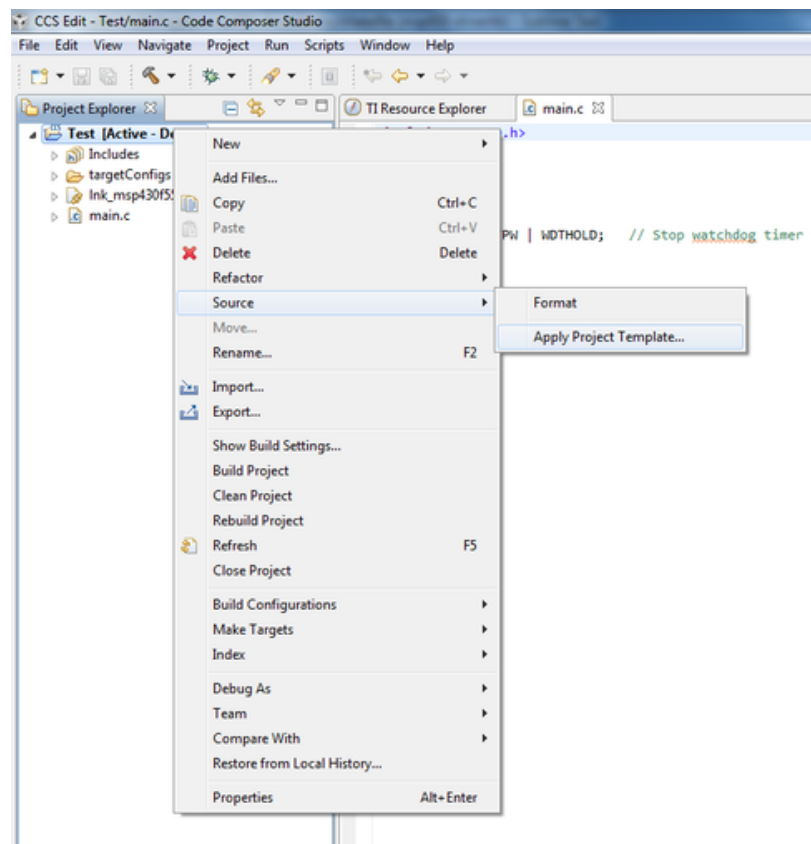
Finally click "Finish" and begin developing with your Driver Library enabled project.

We recommend -O4 compiler settings for more efficient optimizations for projects using driverlib

# 4 How to include driverlib into your existing CCS project

## 4.1 Introduction

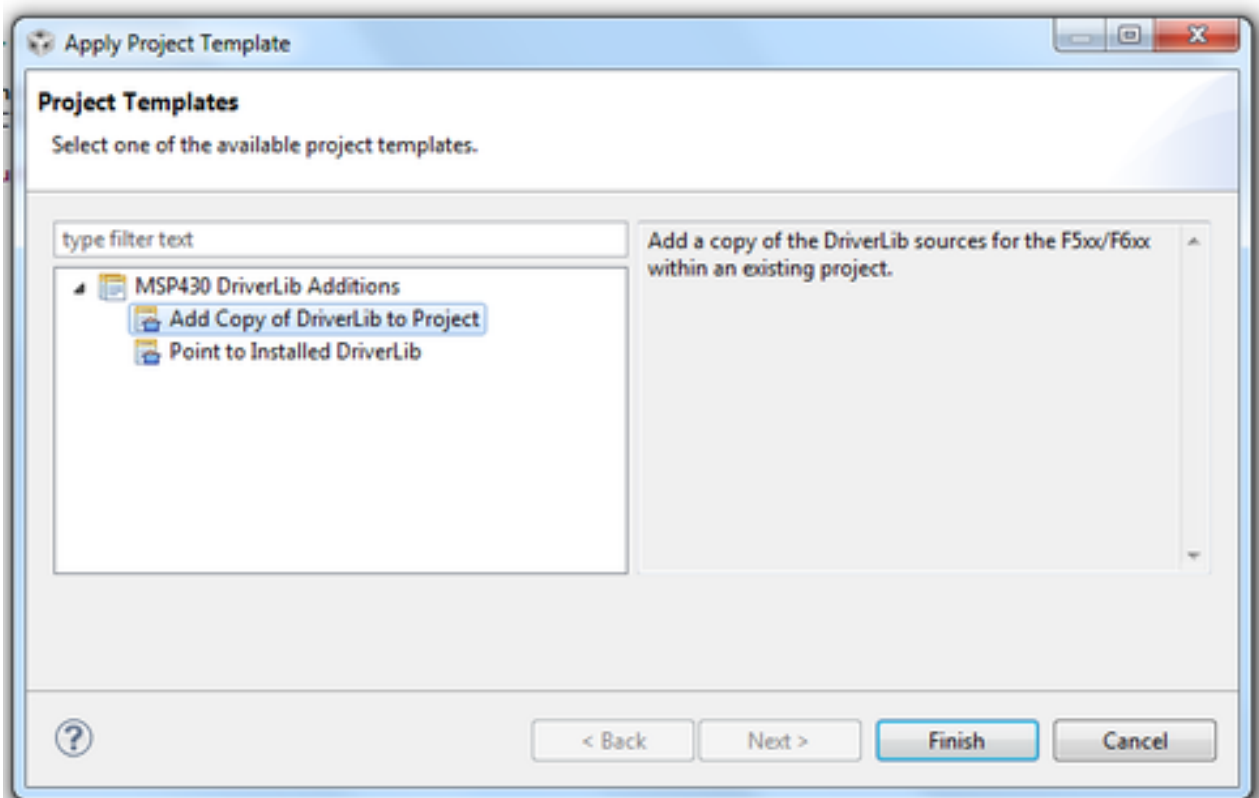
To add driver library to an existing project we recommend using CCS project templates. For driver library to work with project templates CCS must have discovered the driver library RTSC product. For more information refer to the installation steps of the release notes. CCS project templates adds the needed driver library source files and adds the driver library include path. To apply a project template right click on an existing project then go to Source -> Apply Project Template as seen in the screenshot below.



In the "Apply Project Template" dialog box under "MSP430 DriverLib Additions" choose either "Add Local Copy" or "Point to Installed DriverLib" as seen in the screenshot below. Most users will want to add a local copy which copies the DriverLib source into the project and sets the compiler settings needed.

Pointing to an installed DriverLib is for advanced users who are including a static library in their project and want to add the DriverLib header files to their include path.



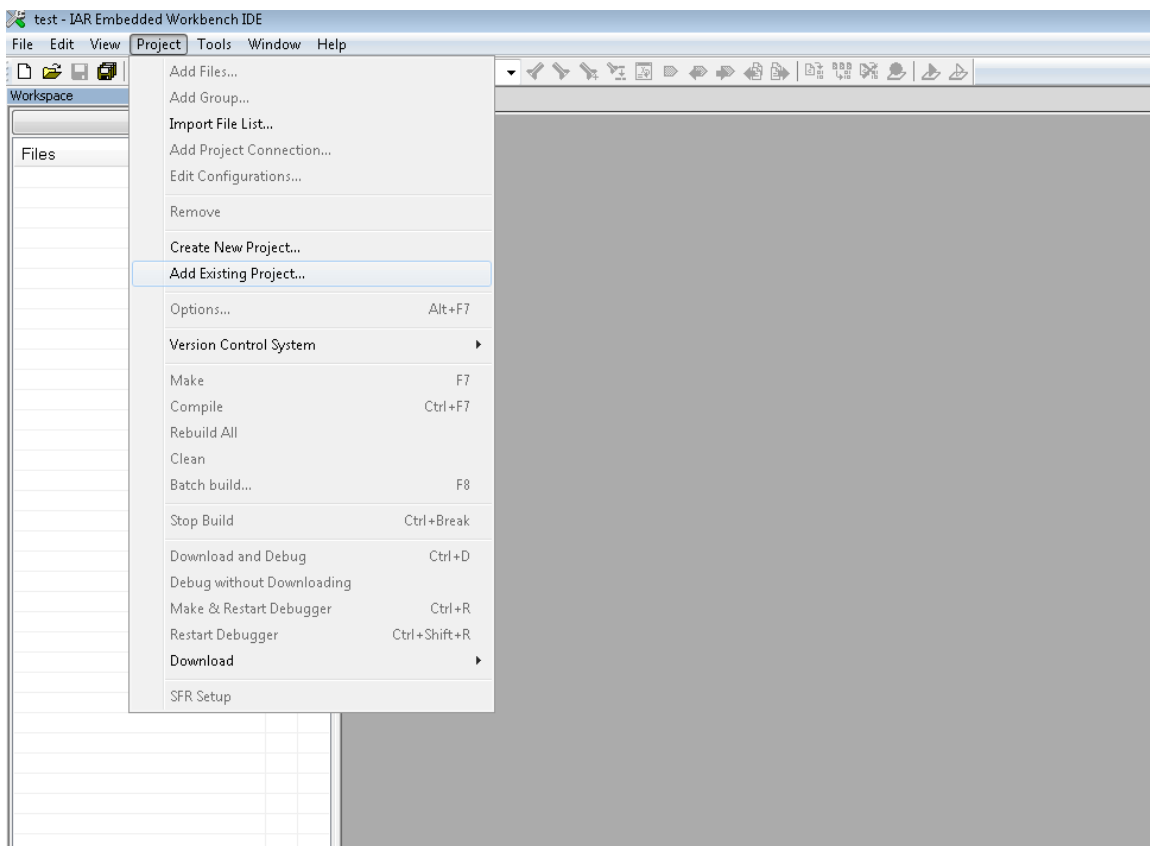


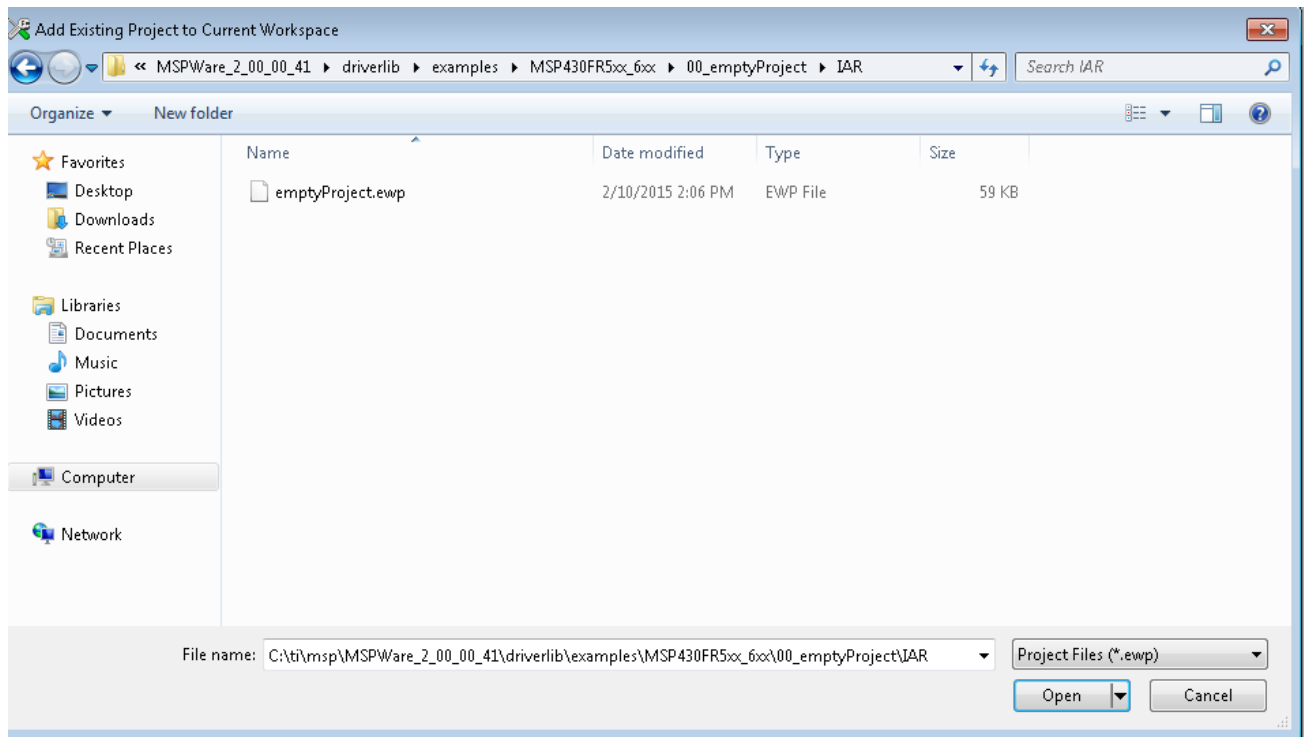
Click "Finish" and start developing with driver library in your project.

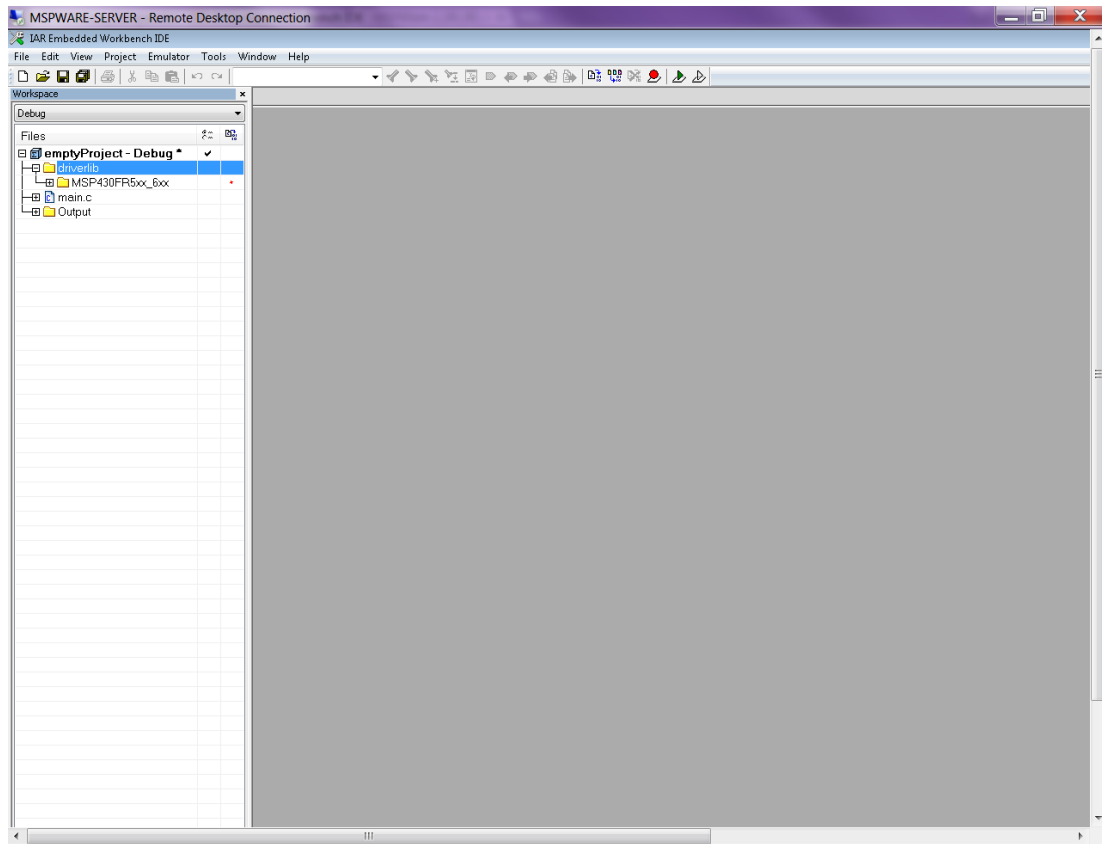
# 5 How to create a new IAR project that uses Driverlib

## 5.1 Introduction

It is recommended to get started with an Empty Driverlib Project. Browse to the empty project in your device's family. This is available in the driverlib instal folder\00\_emptyProject



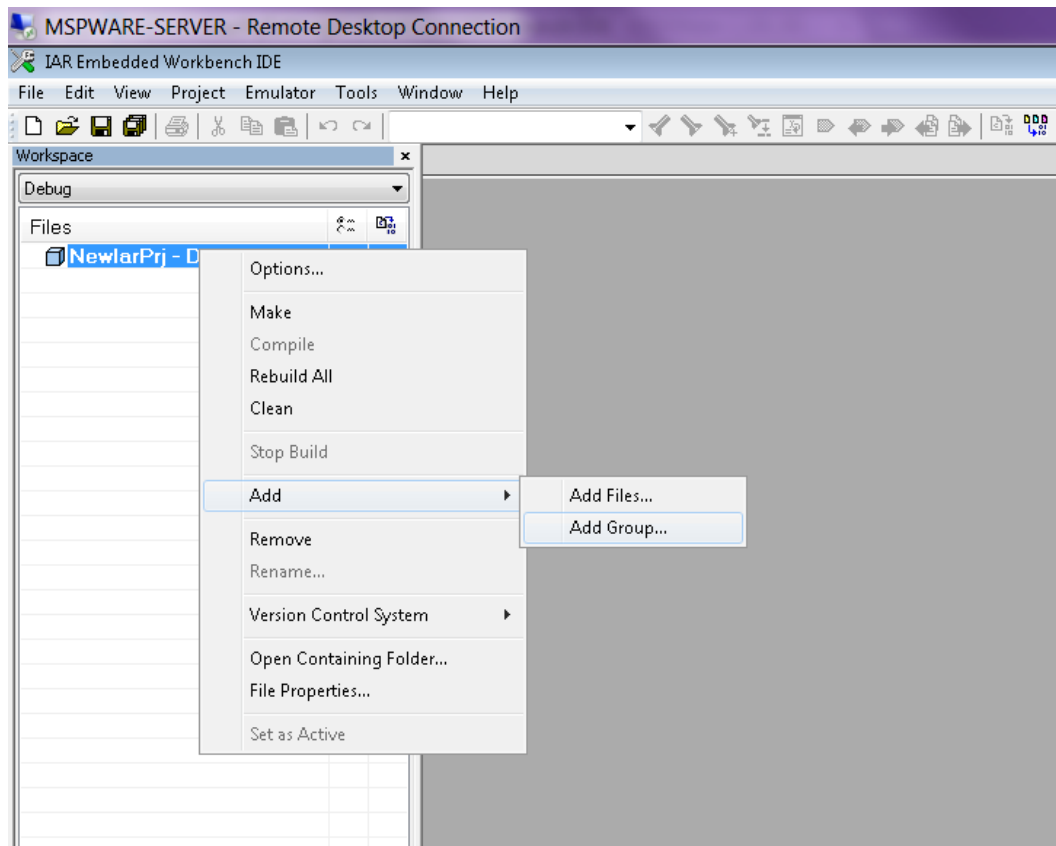




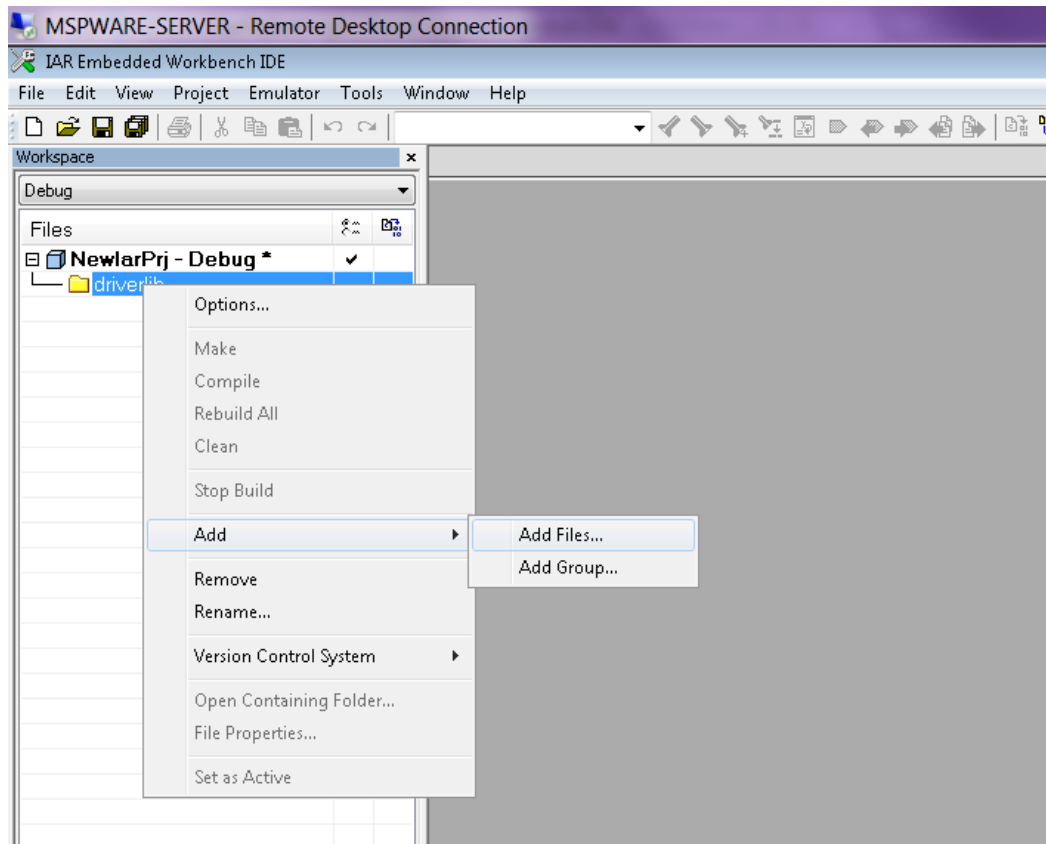
# 6 How to include driverlib into your existing IAR project

## 6.1 Introduction

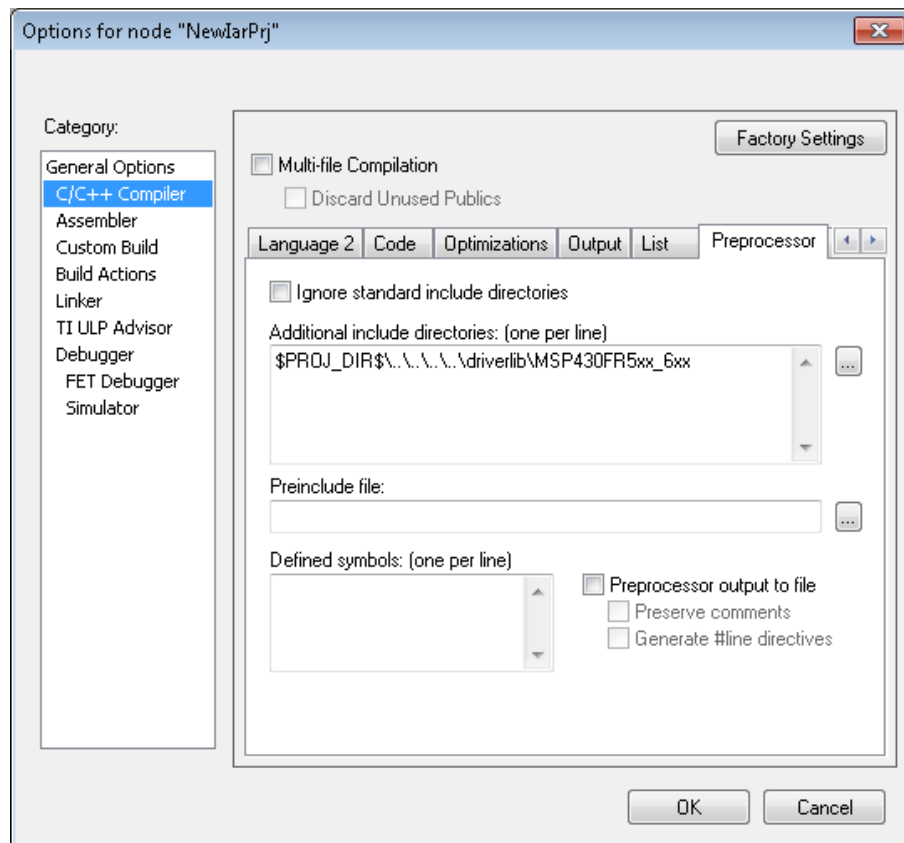
To add driver library to an existing project, right click project click on Add Group - "driverlib"



Now click Add files and browse through driverlib folder and add all source files of the family the device belongs to.



Add another group via "Add Group" and add inc folder. Add all files in the same driverlib family inc folder



Click "Finish" and start developing with driver library in your project.

# 7 10-Bit Analog-to-Digital Converter (ADC10\_A)

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## 7.1 Introduction

The 10-Bit Analog-to-Digital (ADC10\_A) API provides a set of functions for using the MSP430Ware ADC10\_A modules. Functions are provided to initialize the ADC10\_A modules, setup signal sources and reference voltages, and manage interrupts for the ADC10\_A modules.

The ADC10\_A module provides the ability to convert analog signals into a digital value in respect to given reference voltages. The ADC10\_A can generate digital values from 0 to V<sub>cc</sub> with an 8- or 10-bit resolution. It operates in 2 different sampling modes, and 4 different conversion modes. The sampling modes are extended sampling and pulse sampling, in extended sampling the sample/hold signal must stay high for the duration of sampling, while in pulse mode a sampling timer is setup to start on a rising edge of the sample/hold signal and sample for a specified amount of clock cycles. The 4 conversion modes are single-channel single conversion, sequence of channels single-conversion, repeated single channel conversions, and repeated sequence of channels conversions.

The ADC10\_A module can generate multiple interrupts. An interrupt can be asserted when a conversion is complete, when a conversion is about to overwrite the converted data in the memory buffer before it has been read out, and/or when a conversion is about to start before the last conversion is complete. The ADC10\_A also has a window comparator feature which asserts interrupts when the input signal is above a high threshold, below a low threshold, or between the two at any given moment.

## 7.2 API Functions

### Functions

- `bool ADC10_A_init` (uint16\_t baseAddress, uint16\_t sampleHoldSignalSourceSelect, uint8\_t clockSourceSelect, uint16\_t clockSourceDivider)  
*Initializes the ADC10\_A Module.*
- `void ADC10_A_enable` (uint16\_t baseAddress)  
*Enables the ADC10\_A block.*
- `void ADC10_A_disable` (uint16\_t baseAddress)  
*Disables the ADC10\_A block.*
- `void ADC10_A_setupSamplingTimer` (uint16\_t baseAddress, uint16\_t clockCycleHoldCount, uint16\_t multipleSamplesEnabled)  
*Sets up and enables the Sampling Timer Pulse Mode.*
- `void ADC10_A_disableSamplingTimer` (uint16\_t baseAddress)  
*Disables Sampling Timer Pulse Mode.*



- void `ADC10_A_configureMemory` (uint16\_t baseAddress, uint8\_t inputSourceSelect, uint8\_t positiveRefVoltageSourceSelect, uint8\_t negativeRefVoltageSourceSelect)  
*Configures the controls of the selected memory buffer.*
- void `ADC10_A_enableInterrupt` (uint16\_t baseAddress, uint8\_t interruptMask)  
*Enables selected ADC10\_A interrupt sources.*
- void `ADC10_A_disableInterrupt` (uint16\_t baseAddress, uint8\_t interruptMask)  
*Disables selected ADC10\_A interrupt sources.*
- void `ADC10_A_clearInterrupt` (uint16\_t baseAddress, uint8\_t interruptFlagMask)  
*Clears ADC10\_A selected interrupt flags.*
- uint16\_t `ADC10_A_getInterruptStatus` (uint16\_t baseAddress, uint8\_t interruptFlagMask)  
*Returns the status of the selected memory interrupt flags.*
- void `ADC10_A_startConversion` (uint16\_t baseAddress, uint8\_t conversionSequenceModeSelect)  
*Enables/Starts an Analog-to-Digital Conversion.*
- void `ADC10_A_disableConversions` (uint16\_t baseAddress, bool preempt)  
*Disables the ADC from converting any more signals.*
- int16\_t `ADC10_A_getResults` (uint16\_t baseAddress)  
*Returns the raw contents of the specified memory buffer.*
- void `ADC10_A_setResolution` (uint16\_t baseAddress, uint8\_t resolutionSelect)  
*Use to change the resolution of the converted data.*
- void `ADC10_A_setSampleHoldSignalInversion` (uint16\_t baseAddress, uint16\_t invertedSignal)  
*Use to invert or un-invert the sample/hold signal.*
- void `ADC10_A_setDataReadBackFormat` (uint16\_t baseAddress, uint16\_t readBackFormat)  
*Use to set the read-back format of the converted data.*
- void `ADC10_A_enableReferenceBurst` (uint16\_t baseAddress)  
*Enables the reference buffer's burst ability.*
- void `ADC10_A_disableReferenceBurst` (uint16\_t baseAddress)  
*Disables the reference buffer's burst ability.*
- void `ADC10_A_setReferenceBufferSamplingRate` (uint16\_t baseAddress, uint16\_t samplingRateSelect)  
*Use to set the reference buffer's sampling rate.*
- void `ADC10_A_setWindowComp` (uint16\_t baseAddress, uint16\_t highThreshold, uint16\_t lowThreshold)  
*Sets the high and low threshold for the window comparator feature.*
- uint32\_t `ADC10_A_getMemoryAddressForDMA` (uint16\_t baseAddress)  
*Returns the address of the memory buffer for the DMA module.*
- uint16\_t `ADC10_A_isBusy` (uint16\_t baseAddress)  
*Returns the busy status of the ADC10\_A core.*

## 7.2.1 Detailed Description

The ADC10\_A API is broken into three groups of functions: those that deal with initialization and conversions, those that handle interrupts, and those that handle auxiliary features of the ADC10\_A.

The ADC10\_A initialization and conversion functions are

- `ADC10_A_init()`
- `ADC10_A_configureMemory()`
- `ADC10_A_setupSamplingTimer()`
- `ADC10_A_disableSamplingTimer()`
- `ADC10_A_setWindowComp()`

- `ADC10_A.startConversion()`
- `ADC10_A.disableConversions()`
- `ADC10_A.getResults()`
- `ADC10_A.isBusy()`

The ADC10\_A interrupts are handled by

- `ADC10_A.enableInterrupt()`
- `ADC10_A.disableInterrupt()`
- `ADC10_A.clearInterrupt()`
- `ADC10_A.getInterruptStatus()`

Auxiliary features of the ADC10\_A are handled by

- `ADC10_A.setResolution()`
- `ADC10_A.setSampleHoldSignalInversion()`
- `ADC10_A.setDataReadBackFormat()`
- `ADC10_A.enableReferenceBurst()`
- `ADC10_A.disableReferenceBurst()`
- `ADC10_A.setReferenceBufferSamplingRate()`
- `ADC10_A.getMemoryAddressForDMA()`
- `ADC10_A.enable()`
- `ADC10_A.disable()`

## 7.2.2 Function Documentation

### ADC10\_A\_clearInterrupt()

```
void ADC10_A_clearInterrupt (
    uint16_t baseAddress,
    uint8_t interruptFlagMask )
```

Clears ADC10\_A selected interrupt flags.

The selected ADC10\_A interrupt flags are cleared, so that it no longer asserts. The memory buffer interrupt flags are only cleared when the memory buffer is accessed.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--

## Parameters

<i>interruptFlagMask</i>	<p>is a bit mask of the interrupt flags to be cleared. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_A_TIMEOVERFLOW_INTFLAG</b> - Interrupts flag when a new conversion is starting before the previous one has finished</li> <li>■ <b>ADC10_A_OVERFLOW_INTFLAG</b> - Interrupts flag when a new conversion is about to overwrite the previous one</li> <li>■ <b>ADC10_A_ABOVETHRESHOLD_INTFLAG</b> - Interrupts flag when the input signal has gone above the high threshold of the window comparator</li> <li>■ <b>ADC10_A_BELOWTHRESHOLD_INTFLAG</b> - Interrupts flag when the input signal has gone below the low threshold of the low window comparator</li> <li>■ <b>ADC10_A_INSIDEWINDOW_INTFLAG</b> - Interrupts flag when the input signal is in between the high and low thresholds of the window comparator</li> <li>■ <b>ADC10_A_COMPLETED_INTFLAG</b> - Interrupt flag for new conversion data in the memory buffer</li> </ul>
--------------------------	--

Modified bits of **ADC10IFG** register.

## Returns

None

## ADC10\_A\_configureMemory()

```
void ADC10_A_configureMemory (
    uint16_t baseAddress,
    uint8_t inputSourceSelect,
    uint8_t positiveRefVoltageSourceSelect,
    uint8_t negativeRefVoltageSourceSelect )
```

Configures the controls of the selected memory buffer.

Maps an input signal conversion into the memory buffer, as well as the positive and negative reference voltages for each conversion being stored into the memory buffer. If the internal reference is used for the positive reference voltage, the internal REF module has to control the voltage level. Note that if a conversion has been started with the startConversion() function, then a call to disableConversions() is required before this function may be called. If conversion is not disabled, this function does nothing.

## Parameters

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--

## Parameters

<i>inputSourceSelect</i>	<p>is the input that will store the converted data into the specified memory buffer. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_A_INPUT_A0</b> [Default]</li> <li>■ <b>ADC10_A_INPUT_A1</b></li> <li>■ <b>ADC10_A_INPUT_A2</b></li> <li>■ <b>ADC10_A_INPUT_A3</b></li> <li>■ <b>ADC10_A_INPUT_A4</b></li> <li>■ <b>ADC10_A_INPUT_A5</b></li> <li>■ <b>ADC10_A_INPUT_A6</b></li> <li>■ <b>ADC10_A_INPUT_A7</b></li> <li>■ <b>ADC10_A_INPUT_A8</b></li> <li>■ <b>ADC10_A_INPUT_A9</b></li> <li>■ <b>ADC10_A_INPUT_TEMPSENSOR</b></li> <li>■ <b>ADC10_A_INPUT_BATTERYMONITOR</b></li> <li>■ <b>ADC10_A_INPUT_A12</b></li> <li>■ <b>ADC10_A_INPUT_A13</b></li> <li>■ <b>ADC10_A_INPUT_A14</b></li> <li>■ <b>ADC10_A_INPUT_A15</b></li> </ul> <p>Modified bits are <b>ADC10INCHx</b> of <b>ADC10MCTL0</b> register.</p>
<i>positiveRefVoltageSourceSelect</i>	<p>is the reference voltage source to set as the upper limit for the conversion that is to be stored in the specified memory buffer. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_A_VREFPOS_AVCC</b> [Default]</li> <li>■ <b>ADC10_A_VREFPOS_EXT</b></li> <li>■ <b>ADC10_A_VREFPOS_INT</b></li> </ul> <p>Modified bits are <b>ADC10SREF</b> of <b>ADC10MCTL0</b> register.</p>
<i>negativeRefVoltageSourceSelect</i>	<p>is the reference voltage source to set as the lower limit for the conversion that is to be stored in the specified memory buffer. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_A_VREFNEG_AVSS</b></li> <li>■ <b>ADC10_A_VREFNEG_EXT</b></li> </ul> <p>Modified bits are <b>ADC10SREF</b> of <b>ADC10CTL0</b> register.</p>

## Returns

None

## ADC10\_A\_disable()

```
void ADC10_A_disable (
    uint16_t baseAddress )
```

Disables the ADC10\_A block.

This will disable operation of the ADC10\_A block.

### Parameters

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--

Modified bits are **ADC10ON** of **ADC10CTL0** register.

### Returns

None

## ADC10\_A\_disableConversions()

```
void ADC10_A_disableConversions (
    uint16_t baseAddress,
    bool preempt )
```

Disables the ADC from converting any more signals.

Disables the ADC from converting any more signals. If there is a conversion in progress, this function can stop it immediately if the preempt parameter is set as **ADC10\_A\_PREEMPTCONVERSION**, by changing the conversion mode to single-channel, single-conversion and disabling conversions. If the conversion mode is set as single-channel, single-conversion and this function is called without preemption, then the ADC core conversion status is polled until the conversion is complete before disabling conversions to prevent unpredictable data. If the [ADC10\\_A\\_startConversion\(\)](#) has been called, then this function has to be called to re-initialize the ADC, reconfigure a memory buffer control, enable/disable the sampling pulse mode, or change the internal reference voltage.

### Parameters

<i>baseAddress</i>	is the base address of the ADC10_A module.
<i>preempt</i>	<p>specifies if the current conversion should be pre-empted before the end of the conversion Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_A_COMPLETECONVERSION</b> - Allows the ADC10_A to end the current conversion before disabling conversions.</li> <li>■ <b>ADC10_A_PREEMPTCONVERSION</b> - Stops the ADC10_A immediately, with unpredictable results of the current conversion. Cannot be used with repeated conversion.</li> </ul>

Modified bits of **ADC10CTL1** register and bits of **ADC10CTL0** register.

**Returns**

None

**ADC10\_A\_disableInterrupt()**

```
void ADC10_A_disableInterrupt (
    uint16_t baseAddress,
    uint8_t interruptMask )
```

Disables selected ADC10\_A interrupt sources.

Disables the indicated ADC10\_A interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
<i>interruptMask</i>	is the bit mask of the memory buffer interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>ADC10_A_TIMEOVERFLOW_INT</b> - Interrupts when a new conversion is starting before the previous one has finished</li> <li>■ <b>ADC10_A_OVERFLOW_INT</b> - Interrupts when a new conversion is about to overwrite the previous one</li> <li>■ <b>ADC10_A_ABOVETHRESHOLD_INT</b> - Interrupts when the input signal has gone above the high threshold of the window comparator</li> <li>■ <b>ADC10_A_BELOWTHRESHOLD_INT</b> - Interrupts when the input signal has gone below the low threshold of the low window comparator</li> <li>■ <b>ADC10_A_INSIDEWINDOW_INT</b> - Interrupts when the input signal is in between the high and low thresholds of the window comparator</li> <li>■ <b>ADC10_A_COMPLETED_INT</b> - Interrupt for new conversion data in the memory buffer</li> </ul>

Modified bits of **ADC10IE** register.

**Returns**

None

**ADC10\_A\_disableReferenceBurst()**

```
void ADC10_A_disableReferenceBurst (
    uint16_t baseAddress )
```

Disables the reference buffer's burst ability.

Disables the reference buffer's burst ability, forcing the reference buffer to remain on continuously.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--

**Returns**

None

**ADC10\_A\_disableSamplingTimer()**

```
void ADC10_A_disableSamplingTimer (  
    uint16_t baseAddress )
```

Disables Sampling Timer Pulse Mode.

Disables the Sampling Timer Pulse Mode. Note that if a conversion has been started with the startConversion() function, then a call to disableConversions() is required before this function may be called.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--

**Returns**

None

**ADC10\_A\_enable()**

```
void ADC10_A_enable (  
    uint16_t baseAddress )
```

Enables the ADC10\_A block.

This will enable operation of the ADC10\_A block.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--

Modified bits are **ADC10ON** of **ADC10CTL0** register.**Returns**

None

**ADC10\_A\_enableInterrupt()**

```
void ADC10_A_enableInterrupt (  
    uint16_t baseAddress )
```

```
uint16_t baseAddress,
uint8_t interruptMask )
```

Enables selected ADC10\_A interrupt sources.

Enables the indicated ADC10\_A interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC10_A module.
<i>interruptMask</i>	is the bit mask of the memory buffer interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>ADC10_A_TIMEOVERFLOW_INT</b> - Interrupts when a new conversion is starting before the previous one has finished</li> <li>■ <b>ADC10_A_OVERFLOW_INT</b> - Interrupts when a new conversion is about to overwrite the previous one</li> <li>■ <b>ADC10_A_ABOVETHRESHOLD_INT</b> - Interrupts when the input signal has gone above the high threshold of the window comparator</li> <li>■ <b>ADC10_A_BELOWTHRESHOLD_INT</b> - Interrupts when the input signal has gone below the low threshold of the low window comparator</li> <li>■ <b>ADC10_A_INSIDEWINDOW_INT</b> - Interrupts when the input signal is in between the high and low thresholds of the window comparator</li> <li>■ <b>ADC10_A_COMPLETED_INT</b> - Interrupt for new conversion data in the memory buffer</li> </ul>

Modified bits of **ADC10IE** register.

#### Returns

None

### ADC10\_A\_enableReferenceBurst()

```
void ADC10_A_enableReferenceBurst (
uint16_t baseAddress )
```

Enables the reference buffer's burst ability.

Enables the reference buffer's burst ability, allowing the reference buffer to turn off while the ADC is not converting, and automatically turning on when the ADC needs the generated reference voltage for a conversion.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--



**Returns**

None

**ADC10\_A\_getInterruptStatus()**

```
uint16_t ADC10_A_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t interruptFlagMask )
```

Returns the status of the selected memory interrupt flags.

Returns the status of the selected interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
<i>interruptFlagMask</i>	<p>is a bit mask of the interrupt flags status to be returned. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_A_TIMEOVERFLOW_INTFLAG</b> - Interrupts flag when a new conversion is starting before the previous one has finished</li> <li>■ <b>ADC10_A_OVERFLOW_INTFLAG</b> - Interrupts flag when a new conversion is about to overwrite the previous one</li> <li>■ <b>ADC10_A_ABOVETHRESHOLD_INTFLAG</b> - Interrupts flag when the input signal has gone above the high threshold of the window comparator</li> <li>■ <b>ADC10_A_BELOWTHRESHOLD_INTFLAG</b> - Interrupts flag when the input signal has gone below the low threshold of the low window comparator</li> <li>■ <b>ADC10_A_INSIDEWINDOW_INTFLAG</b> - Interrupts flag when the input signal is in between the high and low thresholds of the window comparator</li> <li>■ <b>ADC10_A_COMPLETED_INTFLAG</b> - Interrupt flag for new conversion data in the memory buffer</li> </ul>

**Returns**

The current interrupt flag status for the corresponding mask.

**ADC10\_A\_getMemoryAddressForDMA()**

```
uint32_t ADC10_A_getMemoryAddressForDMA (
    uint16_t baseAddress )
```

Returns the address of the memory buffer for the DMA module.

Returns the address of the memory buffer. This can be used in conjunction with the DMA to store the converted data directly to memory.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--

**Returns**

The memory address of the memory buffer

**ADC10\_A\_getResults()**

```
int16_t ADC10_A_getResults (
    uint16_t baseAddress )
```

Returns the raw contents of the specified memory buffer.

Returns the raw contents of the specified memory buffer. The format of the content depends on the read-back format of the data: if the data is in signed 2's complement format then the contents in the memory buffer will be left-justified with the least-significant bits as 0's, whereas if the data is in unsigned format then the contents in the memory buffer will be right-justified with the most-significant bits as 0's.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--

**Returns**

A Signed Integer of the contents of the specified memory buffer.

**ADC10\_A\_init()**

```
bool ADC10_A_init (
    uint16_t baseAddress,
    uint16_t sampleHoldSignalSourceSelect,
    uint8_t clockSourceSelect,
    uint16_t clockSourceDivider )
```

Initializes the ADC10\_A Module.

This function initializes the ADC module to allow for analog-to-digital conversions. Specifically this function sets up the sample-and-hold signal and clock sources for the ADC core to use for conversions. Upon successful completion of the initialization all of the ADC control registers will be reset, excluding the memory controls and reference module bits, the given parameters will be set, and the ADC core will be turned on (Note, that the ADC core only draws power during conversions and remains off when not converting). Note that sample/hold signal sources are device dependent. Note that if re-initializing the ADC after starting a conversion with the startConversion() function, the disableConversion() must be called BEFORE this function can be called.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--

## Parameters

<i>sampleHoldSignalSourceSelect</i>	<p>is the signal that will trigger a sample-and-hold for an input signal to be converted. This parameter is device specific and sources should be found in the device's datasheet Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_A_SAMPLEHOLDSOURCE_SC</b></li> <li>■ <b>ADC10_A_SAMPLEHOLDSOURCE_1</b></li> <li>■ <b>ADC10_A_SAMPLEHOLDSOURCE_2</b></li> <li>■ <b>ADC10_A_SAMPLEHOLDSOURCE_3</b></li> </ul> <p>Modified bits are <b>ADC10SHSx</b> of <b>ADC10CTL1</b> register.</p>
<i>clockSourceSelect</i>	<p>selects the clock that will be used by the ADC10_A core and the sampling timer if a sampling pulse mode is enabled. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_A_CLOCKSOURCE_ADC10OSC</b> [Default] - MODOSC 5 MHz oscillator from the UCS</li> <li>■ <b>ADC10_A_CLOCKSOURCE_ACLK</b> - The Auxiliary Clock</li> <li>■ <b>ADC10_A_CLOCKSOURCE_MCLK</b> - The Master Clock</li> <li>■ <b>ADC10_A_CLOCKSOURCE_SMCLK</b> - The Sub-Master Clock</li> </ul> <p>Modified bits are <b>ADC10SSELx</b> of <b>ADC10CTL1</b> register.</p>

## Parameters

<i>clockSourceDivider</i>	<p>selects the amount that the clock will be divided. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_A_CLOCKDIVIDER_1</b> [Default]</li> <li>■ <b>ADC10_A_CLOCKDIVIDER_2</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_3</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_4</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_5</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_6</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_7</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_8</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_12</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_16</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_20</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_24</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_28</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_32</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_64</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_128</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_192</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_256</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_320</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_384</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_448</b></li> <li>■ <b>ADC10_A_CLOCKDIVIDER_512</b></li> </ul> <p>Modified bits are <b>ADC10DIVx</b> of <b>ADC10CTL1</b> register; bits <b>ADC10PDIVx</b> of <b>ADC10CTL2</b> register.</p>
---------------------------	--

## Returns

STATUS\_SUCCESS or STATUS\_FAILURE of the initialization process.

## ADC10\_A\_isBusy()

```
uint16_t ADC10_A_isBusy (
    uint16_t baseAddress )
```

Returns the busy status of the ADC10\_A core.

Returns the status of the ADC core if there is a conversion currently taking place.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--

**Returns**

One of the following:

- **ADC10\_A\_BUSY**
- **ADC10\_A\_NOTBUSY**  
indicating if there is a conversion currently taking place

**ADC10\_A\_setDataReadBackFormat()**

```
void ADC10_A_setDataReadBackFormat (
    uint16_t baseAddress,
    uint16_t readBackFormat )
```

Use to set the read-back format of the converted data.

Sets the format of the converted data: how it will be stored into the memory buffer, and how it should be read back. The format can be set as right-justified (default), which indicates that the number will be unsigned, or left-justified, which indicates that the number will be signed in 2's complement format. This change affects all memory buffers for subsequent conversions.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
<i>readBackFormat</i>	is the specified format to store the conversions in the memory buffer. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_A_UNSIGNED_BINARY</b> [Default]</li> <li>■ <b>ADC10_A_SIGNED_2SCOMPLEMENT</b> Modified bits are <b>ADC10DF</b> of <b>ADC10CTL2</b> register.</li> </ul>

**Returns**

None

**ADC10\_A\_setReferenceBufferSamplingRate()**

```
void ADC10_A_setReferenceBufferSamplingRate (
    uint16_t baseAddress,
    uint16_t samplingRateSelect )
```

Use to set the reference buffer's sampling rate.

Sets the reference buffer's sampling rate to the selected sampling rate. The default sampling rate is maximum of 200-ksps, and can be reduced to a maximum of 50-ksps to conserve power.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
<i>samplingRateSelect</i>	is the specified maximum sampling rate. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_A_MAXSAMPLINGRATE_200KSPS</b> [Default]</li> <li>■ <b>ADC10_A_MAXSAMPLINGRATE_50KSPS</b></li> </ul> Modified bits are <b>ADC10SR</b> of <b>ADC10CTL2</b> register.

**Returns**

None

**ADC10\_A\_setResolution()**

```
void ADC10_A_setResolution (
    uint16_t baseAddress,
    uint8_t resolutionSelect )
```

Use to change the resolution of the converted data.

This function can be used to change the resolution of the converted data from the default of 12-bits.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
<i>resolutionSelect</i>	determines the resolution of the converted data. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_A_RESOLUTION_8BIT</b></li> <li>■ <b>ADC10_A_RESOLUTION_10BIT</b> [Default]</li> </ul> Modified bits are <b>ADC10RES</b> of <b>ADC10CTL2</b> register.

**Returns**

None

**ADC10\_A\_setSampleHoldSignalInversion()**

```
void ADC10_A_setSampleHoldSignalInversion (
    uint16_t baseAddress,
    uint16_t invertedSignal )
```

Use to invert or un-invert the sample/hold signal.

This function can be used to invert or un-invert the sample/hold signal. Note that if a conversion has been started with the `startConversion()` function, then a call to `disableConversions()` is required before this function may be called.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
<i>invertedSignal</i>	set if the sample/hold signal should be inverted Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_A_NONINVERTEDSIGNAL</b> [Default] - a sample-and-hold of an input signal for conversion will be started on a rising edge of the sample/hold signal.</li> <li>■ <b>ADC10_A_INVERTEDSIGNAL</b> - a sample-and-hold of an input signal for conversion will be started on a falling edge of the sample/hold signal. Modified bits are <b>ADC10ISSH</b> of <b>ADC10CTL1</b> register.</li> </ul>

**Returns**

None

**ADC10\_A\_setupSamplingTimer()**

```
void ADC10_A_setupSamplingTimer (
    uint16_t baseAddress,
    uint16_t clockCycleHoldCount,
    uint16_t multipleSamplesEnabled )
```

Sets up and enables the Sampling Timer Pulse Mode.

This function sets up the sampling timer pulse mode which allows the sample/hold signal to trigger a sampling timer to sample-and-hold an input signal for a specified number of clock cycles without having to hold the sample/hold signal for the entire period of sampling. Note that if a conversion has been started with the startConversion() function, then a call to disableConversions() is required before this function may be called.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--

## Parameters

<i>clockCycleHoldCount</i>	<p>sets the amount of clock cycles to sample-and- hold for the memory buffer. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_A_CYCLEHOLD_4_CYCLES</b> [Default]</li> <li>■ <b>ADC10_A_CYCLEHOLD_8_CYCLES</b></li> <li>■ <b>ADC10_A_CYCLEHOLD_16_CYCLES</b></li> <li>■ <b>ADC10_A_CYCLEHOLD_32_CYCLES</b></li> <li>■ <b>ADC10_A_CYCLEHOLD_64_CYCLES</b></li> <li>■ <b>ADC10_A_CYCLEHOLD_96_CYCLES</b></li> <li>■ <b>ADC10_A_CYCLEHOLD_128_CYCLES</b></li> <li>■ <b>ADC10_A_CYCLEHOLD_192_CYCLES</b></li> <li>■ <b>ADC10_A_CYCLEHOLD_256_CYCLES</b></li> <li>■ <b>ADC10_A_CYCLEHOLD_384_CYCLES</b></li> <li>■ <b>ADC10_A_CYCLEHOLD_512_CYCLES</b></li> <li>■ <b>ADC10_A_CYCLEHOLD_768_CYCLES</b></li> <li>■ <b>ADC10_A_CYCLEHOLD_1024_CYCLES</b></li> </ul> <p>Modified bits are <b>ADC10SHTx</b> of <b>ADC10CTL0</b> register.</p>
<i>multipleSamplesEnabled</i>	<p>allows multiple conversions to start without a trigger signal from the sample/hold signal Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_A_MULTIPLESAMPLESDISABLE</b> - a timer trigger will be needed to start every ADC conversion.</li> <li>■ <b>ADC10_A_MULTIPLESAMPLESENABLE</b> - during a sequenced and/or repeated conversion mode, after the first conversion, no sample/hold signal is necessary to start subsequent samples.</li> </ul> <p>Modified bits are <b>ADC10MSC</b> of <b>ADC10CTL0</b> register.</p>

## Returns

None

## ADC10\_A\_setWindowComp()

```
void ADC10_A_setWindowComp (
    uint16_t baseAddress,
    uint16_t highThreshold,
    uint16_t lowThreshold )
```

Sets the high and low threshold for the window comparator feature.

Sets the high and low threshold for the window comparator feature. Use the ADC10HIIE, ADC10INIE, ADC10LOIE interrupts to utilize this feature.



**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
<i>highThreshold</i>	is the upper bound that could trip an interrupt for the window comparator.
<i>lowThreshold</i>	is the lower bound that could trip on interrupt for the window comparator.

**Returns**

None

**ADC10\_A\_startConversion()**

```
void ADC10_A_startConversion (
    uint16_t baseAddress,
    uint8_t conversionSequenceModeSelect )
```

Enables/Starts an Analog-to-Digital Conversion.

This function enables/starts the conversion process of the ADC. If the sample/hold signal source chosen during initialization was ADC10OSC, then the conversion is started immediately, otherwise the chosen sample/hold signal source starts the conversion by a rising edge of the signal. Keep in mind when selecting conversion modes, that for sequenced and/or repeated modes, to keep the sample/hold-and-convert process continuing without a trigger from the sample/hold signal source, the multiple samples must be enabled using the [ADC10\\_A\\_setupSamplingTimer\(\)](#) function. Also note that when a sequence conversion mode is selected, the first input channel is the one mapped to the memory buffer, the next input channel selected for conversion is one less than the input channel just converted (i.e. A1 comes after A2), until A0 is reached, and if in repeating mode, then the next input channel will again be the one mapped to the memory buffer. Note that after this function is called, the `ADC10_A_stopConversions()` has to be called to re-initialize the ADC, reconfigure a memory buffer control, enable/disable the sampling timer, or to change the internal reference voltage.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10_A module.
--------------------	--

## Parameters

<i>conversionSequenceModeSelect</i>	<p>determines the ADC operating mode. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_A_SINGLECHANNEL</b> [Default] - one-time conversion of a single channel into a single memory buffer</li> <li>■ <b>ADC10_A_SEQOFCHANNELS</b> - one time conversion of multiple channels into the specified starting memory buffer and each subsequent memory buffer up until the conversion is stored in a memory buffer dedicated as the end-of-sequence by the memory's control register</li> <li>■ <b>ADC10_A_REPEATED_SINGLECHANNEL</b> - repeated conversions of one channel into a single memory buffer</li> <li>■ <b>ADC10_A_REPEATED_SEQOFCHANNELS</b> - repeated conversions of multiple channels into the specified starting memory buffer and each subsequent memory buffer up until the conversion is stored in a memory buffer dedicated as the end-of-sequence by the memory's control register</li> </ul> <p>Modified bits are <b>ADC10CONSEQx</b> of <b>ADC10CTL1</b> register.</p>
-------------------------------------	--

## Returns

None

## 7.3 Programming Example

The following example shows how to initialize and use the ADC10\_A API to start a single channel, single conversion.

```
// Initialize ADC10_A with ADC10_A's built-in oscillator
ADC10_A_init (ADC10_A_BASE,
             ADC10_A_SAMPLEHOLDSOURCE_SC,
             ADC10_A_CLOCKSOURCE_ADC10_AOSC,
             ADC10_A_CLOCKDIVIDEBY_1);

//Switch ON ADC10_A
ADC10_A_enable (ADC10_A_BASE);

// Setup sampling timer to sample-and-hold for 16 clock cycles
ADC10_A_setupSamplingTimer (ADC10_A_BASE,
                           ADC10_A_CYCLEHOLD_16_CYCLES,
                           FALSE);

// Configure the Input to the Memory Buffer with the specified Reference Voltages
ADC10_A_configureMemory (ADC10_A_BASE,
                       ADC10_A_INPUT_A0,
                       ADC10_A_VREF_AVCC, // Vref+ = AVcc
                       ADC10_A_VREF_AVSS // Vref- = AVss
                       );

while (1)
{
    // Start a single conversion, no repeating or sequences.
}
```

```
ADC10_A.startConversion (ADC10_A.BASE,  
                        ADC10_A.SINGLECHANNEL);  
  
// Wait for the Interrupt Flag to assert  
while( !(ADC10_A.getInterruptStatus(ADC10_A.BASE,ADC10_A.AIFG0)) );  
  
// Clear the Interrupt Flag and start another conversion  
ADC10_A.clearInterrupt(ADC10_A.BASE,ADC10_A.AIFG0);  
}
```

## 8 12-Bit Analog-to-Digital Converter (ADC12\_A)

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### 8.1 Introduction

The 12-Bit Analog-to-Digital (ADC12.A) API provides a set of functions for using the MSP430Ware ADC12.A modules. Functions are provided to initialize the ADC12.A modules, setup signal sources and reference voltages for each memory buffer, and manage interrupts for the ADC12.A modules.

The ADC12.A module provides the ability to convert analog signals into a digital value in respect to given reference voltages. The ADC12.A can generate digital values from 0 to Vcc with an 8-, 10- or 12-bit resolution, with 16 different memory buffers to store conversion results. It operates in 2 different sampling modes, and 4 different conversion modes. The sampling modes are extended sampling and pulse sampling, in extended sampling the sample/hold signal must stay high for the duration of sampling, while in pulse mode a sampling timer is setup to start on a rising edge of the sample/hold signal and sample for a specified amount of clock cycles. The 4 conversion modes are single-channel single conversion, sequence of channels single-conversion, repeated single channel conversions, and repeated sequence of channels conversions.

The ADC12.A module can generate multiple interrupts. An interrupt can be asserted for each memory buffer when a conversion is complete, or when a conversion is about to overwrite the converted data in any of the memory buffers before it has been read out, and/or when a conversion is about to start before the last conversion is complete.

### 8.2 API Functions

#### Functions

- `bool ADC12_A_init` (uint16\_t baseAddress, uint16\_t sampleHoldSignalSourceSelect, uint8\_t clockSourceSelect, uint16\_t clockSourceDivider)  
*Initializes the ADC12.A Module.*
- `void ADC12_A_enable` (uint16\_t baseAddress)  
*Enables the ADC12.A block.*
- `void ADC12_A_disable` (uint16\_t baseAddress)  
*Disables the ADC12.A block.*
- `void ADC12_A_setupSamplingTimer` (uint16\_t baseAddress, uint16\_t clockCycleHoldCountLowMem, uint16\_t clockCycleHoldCountHighMem, uint16\_t multipleSamplesEnabled)  
*Sets up and enables the Sampling Timer Pulse Mode.*
- `void ADC12_A_disableSamplingTimer` (uint16\_t baseAddress)  
*Disables Sampling Timer Pulse Mode.*

- void `ADC12_A_configureMemory` (uint16\_t baseAddress, `ADC12_A_configureMemoryParam` \*param)  
*Configures the controls of the selected memory buffer.*
- void `ADC12_A_enableInterrupt` (uint16\_t baseAddress, uint32\_t interruptMask)  
*Enables selected ADC12.A interrupt sources.*
- void `ADC12_A_disableInterrupt` (uint16\_t baseAddress, uint32\_t interruptMask)  
*Disables selected ADC12.A interrupt sources.*
- void `ADC12_A_clearInterrupt` (uint16\_t baseAddress, uint16\_t memoryInterruptFlagMask)  
*Clears ADC12.A selected interrupt flags.*
- uint16\_t `ADC12_A_getInterruptStatus` (uint16\_t baseAddress, uint16\_t memoryInterruptFlagMask)  
*Returns the status of the selected memory interrupt flags.*
- void `ADC12_A_startConversion` (uint16\_t baseAddress, uint16\_t startingMemoryBufferIndex, uint8\_t conversionSequenceModeSelect)  
*Enables/Starts an Analog-to-Digital Conversion.*
- void `ADC12_A_disableConversions` (uint16\_t baseAddress, bool preempt)  
*Disables the ADC from converting any more signals.*
- uint16\_t `ADC12_A_getResults` (uint16\_t baseAddress, uint8\_t memoryBufferIndex)  
*A Signed Integer of the contents of the specified memory buffer.*
- void `ADC12_A_setResolution` (uint16\_t baseAddress, uint8\_t resolutionSelect)  
*Use to change the resolution of the converted data.*
- void `ADC12_A_setSampleHoldSignalInversion` (uint16\_t baseAddress, uint16\_t invertedSignal)  
*Use to invert or un-invert the sample/hold signal.*
- void `ADC12_A_setDataReadBackFormat` (uint16\_t baseAddress, uint8\_t readBackFormat)  
*Use to set the read-back format of the converted data.*
- void `ADC12_A_enableReferenceBurst` (uint16\_t baseAddress)  
*Enables the reference buffer's burst ability.*
- void `ADC12_A_disableReferenceBurst` (uint16\_t baseAddress)  
*Disables the reference buffer's burst ability.*
- void `ADC12_A_setReferenceBufferSamplingRate` (uint16\_t baseAddress, uint8\_t samplingRateSelect)  
*Use to set the reference buffer's sampling rate.*
- uint32\_t `ADC12_A_getMemoryAddressForDMA` (uint16\_t baseAddress, uint8\_t memoryIndex)  
*Returns the address of the specified memory buffer for the DMA module.*
- uint16\_t `ADC12_A_isBusy` (uint16\_t baseAddress)  
*Returns the busy status of the ADC12.A core.*

## 8.2.1 Detailed Description

The ADC12.A API is broken into three groups of functions: those that deal with initialization and conversions, those that handle interrupts, and those that handle auxiliary features of the ADC12.A.

The ADC12.A initialization and conversion functions are

- `ADC12_A_init()`
- `ADC12_A_configureMemory()`
- `ADC12_A_setupSamplingTimer()`
- `ADC12_A_disableSamplingTimer()`
- `ADC12_A_startConversion()`
- `ADC12_A_disableConversions()`

- ADC12\_A\_readResults()
- ADC12\_A\_isBusy()

The ADC12\_A interrupts are handled by

- ADC12\_A\_enableInterrupt()
- ADC12\_A\_disableInterrupt()
- ADC12\_A\_clearInterrupt()
- ADC12\_A\_getInterruptStatus()

Auxiliary features of the ADC12\_A are handled by

- ADC12\_A\_setResolution()
- ADC12\_A\_setSampleHoldSignalInversion()
- ADC12\_A\_setDataReadBackFormat()
- ADC12\_A\_enableReferenceBurst()
- ADC12\_A\_disableReferenceBurst()
- ADC12\_A\_setReferenceBufferSamplingRate()
- ADC12\_A\_getMemoryAddressForDMA()
- ADC12\_A\_enable()
- ADC12\_A\_disable()

## 8.2.2 Function Documentation

### ADC12\_A\_clearInterrupt()

```
void ADC12_A_clearInterrupt (
    uint16_t baseAddress,
    uint16_t memoryInterruptFlagMask )
```

Clears ADC12\_A selected interrupt flags.

The selected ADC12\_A interrupt flags are cleared, so that it no longer asserts. The memory buffer interrupt flags are only cleared when the memory buffer is accessed. Note that the overflow interrupts do not have an interrupt flag to clear; they must be accessed directly from the interrupt vector.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC12_A module.
--------------------	--

## Parameters

<i>memoryInterruptFlagMask</i>	<p>is a bit mask of the interrupt flags to be cleared. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ ADC12_A_IFG0</li> <li>■ ADC12_A_IFG1</li> <li>■ ADC12_A_IFG2</li> <li>■ ADC12_A_IFG3</li> <li>■ ADC12_A_IFG4</li> <li>■ ADC12_A_IFG5</li> <li>■ ADC12_A_IFG6</li> <li>■ ADC12_A_IFG7</li> <li>■ ADC12_A_IFG8</li> <li>■ ADC12_A_IFG9</li> <li>■ ADC12_A_IFG10</li> <li>■ ADC12_A_IFG11</li> <li>■ ADC12_A_IFG12</li> <li>■ ADC12_A_IFG13</li> <li>■ ADC12_A_IFG14</li> <li>■ ADC12_A_IFG15</li> </ul>
--------------------------------	---

Modified bits of **ADC12IFG** register.

## Returns

None

## ADC12\_A\_configureMemory()

```
void ADC12_A_configureMemory (
    uint16_t baseAddress,
    ADC12_A_configureMemoryParam * param )
```

Configures the controls of the selected memory buffer.

Maps an input signal conversion into the selected memory buffer, as well as the positive and negative reference voltages for each conversion being stored into this memory buffer. If the internal reference is used for the positive reference voltage, the internal REF module must be used to control the voltage level. Note that if a conversion has been started with the startConversion() function, then a call to disableConversions() is required before this function may be called. If conversion is not disabled, this function does nothing.

## Parameters

<i>baseAddress</i>	is the base address of the ADC12.A module.
<i>param</i>	is the pointer to struct for memory configuration.

**Returns**

None

References `ADC12_A_configureMemoryParam::endOfSequence`,  
`ADC12_A_configureMemoryParam::inputSourceSelect`,  
`ADC12_A_configureMemoryParam::memoryBufferControllIndex`,  
`ADC12_A_configureMemoryParam::negativeRefVoltageSourceSelect`, and  
`ADC12_A_configureMemoryParam::positiveRefVoltageSourceSelect`.

**ADC12\_A\_disable()**

```
void ADC12_A_disable (
    uint16_t baseAddress )
```

Disables the ADC12\_A block.

This will disable operation of the ADC12\_A block.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC12_A module.
--------------------	--

Modified bits are **ADC12ON** of **ADC12CTL0** register.

**Returns**

None

**ADC12\_A\_disableConversions()**

```
void ADC12_A_disableConversions (
    uint16_t baseAddress,
    bool preempt )
```

Disables the ADC from converting any more signals.

Disables the ADC from converting any more signals. If there is a conversion in progress, this function can stop it immediately if the preempt parameter is set as TRUE, by changing the conversion mode to single-channel, single- conversion and disabling conversions. If the conversion mode is set as single-channel, single-conversion and this function is called without preemption, then the ADC core conversion status is polled until the conversion is complete before disabling conversions to prevent unpredictable data. If the [ADC12\\_A.startConversion\(\)](#) has been called, then this function has to be called to re-initialize the ADC, reconfigure a memory buffer control, enable/disable the sampling pulse mode, or change the internal reference voltage.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC12_A module.
--------------------	--



**Parameters**

<i>preempt</i>	<p>specifies if the current conversion should be pre-empted before the end of the conversion. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC12_A.COMPLETECONVERSION</b> - Allows the ADC12_A to end the current conversion before disabling conversions.</li> <li>■ <b>ADC12_A.PREEMPTCONVERSION</b> - Stops the ADC12_A immediately, with unpredictable results of the current conversion.</li> </ul>
----------------	---

Modified bits of **ADC12CTL1** register and bits of **ADC12CTL0** register.

**Returns**

None

References ADC12\_A.isBusy().

**ADC12\_A.disableInterrupt()**

```
void ADC12_A.disableInterrupt (
    uint16_t baseAddress,
    uint32_t interruptMask )
```

Disables selected ADC12\_A interrupt sources.

Disables the indicated ADC12\_A interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt, disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC12_A module.
--------------------	--

## Parameters

<i>interruptMask</i>	<p>Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>ADC12_A_IE0</b></li> <li>■ <b>ADC12_A_IE1</b></li> <li>■ <b>ADC12_A_IE2</b></li> <li>■ <b>ADC12_A_IE3</b></li> <li>■ <b>ADC12_A_IE4</b></li> <li>■ <b>ADC12_A_IE5</b></li> <li>■ <b>ADC12_A_IE6</b></li> <li>■ <b>ADC12_A_IE7</b></li> <li>■ <b>ADC12_A_IE8</b></li> <li>■ <b>ADC12_A_IE9</b></li> <li>■ <b>ADC12_A_IE10</b></li> <li>■ <b>ADC12_A_IE11</b></li> <li>■ <b>ADC12_A_IE12</b></li> <li>■ <b>ADC12_A_IE13</b></li> <li>■ <b>ADC12_A_IE14</b></li> <li>■ <b>ADC12_A_IE15</b></li> <li>■ <b>ADC12_A_OVERFLOW_IE</b></li> <li>■ <b>ADC12_A_CONVERSION_TIME_OVERFLOW_IE</b></li> </ul>
----------------------	---

Modified bits of **ADC12IE** register and bits of **ADC12CTL0** register.

## Returns

None

## ADC12\_A\_disableReferenceBurst()

```
void ADC12_A_disableReferenceBurst (
    uint16_t baseAddress )
```

Disables the reference buffer's burst ability.

Disables the reference buffer's burst ability, forcing the reference buffer to remain on continuously.

## Parameters

<i>baseAddress</i>	is the base address of the ADC12_A module.
--------------------	--

## Returns

None

### ADC12\_A\_disableSamplingTimer()

```
void ADC12_A_disableSamplingTimer (
    uint16_t baseAddress )
```

Disables Sampling Timer Pulse Mode.

Disables the Sampling Timer Pulse Mode. Note that if a conversion has been started with the `startConversion()` function, then a call to `disableConversions()` is required before this function may be called.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC12_A module.
--------------------	--

Modified bits are **ADC12SHP** of **ADC12CTL0** register.

#### Returns

None

### ADC12\_A\_enable()

```
void ADC12_A_enable (
    uint16_t baseAddress )
```

Enables the ADC12\_A block.

This will enable operation of the ADC12\_A block.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC12_A module.
--------------------	--

Modified bits are **ADC12ON** of **ADC12CTL0** register.

#### Returns

None

### ADC12\_A\_enableInterrupt()

```
void ADC12_A_enableInterrupt (
    uint16_t baseAddress,
    uint32_t interruptMask )
```

Enables selected ADC12\_A interrupt sources.

Enables the indicated ADC12\_A interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt, disabled sources have no effect on the processor. Does not clear interrupt flags.

## Parameters

<i>baseAddress</i>	is the base address of the ADC12_A module.
<i>interruptMask</i>	Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>ADC12_A_IE0</b></li> <li>■ <b>ADC12_A_IE1</b></li> <li>■ <b>ADC12_A_IE2</b></li> <li>■ <b>ADC12_A_IE3</b></li> <li>■ <b>ADC12_A_IE4</b></li> <li>■ <b>ADC12_A_IE5</b></li> <li>■ <b>ADC12_A_IE6</b></li> <li>■ <b>ADC12_A_IE7</b></li> <li>■ <b>ADC12_A_IE8</b></li> <li>■ <b>ADC12_A_IE9</b></li> <li>■ <b>ADC12_A_IE10</b></li> <li>■ <b>ADC12_A_IE11</b></li> <li>■ <b>ADC12_A_IE12</b></li> <li>■ <b>ADC12_A_IE13</b></li> <li>■ <b>ADC12_A_IE14</b></li> <li>■ <b>ADC12_A_IE15</b></li> <li>■ <b>ADC12_A_OVERFLOW_IE</b></li> <li>■ <b>ADC12_A_CONVERSION_TIME_OVERFLOW_IE</b></li> </ul>

Modified bits of **ADC12IE** register and bits of **ADC12CTL0** register.

## Returns

None

## ADC12\_A\_enableReferenceBurst()

```
void ADC12_A_enableReferenceBurst (
    uint16_t baseAddress )
```

Enables the reference buffer's burst ability.

Enables the reference buffer's burst ability, allowing the reference buffer to turn off while the ADC is not converting, and automatically turning on when the ADC needs the generated reference voltage for a conversion.

## Parameters

<i>baseAddress</i>	is the base address of the ADC12_A module.
--------------------	--

**Returns**

None

**ADC12\_A\_getInterruptStatus()**

```
uint16_t ADC12_A_getInterruptStatus (
    uint16_t baseAddress,
    uint16_t memoryInterruptFlagMask )
```

Returns the status of the selected memory interrupt flags.

Returns the status of the selected memory interrupt flags. Note that the overflow interrupts do not have an interrupt flag to clear; they must be accessed directly from the interrupt vector.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC12_A module.
<i>memoryInterruptFlagMask</i>	<p>is a bit mask of the interrupt flags status to be returned. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>ADC12_A_IFG0</b></li> <li>■ <b>ADC12_A_IFG1</b></li> <li>■ <b>ADC12_A_IFG2</b></li> <li>■ <b>ADC12_A_IFG3</b></li> <li>■ <b>ADC12_A_IFG4</b></li> <li>■ <b>ADC12_A_IFG5</b></li> <li>■ <b>ADC12_A_IFG6</b></li> <li>■ <b>ADC12_A_IFG7</b></li> <li>■ <b>ADC12_A_IFG8</b></li> <li>■ <b>ADC12_A_IFG9</b></li> <li>■ <b>ADC12_A_IFG10</b></li> <li>■ <b>ADC12_A_IFG11</b></li> <li>■ <b>ADC12_A_IFG12</b></li> <li>■ <b>ADC12_A_IFG13</b></li> <li>■ <b>ADC12_A_IFG14</b></li> <li>■ <b>ADC12_A_IFG15</b></li> </ul>

**Returns**

The current interrupt flag status for the corresponding mask.

**ADC12\_A\_getMemoryAddressForDMA()**

```
uint32_t ADC12_A_getMemoryAddressForDMA (
    uint16_t baseAddress,
```

```
uint8_t memoryIndex )
```

Returns the address of the specified memory buffer for the DMA module.

Returns the address of the specified memory buffer. This can be used in conjunction with the DMA to store the converted data directly to memory.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC12_A module.
<i>memoryIndex</i>	is the memory buffer to return the address of. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC12_A_MEMORY_0</b> [Default]</li> <li>■ <b>ADC12_A_MEMORY_1</b></li> <li>■ <b>ADC12_A_MEMORY_2</b></li> <li>■ <b>ADC12_A_MEMORY_3</b></li> <li>■ <b>ADC12_A_MEMORY_4</b></li> <li>■ <b>ADC12_A_MEMORY_5</b></li> <li>■ <b>ADC12_A_MEMORY_6</b></li> <li>■ <b>ADC12_A_MEMORY_7</b></li> <li>■ <b>ADC12_A_MEMORY_8</b></li> <li>■ <b>ADC12_A_MEMORY_9</b></li> <li>■ <b>ADC12_A_MEMORY_10</b></li> <li>■ <b>ADC12_A_MEMORY_11</b></li> <li>■ <b>ADC12_A_MEMORY_12</b></li> <li>■ <b>ADC12_A_MEMORY_13</b></li> <li>■ <b>ADC12_A_MEMORY_14</b></li> <li>■ <b>ADC12_A_MEMORY_15</b></li> </ul>

#### Returns

address of the specified memory buffer

#### ADC12\_A\_getResults()

```
uint16_t ADC12_A_getResults (
    uint16_t baseAddress,
    uint8_t memoryBufferIndex )
```

A Signed Integer of the contents of the specified memory buffer.

Returns the raw contents of the specified memory buffer. The format of the content depends on the read-back format of the data: if the data is in signed 2's complement format then the contents in the memory buffer will be left-justified with the least-significant bits as 0's, whereas if the data is in unsigned format then the contents in the memory buffer will be right-justified with the most-significant bits as 0's.

## Parameters

<i>baseAddress</i>	is the base address of the ADC12_A module.
<i>memoryBufferIndex</i>	is the specified Memory Buffer to read. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC12_A_MEMORY_0</b> [Default]</li> <li>■ <b>ADC12_A_MEMORY_1</b></li> <li>■ <b>ADC12_A_MEMORY_2</b></li> <li>■ <b>ADC12_A_MEMORY_3</b></li> <li>■ <b>ADC12_A_MEMORY_4</b></li> <li>■ <b>ADC12_A_MEMORY_5</b></li> <li>■ <b>ADC12_A_MEMORY_6</b></li> <li>■ <b>ADC12_A_MEMORY_7</b></li> <li>■ <b>ADC12_A_MEMORY_8</b></li> <li>■ <b>ADC12_A_MEMORY_9</b></li> <li>■ <b>ADC12_A_MEMORY_10</b></li> <li>■ <b>ADC12_A_MEMORY_11</b></li> <li>■ <b>ADC12_A_MEMORY_12</b></li> <li>■ <b>ADC12_A_MEMORY_13</b></li> <li>■ <b>ADC12_A_MEMORY_14</b></li> <li>■ <b>ADC12_A_MEMORY_15</b></li> </ul>

## Returns

A signed integer of the contents of the specified memory buffer

## ADC12\_A\_init()

```
bool ADC12_A_init (
    uint16_t baseAddress,
    uint16_t sampleHoldSignalSourceSelect,
    uint8_t clockSourceSelect,
    uint16_t clockSourceDivider )
```

Initializes the ADC12\_A Module.

This function initializes the ADC module to allow for analog-to-digital conversions. Specifically this function sets up the sample-and-hold signal and clock sources for the ADC core to use for conversions. Upon successful completion of the initialization all of the ADC control registers will be reset, excluding the memory controls and reference module bits, the given parameters will be set, and the ADC core will be turned on (Note, that the ADC core only draws power during conversions and remains off when not converting). Note that sample/hold signal sources are device dependent. Note that if re-initializing the ADC after starting a conversion with the startConversion() function, the disableConversion() must be called BEFORE this function can be called.

## Parameters

<i>baseAddress</i>	is the base address of the ADC12_A module.
<i>sampleHoldSignalSourceSelect</i>	<p>is the signal that will trigger a sample-and-hold for an input signal to be converted. This parameter is device specific and sources should be found in the device's datasheet. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC12_A_SAMPLEHOLDSOURCE_SC</b> [Default]</li> <li>■ <b>ADC12_A_SAMPLEHOLDSOURCE_1</b></li> <li>■ <b>ADC12_A_SAMPLEHOLDSOURCE_2</b></li> <li>■ <b>ADC12_A_SAMPLEHOLDSOURCE_3</b> - This parameter is device specific and sources should be found in the device's datasheet.</li> </ul> <p>Modified bits are <b>ADC12SHSx</b> of <b>ADC12CTL1</b> register.</p>
<i>clockSourceSelect</i>	<p>selects the clock that will be used by the ADC12_A core, and the sampling timer if a sampling pulse mode is enabled. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC12_A_CLOCKSOURCE_ADC12OSC</b> [Default] - MODOSC 5 MHz oscillator from the UCS</li> <li>■ <b>ADC12_A_CLOCKSOURCE_ACLK</b> - The Auxiliary Clock</li> <li>■ <b>ADC12_A_CLOCKSOURCE_MCLK</b> - The Master Clock</li> <li>■ <b>ADC12_A_CLOCKSOURCE_SMCLK</b> - The Sub-Master Clock</li> </ul> <p>Modified bits are <b>ADC12SSELx</b> of <b>ADC12CTL1</b> register.</p>



**Parameters**

<i>clockSourceDivider</i>	<p>selects the amount that the clock will be divided. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC12_A_CLOCKDIVIDER_1</b> [Default]</li> <li>■ <b>ADC12_A_CLOCKDIVIDER_2</b></li> <li>■ <b>ADC12_A_CLOCKDIVIDER_3</b></li> <li>■ <b>ADC12_A_CLOCKDIVIDER_4</b></li> <li>■ <b>ADC12_A_CLOCKDIVIDER_5</b></li> <li>■ <b>ADC12_A_CLOCKDIVIDER_6</b></li> <li>■ <b>ADC12_A_CLOCKDIVIDER_7</b></li> <li>■ <b>ADC12_A_CLOCKDIVIDER_8</b></li> <li>■ <b>ADC12_A_CLOCKDIVIDER_12</b></li> <li>■ <b>ADC12_A_CLOCKDIVIDER_16</b></li> <li>■ <b>ADC12_A_CLOCKDIVIDER_20</b></li> <li>■ <b>ADC12_A_CLOCKDIVIDER_24</b></li> <li>■ <b>ADC12_A_CLOCKDIVIDER_28</b></li> <li>■ <b>ADC12_A_CLOCKDIVIDER_32</b></li> </ul> <p>Modified bits are <b>ADC12PDIV</b> of <b>ADC12CTL2</b> register; bits <b>ADC12DIVx</b> of <b>ADC12CTL1</b> register.</p>
---------------------------	--

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the initialization process.

**ADC12\_A\_isBusy()**

```
uint16_t ADC12_A_isBusy (
    uint16_t baseAddress )
```

Returns the busy status of the ADC12\_A core.

Returns the status of the ADC core if there is a conversion currently taking place.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC12_A module.
--------------------	--

**Returns**

One of the following:

- **ADC12\_A\_NOTBUSY**
- **ADC12\_A\_BUSY**  
indicating if a conversion is taking place

Referenced by ADC12\_A\_disableConversions().

**ADC12\_A\_setDataReadBackFormat()**

```
void ADC12_A_setDataReadBackFormat (
    uint16_t baseAddress,
    uint8_t readBackFormat )
```

Use to set the read-back format of the converted data.

Sets the format of the converted data: how it will be stored into the memory buffer, and how it should be read back. The format can be set as right-justified (default), which indicates that the number will be unsigned, or left-justified, which indicates that the number will be signed in 2's complement format. This change affects all memory buffers for subsequent conversions.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC12_A module.
<i>readBackFormat</i>	is the specified format to store the conversions in the memory buffer. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC12_A_UNSIGNED_BINARY</b> [Default]</li> <li>■ <b>ADC12_A_SIGNED_2SCOMPLEMENT</b> Modified bits are <b>ADC12DF</b> of <b>ADC12CTL2</b> register.</li> </ul>

**Returns**

None

**ADC12\_A\_setReferenceBufferSamplingRate()**

```
void ADC12_A_setReferenceBufferSamplingRate (
    uint16_t baseAddress,
    uint8_t samplingRateSelect )
```

Use to set the reference buffer's sampling rate.

Sets the reference buffer's sampling rate to the selected sampling rate. The default sampling rate is maximum of 200-kSPS, and can be reduced to a maximum of 50-kSPS to conserve power.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC12_A module.
<i>samplingRateSelect</i>	is the specified maximum sampling rate. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC12_A_MAXSAMPLINGRATE_200KSPS</b> [Default]</li> <li>■ <b>ADC12_A_MAXSAMPLINGRATE_50KSPS</b> Modified bits are <b>ADC12SR</b> of <b>ADC12CTL2</b> register.</li> </ul>

**Returns**

None

**ADC12\_A\_setResolution()**

```
void ADC12_A_setResolution (
    uint16_t baseAddress,
    uint8_t resolutionSelect )
```

Use to change the resolution of the converted data.

This function can be used to change the resolution of the converted data from the default of 12-bits.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC12_A module.
<i>resolutionSelect</i>	determines the resolution of the converted data. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC12_A_RESOLUTION_8BIT</b></li> <li>■ <b>ADC12_A_RESOLUTION_10BIT</b></li> <li>■ <b>ADC12_A_RESOLUTION_12BIT</b> [Default]</li> </ul> Modified bits are <b>ADC12RESx</b> of <b>ADC12CTL2</b> register.

**Returns**

None

**ADC12\_A\_setSampleHoldSignalInversion()**

```
void ADC12_A_setSampleHoldSignalInversion (
    uint16_t baseAddress,
    uint16_t invertedSignal )
```

Use to invert or un-invert the sample/hold signal.

This function can be used to invert or un-invert the sample/hold signal. Note that if a conversion has been started with the `startConversion()` function, then a call to `disableConversions()` is required before this function may be called.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC12_A module.
<i>invertedSignal</i>	set if the sample/hold signal should be inverted Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC12_A_NONINVERTEDSIGNAL</b> [Default] - a sample-and-hold of an input signal for conversion will be started on a rising edge of the sample/hold signal.</li> <li>■ <b>ADC12_A_INVERTEDSIGNAL</b> - a sample-and-hold of an input signal for conversion will be started on a falling edge of the sample/hold signal. Modified bits are <b>ADC12ISSH</b> of <b>ADC12CTL1</b> register.</li> </ul>

**Returns**

None

**ADC12\_A\_setupSamplingTimer()**

```
void ADC12_A_setupSamplingTimer (
    uint16_t baseAddress,
    uint16_t clockCycleHoldCountLowMem,
    uint16_t clockCycleHoldCountHighMem,
    uint16_t multipleSamplesEnabled )
```

Sets up and enables the Sampling Timer Pulse Mode.

This function sets up the sampling timer pulse mode which allows the sample/hold signal to trigger a sampling timer to sample-and-hold an input signal for a specified number of clock cycles without having to hold the sample/hold signal for the entire period of sampling. Note that if a conversion has been started with the startConversion() function, then a call to disableConversions() is required before this function may be called.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC12_A module.
<i>clockCycleHoldCountLowMem</i>	sets the amount of clock cycles to sample- and-hold for the higher memory buffers 0-7. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC12_A_CYCLEHOLD_4_CYCLES</b> [Default]</li> <li>■ <b>ADC12_A_CYCLEHOLD_8_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_16_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_32_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_64_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_96_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_128_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_192_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_256_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_384_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_512_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_768_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_1024_CYCLES</b></li> </ul> Modified bits are <b>ADC12SHT0x</b> of <b>ADC12CTL0</b> register.

## Parameters

<i>clockCycleHoldCountHighMem</i>	<p>sets the amount of clock cycles to sample-and-hold for the higher memory buffers 8-15. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC12_A_CYCLEHOLD_4_CYCLES</b> [Default]</li> <li>■ <b>ADC12_A_CYCLEHOLD_8_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_16_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_32_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_64_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_96_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_128_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_192_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_256_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_384_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_512_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_768_CYCLES</b></li> <li>■ <b>ADC12_A_CYCLEHOLD_1024_CYCLES</b></li> </ul> <p>Modified bits are <b>ADC12SHT1x</b> of <b>ADC12CTL0</b> register.</p>
<i>multipleSamplesEnabled</i>	<p>allows multiple conversions to start without a trigger signal from the sample/hold signal Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC12_A_MULTIPLESAMPLESDISABLE</b> [Default] - a timer trigger will be needed to start every ADC conversion.</li> <li>■ <b>ADC12_A_MULTIPLESAMPLESENABLE</b> - during a sequenced and/or repeated conversion mode, after the first conversion, no sample/hold signal is necessary to start subsequent sample/hold and convert processes. Modified bits are <b>ADC12MSC</b> of <b>ADC12CTL0</b> register.</li> </ul>

## Returns

None

## ADC12\_A\_startConversion()

```
void ADC12_A_startConversion (
    uint16_t baseAddress,
    uint16_t startingMemoryBufferIndex,
    uint8_t conversionSequenceModeSelect )
```

Enables/Starts an Analog-to-Digital Conversion.

This function enables/starts the conversion process of the ADC. If the sample/hold signal source chosen during initialization was ADC12OSC, then the conversion is started immediately, otherwise the chosen sample/hold signal source starts the conversion by a rising edge of the signal. Keep in

mind when selecting conversion modes, that for sequenced and/or repeated modes, to keep the sample/hold-and-convert process continuing without a trigger from the sample/hold signal source, the multiple samples must be enabled using the `ADC12_A_setupSamplingTimer()` function. Note that after this function is called, the `ADC12_A_disableConversions()` has to be called to re-initialize the ADC, reconfigure a memory buffer control, enable/disable the sampling timer, or to change the internal reference voltage.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC12_A module.
<i>startingMemoryBufferIndex</i>	<p>is the memory buffer that will hold the first or only conversion. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC12_A_MEMORY_0</b> [Default]</li> <li>■ <b>ADC12_A_MEMORY_1</b></li> <li>■ <b>ADC12_A_MEMORY_2</b></li> <li>■ <b>ADC12_A_MEMORY_3</b></li> <li>■ <b>ADC12_A_MEMORY_4</b></li> <li>■ <b>ADC12_A_MEMORY_5</b></li> <li>■ <b>ADC12_A_MEMORY_6</b></li> <li>■ <b>ADC12_A_MEMORY_7</b></li> <li>■ <b>ADC12_A_MEMORY_8</b></li> <li>■ <b>ADC12_A_MEMORY_9</b></li> <li>■ <b>ADC12_A_MEMORY_10</b></li> <li>■ <b>ADC12_A_MEMORY_11</b></li> <li>■ <b>ADC12_A_MEMORY_12</b></li> <li>■ <b>ADC12_A_MEMORY_13</b></li> <li>■ <b>ADC12_A_MEMORY_14</b></li> <li>■ <b>ADC12_A_MEMORY_15</b></li> </ul> <p>Modified bits are <b>ADC12STARTADDx</b> of <b>ADC12CTL1</b> register.</p>

## Parameters

<i>conversionSequenceModeSelect</i>	<p>determines the ADC operating mode. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC12_A_SINGLECHANNEL</b> [Default] - one-time conversion of a single channel into a single memory buffer.</li> <li>■ <b>ADC12_A_SEQOFCHANNELS</b> - one time conversion of multiple channels into the specified starting memory buffer and each subsequent memory buffer up until the conversion is stored in a memory buffer dedicated as the end-of-sequence by the memory's control register.</li> <li>■ <b>ADC12_A_REPEATED_SINGLECHANNEL</b> - repeated conversions of one channel into a single memory buffer.</li> <li>■ <b>ADC12_A_REPEATED_SEQOFCHANNELS</b> - repeated conversions of multiple channels into the specified starting memory buffer and each subsequent memory buffer up until the conversion is stored in a memory buffer dedicated as the end-of-sequence by the memory's control register.</li> </ul> <p>Modified bits are <b>ADC12CONSEQx</b> of <b>ADC12CTL1</b> register.</p>
-------------------------------------	--

Modified bits of **ADC12CTL1** register and bits of **ADC12CTL0** register.

## Returns

None

## 8.3 Programming Example

The following example shows how to initialize and use the ADC12 API to start a single channel, single conversion.

```
// Initialize ADC12 with ADC12's built-in oscillator
ADC12_A_init (ADC12_A_BASE,
             ADC12_A_SAMPLEHOLDSOURCE_SC,
             ADC12_A_CLOCKSOURCE_ADC12OSC,
             ADC12_A_CLOCKDIVIDEBY_1);

//Switch ON ADC12
ADC12_A_enable (ADC12_A_BASE);

// Setup sampling timer to sample-and-hold for 16 clock cycles
ADC12_A_setupSamplingTimer (ADC12_A_BASE,
                           ADC12_A_CYCLEHOLD_64_CYCLES,
                           ADC12_A_CYCLEHOLD_4_CYCLES,
                           FALSE);

// Configure the Input to the Memory Buffer with the specified Reference Voltages
ADC12_A_configureMemoryParam param = {0};
param.memoryBufferControlIndex = ADC12_A_MEMORY_0;
param.inputSourceSelect = ADC12_A_INPUT_A0;
param.positiveRefVoltageSourceSelect = ADC12_A_VREFPOS_AVCC;
param.negativeRefVoltageSourceSelect = ADC12_A_VREFNEG_AVSS;
```

```
param.endOfSequence = ADC12_A_NOTENDOFSEQUENCE;
ADC12_A_configureMemory(ADC12_A_BASE, &param);
while (1)
{
    // Start a single conversion, no repeating or sequences.
    ADC12_A_startConversion (ADC12_A_BASE,
                            ADC12_A_MEMORY_0,
                            ADC12_A_SINGLECHANNEL);

    // Wait for the Interrupt Flag to assert
    while( !(ADC12_A_getInterruptStatus(ADC12_A_BASE, ADC12_IFG0)) );

    // Clear the Interrupt Flag and start another conversion
    ADC12_A_clearInterrupt(ADC12_A_BASE, ADC12_IFG0);
}
```



## 9 Advanced Encryption Standard (AES)

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### 9.1 Introduction

The AES accelerator module performs encryption and decryption of 128-bit data with 128-bit keys according to the advanced encryption standard (AES) (FIPS PUB 197) in hardware. The AES accelerator features are:

- Encryption and decryption according to AES FIPS PUB 197 with 128-bit key
- On-the-fly key expansion for encryption and decryption
- Off-line key generation for decryption
- Byte and word access to key, input, and output data
- AES ready interrupt flag The AES256 accelerator module performs encryption and decryption of 128-bit data with 128-/192-/256-bit keys according to the advanced encryption standard (AES) (FIPS PUB 197) in hardware. The AES accelerator features are: AES encryption 128 bit - 168 cycles 192 bit - 204 cycles 256 bit - 234 cycles AES decryption 128 bit - 168 cycles 192 bit - 206 cycles 256 bit - 234 cycles
- On-the-fly key expansion for encryption and decryption
- Offline key generation for decryption
- Shadow register storing the initial key for all key lengths
- Byte and word access to key, input data, and output data
- AES ready interrupt flag

### 9.2 API Functions

#### Functions

- uint8\_t [AES\\_setCipherKey](#) (uint16\_t baseAddress, const uint8\_t \*CipherKey)  
*Loads a 128 bit cipher key to AES module.*
- uint8\_t [AES\\_encryptData](#) (uint16\_t baseAddress, const uint8\_t \*Data, uint8\_t \*encryptedData)  
*Encrypts a block of data using the AES module.*
- uint8\_t [AES\\_decryptData](#) (uint16\_t baseAddress, const uint8\_t \*Data, uint8\_t \*decryptedData)  
*Decrypts a block of data using the AES module.*
- uint8\_t [AES\\_setDecipherKey](#) (uint16\_t baseAddress, const uint8\_t \*CipherKey)  
*Sets the decipher key The API.*
- void [AES\\_clearInterrupt](#) (uint16\_t baseAddress)  
*Clears the AES ready interrupt flag.*
- uint32\_t [AES\\_getInterruptStatus](#) (uint16\_t baseAddress)  
*Gets the AES ready interrupt flag status.*
- void [AES\\_enableInterrupt](#) (uint16\_t baseAddress)

- Enables AES ready interrupt.*
- void `AES_disableInterrupt` (uint16\_t baseAddress)
- Disables AES ready interrupt.*
- void `AES_reset` (uint16\_t baseAddress)
- Resets AES Module immediately.*
- uint8\_t `AES_startEncryptData` (uint16\_t baseAddress, const uint8\_t \*Data, uint8\_t \*encryptedData)
- Starts an encryption process on the AES module.*
- uint8\_t `AES_startDecryptData` (uint16\_t baseAddress, const uint8\_t \*Data)
- Decrypts a block of data using the AES module.*
- uint8\_t `AES_startSetDecipherKey` (uint16\_t baseAddress, const uint8\_t \*CipherKey)
- Loads the decipher key.*
- uint8\_t `AES_getDataOut` (uint16\_t baseAddress, uint8\_t \*OutputData)
- Reads back the output data from AES module.*
- uint8\_t `AES_isBusy` (uint16\_t baseAddress)
- Gets the AES module busy status.*
- void `AES_clearErrorFlag` (uint16\_t baseAddress)
- Clears the AES error flag.*
- uint32\_t `AES_getErrorFlagStatus` (uint16\_t baseAddress)
- Gets the AES error flag status.*
- uint8\_t `AES_startDecryptDataUsingEncryptionKey` (uint16\_t baseAddress, const uint8\_t \*Data)
- DEPRECATED Starts an decryption process on the AES module.*
- uint8\_t `AES_decryptDataUsingEncryptionKey` (uint16\_t baseAddress, const uint8\_t \*Data, uint8\_t \*decryptedData)
- DEPRECATED Decrypts a block of data using the AES module.*

## 9.2.1 Detailed Description

The AES module APIs are

- `AES_setCipherKey()`
- `AES_encryptData()`
- `AES_decryptDataUsingEncryptionKey()`
- `AES_setDecipherKey()`
- `AES_decryptData()`
- `AES_reset()`
- `AES_startEncryptData()`
- `AES_startDecryptDataUsingEncryptionKey()`
- `AES_startDecryptData()`
- `AES_startSetDecipherKey()`
- `AES_getDataOut()`

The AES interrupt handler functions

- `AES_enableInterrupt()`
- `AES_disableInterrupt()`
- `AES_clearInterrupt()`
- `AES_getInterruptStatus`

## 9.2.2 Function Documentation

### AES\_clearErrorFlag()

```
void AES_clearErrorFlag (
    uint16_t baseAddress )
```

Clears the AES error flag.

Clears the AES error flag that results from a key or data being written while the AES module is busy. Modified bit is AESERRFG of AESACTL0 register.

#### Parameters

<i>baseAddress</i>	is the base address of the AES module.
--------------------	--

Modified bits are **AESERRFG** of **AESACTL0** register.

#### Returns

None

### AES\_clearInterrupt()

```
void AES_clearInterrupt (
    uint16_t baseAddress )
```

Clears the AES ready interrupt flag.

This function clears the AES ready interrupt flag. This flag is automatically cleared when AESADOUT is read, or when AESAKEY or AESADIN is written. This function should be used when the flag needs to be reset and it has not been automatically cleared by one of the previous actions.

#### Parameters

<i>baseAddress</i>	is the base address of the AES module.
--------------------	--

Modified bits are **AESRDYIFG** of **AESACTL0** register.

#### Returns

None

### AES\_decryptData()

```
uint8_t AES_decryptData (
    uint16_t baseAddress,
    const uint8_t * Data,
    uint8_t * decryptedData )
```

Decrypts a block of data using the AES module.

This function requires a pre-generated decryption key. A key can be loaded and pre-generated by using function [AES\\_startSetDecipherKey\(\)](#) or [AES\\_setDecipherKey\(\)](#). The decryption takes 167 MCLK.

#### Parameters

<i>baseAddress</i>	is the base address of the AES module.
<i>Data</i>	is a pointer to an uint8_t array with a length of 16 bytes that contains encrypted data to be decrypted.
<i>decryptedData</i>	is a pointer to an uint8_t array with a length of 16 bytes in that the decrypted data will be written.

#### Returns

STATUS\_SUCCESS

### AES\_decryptDataUsingEncryptionKey()

```
uint8_t AES_decryptDataUsingEncryptionKey (
    uint16_t baseAddress,
    const uint8_t * Data,
    uint8_t * decryptedData )
```

DEPRECATED Decrypts a block of data using the AES module.

This function can be used to decrypt data by using the same key as used for a previous performed encryption. The decryption takes 214 MCLK.

#### Parameters

<i>baseAddress</i>	is the base address of the AES module.
<i>Data</i>	is a pointer to an uint8_t array with a length of 16 bytes that contains encrypted data to be decrypted.
<i>decryptedData</i>	is a pointer to an uint8_t array with a length of 16 bytes in that the decrypted data will be written.

#### Returns

STATUS\_SUCCESS

### AES\_disableInterrupt()

```
void AES_disableInterrupt (
    uint16_t baseAddress )
```

Disables AES ready interrupt.

Disables AES ready interrupt. This interrupt is reset by a PUC, but not reset by AES\_reset.

**Parameters**

<i>baseAddress</i>	is the base address of the AES module.
--------------------	--

Modified bits are **AESRDYIE** of **AESACTL0** register.

**Returns**

None

**AES\_enableInterrupt()**

```
void AES_enableInterrupt (
    uint16_t baseAddress )
```

Enables AES ready interrupt.

Enables AES ready interrupt. This interrupt is reset by a PUC, but not reset by AES\_reset. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the AES module.
--------------------	--

Modified bits are **AESRDYIE** of **AESACTL0** register.

**Returns**

None

**AES\_encryptData()**

```
uint8_t AES_encryptData (
    uint16_t baseAddress,
    const uint8_t * Data,
    uint8_t * encryptedData )
```

Encrypts a block of data using the AES module.

The cipher key that is used for encryption should be loaded in advance by using function [AES\\_setCipherKey\(\)](#)

**Parameters**

<i>baseAddress</i>	is the base address of the AES module.
<i>Data</i>	is a pointer to an uint8_t array with a length of 16 bytes that contains data to be encrypted.
<i>encryptedData</i>	is a pointer to an uint8_t array with a length of 16 bytes in that the encrypted data will be written.

**Returns**

STATUS\_SUCCESS

**AES\_getDataOut()**

```
uint8_t AES_getDataOut (
    uint16_t baseAddress,
    uint8_t * OutputData )
```

Reads back the output data from AES module.

This function is meant to use after an encryption or decryption process that was started and finished by initiating an interrupt by use of the [AES\\_startEncryptData\(\)](#) or [AES\\_startDecryptData\(\)](#) functions.

**Parameters**

<i>baseAddress</i>	is the base address of the AES module.
<i>OutputData</i>	is a pointer to an uint8_t array with a length of 16 bytes in which the output data of the AES module is available. If AES module is busy returns NULL.

**Returns**

STATUS\_SUCCESS if AES is not busy, STATUS\_FAIL if it is busy

**AES\_getErrorFlagStatus()**

```
uint32_t AES_getErrorFlagStatus (
    uint16_t baseAddress )
```

Gets the AES error flag status.

Checks the AES error flag that results from a key or data being written while the AES module is busy. If the flag is set, it needs to be cleared using [AES\\_clearErrorFlag](#).

**Parameters**

<i>baseAddress</i>	is the base address of the AES module.
--------------------	--

**Returns**

One of the following:

- **AES\_ERROR\_OCCURRED**
- **AES\_NO\_ERROR**

indicating if AESAKEY or AESADIN were written while an AES operation was in progress

**AES\_getInterruptStatus()**

```
uint32_t AES_getInterruptStatus (
    uint16_t baseAddress )
```

Gets the AES ready interrupt flag status.

This function checks the AES ready interrupt flag. This flag is automatically cleared when AESADOUT is read, or when AESAKEY or AESADIN is written. This function can be used to confirm that this has been done.

**Parameters**

<i>baseAddress</i>	is the base address of the AES module.
--------------------	--

**Returns**

uint32\_t - AES\_READY\_INTERRUPT or 0x00.

**AES\_isBusy()**

```
uint8_t AES_isBusy (
    uint16_t baseAddress )
```

Gets the AES module busy status.

Gets the AES module busy status. If a key or data are written while the AES module is busy, an error flag will be thrown.

**Parameters**

<i>baseAddress</i>	is the base address of the AES module.
--------------------	--

**Returns**

One of the following:

- **AES\_BUSY**
- **AES\_NOT\_BUSY**

indicating if encryption/decryption/key generation is taking place

**AES\_reset()**

```
void AES_reset (
    uint16_t baseAddress )
```

Resets AES Module immediately.

This function performs a software reset on the AES Module, note that this does not affect the AES ready interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the AES module.
--------------------	--

Modified bits are **AESSWRST** of **AESACTL0** register.

**Returns**

None

**AES\_setCipherKey()**

```
uint8_t AES_setCipherKey (
    uint16_t baseAddress,
    const uint8_t * CipherKey )
```

Loads a 128 bit cipher key to AES module.

This function loads a 128 bit cipher key to AES module.

**Parameters**

<i>baseAddress</i>	is the base address of the AES module.
<i>CipherKey</i>	is a pointer to an <code>uint8_t</code> array with a length of 16 bytes that contains a 128 bit cipher key.

**Returns**

**STATUS\_SUCCESS**

**AES\_setDecipherKey()**

```
uint8_t AES_setDecipherKey (
```



```
uint16_t baseAddress,
const uint8_t * CipherKey )
```

Sets the decipher key The API.

The API [AES\\_startSetDecipherKey\(\)](#) or [AES\\_setDecipherKey\(\)](#) must be invoked before invoking [AES\\_setDecipherKey\(\)](#).

#### Parameters

<i>baseAddress</i>	is the base address of the AES module.
<i>CipherKey</i>	is a pointer to an uint8_t array with a length of 16 bytes that contains the initial AES key.

#### Returns

STATUS\_SUCCESS

### AES\_startDecryptData()

```
uint8_t AES_startDecryptData (
    uint16_t baseAddress,
    const uint8_t * Data )
```

Decrypts a block of data using the AES module.

This is the non-blocking equivalent of [AES\\_decryptData\(\)](#). This function requires a pre-generated decryption key. A key can be loaded and pre-generated by using function [AES\\_setDecipherKey\(\)](#) or [AES\\_startSetDecipherKey\(\)](#). The decryption takes 167 MCLK. It is recommended to use interrupt to check for procedure completion then using [AES\\_getDataOut\(\)](#) API to retrieve the decrypted data.

#### Parameters

<i>baseAddress</i>	is the base address of the AES module.
<i>Data</i>	is a pointer to an uint8_t array with a length of 16 bytes that contains encrypted data to be decrypted.

#### Returns

STATUS\_SUCCESS

### AES\_startDecryptDataUsingEncryptionKey()

```
uint8_t AES_startDecryptDataUsingEncryptionKey (
    uint16_t baseAddress,
    const uint8_t * Data )
```

DEPRECATED Starts an decryption process on the AES module.

This is the non-blocking equivalent of [AES\\_decryptDataUsingEncryptionKey\(\)](#). This function can

be used to decrypt data by using the same key as used for a previous performed encryption. The decryption takes 214 MCLK.

#### Parameters

<i>baseAddress</i>	is the base address of the AES module.
<i>Data</i>	is a pointer to an uint8_t array with a length of 16 bytes that contains encrypted data to be decrypted.

#### Returns

STATUS\_SUCCESS

### AES\_startEncryptData()

```
uint8_t AES_startEncryptData (
    uint16_t baseAddress,
    const uint8_t * Data,
    uint8_t * encryptedData )
```

Starts an encryption process on the AES module.

This is the non-blocking equivalent of [AES\\_encryptData\(\)](#). The cipher key that is used for decryption should be loaded in advance by using function [AES\\_setCipherKey\(\)](#). It is recommended to use interrupt to check for procedure completion then using [AES\\_getDataOut\(\)](#) API to retrieve the encrypted data.

#### Parameters

<i>baseAddress</i>	is the base address of the AES module.
<i>Data</i>	is a pointer to an uint8_t array with a length of 16 bytes that contains data to be encrypted.
<i>encryptedData</i>	is a pointer to an uint8_t array with a length of 16 bytes in that the encrypted data will be written.

#### Returns

STATUS\_SUCCESS

### AES\_startSetDecipherKey()

```
uint8_t AES_startSetDecipherKey (
    uint16_t baseAddress,
    const uint8_t * CipherKey )
```

Loads the decipher key.

This is the non-blocking equivalent of [AES\\_setDecipherKey\(\)](#). The API [AES\\_startSetDecipherKey\(\)](#) or [AES\\_setDecipherKey\(\)](#) must be invoked before invoking [AES\\_startSetDecipherKey\(\)](#).

**Parameters**

<i>baseAddress</i>	is the base address of the AES module.
<i>CipherKey</i>	is a pointer to an <code>uint8_t</code> array with a length of 16 bytes that contains the initial AES key.

**Returns**

STATUS\_SUCCESS

## 9.3 Programming Example

The following example shows some AES operations using the APIs

```

unsigned char Data[16] =      { 0x30, 0x31, 0x32, 0x33,
                               0x34, 0x35, 0x36, 0x37,
                               0x38, 0x39, 0x0A, 0x0B,
                               0x0C, 0x0D, 0x0E, 0x0F };
unsigned char CipherKey[16] = { 0xAA, 0xBB, 0x02, 0x03,
                               0x04, 0x05, 0x06, 0x07,
                               0x08, 0x09, 0x0A, 0x0B,
                               0x0C, 0x0D, 0x0E, 0x0F };

unsigned char DataAES[16];    // Encrypted data
unsigned char DataunAES[16];  // Decrypted data

// Load a cipher key to module
AES_setCipherKey(AES_BASE, CipherKey);

// Encrypt data with preloaded cipher key
AES_encryptData(AES_BASE, Data, DataAES);

// Decrypt data with keys that were generated during encryption - takes 214 MCLK
// This function will generate all round keys needed for decryption first and then
// the encryption process starts
AES_decryptDataUsingEncryptionKey(AES_BASE, DataAES, DataunAES);

```

# 10 Battery Backup System

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## 10.1 Introduction

The Battery Backup System (BATBCK) API provides a set of functions for using the MSP430Ware BATBCK modules. Functions are provided to handle the backup Battery sub-system, initialize and enable the backup Battery charger, and control access to and from the backup RAM space.

The BATBCK module offers no interrupt, and is used only to control the Battery backup sub-system, Battery charger, and backup RAM space.

## 10.2 API Functions

The BATBCK API is divided into three groups: one that handles the Battery backup sub-system, one that controls the charger, and one that controls access to and from the backup RAM space.

The BATBCK sub-system controls are handled by

- [BattBak\\_unlockBackupSubSystem\(\)](#)
- [BattBak\\_enableBackupSupplyToADC\(\)](#)
- [BattBak\\_disableBackupSupplyToADC\(\)](#)
- [BattBak\\_switchToBackupSupplyManually\(\)](#)
- [BattBak\\_disable\(\)](#)

The BATBCK charger is controlled by

- [BattBak\\_initAndEnableCharger\(\)](#)
- [BattBak\\_disableCharger\(\)](#)

The backup RAM space is accessed by

- [BattBak\\_setBackupRAMData\(\)](#)
- [BattBak\\_getBackupRAMData\(\)](#)

# 11 Comparator (COMP\_B)

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## 11.1 Introduction

The Comparator B (COMP\_B) API provides a set of functions for using the MSP430Ware COMP\_B modules. Functions are provided to initialize the COMP\_B modules, setup reference voltages for input, and manage interrupts for the COMP\_B modules.

The COMP\_B module provides the ability to compare two analog signals and use the output in software and on an output pin. The output represents whether the signal on the positive terminal is higher than the signal on the negative terminal. The COMP\_B may be used to generate a hysteresis. There are 16 different inputs that can be used, as well as the ability to short 2 input together. The COMP\_B module also has control over the REF module to generate a reference voltage as an input.

The COMP\_B module can generate multiple interrupts. An interrupt may be asserted for the output, with separate interrupts on whether the output rises, or falls.

## 11.2 API Functions

### Functions

- `bool Comp_B_init (uint16_t baseAddress, Comp\_B\_initParam *param)`  
*Initializes the Comp\_B Module.*
- `void Comp_B_configureReferenceVoltage (uint16_t baseAddress, Comp\_B\_configureReferenceVoltageParam *param)`  
*Generates a Reference Voltage to the terminal selected during initialization.*
- `void Comp_B_enableInterrupt (uint16_t baseAddress, uint16_t interruptMask)`  
*Enables selected Comp\_B interrupt sources.*
- `void Comp_B_disableInterrupt (uint16_t baseAddress, uint16_t interruptMask)`  
*Disables selected Comp\_B interrupt sources.*
- `void Comp_B_clearInterrupt (uint16_t baseAddress, uint16_t interruptFlagMask)`  
*Clears Comp\_B interrupt flags.*
- `uint8_t Comp_B_getInterruptStatus (uint16_t baseAddress, uint16_t interruptFlagMask)`  
*Gets the current Comp\_B interrupt status.*
- `void Comp_B_setInterruptEdgeDirection (uint16_t baseAddress, uint16_t edgeDirection)`  
*Explicitly sets the edge direction that would trigger an interrupt.*
- `void Comp_B_toggleInterruptEdgeDirection (uint16_t baseAddress)`  
*Toggles the edge direction that would trigger an interrupt.*
- `void Comp_B_enable (uint16_t baseAddress)`  
*Turns on the Comp\_B module.*
- `void Comp_B.disable (uint16_t baseAddress)`  
*Turns off the Comp\_B module.*
- `void Comp_B.shortInputs (uint16_t baseAddress)`

- Shorts the two input pins chosen during initialization.*
- void `Comp_B_unshortInputs` (uint16\_t baseAddress)
- Disables the short of the two input pins chosen during initialization.*
- void `Comp_B_disableInputBuffer` (uint16\_t baseAddress, uint8\_t inputPort)
- Disables the input buffer of the selected input port to effectively allow for analog signals.*
- void `Comp_B_enableInputBuffer` (uint16\_t baseAddress, uint8\_t inputPort)
- Enables the input buffer of the selected input port to allow for digital signals.*
- void `Comp_B_swapIO` (uint16\_t baseAddress)
- Toggles the bit that swaps which terminals the inputs go to, while also inverting the output of the Comp\_B.*
- uint16\_t `Comp_B_outputValue` (uint16\_t baseAddress)
- Returns the output value of the Comp\_B module.*
- void `Comp_B_selectReferenceVoltage` (uint16\_t baseAddress, uint16\_t selectType, uint16\_t selectVRef)
- Modifies how comparator output selects between VREF0 or VREF1.*

## 11.2.1 Detailed Description

The COMP\_B API is broken into three groups of functions: those that deal with initialization and output, those that handle interrupts, and those that handle auxiliary features of the COMP\_B.

The COMP\_B initialization and output functions are

- `Comp_B_init()`
- `Comp_B_configureReferenceVoltage()`
- `Comp_B_selectReferenceVoltage()`
- `Comp_B.enable()`
- `Comp_B.disable()`
- `Comp_B_outputValue()`

The COMP\_B interrupts are handled by

- `Comp_B.enableInterrupt()`
- `Comp_B.disableInterrupt()`
- `Comp_B.clearInterrupt()`
- `Comp_B.getInterruptStatus()`
- `Comp_B.setInterruptEdgeDirection()`
- `Comp_B.toggleInterruptEdgeDirection()`

Auxiliary features of the COMP\_B are handled by

- `Comp_B.shortInputs()`
- `Comp_B.unshortInputs()`
- `Comp_B.disableInputBuffer()`
- `Comp_B.enableInputBuffer()`
- `Comp_B.swapIO()`

## 11.2.2 Function Documentation

### Comp\_B\_clearInterrupt()

```
void Comp_B_clearInterrupt (
    uint16_t baseAddress,
    uint16_t interruptFlagMask )
```

Clears Comp\_B interrupt flags.

The Comp\_B interrupt source is cleared, so that it no longer asserts. The highest interrupt flag is automatically cleared when an interrupt vector generator is used.

#### Parameters

<i>baseAddress</i>	is the base address of the COMP_B module.
<i>interruptFlagMask</i>	is a bit mask of the interrupt sources to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>COMP_B_OUTPUT_FLAG</b> - Output interrupt</li> <li>■ <b>COMP_B_OUTPUTINVERTED_FLAG</b> - Output interrupt inverted polarity</li> </ul> Modified bits of <b>CBINT</b> register.

#### Returns

None

### Comp\_B\_configureReferenceVoltage()

```
void Comp_B_configureReferenceVoltage (
    uint16_t baseAddress,
    Comp_B_configureReferenceVoltageParam * param )
```

Generates a Reference Voltage to the terminal selected during initialization.

Use this function to generate a voltage to serve as a reference to the terminal selected at initialization. The voltage is determined by the equation:  $V_{base} * (\text{Numerator} / 32)$ . If the upper and lower limit voltage numerators are equal, then a static reference is defined, whereas they are different then a hysteresis effect is generated.

#### Parameters

<i>baseAddress</i>	is the base address of the COMP_B module.
<i>param</i>	is the pointer to struct for reference voltage configuration.

**Returns**

None

References `Comp_B_configureReferenceVoltageParam::lowerLimitSupplyVoltageFractionOf32`, `Comp_B_configureReferenceVoltageParam::referenceAccuracy`, `Comp_B_configureReferenceVoltageParam::supplyVoltageReferenceBase`, and `Comp_B_configureReferenceVoltageParam::upperLimitSupplyVoltageFractionOf32`.

**Comp\_B\_disable()**

```
void Comp_B_disable (
    uint16_t baseAddress )
```

Turns off the `Comp_B` module.

This function clears the `CBON` bit disabling the operation of the `Comp_B` module, saving from excess power consumption.

**Parameters**

<i>baseAddress</i>	is the base address of the <code>COMP_B</code> module.
--------------------	--

**Returns**

None

**Comp\_B\_disableInputBuffer()**

```
void Comp_B_disableInputBuffer (
    uint16_t baseAddress,
    uint8_t inputPort )
```

Disables the input buffer of the selected input port to effectively allow for analog signals.

This function sets the bit to disable the buffer for the specified input port to allow for analog signals from any of the `Comp_B` input pins. This bit is automatically set when the input is initialized to be used with the `Comp_B` module. This function should be used whenever an analog input is connected to one of these pins to prevent parasitic voltage from causing unexpected results.

**Parameters**

<i>baseAddress</i>	is the base address of the <code>COMP_B</code> module.
--------------------	--



## Parameters

<i>inputPort</i>	<p>is the port in which the input buffer will be disabled. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>COMP_B_INPUT0</b> [Default]</li> <li>■ <b>COMP_B_INPUT1</b></li> <li>■ <b>COMP_B_INPUT2</b></li> <li>■ <b>COMP_B_INPUT3</b></li> <li>■ <b>COMP_B_INPUT4</b></li> <li>■ <b>COMP_B_INPUT5</b></li> <li>■ <b>COMP_B_INPUT6</b></li> <li>■ <b>COMP_B_INPUT7</b></li> <li>■ <b>COMP_B_INPUT8</b></li> <li>■ <b>COMP_B_INPUT9</b></li> <li>■ <b>COMP_B_INPUT10</b></li> <li>■ <b>COMP_B_INPUT11</b></li> <li>■ <b>COMP_B_INPUT12</b></li> <li>■ <b>COMP_B_INPUT13</b></li> <li>■ <b>COMP_B_INPUT14</b></li> <li>■ <b>COMP_B_INPUT15</b></li> <li>■ <b>COMP_B_VREF</b></li> </ul> <p>Modified bits are <b>CBPDx</b> of <b>CBCTL3</b> register.</p>
------------------	--

## Returns

None

## Comp\_B\_disableInterrupt()

```
void Comp_B_disableInterrupt (
    uint16_t baseAddress,
    uint16_t interruptMask )
```

Disables selected Comp\_B interrupt sources.

Disables the indicated Comp\_B interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

## Parameters

<i>baseAddress</i>	is the base address of the COMP_B module.
<i>interruptMask</i>	<p>is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>COMP_B_OUTPUT_INT</b> - Output interrupt</li> <li>■ <b>COMP_B_OUTPUTINVERTED_INT</b> - Output interrupt inverted polarity</li> </ul> <p>Modified bits of <b>CBINT</b> register.</p>

**Returns**

None

**Comp\_B\_enable()**

```
void Comp_B_enable (
    uint16_t baseAddress )
```

Turns on the Comp\_B module.

This function sets the bit that enables the operation of the Comp\_B module.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_B module.
--------------------	---

**Returns**

None

**Comp\_B\_enableInputBuffer()**

```
void Comp_B_enableInputBuffer (
    uint16_t baseAddress,
    uint8_t inputPort )
```

Enables the input buffer of the selected input port to allow for digital signals.

This function clears the bit to enable the buffer for the specified input port to allow for digital signals from any of the Comp\_B input pins. This should not be reset if there is an analog signal connected to the specified input pin to prevent from unexpected results.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_B module.
--------------------	---

## Parameters

<i>inputPort</i>	<p>is the port in which the input buffer will be enabled. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>COMP_B_INPUT0</b> [Default]</li> <li>■ <b>COMP_B_INPUT1</b></li> <li>■ <b>COMP_B_INPUT2</b></li> <li>■ <b>COMP_B_INPUT3</b></li> <li>■ <b>COMP_B_INPUT4</b></li> <li>■ <b>COMP_B_INPUT5</b></li> <li>■ <b>COMP_B_INPUT6</b></li> <li>■ <b>COMP_B_INPUT7</b></li> <li>■ <b>COMP_B_INPUT8</b></li> <li>■ <b>COMP_B_INPUT9</b></li> <li>■ <b>COMP_B_INPUT10</b></li> <li>■ <b>COMP_B_INPUT11</b></li> <li>■ <b>COMP_B_INPUT12</b></li> <li>■ <b>COMP_B_INPUT13</b></li> <li>■ <b>COMP_B_INPUT14</b></li> <li>■ <b>COMP_B_INPUT15</b></li> <li>■ <b>COMP_B_VREF</b></li> </ul> <p>Modified bits are <b>CBPDx</b> of <b>CBCTL3</b> register.</p>
------------------	---

## Returns

None

## Comp\_B\_enableInterrupt()

```
void Comp_B_enableInterrupt (
    uint16_t baseAddress,
    uint16_t interruptMask )
```

Enables selected Comp\_B interrupt sources.

Enables the indicated Comp\_B interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

## Parameters

<i>baseAddress</i>	is the base address of the COMP_B module.
--------------------	---

**Parameters**

<i>interruptMask</i>	is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>COMP_B_OUTPUT_INT</b> - Output interrupt</li> <li>■ <b>COMP_B_OUTPUTINVERTED_INT</b> - Output interrupt inverted polarity Modified bits of <b>CBINT</b> register.</li> </ul>
----------------------	--

**Returns**

None

**Comp\_B\_getInterruptStatus()**

```
uint8_t Comp_B_getInterruptStatus (
    uint16_t baseAddress,
    uint16_t interruptFlagMask )
```

Gets the current Comp\_B interrupt status.

This returns the interrupt status for the Comp\_B module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_B module.
<i>interruptFlagMask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>COMP_B_OUTPUT_FLAG</b> - Output interrupt</li> <li>■ <b>COMP_B_OUTPUTINVERTED_FLAG</b> - Output interrupt inverted polarity</li> </ul>

**Returns**

Logical OR of any of the following:

- **COMP\_B\_OUTPUT\_FLAG** Output interrupt
- **COMP\_B\_OUTPUTINVERTED\_FLAG** Output interrupt inverted polarity  
indicating the status of the masked interrupts

**Comp\_B\_init()**

```
bool Comp_B_init (
    uint16_t baseAddress,
    Comp_B_initParam * param )
```

Initializes the Comp\_B Module.

Upon successful initialization of the Comp\_B module, this function will have reset all necessary register bits and set the given options in the registers. To actually use the Comp\_B module, the

`Comp_B_enable()` function must be explicitly called before use. If a Reference Voltage is set to a terminal, the Voltage should be set using the `Comp_B_setReferenceVoltage()` function.

#### Parameters

<i>baseAddress</i>	is the base address of the COMP_B module.
<i>param</i>	is the pointer to struct for initialization.

#### Returns

STATUS\_SUCCESS or STATUS\_FAILURE of the initialization process.

References `Comp_B_initParam::invertedOutputPolarity`, `Comp_B_initParam::negativeTerminalInput`, `Comp_B_initParam::outputFilterEnableAndDelayLevel`, `Comp_B_initParam::positiveTerminalInput`, and `Comp_B_initParam::powerModeSelect`.

### Comp\_B\_outputValue()

```
uint16_t Comp_B_outputValue (
    uint16_t baseAddress )
```

Returns the output value of the Comp\_B module.

Returns the output value of the Comp\_B module.

#### Parameters

<i>baseAddress</i>	is the base address of the COMP_B module.
--------------------	---

#### Returns

One of the following:

- **COMP\_B\_LOW**
- **COMP\_B\_HIGH**

indicating the output value of the Comp\_B module

### Comp\_B\_selectReferenceVoltage()

```
void Comp_B_selectReferenceVoltage (
    uint16_t baseAddress,
    uint16_t selectType,
    uint16_t selectVRef )
```

Modifies how comparator output selects between VREF0 or VREF1.

Only applicable in certain Comp\_B reference sources. Consult `Comp_B_configureReferenceVoltage` for details. If `COMP_B_VREF_AUTO_SELECT`, then comparator output state chooses between VREF0 and VREF1. If `COMP_B_VREF_MANUAL_SELECT`, then `selectVRef` param chooses.

## Parameters

<i>baseAddress</i>	is the base address of the COMP_B module.
<i>selectType</i>	determines whether VREF instance is chosen automatically or manually Valid values are: <ul style="list-style-type: none"> <li>■ <b>COMP_B_VREF_AUTO_SELECT</b> [Default] - VREF instance is chosen by comparator output state.</li> <li>■ <b>COMP_B_VREF_MANUAL_SELECT</b> - VREF instance is chosen by user (CBCTL1. CBMRVL bit) Modified bits are <b>CBMRVS</b> of <b>CBCTL1</b> register.</li> </ul>
<i>selectVRef</i>	selects VREF0 or VREF1. Only applicable if VREF instance is set up to be chosen manually Valid values are: <ul style="list-style-type: none"> <li>■ <b>COMP_B_SELECT_VREF0</b> [Default]</li> <li>■ <b>COMP_B_SELECT_VREF1</b> Modified bits are <b>CBMRVL</b> of <b>CBCTL1</b> register.</li> </ul>

## Comp\_B\_setInterruptEdgeDirection()

```
void Comp_B_setInterruptEdgeDirection (
    uint16_t baseAddress,
    uint16_t edgeDirection )
```

Explicitly sets the edge direction that would trigger an interrupt.

This function will set which direction the output will have to go, whether rising or falling, to generate an interrupt based on a non-inverted interrupt.

## Parameters

<i>baseAddress</i>	is the base address of the COMP_B module.
<i>edgeDirection</i>	determines which direction the edge would have to go to generate an interrupt based on the non-inverted interrupt flag. Valid values are: <ul style="list-style-type: none"> <li>■ <b>COMP_B_RISINGEDGE</b> [Default] - sets the bit to generate an interrupt when the output of the Comp_B falls from LOW to HIGH if the normal interrupt bit is set(and HIGH to LOW if the inverted interrupt enable bit is set).</li> <li>■ <b>COMP_B_FALLINGEDGE</b> - sets the bit to generate an interrupt when the output of the Comp_B rises from HIGH to LOW if the normal interrupt bit is set(and LOW to HIGH if the inverted interrupt enable bit is set). Modified bits are <b>CBIES</b> of <b>CBCTL1</b> register.</li> </ul>

**Returns**

None

**Comp\_B\_shortInputs()**

```
void Comp_B_shortInputs (
    uint16_t baseAddress )
```

Shorts the two input pins chosen during initialization.

This function sets the bit that shorts the devices attached to the input pins chosen from the initialization of the Comp\_B.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_B module.
--------------------	---

**Returns**

None

**Comp\_B\_swapIO()**

```
void Comp_B_swapIO (
    uint16_t baseAddress )
```

Toggles the bit that swaps which terminals the inputs go to, while also inverting the output of the Comp\_B.

This function toggles the bit that controls which input goes to which terminal. After initialization, this bit is set to 0, after toggling it once the inputs are routed to the opposite terminal and the output is inverted.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_B module.
--------------------	---

**Returns**

None

**Comp\_B\_toggleInterruptEdgeDirection()**

```
void Comp_B_toggleInterruptEdgeDirection (
    uint16_t baseAddress )
```

Toggles the edge direction that would trigger an interrupt.

This function will toggle which direction the output will have to go, whether rising or falling, to generate an interrupt based on a non-inverted interrupt. If the direction was rising, it is now falling,

if it was falling, it is now rising.

#### Parameters

<i>baseAddress</i>	is the base address of the COMP_B module.
--------------------	---

#### Returns

None

### Comp\_B\_unshortInputs()

```
void Comp_B_unshortInputs (
    uint16_t baseAddress )
```

Disables the short of the two input pins chosen during initialization.

This function clears the bit that shorts the devices attached to the input pins chosen from the initialization of the Comp\_B.

#### Parameters

<i>baseAddress</i>	is the base address of the COMP_B module.
--------------------	---

#### Returns

None

## 11.3 Programming Example

The following example shows how to initialize and use the COMP\_B API to turn on an LED when the input to the positive terminal is higher than the input to the negative terminal.

```
// Initialize the Comparator B module
/* Base Address of Comparator B,
   Pin CB0 to Positive(+) Terminal,
   Reference Voltage to Negative(-) Terminal,
   Normal Power Mode,
   Output Filter On with minimal delay,
   Non-Inverted Output Polarity
*/
Comp_B_initParam param = {0};
param.positiveTerminalInput = COMP_B_INPUT0;
param.negativeTerminalInput = COMP_B_VREF;
param.powerModeSelect = COMP_B_POWERMODE_NORMALMODE;
param.outputFilterEnableAndDelayLevel = COMP_B_FILTEROUTPUT_DLYLVL1;
param.invertedOutputPolarity = COMP_B_NORMALOUTPUTPOLARITY;
Comp_B_init (COMP_B_BASE, &param);

// Set the reference voltage that is being supplied to the (-) terminal
/* Base Address of Comparator B,
   Reference Voltage of 2.0 V,
   Upper Limit of 2.0*(32/32) = 2.0V,
   Lower Limit of 2.0*(32/32) = 2.0V
*/
Comp_B_setReferenceVoltage (COMP_B_BASE,
```



```
        COMP_B.VREFBASE2_5V,  
        32,  
        32  
    );  
  
    // Allow power to Comparator module  
    Comp_B.enable(COMP_B.BASE);  
  
    // delay for the reference to settle  
    __delay_cycles(75);
```

# 12 Cyclical Redundancy Check (CRC)

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## 12.1 Introduction

The Cyclic Redundancy Check (CRC) API provides a set of functions for using the MSP430Ware CRC module. Functions are provided to initialize the CRC and create a CRC signature to check the validity of data. This is mostly useful in the communication of data, or as a startup procedure to as a more complex and accurate check of data.

The CRC module offers no interrupts and is used only to generate CRC signatures to verify against pre-made CRC signatures (Checksums).

## 12.2 API Functions

### Functions

- void [CRC.setSeed](#) (uint16\_t baseAddress, uint16\_t seed)  
*Sets the seed for the CRC.*
- void [CRC.set16BitData](#) (uint16\_t baseAddress, uint16\_t dataIn)  
*Sets the 16 bit data to add into the CRC module to generate a new signature.*
- void [CRC.set8BitData](#) (uint16\_t baseAddress, uint8\_t dataIn)  
*Sets the 8 bit data to add into the CRC module to generate a new signature.*
- void [CRC.set16BitDataReversed](#) (uint16\_t baseAddress, uint16\_t dataIn)  
*Translates the 16 bit data by reversing the bits in each byte and then sets this data to add into the CRC module to generate a new signature.*
- void [CRC.set8BitDataReversed](#) (uint16\_t baseAddress, uint8\_t dataIn)  
*Translates the 8 bit data by reversing the bits in each byte and then sets this data to add into the CRC module to generate a new signature.*
- uint16\_t [CRC.getData](#) (uint16\_t baseAddress)  
*Returns the value currently in the Data register.*
- uint16\_t [CRC.getResult](#) (uint16\_t baseAddress)  
*Returns the value of the Signature Result.*
- uint16\_t [CRC.getResultBitsReversed](#) (uint16\_t baseAddress)  
*Returns the bit-wise reversed format of the Signature Result.*

### 12.2.1 Detailed Description

The CRC API is one group that controls the CRC module. The APIs that are used to set the seed and data are

- [CRC.setSeed\(\)](#)
- [CRC.set16BitData\(\)](#)

- [CRC\\_set8BitData\(\)](#)
- [CRC\\_set16BitDataReversed\(\)](#)
- [CRC\\_set8BitDataReversed\(\)](#)
- [CRC\\_setSeed\(\)](#)

The APIs that are used to get the data and results are

- [CRC\\_getData\(\)](#)
- [CRC\\_getResult\(\)](#)
- [CRC\\_getResultBitsReversed\(\)](#)

## 12.2.2 Function Documentation

### CRC\_getData()

```
uint16_t CRC_getData (
    uint16_t baseAddress )
```

Returns the value currently in the Data register.

This function returns the value currently in the data register. If set in byte bits reversed format, then the translated data would be returned.

#### Parameters

<i>baseAddress</i>	is the base address of the CRC module.
--------------------	--

#### Returns

The value currently in the data register

### CRC\_getResult()

```
uint16_t CRC_getResult (
    uint16_t baseAddress )
```

Returns the value of the Signature Result.

This function returns the value of the signature result generated by the CRC.

#### Parameters

<i>baseAddress</i>	is the base address of the CRC module.
--------------------	--

**Returns**

The value currently in the data register

**CRC\_getResultBitsReversed()**

```
uint16_t CRC_getResultBitsReversed (
    uint16_t baseAddress )
```

Returns the bit-wise reversed format of the Signature Result.

This function returns the bit-wise reversed format of the Signature Result.

**Parameters**

<i>baseAddress</i>	is the base address of the CRC module.
--------------------	--

**Returns**

The bit-wise reversed format of the Signature Result

**CRC\_set16BitData()**

```
void CRC_set16BitData (
    uint16_t baseAddress,
    uint16_t dataIn )
```

Sets the 16 bit data to add into the CRC module to generate a new signature.

This function sets the given data into the CRC module to generate the new signature from the current signature and new data.

**Parameters**

<i>baseAddress</i>	is the base address of the CRC module.
<i>dataIn</i>	is the data to be added, through the CRC module, to the signature. Modified bits are <b>CRCDI</b> of <b>CRCDI</b> register.

**Returns**

None

**CRC\_set16BitDataReversed()**

```
void CRC_set16BitDataReversed (
    uint16_t baseAddress,
    uint16_t dataIn )
```

Translates the 16 bit data by reversing the bits in each byte and then sets this data to add into the CRC module to generate a new signature.

This function first reverses the bits in each byte of the data and then generates the new signature from the current signature and new translated data.

#### Parameters

<i>baseAddress</i>	is the base address of the CRC module.
<i>dataIn</i>	is the data to be added, through the CRC module, to the signature. Modified bits are <b>CRCDIRB</b> of <b>CRCDIRB</b> register.

#### Returns

None

### CRC\_set8BitData()

```
void CRC_set8BitData (
    uint16_t baseAddress,
    uint8_t dataIn )
```

Sets the 8 bit data to add into the CRC module to generate a new signature.

This function sets the given data into the CRC module to generate the new signature from the current signature and new data.

#### Parameters

<i>baseAddress</i>	is the base address of the CRC module.
<i>dataIn</i>	is the data to be added, through the CRC module, to the signature. Modified bits are <b>CRCDI</b> of <b>CRCDI</b> register.

#### Returns

None

### CRC\_set8BitDataReversed()

```
void CRC_set8BitDataReversed (
    uint16_t baseAddress,
    uint8_t dataIn )
```

Translates the 8 bit data by reversing the bits in each byte and then sets this data to add into the CRC module to generate a new signature.

This function first reverses the bits in each byte of the data and then generates the new signature from the current signature and new translated data.

#### Parameters

<i>baseAddress</i>	is the base address of the CRC module.
<i>dataIn</i>	is the data to be added, through the CRC module, to the signature. Modified bits are <b>CRCDIRB</b> of <b>CRCDIRB</b> register.

**Returns**

None

**CRC\_setSeed()**

```
void CRC_setSeed (
    uint16_t baseAddress,
    uint16_t seed )
```

Sets the seed for the CRC.

This function sets the seed for the CRC to begin generating a signature with the given seed and all passed data. Using this function resets the CRC signature.

**Parameters**

<i>baseAddress</i>	is the base address of the CRC module.
<i>seed</i>	is the seed for the CRC to start generating a signature from. Modified bits are <b>CRCINIRES</b> of <b>CRCINIRES</b> register.

**Returns**

None

## 12.3 Programming Example

The following example shows how to initialize and use the CRC API to generate a CRC signature on an array of data.

```
unsigned int crcSeed = 0xBEEF;
unsigned int data[] = {0x0123,
                      0x4567,
                      0x8910,
                      0x1112,
                      0x1314};
unsigned int crcResult;
int i;

// Stop WDT
WDT_hold(WDTA_BASE);

// Set P1.0 as an output
GPIO_setAsOutputPin(GPIO_PORT_P1,
                    GPIO_PIN0);

// Set the CRC seed
CRC_setSeed(CRC_BASE,
            crcSeed);

for (i = 0; i < 5; i++)
{
    //Add all of the values into the CRC signature
    CRC_set16BitData(CRC_BASE,
                    data[i]);
}

// Save the current CRC signature checksum to be compared for later
crcResult = CRC_getResult(CRC_BASE);
```

# 13 16-Bit Sigma Delta Converter (CTSD16)

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## 13.1 Introduction

The CTSD16 module consists of up to seven independent sigma-delta analog-to-digital multi-input and multi-converters. The converters are based on second-order oversampling sigma-delta modulators and digital decimation filters. The decimation filters are comb type filters with selectable oversampling ratios of up to 256. Additional filtering can be done in software.

A sigma-delta analog-to-digital converter basically consists of two parts: the analog part

- called modulator - and the digital part - a decimation filter. The modulator of the CTSD16 with fixed frequency 1.024Mhz, provides a bit stream of zeros and ones to the digital decimation filter. The digital filter averages the bitstream from the modulator over a given number of bits (specified by the oversampling rate) and provides samples at a reduced rate for further processing to the CPU.

As commonly known averaging can be used to increase the signal-to-noise performance of a conversion. With a conventional ADC each factor-of-4 oversampling can improve the SNR by about 6 dB or 1 bit. To achieve a 16-bit resolution out of a simple 1-bit ADC would require an impractical oversampling rate of  $4^{15} = 1.073.741.824$ . To overcome this limitation the sigma-delta modulator implements a technique called noise-shaping - due to an implemented feedback-loop and integrators the quantization noise is pushed to higher frequencies and thus much lower oversampling rates are sufficient to achieve high resolutions.

## 13.2 API Functions

The CTSD16 API is broken into three groups of functions: those that deal with initialization and conversions, those that handle interrupts, and those that handle auxiliary features of the CTSD16.

The CTSD16 initialization and conversion functions are

- CTSD16\_init()
- CTSD16\_initConverter()
- CTSD16\_initConverterAdvanced()
- CTSD16\_stopConverterConversion()
- CTSD16\_startConverterConversion()
- CTSD16\_getResults()

The CTSD16 interrupts are handled by

- CTSD16\_enableInterrupt()
- CTSD16\_disableInterrupt()

- CTSD16\_clearInterrupt()
- CTSD16\_getInterruptStatus()

Auxiliary features of the CTSD16 are handled by

- CTSD16\_setInputChannel()
- CTSD16\_setDataFormat()
- CTSD16\_setInterruptDelay()
- CTSD16\_setConversionDelay()
- CTSD16\_setOversampling()
- CTSD16\_setGain()
- CTSD16\_setRailToRailInput()
- CTSD16\_isRailToRailInputReady()

## 13.3 Programming Example

The following example shows how to initialize and use the CTSD16 API to start a single channel, single conversion.

```
uint16_t result;

// Initialize CTSD16 using internal reference and internal resistor for clock
CTSD16_init(CTSD16_BASE,
            CTSD16_RTR_INPUT_CHARGE_PUMP_BURST_REQUEST_DISABLE, CTSD16_REF_INTERNAL);

// Initialize converter 0: AD0+ / AD0- as input, 2s complement, channel 9
CTSD16_initConverterParam convParam = {0};
convParam.converter = CTSD16_CONVERTER_0;
convParam.conversionMode = CTSD16_SINGLE_MODE;
convParam.groupEnable = CTSD16_NOT_GROUPED;
convParam.inputChannel = CTSD16_INPUT_CH9;
convParam.dataFormat = CTSD16_DATA_FORMAT_2_COMPLEMENT;
convParam.railToRailInput = CTSD16_RTR_INPUT_DISABLE;
convParam.interruptDelay = CTSD16_FOURTH_SAMPLE_INTERRUPT;
convParam.oversampleRatio = CTSD16_OVERSAMPLE_256;
convParam.gain = CTSD16_GAIN_1;
CTSD16_initConverter(CTSD16_BASE, &convParam);

// Delay ~120us for 1.2V ref to settle
__delay_cycles(2000);

while(1) {
    // Set bit to start conversion
    CTSD16_startConverterConversion(CTSD16_BASE, CTSD16_CONVERTER_0);

    // Poll IFG until conversion completes
    while(!CTSD16_getInterruptStatus(CTSD16_BASE, CTSD16_CONVERTER_0, CTSD16_CONVERTER_INTERRUPT));

    // Save CTSD16 conversion results
    result = CTSD16_getResults(CTSD16_BASE, CTSD16_CONVERTER_0);
}
```



# 14 12-bit Digital-to-Analog Converter (DAC12\_A)

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## 14.1 Introduction

The 12-Bit Digital-to-Analog (DAC12\_A) API provides a set of functions for using the MSP430Ware DAC12\_A modules. Functions are provided to initialize setup the DAC12\_A modules, calibrate the output signal, and manage the interrupts for the DAC12\_A modules.

The DAC12\_A module provides the ability to convert digital values into an analog signal for output to a pin. The DAC12\_A can generate signals from 0 to Vcc from an 8- or 12-bit value. There can be one or two DAC12\_A modules in a device, and if there are two they can be grouped together to create two analog signals in simultaneously. There are 3 ways to latch data in to the DAC module, and those are by software with the startConversion API function call, as well as by the Timer A output of CCR1 or Timer B output of CCR2.

The calibration API will unlock and start calibration, then wait for the calibration to end before locking it back up, all in one API. There are also functions to read out the calibration data, as well as be able to set it manually.

The DAC12\_A module can generate one interrupt for each DAC module. It will generate the interrupt when the data has been latched into the DAC module to be output into an analog signal.

## 14.2 API Functions

### Functions

- bool `DAC12_A_init` (uint16\_t baseAddress, `DAC12_A_initParam` \*param)  
*Initializes the DAC12\_A module with the specified settings.*
- void `DAC12_A_setAmplifierSetting` (uint16\_t baseAddress, uint8\_t submoduleSelect, uint8\_t amplifierSetting)  
*Sets the amplifier settings for the Vref+ and Vout buffers.*
- void `DAC12_A_disable` (uint16\_t baseAddress, uint8\_t submoduleSelect)  
*Clears the amplifier settings to disable the DAC12\_A module.*
- void `DAC12_A_enableGrouping` (uint16\_t baseAddress)  
*Enables grouping of two DAC12\_A modules in a dual DAC12\_A system.*
- void `DAC12_A_disableGrouping` (uint16\_t baseAddress)  
*Disables grouping of two DAC12\_A modules in a dual DAC12\_A system.*
- void `DAC12_A_enableInterrupt` (uint16\_t baseAddress, uint8\_t submoduleSelect)  
*Enables the DAC12\_A module interrupt source.*
- void `DAC12_A_disableInterrupt` (uint16\_t baseAddress, uint8\_t submoduleSelect)  
*Disables the DAC12\_A module interrupt source.*
- uint16\_t `DAC12_A_getInterruptStatus` (uint16\_t baseAddress, uint8\_t submoduleSelect)

- Returns the status of the DAC12\_A module interrupt flag.*
- void `DAC12_A_clearInterrupt` (uint16\_t baseAddress, uint8\_t submoduleSelect)  
*Clears the DAC12\_A module interrupt flag.*
- void `DAC12_A_calibrateOutput` (uint16\_t baseAddress, uint8\_t submoduleSelect)  
*Calibrates the output offset.*
- uint16\_t `DAC12_A_getCalibrationData` (uint16\_t baseAddress, uint8\_t submoduleSelect)  
*Returns the calibrated offset of the output buffer.*
- void `DAC12_A_setCalibrationOffset` (uint16\_t baseAddress, uint8\_t submoduleSelect, uint16\_t calibrationOffsetValue)  
*Returns the calibrated offset of the output buffer.*
- void `DAC12_A_enableConversions` (uint16\_t baseAddress, uint8\_t submoduleSelect)  
*Enables triggers to start conversions.*
- void `DAC12_A_setData` (uint16\_t baseAddress, uint8\_t submoduleSelect, uint16\_t data)  
*Sets the given data into the buffer to be converted.*
- void `DAC12_A_disableConversions` (uint16\_t baseAddress, uint8\_t submoduleSelect)  
*Disables triggers to start conversions.*
- void `DAC12_A_setResolution` (uint16\_t baseAddress, uint8\_t submoduleSelect, uint16\_t resolutionSelect)  
*Sets the resolution to be used by the DAC12\_A module.*
- void `DAC12_A_setInputDataFormat` (uint16\_t baseAddress, uint8\_t submoduleSelect, uint8\_t inputJustification, uint8\_t inputSign)  
*Sets the input data format for the DAC12\_A module.*
- uint32\_t `DAC12_A_getDataBufferMemoryAddressForDMA` (uint16\_t baseAddress, uint8\_t submoduleSelect)  
*Returns the address of the specified DAC12\_A data buffer for the DMA module.*

## 14.2.1 Detailed Description

The DAC12\_A API is broken into three groups of functions: those that deal with initialization and conversions, those that deal with calibration of the output, and those that handle interrupts.

The DAC12\_A initialization and conversion functions are

- `DAC12_A_init()`
- `DAC12_A_setAmplifierSetting()`
- `DAC12_A_disable()`
- `DAC12_A_enableGrouping()`
- `DAC12_A_disableGrouping()`
- `DAC12_A_enableConversions()`
- `DAC12_A_setData()`
- `DAC12_A_disableConversions()`
- `DAC12_A_setResolution()`
- `DAC12_A_setInputDataFormat()`
- `DAC12_A_getDataBufferMemoryAddressForDMA()`

Calibration features of the DAC12\_A are handled by

- `DAC12_A_calibrateOutput()`
- `DAC12_A_getCalibrationData()`

- [DAC12\\_A.setCalibrationOffset\(\)](#)

The DAC12\_A interrupts are handled by

- [DAC12\\_A.enableInterrupt\(\)](#)
- [DAC12\\_A.disableInterrupt\(\)](#)
- [DAC12\\_A.getInterruptStatus\(\)](#)
- [DAC12\\_A.clearInterrupt\(\)](#)

## 14.2.2 Function Documentation

### DAC12\_A\_calibrateOutput()

```
void DAC12_A_calibrateOutput (
    uint16_t baseAddress,
    uint8_t submoduleSelect )
```

Calibrates the output offset.

This function disables the calibration lock, starts the calibration, waits for the calibration to complete, and then re-locks the calibration lock. Please note, this function should be called after initializing the dac12 module, and before using it.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A.SUBMODULE_0</b></li> <li>■ <b>DAC12_A.SUBMODULE_1</b></li> </ul>

Modified bits are **DAC12CALON** of **DAC12\_xCTL0** register; bits **DAC12PW** of **DAC12\_xCALCTL** register.

#### Returns

None

### DAC12\_A\_clearInterrupt()

```
void DAC12_A_clearInterrupt (
    uint16_t baseAddress,
    uint8_t submoduleSelect )
```

Clears the DAC12\_A module interrupt flag.

The DAC12\_A module interrupt flag is cleared, so that it no longer asserts. Note that an interrupt is not thrown when **DAC12\_A.TRIGGER\_ENCBYPASS** has been set for the parameter **conversionTriggerSelect** in initialization.

**Parameters**

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A.SUBMODULE_0</b></li> <li>■ <b>DAC12_A.SUBMODULE_1</b></li> </ul>

Modified bits are **DAC12IFG** of **DAC12\_xCTL0** register.

**Returns**

None

**DAC12\_A\_disable()**

```
void DAC12_A_disable (
    uint16_t baseAddress,
    uint8_t submoduleSelect )
```

Clears the amplifier settings to disable the DAC12\_A module.

This function clears the amplifier settings for the selected DAC12\_A module to disable the DAC12\_A module.

**Parameters**

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A.SUBMODULE_0</b></li> <li>■ <b>DAC12_A.SUBMODULE_1</b></li> </ul>

Modified bits are **DAC12AMP\_7** of **DAC12\_xCTL0** register.

**Returns**

None

**DAC12\_A\_disableConversions()**

```
void DAC12_A_disableConversions (
    uint16_t baseAddress,
    uint8_t submoduleSelect )
```

Disables triggers to start conversions.

This function is used to disallow triggers to start a conversion. Note that this function does not

have any affect if `DAC12_A_TRIGGER_ENCBYPASS` was set for the conversionTriggerSelect parameter during initialization.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A_SUBMODULE_0</b></li> <li>■ <b>DAC12_A_SUBMODULE_1</b></li> </ul>

Modified bits are **DAC12ENC** of **DAC12\_xCTL0** register.

#### Returns

None

### DAC12\_A\_disableGrouping()

```
void DAC12_A_disableGrouping (
    uint16_t baseAddress )
```

Disables grouping of two DAC12\_A modules in a dual DAC12\_A system.

This function disables grouping of two DAC12\_A modules in a dual DAC12\_A system.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
--------------------	--

#### Returns

None

### DAC12\_A\_disableInterrupt()

```
void DAC12_A_disableInterrupt (
    uint16_t baseAddress,
    uint8_t submoduleSelect )
```

Disables the DAC12\_A module interrupt source.

Enables the DAC12\_A module interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
--------------------	--

**Parameters**

<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A.SUBMODULE_0</b></li> <li>■ <b>DAC12_A.SUBMODULE_1</b></li> </ul>
------------------------	---

**Returns**

None

**DAC12\_A\_enableConversions()**

```
void DAC12_A_enableConversions (
    uint16_t baseAddress,
    uint8_t submoduleSelect )
```

Enables triggers to start conversions.

This function is used to allow triggers to start a conversion. Note that this function does not need to be used if DAC12\_A.TRIGGER.ENCBYPASS was set for the conversionTriggerSelect parameter during initialization. If DAC grouping is enabled, this has to be called for both DAC's.

**Parameters**

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A.SUBMODULE_0</b></li> <li>■ <b>DAC12_A.SUBMODULE_1</b></li> </ul>

Modified bits are **DAC12ENC** of **DAC12\_xCTL0** register.

**Returns**

None

**DAC12\_A\_enableGrouping()**

```
void DAC12_A_enableGrouping (
    uint16_t baseAddress )
```

Enables grouping of two DAC12\_A modules in a dual DAC12\_A system.

This function enables grouping two DAC12\_A modules in a dual DAC12\_A system. Both DAC12\_A modules will work in sync, converting data at the same time. To convert data, the same trigger should be set for both DAC12\_A modules during initialization (which should not be

DAC12\_A\_TRIGGER\_ENCBYPASS), the `enableConversions()` function needs to be called with both DAC12\_A modules, and data needs to be set for both DAC12\_A modules separately.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
--------------------	--

Modified bits are **DAC12GRP** of **DAC12\_xCTL0** register.

#### Returns

None

### DAC12\_A\_enableInterrupt()

```
void DAC12_A_enableInterrupt (
    uint16_t baseAddress,
    uint8_t submoduleSelect )
```

Enables the DAC12\_A module interrupt source.

This function to enable the DAC12\_A module interrupt, which throws an interrupt when the data buffer is available for new data to be set. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Note that an interrupt is not thrown when DAC12\_A\_TRIGGER\_ENCBYPASS has been set for the parameter `conversionTriggerSelect` in initialization. Does not clear interrupt flags.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A.SUBMODULE_0</b></li> <li>■ <b>DAC12_A.SUBMODULE_1</b></li> </ul>

#### Returns

None

### DAC12\_A\_getCalibrationData()

```
uint16_t DAC12_A_getCalibrationData (
    uint16_t baseAddress,
    uint8_t submoduleSelect )
```

Returns the calibrated offset of the output buffer.

This function returns the calibrated offset of the output buffer. The output buffer offset is used to obtain accurate results from the output pin. This function should only be used while the calibration

lock is enabled. Only the lower byte of the word of the register is returned, and the value is between -128 and +127.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A.SUBMODULE_0</b></li> <li>■ <b>DAC12_A.SUBMODULE_1</b></li> </ul>

#### Returns

The calibrated offset of the output buffer.

### DAC12\_A\_getDataBufferMemoryAddressForDMA()

```
uint32_t DAC12_A_getDataBufferMemoryAddressForDMA (
    uint16_t baseAddress,
    uint8_t submoduleSelect )
```

Returns the address of the specified DAC12\_A data buffer for the DMA module.

Returns the address of the specified memory buffer. This can be used in conjunction with the DMA to obtain the data directly from memory.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A.SUBMODULE_0</b></li> <li>■ <b>DAC12_A.SUBMODULE_1</b></li> </ul>

#### Returns

The address of the specified memory buffer

### DAC12\_A\_getInterruptStatus()

```
uint16_t DAC12_A_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t submoduleSelect )
```

Returns the status of the DAC12\_A module interrupt flag.

This function returns the status of the DAC12\_A module interrupt flag. Note that an interrupt is not thrown when DAC12\_A.TRIGGER\_ENCBYPASS has been set for the conversionTriggerSelect



parameter in initialization.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A_SUBMODULE_0</b></li> <li>■ <b>DAC12_A_SUBMODULE_1</b></li> </ul>

#### Returns

One of the following:

- **DAC12\_A\_INT\_ACTIVE**
  - **DAC12\_A\_INT\_INACTIVE**
- indicating the status for the selected DAC12\_A module

### DAC12\_A\_init()

```
bool DAC12_A_init (
    uint16_t baseAddress,
    DAC12_A_initParam * param )
```

Initializes the DAC12\_A module with the specified settings.

This function initializes the DAC12\_A module with the specified settings. Upon successful completion of the initialization of this module the control registers and interrupts of this module are all reset, and the specified variables will be set. Please note, that if conversions are enabled with the `enableConversions()` function, then `disableConversions()` must be called before re-initializing the DAC12\_A module with this function.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>param</i>	is the pointer to struct for initialization.

#### Returns

STATUS\_SUCCESS or STATUS\_FAILURE of the initialization process.

References `DAC12_A_initParam::amplifierSetting`, `DAC12_A_initParam::conversionTriggerSelect`, `DAC12_A_initParam::outputSelect`, `DAC12_A_initParam::outputVoltageMultiplier`, `DAC12_A_initParam::positiveReferenceVoltage`, and `DAC12_A_initParam::submoduleSelect`.

### DAC12\_A\_setAmplifierSetting()

```
void DAC12_A_setAmplifierSetting (
    uint16_t baseAddress,
```

```
uint8_t submoduleSelect,
uint8_t amplifierSetting )
```

Sets the amplifier settings for the Vref+ and Vout buffers.

This function sets the amplifier settings of the DAC12\_A module for the Vref+ and Vout buffers without re-initializing the DAC12\_A module. This can be used to disable the control of the pin by the DAC12\_A module.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A.SUBMODULE_0</b></li> <li>■ <b>DAC12_A.SUBMODULE_1</b></li> </ul>
<i>amplifierSetting</i>	is the setting of the settling speed and current of the Vref+ and the Vout buffer. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A.AMP_OFF_PINOUTHIGHZ</b> [Default] - Initialize the DAC12_A Module with settings, but do not turn it on.</li> <li>■ <b>DAC12_A.AMP_OFF_PINOUTLOW</b> - Initialize the DAC12_A Module with settings, and allow it to take control of the selected output pin to pull it low (Note: this takes control away port mapping module).</li> <li>■ <b>DAC12_A.AMP_LOWIN_LOWOUT</b> - Select a slow settling speed and current for Vref+ input buffer and for Vout output buffer.</li> <li>■ <b>DAC12_A.AMP_LOWIN_MEDOUT</b> - Select a slow settling speed and current for Vref+ input buffer and a medium settling speed and current for Vout output buffer.</li> <li>■ <b>DAC12_A.AMP_LOWIN_HIGHOUT</b> - Select a slow settling speed and current for Vref+ input buffer and a high settling speed and current for Vout output buffer.</li> <li>■ <b>DAC12_A.AMP_MEDIN_MEDOUT</b> - Select a medium settling speed and current for Vref+ input buffer and for Vout output buffer.</li> <li>■ <b>DAC12_A.AMP_MEDIN_HIGHOUT</b> - Select a medium settling speed and current for Vref+ input buffer and a high settling speed and current for Vout output buffer.</li> <li>■ <b>DAC12_A.AMP_HIGHIN_HIGHOUT</b> - Select a high settling speed and current for Vref+ input buffer and for Vout output buffer.</li> </ul>

#### Returns

None

#### DAC12\_A\_setCalibrationOffset()

```
void DAC12_A_setCalibrationOffset (
uint16_t baseAddress,
uint8_t submoduleSelect,
```

```
uint16_t calibrationOffsetValue )
```

Returns the calibrated offset of the output buffer.

This function is used to manually set the calibration offset value. The calibration is automatically unlocked and re-locked to be able to allow for the offset value to be set.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A_SUBMODULE_0</b></li> <li>■ <b>DAC12_A_SUBMODULE_1</b></li> </ul>
<i>calibrationOffsetValue</i>	calibration offset value

Modified bits are **DAC12LOCK** of **DAC12\_xCALDAT** register; bits **DAC12PW** of **DAC12\_xCTL0** register; bits **DAC12PW** of **DAC12\_xCALCTL** register.

#### Returns

None

### DAC12\_A\_setData()

```
void DAC12_A_setData (
    uint16_t baseAddress,
    uint8_t submoduleSelect,
    uint16_t data )
```

Sets the given data into the buffer to be converted.

This function is used to set the given data into the data buffer of the DAC12\_A module. The data given should be in the format set (12-bit Unsigned, Right-justified by default). Note if **DAC12\_A\_TRIGGER\_ENCBYPASS** was set for the conversionTriggerSelect during initialization then using this function will set the data and automatically trigger a conversion. If any other trigger was set during initialization, then the [DAC12\\_A\\_enableConversions\(\)](#) function needs to be called before a conversion can be started. If grouping DAC's and **DAC12\_A\_TRIGGER\_ENC** was set during initialization, then both data buffers must be set before a conversion will be started.

#### Parameters

<i>baseAddress</i>	is the base address of the DAC12_A module.
<i>submoduleSelect</i>	decides which DAC12_A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12_A_SUBMODULE_0</b></li> <li>■ <b>DAC12_A_SUBMODULE_1</b></li> </ul>
<i>data</i>	is the data to be set into the DAC12_A data buffer to be converted. Modified bits are <b>DAC12_DATA</b> of <b>DAC12_xDAT</b> register.

Modified bits of **DAC12\_xDAT** register.

**Returns**

None

### DAC12\_A\_setInputDataFormat()

```
void DAC12_A_setInputDataFormat (
    uint16_t baseAddress,
    uint8_t submoduleSelect,
    uint8_t inputJustification,
    uint8_t inputSign )
```

Sets the input data format for the DAC12.A module.

This function sets the input format for the binary data to be converted.

**Parameters**

<i>baseAddress</i>	is the base address of the DAC12.A module.
<i>submoduleSelect</i>	decides which DAC12.A sub-module to configure. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12.A.SUBMODULE_0</b></li> <li>■ <b>DAC12.A.SUBMODULE_1</b></li> </ul>
<i>inputJustification</i>	is the justification of the data to be converted. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12.A.JUSTIFICATION_RIGHT</b> [Default]</li> <li>■ <b>DAC12.A.JUSTIFICATION_LEFT</b></li> </ul> Modified bits are <b>DAC12DFJ</b> of <b>DAC12_xCTL1</b> register.
<i>inputSign</i>	is the sign of the data to be converted. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DAC12.A.UNSIGNED_BINARY</b> [Default]</li> <li>■ <b>DAC12.A.SIGNED_2SCOMPLEMENT</b></li> </ul> Modified bits are <b>DAC12DF</b> of <b>DAC12_xCTL0</b> register.

**Returns**

None

### DAC12\_A\_setResolution()

```
void DAC12_A_setResolution (
    uint16_t baseAddress,
    uint8_t submoduleSelect,
    uint16_t resolutionSelect )
```

Sets the resolution to be used by the DAC12.A module.

This function sets the resolution of the data to be converted.



# 15 Direct Memory Access (DMA)

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## 15.1 Introduction

The Direct Memory Access (DMA) API provides a set of functions for using the MSP430Ware DMA modules. Functions are provided to initialize and setup each DMA channel with the source and destination addresses, manage the interrupts for each channel, and set bits that affect all DMA channels.

The DMA module provides the ability to move data from one address in the device to another, and that includes other peripheral addresses to RAM or vice-versa, all without the actual use of the CPU. Please be advised, that the DMA module does halt the CPU for 2 cycles while transferring, but does not have to edit any registers or anything. The DMA can transfer by bytes or words at a time, and will automatically increment or decrement the source or destination address if desired. There are also 6 different modes to transfer by, including single-transfer, block-transfer, and burst-block-transfer, as well as repeated versions of those three different kinds which allows transfers to be repeated without having re-enable transfers.

The DMA settings that affect all DMA channels include prioritization, from a fixed priority to dynamic round-robin priority. Another setting that can be changed is when transfers occur, the CPU may be in a read-modify-write operation which can be disastrous to time sensitive material, so this can be disabled. And Non-Maskable-Interrupts can indeed be maskable to the DMA module if not enabled.

The DMA module can generate one interrupt per channel. The interrupt is only asserted when the specified amount of transfers has been completed. With single-transfer, this occurs when that many single transfers have occurred, while with block or burst-block transfers, once the block is completely transferred the interrupt is asserted.

## 15.2 API Functions

### Functions

- void `DMA_init` (`DMA_initParam *param`)  
*Initializes the specified DMA channel.*
- void `DMA_setTransferSize` (`uint8_t channelSelect`, `uint16_t transferSize`)  
*Sets the specified amount of transfers for the selected DMA channel.*
- `uint16_t DMA_getTransferSize` (`uint8_t channelSelect`)  
*Gets the amount of transfers for the selected DMA channel.*
- void `DMA_setSrcAddress` (`uint8_t channelSelect`, `uint32_t srcAddress`, `uint16_t directionSelect`)  
*Sets source address and the direction that the source address will move after a transfer.*
- void `DMA_setDstAddress` (`uint8_t channelSelect`, `uint32_t dstAddress`, `uint16_t directionSelect`)

- Sets the destination address and the direction that the destination address will move after a transfer.*

  - void [DMA\\_enableTransfers](#) (uint8\_t channelSelect)  
*Enables transfers to be triggered.*
  - void [DMA\\_disableTransfers](#) (uint8\_t channelSelect)  
*Disables transfers from being triggered.*
  - void [DMA\\_startTransfer](#) (uint8\_t channelSelect)  
*Starts a transfer if using the default trigger source selected in initialization.*
  - void [DMA\\_enableInterrupt](#) (uint8\_t channelSelect)  
*Enables the DMA interrupt for the selected channel.*
  - void [DMA\\_disableInterrupt](#) (uint8\_t channelSelect)  
*Disables the DMA interrupt for the selected channel.*
  - uint16\_t [DMA\\_getInterruptStatus](#) (uint8\_t channelSelect)  
*Returns the status of the interrupt flag for the selected channel.*
  - void [DMA\\_clearInterrupt](#) (uint8\_t channelSelect)  
*Clears the interrupt flag for the selected channel.*
  - uint16\_t [DMA\\_getNMIAbortStatus](#) (uint8\_t channelSelect)  
*Returns the status of the NMIAbort for the selected channel.*
  - void [DMA\\_clearNMIAbort](#) (uint8\_t channelSelect)  
*Clears the status of the NMIAbort to proceed with transfers for the selected channel.*
  - void [DMA\\_disableTransferDuringReadModifyWrite](#) (void)  
*Disables the DMA from stopping the CPU during a Read-Modify-Write Operation to start a transfer.*
  - void [DMA\\_enableTransferDuringReadModifyWrite](#) (void)  
*Enables the DMA to stop the CPU during a Read-Modify-Write Operation to start a transfer.*
  - void [DMA\\_enableRoundRobinPriority](#) (void)  
*Enables Round Robin prioritization.*
  - void [DMA\\_disableRoundRobinPriority](#) (void)  
*Disables Round Robin prioritization.*
  - void [DMA\\_enableNMIAbort](#) (void)  
*Enables a NMI to interrupt a DMA transfer.*
  - void [DMA\\_disableNMIAbort](#) (void)  
*Disables any NMI from interrupting a DMA transfer.*

## 15.2.1 Detailed Description

The DMA API is broken into three groups of functions: those that deal with initialization and transfers, those that handle interrupts, and those that affect all DMA channels.

The DMA initialization and transfer functions are: [DMA\\_init\(\)](#) [DMA\\_setSrcAddress\(\)](#) [DMA\\_setDstAddress\(\)](#) [DMA\\_enableTransfers\(\)](#) [DMA\\_disableTransfers\(\)](#) [DMA\\_startTransfer\(\)](#) [DMA\\_setTransferSize\(\)](#) [DMA\\_getTransferSize\(\)](#)

The DMA interrupts are handled by: [DMA\\_enableInterrupt\(\)](#) [DMA\\_disableInterrupt\(\)](#) [DMA\\_getInterruptStatus\(\)](#) [DMA\\_clearInterrupt\(\)](#) [DMA\\_getNMIAbortStatus\(\)](#) [DMA\\_clearNMIAbort\(\)](#)

Features of the DMA that affect all channels are handled by: [DMA\\_disableTransferDuringReadModifyWrite\(\)](#) [DMA\\_enableTransferDuringReadModifyWrite\(\)](#) [DMA\\_enableRoundRobinPriority\(\)](#) [DMA\\_disableRoundRobinPriority\(\)](#) [DMA\\_enableNMIAbort\(\)](#) [DMA\\_disableNMIAbort\(\)](#)

## 15.2.2 Function Documentation

### DMA\_clearInterrupt()

```
void DMA_clearInterrupt (
    uint8_t channelSelect )
```

Clears the interrupt flag for the selected channel.

This function clears the DMA interrupt flag is cleared, so that it no longer asserts.

#### Parameters

<i>channelSelect</i>	is the specified channel to clear the interrupt flag for. Valid values are: <ul style="list-style-type: none"><li>■ <b>DMA_CHANNEL_0</b></li><li>■ <b>DMA_CHANNEL_1</b></li><li>■ <b>DMA_CHANNEL_2</b></li><li>■ <b>DMA_CHANNEL_3</b></li><li>■ <b>DMA_CHANNEL_4</b></li><li>■ <b>DMA_CHANNEL_5</b></li><li>■ <b>DMA_CHANNEL_6</b></li><li>■ <b>DMA_CHANNEL_7</b></li></ul>
----------------------	---

#### Returns

None

### DMA\_clearNMIAbort()

```
void DMA_clearNMIAbort (
    uint8_t channelSelect )
```

Clears the status of the NMIAbort to proceed with transfers for the selected channel.

This function clears the status of the NMI Abort flag for the selected channel to allow for transfers on the channel to continue.



**Parameters**

<i>channelSelect</i>	<p>is the specified channel to clear the NMI Abort flag for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	---

**Returns**

None

**DMA\_disableInterrupt()**

```
void DMA_disableInterrupt (
    uint8_t channelSelect )
```

Disables the DMA interrupt for the selected channel.

Disables the DMA interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>channelSelect</i>	<p>is the specified channel to disable the interrupt for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	--

**Returns**

None

**DMA\_disableNMIAbort()**

```
void DMA_disableNMIAbort (
    void )
```

Disables any NMI from interrupting a DMA transfer.

This function disables NMI's from interrupting any DMA transfer currently in progress.

**Returns**

None

**DMA\_disableRoundRobinPriority()**

```
void DMA_disableRoundRobinPriority (
    void )
```

Disables Round Robin prioritization.

This function disables Round Robin Prioritization, enabling static prioritization of the DMA channels. In static prioritization, the DMA channels are prioritized with the lowest DMA channel index having the highest priority (i.e. DMA Channel 0 has the highest priority).

**Returns**

None

**DMA\_disableTransferDuringReadModifyWrite()**

```
void DMA_disableTransferDuringReadModifyWrite (
    void )
```

Disables the DMA from stopping the CPU during a Read-Modify-Write Operation to start a transfer.

This function allows the CPU to finish any read-modify-write operations it may be in the middle of before transfers of and DMA channel stop the CPU.

**Returns**

None

**DMA\_disableTransfers()**

```
void DMA_disableTransfers (
    uint8_t channelSelect )
```

Disables transfers from being triggered.

This function disables transfer from being triggered for the selected channel. This function should be called before any re-initialization of the selected DMA channel.

#### Parameters

<i>channelSelect</i>	<p>is the specified channel to disable transfers for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	--

#### Returns

None

#### DMA\_enableInterrupt()

```
void DMA_enableInterrupt (
    uint8_t channelSelect )
```

Enables the DMA interrupt for the selected channel.

Enables the DMA interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

#### Parameters

<i>channelSelect</i>	<p>is the specified channel to enable the interrupt for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	---

**Returns**

None

**DMA\_enableNMIAbort()**

```
void DMA_enableNMIAbort (
    void )
```

Enables a NMI to interrupt a DMA transfer.

This function allow NMI's to interrupting any DMA transfer currently in progress and stops any future transfers to begin before the NMI is done processing.

**Returns**

None

**DMA\_enableRoundRobinPriority()**

```
void DMA_enableRoundRobinPriority (
    void )
```

Enables Round Robin prioritization.

This function enables Round Robin Prioritization of DMA channels. In the case of Round Robin Prioritization, the last DMA channel to have transferred data then has the last priority, which comes into play when multiple DMA channels are ready to transfer at the same time.

**Returns**

None

**DMA\_enableTransferDuringReadModifyWrite()**

```
void DMA_enableTransferDuringReadModifyWrite (
    void )
```

Enables the DMA to stop the CPU during a Read-Modify-Write Operation to start a transfer.

This function allows the DMA to stop the CPU in the middle of a read- modify-write operation to transfer data.

**Returns**

None

**DMA\_enableTransfers()**

```
void DMA_enableTransfers (
    uint8_t channelSelect )
```

Enables transfers to be triggered.

This function enables transfers upon appropriate trigger of the selected trigger source for the selected channel.

#### Parameters

<i>channelSelect</i>	<p>is the specified channel to enable transfer for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	--

#### Returns

None

### DMA\_getInterruptStatus()

```
uint16_t DMA_getInterruptStatus (
    uint8_t channelSelect )
```

Returns the status of the interrupt flag for the selected channel.

Returns the status of the interrupt flag for the selected channel.

#### Parameters

<i>channelSelect</i>	<p>is the specified channel to return the interrupt flag status from. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	--

**Returns**

One of the following:

- **DMA\_INT\_INACTIVE**
  - **DMA\_INT\_ACTIVE**
- indicating the status of the current interrupt flag

**DMA\_getNMIAbortStatus()**

```
uint16_t DMA_getNMIAbortStatus (
    uint8_t channelSelect )
```

Returns the status of the NMIAbort for the selected channel.

This function returns the status of the NMI Abort flag for the selected channel. If this flag has been set, it is because a transfer on this channel was aborted due to a interrupt from an NMI.

**Parameters**

<i>channelSelect</i>	<p>is the specified channel to return the status of the NMI Abort flag for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	--

**Returns**

One of the following:

- **DMA\_NOTABORTED**
  - **DMA\_ABORTED**
- indicating the status of the NMIAbort for the selected channel

**DMA\_getTransferSize()**

```
uint16_t DMA_getTransferSize (
    uint8_t channelSelect )
```

Gets the amount of transfers for the selected DMA channel.

This function gets the amount of transfers for the selected DMA channel without having to reinitialize the DMA channel.

**Parameters**

<i>channelSelect</i>	<p>is the specified channel to set source address direction for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	---

**Returns**

the amount of transfers

**DMA\_init()**

```
void DMA_init (
    DMA_initParam * param )
```

Initializes the specified DMA channel.

This function initializes the specified DMA channel. Upon successful completion of initialization of the selected channel the control registers will be cleared and the given variables will be set. Please note, if transfers have been enabled with the `enableTransfers()` function, then a call to `disableTransfers()` is necessary before re-initialization. Also note, that the trigger sources are device dependent and can be found in the device family data sheet. The amount of DMA channels available are also device specific.

**Parameters**

<i>param</i>	is the pointer to struct for initialization.
--------------	--

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the initialization process.

References `DMA_initParam::channelSelect`, `DMA_initParam::transferModeSelect`, `DMA_initParam::transferSize`, `DMA_initParam::transferUnitSelect`, `DMA_initParam::triggerSourceSelect`, and `DMA_initParam::triggerTypeSelect`.

**DMA\_setDstAddress()**

```
void DMA_setDstAddress (
    uint8_t channelSelect,
    uint32_t dstAddress,
```

```
uint16_t directionSelect )
```

Sets the destination address and the direction that the destination address will move after a transfer.

This function sets the destination address and the direction that the destination address will move after a transfer is complete. It may be incremented, decremented, or unchanged.

#### Parameters

<i>channelSelect</i>	is the specified channel to set the destination address direction for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
<i>dstAddress</i>	is the address of where the data will be transferred to. Modified bits are <b>DMAxDA</b> of <b>DMAxDA</b> register.
<i>directionSelect</i>	is the specified direction of the destination address after a transfer. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_DIRECTION_UNCHANGED</b></li> <li>■ <b>DMA_DIRECTION_DECREMENT</b></li> <li>■ <b>DMA_DIRECTION_INCREMENT</b></li> </ul> Modified bits are <b>DMADSTINCR</b> of <b>DMAxCTL</b> register.

#### Returns

None

#### DMA\_setSrcAddress()

```
void DMA_setSrcAddress (
    uint8_t channelSelect,
    uint32_t srcAddress,
    uint16_t directionSelect )
```

Sets source address and the direction that the source address will move after a transfer.

This function sets the source address and the direction that the source address will move after a transfer is complete. It may be incremented, decremented or unchanged.



## Parameters

<i>channelSelect</i>	is the specified channel to set source address direction for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
<i>srcAddress</i>	is the address of where the data will be transferred from. Modified bits are <b>DMAxSA</b> of <b>DMAxSA</b> register.
<i>directionSelect</i>	is the specified direction of the source address after a transfer. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_DIRECTION_UNCHANGED</b></li> <li>■ <b>DMA_DIRECTION_DECREMENT</b></li> <li>■ <b>DMA_DIRECTION_INCREMENT</b></li> </ul> Modified bits are <b>DMASRCINCR</b> of <b>DMAxCTL</b> register.

## Returns

None

## DMA\_setTransferSize()

```
void DMA_setTransferSize (
    uint8_t channelSelect,
    uint16_t transferSize )
```

Sets the specified amount of transfers for the selected DMA channel.

This function sets the specified amount of transfers for the selected DMA channel without having to reinitialize the DMA channel.

## Parameters

<i>channelSelect</i>	is the specified channel to set source address direction for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
<i>transferSize</i>	is the amount of transfers to complete in a block transfer mode, as well as how many transfers to complete before the interrupt flag is set. Valid value is between 1-65535, if 0, no transfers will occur. Modified bits are <b>DMAxSZ</b> of <b>DMAxSZ</b> register.

## Returns

None

## DMA\_startTransfer()

```
void DMA_startTransfer (
    uint8_t channelSelect )
```

Starts a transfer if using the default trigger source selected in initialization.

This functions triggers a transfer of data from source to destination if the trigger source chosen from initialization is the DMA\_TRIGGERSOURCE\_0. Please note, this function needs to be called for each (repeated-)single transfer, and when transferAmount of transfers have been complete in (repeated-)block transfers.

## Parameters

<i>channelSelect</i>	is the specified channel to start transfers for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	---

**Returns**

None

## 15.3 Programming Example

The following example shows how to initialize and use the DMA API to transfer words from one spot in RAM to another.

```
// Initialize and Setup DMA Channel 0
/*
 * Base Address of the DMA Module
 * Configure DMA channel 0
 * Configure channel for repeated block transfers
 * DMA interrupt flag will be set after every 16 transfers
 * Use DMA_startTransfer() function to trigger transfers
 * Transfer Word-to-Word
 * Trigger upon Rising Edge of Trigger Source Signal
 */
DMA_initParam param = {0};
param.channelSelect = DMA_CHANNEL_0;
param.transferModeSelect = DMA_TRANSFER_REPEATED_BLOCK;
param.transferSize = 16;
param.triggerSourceSelect = DMA_TRIGGERSOURCE_0;
param.transferUnitSelect = DMA_SIZE_SRCWORD_DSTWORD;
param.triggerTypeSelect = DMA_TRIGGER_RISINGEDGE;
DMA_init(&param);
/*
 * Base Address of the DMA Module
 * Configure DMA channel 0
 * Use 0x1C00 as source
 * Increment source address after every transfer
 */
DMA_setSrcAddress(DMA_CHANNEL_0,
                 0x1C00,
                 DMA_DIRECTION_INCREMENT);
/*
 * Base Address of the DMA Module
 * Configure DMA channel 0
 * Use 0x1C20 as destination
 * Increment destination address after every transfer
 */
DMA_setDstAddress(DMA_CHANNEL_0,
                 0x1C20,
                 DMA_DIRECTION_INCREMENT);

// Enable transfers on DMA channel 0
DMA_enableTransfers(DMA_CHANNEL_0);

while(1)
{
    // Start block transfer on DMA channel 0
    DMA_startTransfer(DMA_CHANNEL_0);
}
```

# 16 EUSCI Universal Asynchronous Receiver/Transmitter (EUSCI\_A\_UART)

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## 16.1 Introduction

The MSP430Ware library for UART mode features include:

- Odd, even, or non-parity
- Independent transmit and receive shift registers
- Separate transmit and receive buffer registers
- LSB-first or MSB-first data transmit and receive
- Built-in idle-line and address-bit communication protocols for multiprocessor systems
- Receiver start-edge detection for auto wake up from LPMx modes
- Status flags for error detection and suppression
- Status flags for address detection
- Independent interrupt capability for receive and transmit

In UART mode, the USCI transmits and receives characters at a bit rate asynchronous to another device. Timing for each character is based on the selected baud rate of the USCI. The transmit and receive functions use the same baud-rate frequency.

## 16.2 API Functions

### Functions

- bool [EUSCI\\_A\\_UART\\_init](#) (uint16\_t baseAddress, [EUSCI\\_A\\_UART\\_initParam](#) \*param)  
*Advanced initialization routine for the UART block. The values to be written into the clockPrescaler, firstModReg, secondModReg and overSampling parameters should be pre-computed and passed into the initialization function.*
- void [EUSCI\\_A\\_UART\\_transmitData](#) (uint16\_t baseAddress, uint8\_t transmitData)  
*Transmits a byte from the UART Module. Please note that if TX interrupt is disabled, this function manually polls the TX IFG flag waiting for an indication that it is safe to write to the transmit buffer and does not time-out.*
- uint8\_t [EUSCI\\_A\\_UART\\_receiveData](#) (uint16\_t baseAddress)  
*Receives a byte that has been sent to the UART Module.*
- void [EUSCI\\_A\\_UART\\_enableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Enables individual UART interrupt sources.*
- void [EUSCI\\_A\\_UART\\_disableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Disables individual UART interrupt sources.*
- uint8\_t [EUSCI\\_A\\_UART\\_getInterruptStatus](#) (uint16\_t baseAddress, uint8\_t mask)

- Gets the current UART interrupt status.*
- void `EUSCI_A_UART_clearInterrupt` (uint16\_t baseAddress, uint8\_t mask)
  - Clears UART interrupt sources.*
- void `EUSCI_A_UART_enable` (uint16\_t baseAddress)
  - Enables the UART block.*
- void `EUSCI_A_UART_disable` (uint16\_t baseAddress)
  - Disables the UART block.*
- uint8\_t `EUSCI_A_UART_queryStatusFlags` (uint16\_t baseAddress, uint8\_t mask)
  - Gets the current UART status flags.*
- void `EUSCI_A_UART_setDormant` (uint16\_t baseAddress)
  - Sets the UART module in dormant mode.*
- void `EUSCI_A_UART_resetDormant` (uint16\_t baseAddress)
  - Re-enables UART module from dormant mode.*
- void `EUSCI_A_UART_transmitAddress` (uint16\_t baseAddress, uint8\_t transmitAddress)
  - Transmits the next byte to be transmitted marked as address depending on selected multiprocessor mode.*
- void `EUSCI_A_UART_transmitBreak` (uint16\_t baseAddress)
  - Transmit break.*
- uint32\_t `EUSCI_A_UART_getReceiveBufferAddress` (uint16\_t baseAddress)
  - Returns the address of the RX Buffer of the UART for the DMA module.*
- uint32\_t `EUSCI_A_UART_getTransmitBufferAddress` (uint16\_t baseAddress)
  - Returns the address of the TX Buffer of the UART for the DMA module.*
- void `EUSCI_A_UART_selectDeglitchTime` (uint16\_t baseAddress, uint16\_t deglitchTime)
  - Sets the deglitch time.*

## 16.2.1 Detailed Description

The EUSCI\_A\_UART API provides the set of functions required to implement an interrupt driven EUSCI\_A\_UART driver. The EUSCI\_A\_UART initialization with the various modes and features is done by the `EUSCI_A_UART_init()`. At the end of this function EUSCI\_A\_UART is initialized and stays disabled. `EUSCI_A_UART_enable()` enables the EUSCI\_A\_UART and the module is now ready for transmit and receive. It is recommended to initialize the EUSCI\_A\_UART via `EUSCI_A_UART_init()`, enable the required interrupts and then enable EUSCI\_A\_UART via `EUSCI_A_UART_enable()`.

The EUSCI\_A\_UART API is broken into three groups of functions: those that deal with configuration and control of the EUSCI\_A\_UART modules, those used to send and receive data, and those that deal with interrupt handling and those dealing with DMA.

Configuration and control of the EUSCI\_UART are handled by the

- `EUSCI_A_UART_init()`
- `EUSCI_A_UART_initAdvance()`
- `EUSCI_A_UART_enable()`
- `EUSCI_A_UART_disable()`
- `EUSCI_A_UART_setDormant()`
- `EUSCI_A_UART_resetDormant()`
- `EUSCI_A_UART_selectDeglitchTime()`

Sending and receiving data via the EUSCI\_UART is handled by the

- `EUSCI_A_UART_transmitData()`

- EUSCI\_A\_UART\_receiveData()
- EUSCI\_A\_UART\_transmitAddress()
- EUSCI\_A\_UART\_transmitBreak()
- EUSCI\_A\_UART\_getTransmitBufferAddress()
- EUSCI\_A\_UART\_getTransmitBufferAddress()

Managing the EUSCI\_UART interrupts and status are handled by the

- EUSCI\_A\_UART\_enableInterrupt()
- EUSCI\_A\_UART\_disableInterrupt()
- EUSCI\_A\_UART\_getInterruptStatus()
- EUSCI\_A\_UART\_clearInterrupt()
- EUSCI\_A\_UART\_queryStatusFlags()

## 16.2.2 Function Documentation

### EUSCI\_A\_UART\_clearInterrupt()

```
void EUSCI_A_UART_clearInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Clears UART interrupt sources.

The UART interrupt source is cleared, so that it no longer asserts. The highest interrupt flag is automatically cleared when an interrupt vector generator is used.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>mask</i>	is a bit mask of the interrupt sources to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ EUSCI_A_UART_RECEIVE_INTERRUPT_FLAG</li> <li>■ EUSCI_A_UART_TRANSMIT_INTERRUPT_FLAG</li> <li>■ EUSCI_A_UART_STARTBIT_INTERRUPT_FLAG</li> <li>■ EUSCI_A_UART_TRANSMIT_COMPLETE_INTERRUPT_FLAG</li> </ul>

Modified bits of **UCAxIFG** register.

#### Returns

None

### EUSCI\_A\_UART\_disable()

```
void EUSCI_A_UART_disable (
```

```
uint16_t baseAddress )
```

Disables the UART block.

This will disable operation of the UART block.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

Modified bits are **UCSWRST** of **UCAxCTL1** register.

**Returns**

None

**EUSCI\_A\_UART\_disableInterrupt()**

```
void EUSCI_A_UART_disableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Disables individual UART interrupt sources.

Disables the indicated UART interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>mask</i>	is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_UART_RECEIVE_INTERRUPT</b> - Receive interrupt</li> <li>■ <b>EUSCI_A_UART_TRANSMIT_INTERRUPT</b> - Transmit interrupt</li> <li>■ <b>EUSCI_A_UART_RECEIVE_ERRONEOUSCHAR_INTERRUPT</b> - Receive erroneous-character interrupt enable</li> <li>■ <b>EUSCI_A_UART_BREAKCHAR_INTERRUPT</b> - Receive break character interrupt enable</li> <li>■ <b>EUSCI_A_UART_STARTBIT_INTERRUPT</b> - Start bit received interrupt enable</li> <li>■ <b>EUSCI_A_UART_TRANSMIT_COMPLETE_INTERRUPT</b> - Transmit complete interrupt enable</li> </ul>

Modified bits of **UCAxCTL1** register and bits of **UCAxIE** register.

**Returns**

None

**EUSCI\_A\_UART\_enable()**

```
void EUSCI_A_UART_enable (
    uint16_t baseAddress )
```

Enables the UART block.

This will enable operation of the UART block.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

Modified bits are **UCSWRST** of **UCAxCTL1** register.

**Returns**

None

**EUSCI\_A\_UART\_enableInterrupt()**

```
void EUSCI_A_UART_enableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Enables individual UART interrupt sources.

Enables the indicated UART interrupt sources. The interrupt flag is first and then the corresponding interrupt is enabled. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---



**Parameters**

<i>mask</i>	<p>is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_UART_RECEIVE_INTERRUPT</b> - Receive interrupt</li> <li>■ <b>EUSCI_A_UART_TRANSMIT_INTERRUPT</b> - Transmit interrupt</li> <li>■ <b>EUSCI_A_UART_RECEIVE_ERRONEOUSCHAR_INTERRUPT</b> - Receive erroneous-character interrupt enable</li> <li>■ <b>EUSCI_A_UART_BREAKCHAR_INTERRUPT</b> - Receive break character interrupt enable</li> <li>■ <b>EUSCI_A_UART_STARTBIT_INTERRUPT</b> - Start bit received interrupt enable</li> <li>■ <b>EUSCI_A_UART_TRANSMIT_COMPLETE_INTERRUPT</b> - Transmit complete interrupt enable</li> </ul>
-------------	---

Modified bits of **UCAxCTL1** register and bits of **UCAxIE** register.

**Returns**

None

**EUSCI\_A\_UART\_getInterruptStatus()**

```
uint8_t EUSCI_A_UART_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t mask )
```

Gets the current UART interrupt status.

This returns the interrupt status for the UART module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>mask</i>	<p>is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_UART_RECEIVE_INTERRUPT_FLAG</b></li> <li>■ <b>EUSCI_A_UART_TRANSMIT_INTERRUPT_FLAG</b></li> <li>■ <b>EUSCI_A_UART_STARTBIT_INTERRUPT_FLAG</b></li> <li>■ <b>EUSCI_A_UART_TRANSMIT_COMPLETE_INTERRUPT_FLAG</b></li> </ul>

Modified bits of **UCAxIFG** register.

**Returns**

Logical OR of any of the following:

- **EUSCI\_A\_UART\_RECEIVE\_INTERRUPT\_FLAG**

- **EUSCI\_A\_UART\_TRANSMIT\_INTERRUPT\_FLAG**
- **EUSCI\_A\_UART\_STARTBIT\_INTERRUPT\_FLAG**
- **EUSCI\_A\_UART\_TRANSMIT\_COMPLETE\_INTERRUPT\_FLAG**  
indicating the status of the masked flags

## EUSCI\_A\_UART\_getReceiveBufferAddress()

```
uint32_t EUSCI_A_UART_getReceiveBufferAddress (
    uint16_t baseAddress )
```

Returns the address of the RX Buffer of the UART for the DMA module.

Returns the address of the UART RX Buffer. This can be used in conjunction with the DMA to store the received data directly to memory.

### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

### Returns

Address of RX Buffer

## EUSCI\_A\_UART\_getTransmitBufferAddress()

```
uint32_t EUSCI_A_UART_getTransmitBufferAddress (
    uint16_t baseAddress )
```

Returns the address of the TX Buffer of the UART for the DMA module.

Returns the address of the UART TX Buffer. This can be used in conjunction with the DMA to obtain transmitted data directly from memory.

### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

### Returns

Address of TX Buffer

## EUSCI\_A\_UART\_init()

```
bool EUSCI_A_UART_init (
    uint16_t baseAddress,
    EUSCI_A_UART_initParam * param )
```

Advanced initialization routine for the UART block. The values to be written into the clockPrescalar, firstModReg, secondModReg and overSampling parameters should be pre-computed and passed into the initialization function.

Upon successful initialization of the UART block, this function will have initialized the module, but the UART block still remains disabled and must be enabled with [EUSCI\\_A\\_UART\\_enable\(\)](#). To calculate values for clockPrescalar, firstModReg, secondModReg and overSampling please use the link below.

[http://software-dl.ti.com/msp430/msp430\\_public\\_sw/mcu/msp430/MSP430BaudRateConverter/index.html](http://software-dl.ti.com/msp430/msp430_public_sw/mcu/msp430/MSP430BaudRateConverter/index.html)

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>param</i>	is the pointer to struct for initialization.

Modified bits are **UCPEN**, **UCPAR**, **UCMSB**, **UC7BIT**, **UCSPB**, **UCMODEx** and **UCSYNC** of **UCAxCTL0** register; bits **UCSSELx** and **UCSWRST** of **UCAxCTL1** register.

#### Returns

STATUS\_SUCCESS or STATUS\_FAIL of the initialization process

References EUSCI\_A\_UART\_initParam::clockPrescalar, EUSCI\_A\_UART\_initParam::firstModReg, EUSCI\_A\_UART\_initParam::msborLsbFirst, EUSCI\_A\_UART\_initParam::numberOfStopBits, EUSCI\_A\_UART\_initParam::overSampling, EUSCI\_A\_UART\_initParam::parity, EUSCI\_A\_UART\_initParam::secondModReg, EUSCI\_A\_UART\_initParam::selectClockSource, and EUSCI\_A\_UART\_initParam::uartMode.

### EUSCI\_A\_UART\_queryStatusFlags()

```
uint8_t EUSCI_A_UART_queryStatusFlags (
    uint16_t baseAddress,
    uint8_t mask )
```

Gets the current UART status flags.

This returns the status for the UART module based on which flag is passed.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ EUSCI_A_UART_LISTEN_ENABLE</li> <li>■ EUSCI_A_UART_FRAMING_ERROR</li> <li>■ EUSCI_A_UART_OVERRUN_ERROR</li> <li>■ EUSCI_A_UART_PARITY_ERROR</li> <li>■ EUSCI_A_UART_BREAK_DETECT</li> <li>■ EUSCI_A_UART_RECEIVE_ERROR</li> <li>■ EUSCI_A_UART_ADDRESS_RECEIVED</li> <li>■ EUSCI_A_UART_IDLELINE</li> <li>■ EUSCI_A_UART_BUSY</li> </ul>

Modified bits of **UCAxSTAT** register.

#### Returns

Logical OR of any of the following:

- **EUSCI\_A\_UART\_LISTEN\_ENABLE**
- **EUSCI\_A\_UART\_FRAMING\_ERROR**
- **EUSCI\_A\_UART\_OVERRUN\_ERROR**
- **EUSCI\_A\_UART\_PARITY\_ERROR**
- **EUSCI\_A\_UART\_BREAK\_DETECT**
- **EUSCI\_A\_UART\_RECEIVE\_ERROR**
- **EUSCI\_A\_UART\_ADDRESS\_RECEIVED**
- **EUSCI\_A\_UART\_IDLELINE**
- **EUSCI\_A\_UART\_BUSY**

indicating the status of the masked interrupt flags

### EUSCI\_A\_UART\_receiveData()

```
uint8_t EUSCI_A_UART_receiveData (
    uint16_t baseAddress )
```

Receives a byte that has been sent to the UART Module.

This function reads a byte of data from the UART receive data Register.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

Modified bits of **UCAxRXBUF** register.

#### Returns

Returns the byte received from by the UART module, cast as an uint8.t.

### EUSCI\_A\_UART\_resetDormant()

```
void EUSCI_A_UART_resetDormant (
    uint16_t baseAddress )
```

Re-enables UART module from dormant mode.

Not dormant. All received characters set UCRXIFG.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

Modified bits are **UCDORM** of **UCAxCTL1** register.

**Returns**

None

**EUSCI\_A\_UART\_selectDeglitchTime()**

```
void EUSCI_A_UART_selectDeglitchTime (
    uint16_t baseAddress,
    uint16_t deglitchTime )
```

Sets the deglitch time.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>deglitchTime</i>	is the selected deglitch time Valid values are: <ul style="list-style-type: none"> <li>■ EUSCI_A_UART_DEGLITCH_TIME_2ns</li> <li>■ EUSCI_A_UART_DEGLITCH_TIME_50ns</li> <li>■ EUSCI_A_UART_DEGLITCH_TIME_100ns</li> <li>■ EUSCI_A_UART_DEGLITCH_TIME_200ns</li> </ul>

**Returns**

None

**EUSCI\_A\_UART\_setDormant()**

```
void EUSCI_A_UART_setDormant (
    uint16_t baseAddress )
```

Sets the UART module in dormant mode.

Puts USCI in sleep mode Only characters that are preceded by an idle-line or with address bit set UCRXIFG. In UART mode with automatic baud-rate detection, only the combination of a break and sync field sets UCRXIFG.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

Modified bits of **UCAxCTL1** register.

**Returns**

None

**EUSCI\_A\_UART\_transmitAddress()**

```
void EUSCI_A_UART_transmitAddress (
    uint16_t baseAddress,
    uint8_t transmitAddress )
```

Transmits the next byte to be transmitted marked as address depending on selected multiprocessor mode.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>transmitAddress</i>	is the next byte to be transmitted

Modified bits of **UCAxTXBUF** register and bits of **UCAxCTL1** register.

**Returns**

None

**EUSCI\_A\_UART\_transmitBreak()**

```
void EUSCI_A_UART_transmitBreak (
    uint16_t baseAddress )
```

Transmit break.

Transmits a break with the next write to the transmit buffer. In UART mode with automatic baud-rate detection, EUSCI\_A\_UART\_AUTOMATICBAUDRATE\_SYNC(0x55) must be written into UCAxTXBUF to generate the required break/sync fields. Otherwise, DEFAULT\_SYNC(0x00) must be written into the transmit buffer. Also ensures module is ready for transmitting the next data.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

Modified bits of **UCAxTXBUF** register and bits of **UCAxCTL1** register.

**Returns**

None

**EUSCI\_A\_UART\_transmitData()**

```
void EUSCI_A_UART_transmitData (
    uint16_t baseAddress,
```

```
uint8_t transmitData )
```

Transmits a byte from the UART Module. Please note that if TX interrupt is disabled, this function manually polls the TX IFG flag waiting for an indication that it is safe to write to the transmit buffer and does not time-out.

This function will place the supplied data into UART transmit data register to start transmission

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>transmitData</i>	data to be transmitted from the UART module

Modified bits of **UCAxTXBUF** register.

#### Returns

None

## 16.3 Programming Example

The following example shows how to use the EUSCI\_UART API to initialize the EUSCI\_UART, transmit characters, and receive characters.

```
// Configure UART
EUSCI_A_UART_initParam param = {0};
param.selectClockSource = EUSCI_A_UART_CLOCKSOURCE_ACLK;
param.clockPrescalar = 15;
param.firstModReg = 0;
param.secondModReg = 68;
param.parity = EUSCI_A_UART_NO_PARITY;
param.msborLsbFirst = EUSCI_A_UART_LSB_FIRST;
param.numberofStopBits = EUSCI_A_UART_ONE_STOP_BIT;
param.uartMode = EUSCI_A_UART_MODE;
param.overSampling = EUSCI_A_UART_LOW_FREQUENCY_BAUDRATE_GENERATION;

if (STATUS_FAIL == EUSCI_A_UART_init(EUSCI_A0_BASE, &param)) {
    return;
}

EUSCI_A_UART_enable(EUSCI_A0_BASE);

// Enable USCIA0 RX interrupt
EUSCI_A_UART_enableInterrupt(EUSCI_A0_BASE,
    EUSCI_A_UART_RECEIVE_INTERRUPT);
```

# 17 EUSCI Synchronous Peripheral Interface (EUSCI\_A\_SPI)

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## 17.1 Introduction

The Serial Peripheral Interface Bus or SPI bus is a synchronous serial data link standard named by Motorola that operates in full duplex mode. Devices communicate in master/slave mode where the master device initiates the data frame.

This library provides the API for handling a SPI communication using EUSCI.

The SPI module can be configured as either a master or a slave device.

The SPI module also includes a programmable bit rate clock divider and prescaler to generate the output serial clock derived from the module's input clock.

## 17.2 Functions

### Functions

- void [EUSCI\\_A\\_SPI\\_initMaster](#) (uint16\_t baseAddress, [EUSCI\\_A\\_SPI\\_initMasterParam](#) \*param)  
*Initializes the SPI Master block.*
- void [EUSCI\\_A\\_SPI\\_select4PinFunctionality](#) (uint16\_t baseAddress, uint8\_t select4PinFunctionality)  
*Selects 4Pin Functionality.*
- void [EUSCI\\_A\\_SPI\\_changeMasterClock](#) (uint16\_t baseAddress, [EUSCI\\_A\\_SPI\\_changeMasterClockParam](#) \*param)  
*Initializes the SPI Master clock. At the end of this function call, SPI module is left enabled.*
- void [EUSCI\\_A\\_SPI\\_initSlave](#) (uint16\_t baseAddress, [EUSCI\\_A\\_SPI\\_initSlaveParam](#) \*param)  
*Initializes the SPI Slave block.*
- void [EUSCI\\_A\\_SPI\\_changeClockPhasePolarity](#) (uint16\_t baseAddress, uint16\_t clockPhase, uint16\_t clockPolarity)  
*Changes the SPI clock phase and polarity. At the end of this function call, SPI module is left enabled.*
- void [EUSCI\\_A\\_SPI\\_transmitData](#) (uint16\_t baseAddress, uint8\_t transmitData)  
*Transmits a byte from the SPI Module.*
- uint8\_t [EUSCI\\_A\\_SPI\\_receiveData](#) (uint16\_t baseAddress)  
*Receives a byte that has been sent to the SPI Module.*
- void [EUSCI\\_A\\_SPI\\_enableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Enables individual SPI interrupt sources.*
- void [EUSCI\\_A\\_SPI\\_disableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Disables individual SPI interrupt sources.*
- uint8\_t [EUSCI\\_A\\_SPI\\_getInterruptStatus](#) (uint16\_t baseAddress, uint8\_t mask)



- Gets the current SPI interrupt status.*
- void [EUSCIA\\_SPI\\_clearInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)
  - Clears the selected SPI interrupt status flag.*
- void [EUSCIA\\_SPI\\_enable](#) (uint16\_t baseAddress)
  - Enables the SPI block.*
- void [EUSCIA\\_SPI\\_disable](#) (uint16\_t baseAddress)
  - Disables the SPI block.*
- uint32\_t [EUSCIA\\_SPI\\_getReceiveBufferAddress](#) (uint16\_t baseAddress)
  - Returns the address of the RX Buffer of the SPI for the DMA module.*
- uint32\_t [EUSCIA\\_SPI\\_getTransmitBufferAddress](#) (uint16\_t baseAddress)
  - Returns the address of the TX Buffer of the SPI for the DMA module.*
- uint16\_t [EUSCIA\\_SPI\\_isBusy](#) (uint16\_t baseAddress)
  - Indicates whether or not the SPI bus is busy.*

## 17.2.1 Detailed Description

To use the module as a master, the user must call [EUSCIA\\_SPI\\_initMaster\(\)](#) to configure the SPI Master. This is followed by enabling the SPI module using [EUSCIA\\_SPI\\_enable\(\)](#). The interrupts are then enabled (if needed). It is recommended to enable the SPI module before enabling the interrupts. A data transmit is then initiated using [EUSCIA\\_SPI\\_transmitData\(\)](#) and then when the receive flag is set, the received data is read using [EUSCIA\\_SPI\\_receiveData\(\)](#) and this indicates that an RX/TX operation is complete.

To use the module as a slave, initialization is done using [EUSCIA\\_SPI\\_initSlave\(\)](#) and this is followed by enabling the module using [EUSCIA\\_SPI\\_enable\(\)](#). Following this, the interrupts may be enabled as needed. When the receive flag is set, data is first transmitted using [EUSCIA\\_SPI\\_transmitData\(\)](#) and this is followed by a data reception by [EUSCIA\\_SPI\\_receiveData\(\)](#)

The SPI API is broken into 3 groups of functions: those that deal with status and initialization, those that handle data, and those that manage interrupts.

The status and initialization of the SPI module are managed by

- [EUSCIA\\_SPI\\_initMaster\(\)](#)
- [EUSCIA\\_SPI\\_initSlave\(\)](#)
- [EUSCIA\\_SPI\\_disable\(\)](#)
- [EUSCIA\\_SPI\\_enable\(\)](#)
- [EUSCIA\\_SPI\\_masterChangeClock\(\)](#)
- [EUSCIA\\_SPI\\_isBusy\(\)](#)
- [EUSCIA\\_SPI\\_select4PinFunctionality\(\)](#)
- [EUSCIA\\_SPI\\_changeClockPhasePolarity\(\)](#)

Data handling is done by

- [EUSCIA\\_SPI\\_transmitData\(\)](#)
- [EUSCIA\\_SPI\\_receiveData\(\)](#)

Interrupts from the SPI module are managed using

- [EUSCIA\\_SPI\\_disableInterrupt\(\)](#)

- [EUSCI\\_A\\_SPI.enableInterrupt\(\)](#)
- [EUSCI\\_A\\_SPI.getInterruptStatus\(\)](#)
- [EUSCI\\_A\\_SPI.clearInterrupt\(\)](#)

DMA related

- [EUSCI\\_A\\_SPI.getReceiveBufferAddressForDMA\(\)](#)
- [EUSCI\\_A\\_SPI.getTransmitBufferAddressForDMA\(\)](#)

## 17.2.2 Function Documentation

### EUSCI\_A\_SPI\_changeClockPhasePolarity()

```
void EUSCI_A_SPI_changeClockPhasePolarity (
    uint16_t baseAddress,
    uint16_t clockPhase,
    uint16_t clockPolarity )
```

Changes the SPI clock phase and polarity. At the end of this function call, SPI module is left enabled.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
<i>clockPhase</i>	is clock phase select. Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_↔NEXT</b> [Default]</li> <li>■ <b>EUSCI_A_SPI_PHASE_DATA_CAPTURED_ONFIRST_CHANGED_ON_↔NEXT</b></li> </ul>
<i>clockPolarity</i>	is clock polarity select Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_SPI_CLOCKPOLARITY_INACTIVITY_HIGH</b></li> <li>■ <b>EUSCI_A_SPI_CLOCKPOLARITY_INACTIVITY_LOW</b> [Default]</li> </ul>

Modified bits are **UCCKPL**, **UCCKPH** and **UCSWRST** of **UCAxCTLW0** register.

#### Returns

None

### EUSCI\_A\_SPI\_changeMasterClock()

```
void EUSCI_A_SPI_changeMasterClock (
    uint16_t baseAddress,
    EUSCI_A_SPI_changeMasterClockParam * param )
```

Initializes the SPI Master clock. At the end of this function call, SPI module is left enabled.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
<i>param</i>	is the pointer to struct for master clock setting.

Modified bits are **UCSWRST** of **UCAxCTLW0** register.

**Returns**

None

References EUSCI\_A\_SPI\_changeMasterClockParam::clockSourceFrequency, and EUSCI\_A\_SPI\_changeMasterClockParam::desiredSpiClock.

**EUSCI\_A\_SPI\_clearInterrupt()**

```
void EUSCI_A_SPI_clearInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Clears the selected SPI interrupt status flag.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
<i>mask</i>	is the masked interrupt flag to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI_A_SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIFG** register.

**Returns**

None

**EUSCI\_A\_SPI\_disable()**

```
void EUSCI_A_SPI_disable (
    uint16_t baseAddress )
```

Disables the SPI block.

This will disable operation of the SPI block.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
--------------------	--

Modified bits are **UCSWRST** of **UCAxCTLW0** register.

**Returns**

None

### EUSCI\_A\_SPI\_disableInterrupt()

```
void EUSCI_A_SPI_disableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Disables individual SPI interrupt sources.

Disables the indicated SPI interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
<i>mask</i>	is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI_A_SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIE** register.

**Returns**

None

### EUSCI\_A\_SPI\_enable()

```
void EUSCI_A_SPI_enable (
    uint16_t baseAddress )
```

Enables the SPI block.

This will enable operation of the SPI block.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
--------------------	--

Modified bits are **UCSWRST** of **UCAxCTLW0** register.

**Returns**

None

**EUSCI\_A\_SPI\_enableInterrupt()**

```
void EUSCI_A_SPI_enableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Enables individual SPI interrupt sources.

Enables the indicated SPI interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
<i>mask</i>	is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI_A_SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIFG** register and bits of **UCAxIE** register.

**Returns**

None

**EUSCI\_A\_SPI\_getInterruptStatus()**

```
uint8_t EUSCI_A_SPI_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t mask )
```

Gets the current SPI interrupt status.

This returns the interrupt status for the SPI module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI_A_SPI_RECEIVE_INTERRUPT</b></li> </ul>

**Returns**

Logical OR of any of the following:

- **EUSCI\_A\_SPI\_TRANSMIT\_INTERRUPT**
  - **EUSCI\_A\_SPI\_RECEIVE\_INTERRUPT**
- indicating the status of the masked interrupts

## EUSCI\_A\_SPI\_getReceiveBufferAddress()

```
uint32_t EUSCI_A_SPI_getReceiveBufferAddress (
    uint16_t baseAddress )
```

Returns the address of the RX Buffer of the SPI for the DMA module.

Returns the address of the SPI RX Buffer. This can be used in conjunction with the DMA to store the received data directly to memory.

### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
--------------------	--

### Returns

the address of the RX Buffer

## EUSCI\_A\_SPI\_getTransmitBufferAddress()

```
uint32_t EUSCI_A_SPI_getTransmitBufferAddress (
    uint16_t baseAddress )
```

Returns the address of the TX Buffer of the SPI for the DMA module.

Returns the address of the SPI TX Buffer. This can be used in conjunction with the DMA to obtain transmitted data directly from memory.

### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
--------------------	--

### Returns

the address of the TX Buffer

## EUSCI\_A\_SPI\_initMaster()

```
void EUSCI_A_SPI_initMaster (
    uint16_t baseAddress,
    EUSCI_A_SPI_initMasterParam * param )
```

Initializes the SPI Master block.

Upon successful initialization of the SPI master block, this function will have set the bus speed for the master, but the SPI Master block still remains disabled and must be enabled with [EUSCI\\_A\\_SPI\\_enable\(\)](#)

### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI Master module.
<i>param</i>	is the pointer to struct for master initialization.

Modified bits are **UCCKPH**, **UCCKPL**, **UC7BIT**, **UCMSB**, **UCSELx** and **UCSWRST** of **UCAxCTLW0** register.

#### Returns

STATUS\_SUCCESS

References EUSCI\_A\_SPI\_initMasterParam::clockPhase, EUSCI\_A\_SPI\_initMasterParam::clockPolarity, EUSCI\_A\_SPI\_initMasterParam::clockSourceFrequency, EUSCI\_A\_SPI\_initMasterParam::desiredSpiClock, EUSCI\_A\_SPI\_initMasterParam::msbFirst, EUSCI\_A\_SPI\_initMasterParam::selectClockSource, and EUSCI\_A\_SPI\_initMasterParam::spiMode.

### EUSCI\_A\_SPI\_initSlave()

```
void EUSCI_A_SPI_initSlave (
    uint16_t baseAddress,
    EUSCI_A_SPI_initSlaveParam * param )
```

Initializes the SPI Slave block.

Upon successful initialization of the SPI slave block, this function will have initialized the slave block, but the SPI Slave block still remains disabled and must be enabled with [EUSCI\\_A\\_SPI\\_enable\(\)](#)

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI Slave module.
<i>param</i>	is the pointer to struct for slave initialization.

Modified bits are **UCMSB**, **UCMST**, **UC7BIT**, **UCCKPL**, **UCCKPH**, **UCMODE** and **UCSWRST** of **UCAxCTLW0** register.

#### Returns

STATUS\_SUCCESS

References EUSCI\_A\_SPI\_initSlaveParam::clockPhase, EUSCI\_A\_SPI\_initSlaveParam::clockPolarity, EUSCI\_A\_SPI\_initSlaveParam::msbFirst, and EUSCI\_A\_SPI\_initSlaveParam::spiMode.

### EUSCI\_A\_SPI\_isBusy()

```
uint16_t EUSCI_A_SPI_isBusy (
    uint16_t baseAddress )
```

Indicates whether or not the SPI bus is busy.

This function returns an indication of whether or not the SPI bus is busy. This function checks the status of the bus via UCBBUSY bit

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
--------------------	--

**Returns**

One of the following:

- **EUSCI\_A\_SPI\_BUSY**
- **EUSCI\_A\_SPI\_NOT\_BUSY**  
indicating if the EUSCI\_A\_SPI is busy

**EUSCI\_A\_SPI\_receiveData()**

```
uint8_t EUSCI_A_SPI_receiveData (
    uint16_t baseAddress )
```

Receives a byte that has been sent to the SPI Module.

This function reads a byte of data from the SPI receive data Register.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
--------------------	--

**Returns**

Returns the byte received from by the SPI module, cast as an uint8\_t.

**EUSCI\_A\_SPI\_select4PinFunctionality()**

```
void EUSCI_A_SPI_select4PinFunctionality (
    uint16_t baseAddress,
    uint8_t select4PinFunctionality )
```

Selects 4Pin Functionality.

This function should be invoked only in 4-wire mode. Invoking this function has no effect in 3-wire mode.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
<i>select4PinFunctionality</i>	selects 4 pin functionality Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_SPI_PREVENT_CONFLICTS_WITH_OTHER_MASTERS</b>↔</li> <li>■ <b>EUSCI_A_SPI_ENABLE_SIGNAL_FOR_4WIRE_SLAVE</b></li> </ul>

Modified bits are **UCSTEM** of **UCAxCTLW0** register.



**Returns**

None

**EUSCI\_A\_SPI\_transmitData()**

```
void EUSCI_A_SPI_transmitData (
    uint16_t baseAddress,
    uint8_t transmitData )
```

Transmits a byte from the SPI Module.

This function will place the supplied data into SPI transmit data register to start transmission.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
<i>transmitData</i>	data to be transmitted from the SPI module

**Returns**

None

## 17.3 Programming Example

The following example shows how to use the SPI API to configure the SPI module as a master device, and how to do a simple send of data.

```
//Initialize slave to MSB first, inactive high clock polarity and 3 wire SPI
EUSCI_A_SPI_initSlaveParam param = {0};
param.msbFirst = EUSCI_A_SPI_MSB_FIRST;
param.clockPhase = EUSCI_A_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT;
param.clockPolarity = EUSCI_A_SPI_CLOCKPOLARITY_INACTIVITY_HIGH;
param.spiMode = EUSCI_A_SPI_3PIN;
EUSCI_A_SPI_initSlave(EUSCI_A0_BASE, &param);

//Enable SPI Module
EUSCI_A_SPI_enable(EUSCI_A0_BASE);

//Enable Receive interrupt
EUSCI_A_SPI_enableInterrupt(EUSCI_A0_BASE,
    EUSCI_A_SPI_RECEIVE_INTERRUPT
);
```

# 18 EUSCI Synchronous Peripheral Interface (EUSCI\_B\_SPI)

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## 18.1 Introduction

The Serial Peripheral Interface Bus or SPI bus is a synchronous serial data link standard named by Motorola that operates in full duplex mode. Devices communicate in master/slave mode where the master device initiates the data frame.

This library provides the API for handling a SPI communication using EUSCI.

The SPI module can be configured as either a master or a slave device.

The SPI module also includes a programmable bit rate clock divider and prescaler to generate the output serial clock derived from the module's input clock.

## 18.2 Functions

### Functions

- void [EUSCI\\_B\\_SPI\\_initMaster](#) (uint16\_t baseAddress, [EUSCI\\_B\\_SPI\\_initMasterParam](#) \*param)  
*Initializes the SPI Master block.*
- void [EUSCI\\_B\\_SPI\\_select4PinFunctionality](#) (uint16\_t baseAddress, uint8\_t select4PinFunctionality)  
*Selects 4Pin Functionality.*
- void [EUSCI\\_B\\_SPI\\_changeMasterClock](#) (uint16\_t baseAddress, [EUSCI\\_B\\_SPI\\_changeMasterClockParam](#) \*param)  
*Initializes the SPI Master clock. At the end of this function call, SPI module is left enabled.*
- void [EUSCI\\_B\\_SPI\\_initSlave](#) (uint16\_t baseAddress, [EUSCI\\_B\\_SPI\\_initSlaveParam](#) \*param)  
*Initializes the SPI Slave block.*
- void [EUSCI\\_B\\_SPI\\_changeClockPhasePolarity](#) (uint16\_t baseAddress, uint16\_t clockPhase, uint16\_t clockPolarity)  
*Changes the SPI clock phase and polarity. At the end of this function call, SPI module is left enabled.*
- void [EUSCI\\_B\\_SPI\\_transmitData](#) (uint16\_t baseAddress, uint8\_t transmitData)  
*Transmits a byte from the SPI Module.*
- uint8\_t [EUSCI\\_B\\_SPI\\_receiveData](#) (uint16\_t baseAddress)  
*Receives a byte that has been sent to the SPI Module.*
- void [EUSCI\\_B\\_SPI\\_enableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Enables individual SPI interrupt sources.*
- void [EUSCI\\_B\\_SPI\\_disableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Disables individual SPI interrupt sources.*
- uint8\_t [EUSCI\\_B\\_SPI\\_getInterruptStatus](#) (uint16\_t baseAddress, uint8\_t mask)

- Gets the current SPI interrupt status.*
- void `EUSCI_B_SPI_clearInterrupt` (uint16\_t baseAddress, uint8\_t mask)
  - Clears the selected SPI interrupt status flag.*
- void `EUSCI_B_SPI_enable` (uint16\_t baseAddress)
  - Enables the SPI block.*
- void `EUSCI_B_SPI_disable` (uint16\_t baseAddress)
  - Disables the SPI block.*
- uint32\_t `EUSCI_B_SPI_getReceiveBufferAddress` (uint16\_t baseAddress)
  - Returns the address of the RX Buffer of the SPI for the DMA module.*
- uint32\_t `EUSCI_B_SPI_getTransmitBufferAddress` (uint16\_t baseAddress)
  - Returns the address of the TX Buffer of the SPI for the DMA module.*
- uint16\_t `EUSCI_B_SPI_isBusy` (uint16\_t baseAddress)
  - Indicates whether or not the SPI bus is busy.*

## 18.2.1 Detailed Description

To use the module as a master, the user must call `EUSCI_B_SPI_masterInit()` to configure the SPI Master. This is followed by enabling the SPI module using `EUSCI_B_SPI_enable()`. The interrupts are then enabled (if needed). It is recommended to enable the SPI module before enabling the interrupts. A data transmit is then initiated using `EUSCI_B_SPI_transmitData()` and then when the receive flag is set, the received data is read using `EUSCI_B_SPI_receiveData()` and this indicates that an RX/TX operation is complete.

To use the module as a slave, initialization is done using `EUSCI_B_SPI_slaveInit()` and this is followed by enabling the module using `EUSCI_B_SPI_enable()`. Following this, the interrupts may be enabled as needed. When the receive flag is set, data is first transmitted using `EUSCI_B_SPI_transmitData()` and this is followed by a data reception by `EUSCI_B_SPI_receiveData()`

The SPI API is broken into 3 groups of functions: those that deal with status and initialization, those that handle data, and those that manage interrupts.

The status and initialization of the SPI module are managed by

- `EUSCI_B_SPI_masterInit()`
- `EUSCI_B_SPI_slaveInit()`
- `EUSCI_B_SPI_disable()`
- `EUSCI_B_SPI_enable()`
- `EUSCI_B_SPI_masterChangeClock()`
- `EUSCI_B_SPI_isBusy()`
- `EUSCI_B_SPI_select4PinFunctionality()`
- `EUSCI_B_SPI_changeClockPhasePolarity()`

Data handling is done by

- `EUSCI_B_SPI_transmitData()`
- `EUSCI_B_SPI_receiveData()`

Interrupts from the SPI module are managed using

- `EUSCI_B_SPI_disableInterrupt()`

- [EUSCI\\_B\\_SPI.enableInterrupt\(\)](#)
- [EUSCI\\_B\\_SPI.getInterruptStatus\(\)](#)
- [EUSCI\\_B\\_SPI.clearInterrupt\(\)](#)

DMA related

- [EUSCI\\_B\\_SPI.getReceiveBufferAddressForDMA\(\)](#)
- [EUSCI\\_B\\_SPI.getTransmitBufferAddressForDMA\(\)](#)

## 18.2.2 Function Documentation

### EUSCI\_B\_SPI\_changeClockPhasePolarity()

```
void EUSCI_B_SPI_changeClockPhasePolarity (
    uint16_t baseAddress,
    uint16_t clockPhase,
    uint16_t clockPolarity )
```

Changes the SPI clock phase and polarity. At the end of this function call, SPI module is left enabled.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
<i>clockPhase</i>	is clock phase select. Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_↔NEXT</b> [Default]</li> <li>■ <b>EUSCI_B_SPI_PHASE_DATA_CAPTURED_ONFIRST_CHANGED_ON_↔NEXT</b></li> </ul>
<i>clockPolarity</i>	is clock polarity select Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_SPI_CLOCKPOLARITY_INACTIVITY_HIGH</b></li> <li>■ <b>EUSCI_B_SPI_CLOCKPOLARITY_INACTIVITY_LOW</b> [Default]</li> </ul>

Modified bits are **UCCKPL**, **UCCKPH** and **UCSWRST** of **UCAxCTLW0** register.

#### Returns

None

### EUSCI\_B\_SPI\_changeMasterClock()

```
void EUSCI_B_SPI_changeMasterClock (
    uint16_t baseAddress,
    EUSCI_B_SPI_changeMasterClockParam * param )
```

Initializes the SPI Master clock. At the end of this function call, SPI module is left enabled.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
<i>param</i>	is the pointer to struct for master clock setting.

Modified bits are **UCSWRST** of **UCAxCTLW0** register.

**Returns**

None

References EUSCI\_B\_SPI\_changeMasterClockParam::clockSourceFrequency, and EUSCI\_B\_SPI\_changeMasterClockParam::desiredSpiClock.

**EUSCI\_B\_SPI\_clearInterrupt()**

```
void EUSCI_B_SPI_clearInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Clears the selected SPI interrupt status flag.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
<i>mask</i>	is the masked interrupt flag to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI_B_SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIFG** register.

**Returns**

None

**EUSCI\_B\_SPI\_disable()**

```
void EUSCI_B_SPI_disable (
    uint16_t baseAddress )
```

Disables the SPI block.

This will disable operation of the SPI block.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
--------------------	--

Modified bits are **UCSWRST** of **UCAxCTLW0** register.

**Returns**

None

### EUSCI\_B\_SPI\_disableInterrupt()

```
void EUSCI_B_SPI_disableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Disables individual SPI interrupt sources.

Disables the indicated SPI interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
<i>mask</i>	is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI_B_SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIE** register.

**Returns**

None

### EUSCI\_B\_SPI\_enable()

```
void EUSCI_B_SPI_enable (
    uint16_t baseAddress )
```

Enables the SPI block.

This will enable operation of the SPI block.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
--------------------	--

Modified bits are **UCSWRST** of **UCAxCTLW0** register.

**Returns**

None

**EUSCI\_B\_SPI\_enableInterrupt()**

```
void EUSCI_B_SPI_enableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Enables individual SPI interrupt sources.

Enables the indicated SPI interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
<i>mask</i>	is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI_B_SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIFG** register and bits of **UCAxIE** register.

**Returns**

None

**EUSCI\_B\_SPI\_getInterruptStatus()**

```
uint8_t EUSCI_B_SPI_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t mask )
```

Gets the current SPI interrupt status.

This returns the interrupt status for the SPI module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI_B_SPI_RECEIVE_INTERRUPT</b></li> </ul>

**Returns**

Logical OR of any of the following:

- **EUSCI\_B\_SPI\_TRANSMIT\_INTERRUPT**
  - **EUSCI\_B\_SPI\_RECEIVE\_INTERRUPT**
- indicating the status of the masked interrupts

**EUSCI\_B\_SPI\_getReceiveBufferAddress()**

```
uint32_t EUSCI_B_SPI_getReceiveBufferAddress (
    uint16_t baseAddress )
```

Returns the address of the RX Buffer of the SPI for the DMA module.

Returns the address of the SPI RX Buffer. This can be used in conjunction with the DMA to store the received data directly to memory.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
--------------------	--

**Returns**

the address of the RX Buffer

**EUSCI\_B\_SPI\_getTransmitBufferAddress()**

```
uint32_t EUSCI_B_SPI_getTransmitBufferAddress (
    uint16_t baseAddress )
```

Returns the address of the TX Buffer of the SPI for the DMA module.

Returns the address of the SPI TX Buffer. This can be used in conjunction with the DMA to obtain transmitted data directly from memory.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
--------------------	--

**Returns**

the address of the TX Buffer

**EUSCI\_B\_SPI\_initMaster()**

```
void EUSCI_B_SPI_initMaster (
    uint16_t baseAddress,
    EUSCI_B_SPI_initMasterParam * param )
```

Initializes the SPI Master block.

Upon successful initialization of the SPI master block, this function will have set the bus speed for the master, but the SPI Master block still remains disabled and must be enabled with [EUSCI\\_B\\_SPI.enable\(\)](#)

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI Master module.
<i>param</i>	is the pointer to struct for master initialization.



Modified bits are **UCCKPH**, **UCCKPL**, **UC7BIT**, **UCMSB**, **UCSELx** and **UCSWRST** of **UCAxCTLW0** register.

#### Returns

STATUS\_SUCCESS

References EUSCI.B.SPI.initMasterParam::clockPhase, EUSCI.B.SPI.initMasterParam::clockPolarity, EUSCI.B.SPI.initMasterParam::clockSourceFrequency, EUSCI.B.SPI.initMasterParam::desiredSpiClock, EUSCI.B.SPI.initMasterParam::msbFirst, EUSCI.B.SPI.initMasterParam::selectClockSource, and EUSCI.B.SPI.initMasterParam::spiMode.

### EUSCI\_B\_SPI\_initSlave()

```
void EUSCI_B_SPI_initSlave (
    uint16_t baseAddress,
    EUSCI_B_SPI_initSlaveParam * param )
```

Initializes the SPI Slave block.

Upon successful initialization of the SPI slave block, this function will have initialized the slave block, but the SPI Slave block still remains disabled and must be enabled with [EUSCI.B.SPI.enable\(\)](#)

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI Slave module.
<i>param</i>	is the pointer to struct for slave initialization.

Modified bits are **UCMSB**, **UCMST**, **UC7BIT**, **UCCKPL**, **UCCKPH**, **UCMODE** and **UCSWRST** of **UCAxCTLW0** register.

#### Returns

STATUS\_SUCCESS

References EUSCI.B.SPI.initSlaveParam::clockPhase, EUSCI.B.SPI.initSlaveParam::clockPolarity, EUSCI.B.SPI.initSlaveParam::msbFirst, and EUSCI.B.SPI.initSlaveParam::spiMode.

### EUSCI\_B\_SPI\_isBusy()

```
uint16_t EUSCI_B_SPI_isBusy (
    uint16_t baseAddress )
```

Indicates whether or not the SPI bus is busy.

This function returns an indication of whether or not the SPI bus is busy. This function checks the status of the bus via UCBBUSY bit

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
--------------------	--

**Returns**

One of the following:

- **EUSCI\_B\_SPI\_BUSY**
- **EUSCI\_B\_SPI\_NOT\_BUSY**  
indicating if the EUSCI\_B\_SPI is busy

**EUSCI\_B\_SPI\_receiveData()**

```
uint8_t EUSCI_B_SPI_receiveData (
    uint16_t baseAddress )
```

Receives a byte that has been sent to the SPI Module.

This function reads a byte of data from the SPI receive data Register.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
--------------------	--

**Returns**

Returns the byte received from by the SPI module, cast as an uint8\_t.

**EUSCI\_B\_SPI\_select4PinFunctionality()**

```
void EUSCI_B_SPI_select4PinFunctionality (
    uint16_t baseAddress,
    uint8_t select4PinFunctionality )
```

Selects 4Pin Functionality.

This function should be invoked only in 4-wire mode. Invoking this function has no effect in 3-wire mode.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
<i>select4PinFunctionality</i>	selects 4 pin functionality Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_SPI_PREVENT_CONFLICTS_WITH_OTHER_MASTERS</b>↔</li> <li>■ <b>EUSCI_B_SPI_ENABLE_SIGNAL_FOR_4WIRE_SLAVE</b></li> </ul>

Modified bits are **UCSTEM** of **UCAxCTLW0** register.

**Returns**

None

**EUSCI\_B\_SPI\_transmitData()**

```
void EUSCI_B_SPI_transmitData (
    uint16_t baseAddress,
    uint8_t transmitData )
```

Transmits a byte from the SPI Module.

This function will place the supplied data into SPI transmit data register to start transmission.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
<i>transmitData</i>	data to be transmitted from the SPI module

**Returns**

None

## 18.3 Programming Example

The following example shows how to use the SPI API to configure the SPI module as a master device, and how to do a simple send of data.

```
//Initialize slave to MSB first, inactive high clock polarity and 3 wire SPI
EUSCI_B_SPI_initSlaveParam param = {0};
param.msbFirst = EUSCI_B_SPI_MSB_FIRST;
param.clockPhase = EUSCI_B_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT;
param.clockPolarity = EUSCI_B_SPI_CLOCKPOLARITY_INACTIVITY_HIGH;
param.spiMode = EUSCI_B_SPI_3PIN;
EUSCI_B_SPI_initSlave(EUSCI_B0_BASE, &param);

//Enable SPI Module
EUSCI_B_SPI_enable(EUSCI_B0_BASE);

//Enable Receive interrupt
EUSCI_B_SPI_enableInterrupt(EUSCI_B0_BASE,
    EUSCI_B_SPI_RECEIVE_INTERRUPT
);
```

# 19 EUSCI Inter-Integrated Circuit (EUSCI\_B\_I2C)

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## 19.1 Introduction

In I2C mode, the eUSCI\_B module provides an interface between the device and I2C-compatible devices connected by the two-wire I2C serial bus. External components attached to the I2C bus serially transmit and/or receive serial data to/from the eUSCI\_B module through the 2-wire I2C interface. The Inter-Integrated Circuit (I2C) API provides a set of functions for using the MSP430Ware I2C modules. Functions are provided to initialize the I2C modules, to send and receive data, obtain status, and to manage interrupts for the I2C modules.

The I2C module provide the ability to communicate to other IC devices over an I2C bus. The I2C bus is specified to support devices that can both transmit and receive (write and read) data. Also, devices on the I2C bus can be designated as either a master or a slave. The MSP430Ware I2C modules support both sending and receiving data as either a master or a slave, and also support the simultaneous operation as both a master and a slave.

I2C module can generate interrupts. The I2C module configured as a master will generate interrupts when a transmit or receive operation is completed (or aborted due to an error). The I2C module configured as a slave will generate interrupts when data has been sent or requested by a master.

## 19.2 Master Operations

To drive the master module, the APIs need to be invoked in the following order

- **EUSCI\_B\_I2C.initMaster**
- **EUSCI\_B\_I2C.setSlaveAddress**
- **EUSCI\_B\_I2C.setMode**
- **EUSCI\_B\_I2C.enable**
- **EUSCI\_B\_I2C.enableInterrupt** ( if interrupts are being used ) This may be followed by the APIs for transmit or receive as required

The user must first initialize the I2C module and configure it as a master with a call to [EUSCI\\_B\\_I2C.initMaster\(\)](#). That function will set the clock and data rates. This is followed by a call to set the slave address with which the master intends to communicate with using [EUSCI\\_B\\_I2C.setSlaveAddress](#). Then the mode of operation (transmit or receive) is chosen using [EUSCI\\_B\\_I2C.setMode](#). The I2C module may now be enabled using [EUSCI\\_B\\_I2C.enable](#). It is recommended to enable the [EUSCI\\_B\\_I2C](#) module before enabling the interrupts. Any transmission or reception of data may be initiated at this point after interrupts are enabled (if any).

The transaction can then be initiated on the bus by calling the transmit or receive related APIs as listed below.

Master Single Byte Transmission

- [EUSCI\\_B\\_I2C.masterSendSingleByte\(\)](#)

Master Multiple Byte Transmission

- [EUSCI\\_B\\_I2C.masterSendMultiByteStart\(\)](#)
- [EUSCI\\_B\\_I2C.masterSendMultiByteNext\(\)](#)
- [EUSCI\\_B\\_I2C.masterSendMultiByteStop\(\)](#)

Master Single Byte Reception

- [EUSCI\\_B\\_I2C.masterReceiveSingleByte\(\)](#)

Master Multiple Byte Reception

- [EUSCI\\_B\\_I2C.masterMultiByteReceiveStart\(\)](#)
- [EUSCI\\_B\\_I2C.masterReceiveMultiByteNext\(\)](#)
- [EUSCI\\_B\\_I2C.masterReceiveMultiByteFinish\(\)](#)
- [EUSCI\\_B\\_I2C.masterReceiveMultiByteStop\(\)](#)

For the interrupt-driven transaction, the user must register an interrupt handler for the I2C devices and enable the I2C interrupt.

## 19.3 Slave Operations

To drive the slave module, the APIs need to be invoked in the following order

- [EUSCI\\_B\\_I2C.initSlave\(\)](#)
- [EUSCI\\_B\\_I2C.setMode\(\)](#)
- [EUSCI\\_B\\_I2C.enable\(\)](#)
- [EUSCI\\_B\\_I2C.enableInterrupt\(\)](#) ( if interrupts are being used ) This may be followed by the APIs for transmit or receive as required

The user must first call the [EUSCI\\_B\\_I2C.initSlave](#) to initialize the slave module in I2C mode and set the slave address. This is followed by a call to set the mode of operation ( transmit or receive ).The I2C module may now be enabled using [EUSCI\\_B\\_I2C.enable](#). It is recommended to enable the I2C module before enabling the interrupts. Any transmission or reception of data may be initiated at this point after interrupts are enabled (if any).

The transaction can then be initiated on the bus by calling the transmit or receive related APIs as listed below.

Slave Transmission API

- [EUSCI\\_B\\_I2C.slavePutData\(\)](#)

Slave Reception API

- [EUSCI\\_B\\_I2C\\_slaveGetData\(\)](#)

For the interrupt-driven transaction, the user must register an interrupt handler for the I2C devices and enable the I2C interrupt.

## 19.4 API Functions

### Functions

- void [EUSCI\\_B\\_I2C\\_initMaster](#) (uint16\_t baseAddress, [EUSCI\\_B\\_I2C\\_initMasterParam](#) \*param)  
*Initializes the I2C Master block.*
- void [EUSCI\\_B\\_I2C\\_initSlave](#) (uint16\_t baseAddress, [EUSCI\\_B\\_I2C\\_initSlaveParam](#) \*param)  
*Initializes the I2C Slave block.*
- void [EUSCI\\_B\\_I2C\\_enable](#) (uint16\_t baseAddress)  
*Enables the I2C block.*
- void [EUSCI\\_B\\_I2C\\_disable](#) (uint16\_t baseAddress)  
*Disables the I2C block.*
- void [EUSCI\\_B\\_I2C\\_setSlaveAddress](#) (uint16\_t baseAddress, uint8\_t slaveAddress)  
*Sets the address that the I2C Master will place on the bus.*
- void [EUSCI\\_B\\_I2C\\_setMode](#) (uint16\_t baseAddress, uint8\_t mode)  
*Sets the mode of the I2C device.*
- uint8\_t [EUSCI\\_B\\_I2C\\_getMode](#) (uint16\_t baseAddress)  
*Gets the mode of the I2C device.*
- void [EUSCI\\_B\\_I2C\\_slavePutData](#) (uint16\_t baseAddress, uint8\_t transmitData)  
*Transmits a byte from the I2C Module.*
- uint8\_t [EUSCI\\_B\\_I2C\\_slaveGetData](#) (uint16\_t baseAddress)  
*Receives a byte that has been sent to the I2C Module.*
- uint16\_t [EUSCI\\_B\\_I2C\\_isBusBusy](#) (uint16\_t baseAddress)  
*Indicates whether or not the I2C bus is busy.*
- uint16\_t [EUSCI\\_B\\_I2C\\_masterIsStopSent](#) (uint16\_t baseAddress)  
*Indicates whether STOP got sent.*
- uint16\_t [EUSCI\\_B\\_I2C\\_masterIsStartSent](#) (uint16\_t baseAddress)  
*Indicates whether Start got sent.*
- void [EUSCI\\_B\\_I2C\\_enableInterrupt](#) (uint16\_t baseAddress, uint16\_t mask)  
*Enables individual I2C interrupt sources.*
- void [EUSCI\\_B\\_I2C\\_disableInterrupt](#) (uint16\_t baseAddress, uint16\_t mask)  
*Disables individual I2C interrupt sources.*
- void [EUSCI\\_B\\_I2C\\_clearInterrupt](#) (uint16\_t baseAddress, uint16\_t mask)  
*Clears I2C interrupt sources.*
- uint16\_t [EUSCI\\_B\\_I2C\\_getInterruptStatus](#) (uint16\_t baseAddress, uint16\_t mask)  
*Gets the current I2C interrupt status.*
- void [EUSCI\\_B\\_I2C\\_masterSendSingleByte](#) (uint16\_t baseAddress, uint8\_t txData)  
*Does single byte transmission from Master to Slave.*
- uint8\_t [EUSCI\\_B\\_I2C\\_masterReceiveSingleByte](#) (uint16\_t baseAddress)  
*Does single byte reception from Slave.*
- bool [EUSCI\\_B\\_I2C\\_masterSendSingleByteWithTimeout](#) (uint16\_t baseAddress, uint8\_t txData, uint32\_t timeout)  
*Does single byte transmission from Master to Slave with timeout.*
- void [EUSCI\\_B\\_I2C\\_masterSendMultiByteStart](#) (uint16\_t baseAddress, uint8\_t txData)  
*Starts multi-byte transmission from Master to Slave.*

- bool `EUSCI_B_I2C_masterSendMultiByteStartWithTimeout` (uint16\_t baseAddress, uint8\_t txData, uint32\_t timeout)  
*Starts multi-byte transmission from Master to Slave with timeout.*
- void `EUSCI_B_I2C_masterSendMultiByteNext` (uint16\_t baseAddress, uint8\_t txData)  
*Continues multi-byte transmission from Master to Slave.*
- bool `EUSCI_B_I2C_masterSendMultiByteNextWithTimeout` (uint16\_t baseAddress, uint8\_t txData, uint32\_t timeout)  
*Continues multi-byte transmission from Master to Slave with timeout.*
- void `EUSCI_B_I2C_masterSendMultiByteFinish` (uint16\_t baseAddress, uint8\_t txData)  
*Finishes multi-byte transmission from Master to Slave.*
- bool `EUSCI_B_I2C_masterSendMultiByteFinishWithTimeout` (uint16\_t baseAddress, uint8\_t txData, uint32\_t timeout)  
*Finishes multi-byte transmission from Master to Slave with timeout.*
- void `EUSCI_B_I2C_masterSendStart` (uint16\_t baseAddress)  
*This function is used by the Master module to initiate START.*
- void `EUSCI_B_I2C_masterSendMultiByteStop` (uint16\_t baseAddress)  
*Send STOP byte at the end of a multi-byte transmission from Master to Slave.*
- bool `EUSCI_B_I2C_masterSendMultiByteStopWithTimeout` (uint16\_t baseAddress, uint32\_t timeout)  
*Send STOP byte at the end of a multi-byte transmission from Master to Slave with timeout.*
- void `EUSCI_B_I2C_masterReceiveStart` (uint16\_t baseAddress)  
*Starts reception at the Master end.*
- uint8\_t `EUSCI_B_I2C_masterReceiveMultiByteNext` (uint16\_t baseAddress)  
*Starts multi-byte reception at the Master end one byte at a time.*
- uint8\_t `EUSCI_B_I2C_masterReceiveMultiByteFinish` (uint16\_t baseAddress)  
*Finishes multi-byte reception at the Master end.*
- bool `EUSCI_B_I2C_masterReceiveMultiByteFinishWithTimeout` (uint16\_t baseAddress, uint8\_t \*txData, uint32\_t timeout)  
*Finishes multi-byte reception at the Master end with timeout.*
- void `EUSCI_B_I2C_masterReceiveMultiByteStop` (uint16\_t baseAddress)  
*Sends the STOP at the end of a multi-byte reception at the Master end.*
- void `EUSCI_B_I2C_enableMultiMasterMode` (uint16\_t baseAddress)  
*Enables Multi Master Mode.*
- void `EUSCI_B_I2C_disableMultiMasterMode` (uint16\_t baseAddress)  
*Disables Multi Master Mode.*
- uint8\_t `EUSCI_B_I2C_masterReceiveSingle` (uint16\_t baseAddress)  
*receives a byte that has been sent to the I2C Master Module.*
- uint32\_t `EUSCI_B_I2C_getReceiveBufferAddress` (uint16\_t baseAddress)  
*Returns the address of the RX Buffer of the I2C for the DMA module.*
- uint32\_t `EUSCI_B_I2C_getTransmitBufferAddress` (uint16\_t baseAddress)  
*Returns the address of the TX Buffer of the I2C for the DMA module.*
- void `EUSCI_B_I2C_setTimeout` (uint16\_t baseAddress, uint16\_t timeout)  
*Enforces a timeout if the I2C clock is held low longer than a defined time.*

## 19.4.1 Detailed Description

The eUSCI I2C API is broken into three groups of functions: those that deal with interrupts, those that handle status and initialization, and those that deal with sending and receiving data.

The I2C master and slave interrupts are handled by

- `EUSCI_B_I2C_enableInterrupt`

- EUSCI\_B\_I2C\_disableInterrupt
- EUSCI\_B\_I2C\_clearInterrupt
- EUSCI\_B\_I2C\_getInterruptStatus

Status and initialization functions for the I2C modules are

- EUSCI\_B\_I2C\_initMaster
- EUSCI\_B\_I2C\_enable
- EUSCI\_B\_I2C\_disable
- EUSCI\_B\_I2C\_isBusBusy
- EUSCI\_B\_I2C\_isBusy
- EUSCI\_B\_I2C\_initSlave
- EUSCI\_B\_I2C\_interruptStatus
- EUSCI\_B\_I2C\_setSlaveAddress
- EUSCI\_B\_I2C\_setMode
- EUSCI\_B\_I2C\_masterIsStopSent
- EUSCI\_B\_I2C\_masterIsStartSent
- EUSCI\_B\_I2C\_selectMasterEnvironmentSelect

Sending and receiving data from the I2C slave module is handled by

- EUSCI\_B\_I2C\_slavePutData
- EUSCI\_B\_I2C\_slaveGetData

Sending and receiving data from the I2C slave module is handled by

- EUSCI\_B\_I2C\_masterSendSingleByte
- EUSCI\_B\_I2C\_masterSendStart
- EUSCI\_B\_I2C\_masterSendMultiByteStart
- EUSCI\_B\_I2C\_masterSendMultiByteNext
- EUSCI\_B\_I2C\_masterSendMultiByteFinish
- EUSCI\_B\_I2C\_masterSendMultiByteStop
- EUSCI\_B\_I2C\_masterReceiveMultiByteNext
- EUSCI\_B\_I2C\_masterReceiveMultiByteFinish
- EUSCI\_B\_I2C\_masterReceiveMultiByteStop
- EUSCI\_B\_I2C\_masterReceiveStart
- EUSCI\_B\_I2C\_masterReceiveSingle

## 19.4.2 Function Documentation

### EUSCI\_B\_I2C\_clearInterrupt()

```
void EUSCI_B_I2C_clearInterrupt (
    uint16_t baseAddress,
    uint16_t mask )
```



Clears I2C interrupt sources.

The I2C interrupt source is cleared, so that it no longer asserts. The highest interrupt flag is automatically cleared when an interrupt vector generator is used.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C module.
<i>mask</i>	<p>is a bit mask of the interrupt sources to be cleared. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_I2C_NAK_INTERRUPT</b> - Not-acknowledge interrupt</li> <li>■ <b>EUSCI_B_I2C_ARBITRATIONLOST_INTERRUPT</b> - Arbitration lost interrupt</li> <li>■ <b>EUSCI_B_I2C_STOP_INTERRUPT</b> - STOP condition interrupt</li> <li>■ <b>EUSCI_B_I2C_START_INTERRUPT</b> - START condition interrupt</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT0</b> - Transmit interrupt0</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT1</b> - Transmit interrupt1</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT2</b> - Transmit interrupt2</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT3</b> - Transmit interrupt3</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT0</b> - Receive interrupt0</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT1</b> - Receive interrupt1</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT2</b> - Receive interrupt2</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT3</b> - Receive interrupt3</li> <li>■ <b>EUSCI_B_I2C_BIT9_POSITION_INTERRUPT</b> - Bit position 9 interrupt</li> <li>■ <b>EUSCI_B_I2C_CLOCK_LOW_TIMEOUT_INTERRUPT</b> - Clock low timeout interrupt enable</li> <li>■ <b>EUSCI_B_I2C_BYTE_COUNTER_INTERRUPT</b> - Byte counter interrupt enable</li> </ul>

Modified bits of **UCBxIFG** register.

#### Returns

None

### EUSCI\_B\_I2C\_disable()

```
void EUSCI_B_I2C_disable (
    uint16_t baseAddress )
```

Disables the I2C block.

This will disable operation of the I2C block.

#### Parameters

<i>baseAddress</i>	is the base address of the USCI I2C module.
--------------------	---

Modified bits are **UCSWRST** of **UCBxCTLW0** register.

**Returns**

None

### EUSCI\_B\_I2C\_disableInterrupt()

```
void EUSCI_B_I2C_disableInterrupt (
    uint16_t baseAddress,
    uint16_t mask )
```

Disables individual I2C interrupt sources.

Disables the indicated I2C interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
<i>mask</i>	<p>is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_I2C_NAK_INTERRUPT</b> - Not-acknowledge interrupt</li> <li>■ <b>EUSCI_B_I2C_ARBITRATIONLOST_INTERRUPT</b> - Arbitration lost interrupt</li> <li>■ <b>EUSCI_B_I2C_STOP_INTERRUPT</b> - STOP condition interrupt</li> <li>■ <b>EUSCI_B_I2C_START_INTERRUPT</b> - START condition interrupt</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT0</b> - Transmit interrupt0</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT1</b> - Transmit interrupt1</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT2</b> - Transmit interrupt2</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT3</b> - Transmit interrupt3</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT0</b> - Receive interrupt0</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT1</b> - Receive interrupt1</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT2</b> - Receive interrupt2</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT3</b> - Receive interrupt3</li> <li>■ <b>EUSCI_B_I2C_BIT9_POSITION_INTERRUPT</b> - Bit position 9 interrupt</li> <li>■ <b>EUSCI_B_I2C_CLOCK_LOW_TIMEOUT_INTERRUPT</b> - Clock low timeout interrupt enable</li> <li>■ <b>EUSCI_B_I2C_BYTE_COUNTER_INTERRUPT</b> - Byte counter interrupt enable</li> </ul>

Modified bits of **UCBxIE** register.

**Returns**

None

**EUSCI\_B\_I2C\_disableMultiMasterMode()**

```
void EUSCI_B_I2C_disableMultiMasterMode (
    uint16_t baseAddress )
```

Disables Multi Master Mode.

At the end of this function, the I2C module is still disabled till EUSCI\_B\_I2C\_enable is invoked

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

Modified bits are **UCSWRST** and **UCMM** of **UCBxCTLW0** register.

**Returns**

None

**EUSCI\_B\_I2C\_enable()**

```
void EUSCI_B_I2C_enable (
    uint16_t baseAddress )
```

Enables the I2C block.

This will enable operation of the I2C block.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI I2C module.
--------------------	---

Modified bits are **UCSWRST** of **UCBxCTLW0** register.

**Returns**

None

**EUSCI\_B\_I2C\_enableInterrupt()**

```
void EUSCI_B_I2C_enableInterrupt (
    uint16_t baseAddress,
    uint16_t mask )
```

Enables individual I2C interrupt sources.

Enables the indicated I2C interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

## Parameters

<i>mask</i>	<p>is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_I2C_NAK_INTERRUPT</b> - Not-acknowledge interrupt</li> <li>■ <b>EUSCI_B_I2C_ARBITRATIONLOST_INTERRUPT</b> - Arbitration lost interrupt</li> <li>■ <b>EUSCI_B_I2C_STOP_INTERRUPT</b> - STOP condition interrupt</li> <li>■ <b>EUSCI_B_I2C_START_INTERRUPT</b> - START condition interrupt</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT0</b> - Transmit interrupt0</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT1</b> - Transmit interrupt1</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT2</b> - Transmit interrupt2</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT3</b> - Transmit interrupt3</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT0</b> - Receive interrupt0</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT1</b> - Receive interrupt1</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT2</b> - Receive interrupt2</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT3</b> - Receive interrupt3</li> <li>■ <b>EUSCI_B_I2C_BIT9_POSITION_INTERRUPT</b> - Bit position 9 interrupt</li> <li>■ <b>EUSCI_B_I2C_CLOCK_LOW_TIMEOUT_INTERRUPT</b> - Clock low timeout interrupt enable</li> <li>■ <b>EUSCI_B_I2C_BYTE_COUNTER_INTERRUPT</b> - Byte counter interrupt enable</li> </ul>
-------------	---

Modified bits of **UCBxIE** register.

## Returns

None

## EUSCI\_B\_I2C\_enableMultiMasterMode()

```
void EUSCI_B_I2C_enableMultiMasterMode (
    uint16_t baseAddress )
```

Enables Multi Master Mode.

At the end of this function, the I2C module is still disabled till EUSCI\_B\_I2C\_enable is invoked

## Parameters

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

Modified bits are **UCSWRST** and **UCMM** of **UCBxCTLW0** register.

**Returns**

None

**EUSCI\_B\_I2C\_getInterruptStatus()**

```
uint16_t EUSCI_B_I2C_getInterruptStatus (
    uint16_t baseAddress,
    uint16_t mask )
```

Gets the current I2C interrupt status.

This returns the interrupt status for the I2C module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
<i>mask</i>	<p>is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_I2C_NAK_INTERRUPT</b> - Not-acknowledge interrupt</li> <li>■ <b>EUSCI_B_I2C_ARBITRATIONLOST_INTERRUPT</b> - Arbitration lost interrupt</li> <li>■ <b>EUSCI_B_I2C_STOP_INTERRUPT</b> - STOP condition interrupt</li> <li>■ <b>EUSCI_B_I2C_START_INTERRUPT</b> - START condition interrupt</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT0</b> - Transmit interrupt0</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT1</b> - Transmit interrupt1</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT2</b> - Transmit interrupt2</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT3</b> - Transmit interrupt3</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT0</b> - Receive interrupt0</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT1</b> - Receive interrupt1</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT2</b> - Receive interrupt2</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT3</b> - Receive interrupt3</li> <li>■ <b>EUSCI_B_I2C_BIT9_POSITION_INTERRUPT</b> - Bit position 9 interrupt</li> <li>■ <b>EUSCI_B_I2C_CLOCK_LOW_TIMEOUT_INTERRUPT</b> - Clock low timeout interrupt enable</li> <li>■ <b>EUSCI_B_I2C_BYTE_COUNTER_INTERRUPT</b> - Byte counter interrupt enable</li> </ul>

**Returns**

Logical OR of any of the following:

- **EUSCI\_B\_I2C\_NAK\_INTERRUPT** Not-acknowledge interrupt
- **EUSCI\_B\_I2C\_ARBITRATIONLOST\_INTERRUPT** Arbitration lost interrupt
- **EUSCI\_B\_I2C\_STOP\_INTERRUPT** STOP condition interrupt
- **EUSCI\_B\_I2C\_START\_INTERRUPT** START condition interrupt
- **EUSCI\_B\_I2C\_TRANSMIT\_INTERRUPT0** Transmit interrupt0

- **EUSCI\_B\_I2C\_TRANSMIT\_INTERRUPT1** Transmit interrupt1
- **EUSCI\_B\_I2C\_TRANSMIT\_INTERRUPT2** Transmit interrupt2
- **EUSCI\_B\_I2C\_TRANSMIT\_INTERRUPT3** Transmit interrupt3
- **EUSCI\_B\_I2C\_RECEIVE\_INTERRUPT0** Receive interrupt0
- **EUSCI\_B\_I2C\_RECEIVE\_INTERRUPT1** Receive interrupt1
- **EUSCI\_B\_I2C\_RECEIVE\_INTERRUPT2** Receive interrupt2
- **EUSCI\_B\_I2C\_RECEIVE\_INTERRUPT3** Receive interrupt3
- **EUSCI\_B\_I2C\_BIT9\_POSITION\_INTERRUPT** Bit position 9 interrupt
- **EUSCI\_B\_I2C\_CLOCK\_LOW\_TIMEOUT\_INTERRUPT** Clock low timeout interrupt enable
- **EUSCI\_B\_I2C\_BYTE\_COUNTER\_INTERRUPT** Byte counter interrupt enable indicating the status of the masked interrupts

### EUSCI\_B\_I2C\_getMode()

```
uint8_t EUSCI_B_I2C_getMode (
    uint16_t baseAddress )
```

Gets the mode of the I2C device.

Current I2C transmit/receive mode.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

Modified bits are **UCTR** of **UCBxCTLW0** register.

#### Returns

One of the following:

- **EUSCI\_B\_I2C\_TRANSMIT\_MODE**
  - **EUSCI\_B\_I2C\_RECEIVE\_MODE**
- indicating the current mode

### EUSCI\_B\_I2C\_getReceiveBufferAddress()

```
uint32_t EUSCI_B_I2C_getReceiveBufferAddress (
    uint16_t baseAddress )
```

Returns the address of the RX Buffer of the I2C for the DMA module.

Returns the address of the I2C RX Buffer. This can be used in conjunction with the DMA to store the received data directly to memory.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

**Returns**

The address of the I2C RX Buffer

**EUSCI\_B\_I2C\_getTransmitBufferAddress()**

```
uint32_t EUSCI_B_I2C_getTransmitBufferAddress (
    uint16_t baseAddress )
```

Returns the address of the TX Buffer of the I2C for the DMA module.

Returns the address of the I2C TX Buffer. This can be used in conjunction with the DMA to obtain transmitted data directly from memory.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

**Returns**

The address of the I2C TX Buffer

**EUSCI\_B\_I2C\_initMaster()**

```
void EUSCI_B_I2C_initMaster (
    uint16_t baseAddress,
    EUSCI_B_I2C_initMasterParam * param )
```

Initializes the I2C Master block.

This function initializes operation of the I2C Master block. Upon successful initialization of the I2C block, this function will have set the bus speed for the master; however I2C module is still disabled till EUSCI\_B\_I2C\_enable is invoked.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>param</i>	is the pointer to the struct for master initialization.

**Returns**

None

References EUSCI\_B\_I2C\_initMasterParam::autoSTOPGeneration, EUSCI\_B\_I2C\_initMasterParam::byteCounterThreshold, EUSCI\_B\_I2C\_initMasterParam::dataRate, EUSCI\_B\_I2C\_initMasterParam::i2cClk, and EUSCI\_B\_I2C\_initMasterParam::selectClockSource.

**EUSCI\_B\_I2C\_initSlave()**

```
void EUSCI_B_I2C_initSlave (
```

```
uint16_t baseAddress,
EUSCI_B_I2C_initSlaveParam * param )
```

Initializes the I2C Slave block.

This function initializes operation of the I2C as a Slave mode. Upon successful initialization of the I2C blocks, this function will have set the slave address but the I2C module is still disabled till EUSCI\_B\_I2C\_enable is invoked.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Slave module.
<i>param</i>	is the pointer to the struct for slave initialization.

#### Returns

None

References EUSCI\_B\_I2C\_initSlaveParam::slaveAddress, EUSCI\_B\_I2C\_initSlaveParam::slaveAddressOffset, and EUSCI\_B\_I2C\_initSlaveParam::slaveOwnAddressEnable.

### EUSCI\_B\_I2C\_isBusBusy()

```
uint16_t EUSCI_B_I2C_isBusBusy (
    uint16_t baseAddress )
```

Indicates whether or not the I2C bus is busy.

This function returns an indication of whether or not the I2C bus is busy. This function checks the status of the bus via UCBBUSY bit in UCBxSTAT register.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

#### Returns

One of the following:

- **EUSCI\_B\_I2C\_BUS\_BUSY**
  - **EUSCI\_B\_I2C\_BUS\_NOT\_BUSY**
- indicating whether the bus is busy

### EUSCI\_B\_I2C\_masterIsStartSent()

```
uint16_t EUSCI_B_I2C_masterIsStartSent (
    uint16_t baseAddress )
```

Indicates whether Start got sent.

This function returns an indication of whether or not Start got sent This function checks the status of the bus via UCTXSTT bit in UCBxCTL1 register.



**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

**Returns**

One of the following:

- **EUSCI\_B\_I2C\_START\_SEND\_COMPLETE**
- **EUSCI\_B\_I2C\_SENDING\_START**  
indicating whether the start was sent

**EUSCI\_B\_I2C\_masterIsStopSent()**

```
uint16_t EUSCI_B_I2C_masterIsStopSent (
    uint16_t baseAddress )
```

Indicates whether STOP got sent.

This function returns an indication of whether or not STOP got sent This function checks the status of the bus via UCTXSTP bit in UCBxCTL1 register.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

**Returns**

One of the following:

- **EUSCI\_B\_I2C\_STOP\_SEND\_COMPLETE**
- **EUSCI\_B\_I2C\_SENDING\_STOP**  
indicating whether the stop was sent

**EUSCI\_B\_I2C\_masterReceiveMultiByteFinish()**

```
uint8_t EUSCI_B_I2C_masterReceiveMultiByteFinish (
    uint16_t baseAddress )
```

Finishes multi-byte reception at the Master end.

This function is used by the Master module to initiate completion of a multi-byte reception. This function receives the current byte and initiates the STOP from master to slave.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTP** of **UCBxCTLW0** register.

**Returns**

Received byte at Master end.

**EUSCI\_B\_I2C\_masterReceiveMultiByteFinishWithTimeout()**

```
bool EUSCI_B_I2C_masterReceiveMultiByteFinishWithTimeout (
    uint16_t baseAddress,
    uint8_t * txData,
    uint32_t timeout )
```

Finishes multi-byte reception at the Master end with timeout.

This function is used by the Master module to initiate completion of a multi-byte reception. This function receives the current byte and initiates the STOP from master to slave.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is a pointer to the location to store the received byte at master end
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits are **UCTXSTP** of **UCBxCTLW0** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the reception process

**EUSCI\_B\_I2C\_masterReceiveMultiByteNext()**

```
uint8_t EUSCI_B_I2C_masterReceiveMultiByteNext (
    uint16_t baseAddress )
```

Starts multi-byte reception at the Master end one byte at a time.

This function is used by the Master module to receive each byte of a multi-byte reception. This function reads currently received byte.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

**Returns**

Received byte at Master end.

**EUSCI\_B\_I2C\_masterReceiveMultiByteStop()**

```
void EUSCI_B_I2C_masterReceiveMultiByteStop (
    uint16_t baseAddress )
```

Sends the STOP at the end of a multi-byte reception at the Master end.  
This function is used by the Master module to initiate STOP

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTP** of **UCBxCTLW0** register.

#### Returns

None

### EUSCI\_B\_I2C\_masterReceiveSingle()

```
uint8_t EUSCI_B_I2C_masterReceiveSingle (
    uint16_t baseAddress )
```

receives a byte that has been sent to the I2C Master Module.

This function reads a byte of data from the I2C receive data Register.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

#### Returns

Returns the byte received from by the I2C module, cast as an `uint8_t`.

### EUSCI\_B\_I2C\_masterReceiveSingleByte()

```
uint8_t EUSCI_B_I2C_masterReceiveSingleByte (
    uint16_t baseAddress )
```

Does single byte reception from Slave.

This function is used by the Master module to receive a single byte. This function sends start and stop, waits for data reception and then receives the data from the slave

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits of **UCBxTXBUF** register, bits of **UCBxCTLW0** register, bits of **UCBxIE** register and bits of **UCBxIFG** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

**EUSCI\_B\_I2C\_masterReceiveStart()**

```
void EUSCI_B_I2C_masterReceiveStart (
    uint16_t baseAddress )
```

Starts reception at the Master end.

This function is used by the Master module initiate reception of a single byte. This function sends a start.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTT** of **UCBxCTLW0** register.

**Returns**

None

**EUSCI\_B\_I2C\_masterSendMultiByteFinish()**

```
void EUSCI_B_I2C_masterSendMultiByteFinish (
    uint16_t baseAddress,
    uint8_t txData )
```

Finishes multi-byte transmission from Master to Slave.

This function is used by the Master module to send the last byte and STOP. This function transmits the last data byte of a multi-byte transmission to the slave and then sends a stop.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the last data byte to be transmitted in a multi-byte transmission

Modified bits of **UCBxTXBUF** register and bits of **UCBxCTLW0** register.

**Returns**

None

**EUSCI\_B\_I2C\_masterSendMultiByteFinishWithTimeout()**

```
bool EUSCI_B_I2C_masterSendMultiByteFinishWithTimeout (
    uint16_t baseAddress,
    uint8_t txData,
```

```
uint32_t timeout )
```

Finishes multi-byte transmission from Master to Slave with timeout.

This function is used by the Master module to send the last byte and STOP. This function transmits the last data byte of a multi-byte transmission to the slave and then sends a stop.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the last data byte to be transmitted in a multi-byte transmission
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits of **UCBxTXBUF** register and bits of **UCBxCTLW0** register.

#### Returns

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

### EUSCI\_B\_I2C\_masterSendMultiByteNext()

```
void EUSCI_B_I2C_masterSendMultiByteNext (
    uint16_t baseAddress,
    uint8_t txData )
```

Continues multi-byte transmission from Master to Slave.

This function is used by the Master module continue each byte of a multi- byte transmission. This function transmits each data byte of a multi-byte transmission to the slave.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the next data byte to be transmitted

Modified bits of **UCBxTXBUF** register.

#### Returns

None

### EUSCI\_B\_I2C\_masterSendMultiByteNextWithTimeout()

```
bool EUSCI_B_I2C_masterSendMultiByteNextWithTimeout (
    uint16_t baseAddress,
    uint8_t txData,
    uint32_t timeout )
```

Continues multi-byte transmission from Master to Slave with timeout.

This function is used by the Master module continue each byte of a multi- byte transmission. This function transmits each data byte of a multi-byte transmission to the slave.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the next data byte to be transmitted
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits of **UCBxTXBUF** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

**EUSCI\_B\_I2C\_masterSendMultiByteStart()**

```
void EUSCI_B_I2C_masterSendMultiByteStart (
    uint16_t baseAddress,
    uint8_t txData )
```

Starts multi-byte transmission from Master to Slave.

This function is used by the master module to start a multi byte transaction.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the first data byte to be transmitted

Modified bits of **UCBxTXBUF** register, bits of **UCBxCTLW0** register, bits of **UCBxIE** register and bits of **UCBxIFG** register.

**Returns**

None

**EUSCI\_B\_I2C\_masterSendMultiByteStartWithTimeout()**

```
bool EUSCI_B_I2C_masterSendMultiByteStartWithTimeout (
    uint16_t baseAddress,
    uint8_t txData,
    uint32_t timeout )
```

Starts multi-byte transmission from Master to Slave with timeout.

This function is used by the master module to start a multi byte transaction.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the first data byte to be transmitted
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits of **UCBxTXBUF** register, bits of **UCBxCTLW0** register, bits of **UCBxIE** register and bits of **UCBxIFG** register.

#### Returns

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

### EUSCI\_B\_I2C\_masterSendMultiByteStop()

```
void EUSCI_B_I2C_masterSendMultiByteStop (
    uint16_t baseAddress )
```

Send STOP byte at the end of a multi-byte transmission from Master to Slave.

This function is used by the Master module send STOP at the end of a multi- byte transmission. This function sends a stop after current transmission is complete.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTP** of **UCBxCTLW0** register.

#### Returns

None

### EUSCI\_B\_I2C\_masterSendMultiByteStopWithTimeout()

```
bool EUSCI_B_I2C_masterSendMultiByteStopWithTimeout (
    uint16_t baseAddress,
    uint32_t timeout )
```

Send STOP byte at the end of a multi-byte transmission from Master to Slave with timeout.

This function is used by the Master module send STOP at the end of a multi- byte transmission. This function sends a stop after current transmission is complete.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits are **UCTXSTP** of **UCBxCTLW0** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

**EUSCI\_B\_I2C\_masterSendSingleByte()**

```
void EUSCI_B_I2C_masterSendSingleByte (
    uint16_t baseAddress,
    uint8_t txData )
```

Does single byte transmission from Master to Slave.

This function is used by the Master module to send a single byte. This function sends a start, then transmits the byte to the slave and then sends a stop.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the data byte to be transmitted

Modified bits of **UCBxTXBUF** register, bits of **UCBxCTLW0** register, bits of **UCBxIE** register and bits of **UCBxIFG** register.

**Returns**

None

**EUSCI\_B\_I2C\_masterSendSingleByteWithTimeout()**

```
bool EUSCI_B_I2C_masterSendSingleByteWithTimeout (
    uint16_t baseAddress,
    uint8_t txData,
    uint32_t timeout )
```

Does single byte transmission from Master to Slave with timeout.

This function is used by the Master module to send a single byte. This function sends a start, then transmits the byte to the slave and then sends a stop.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the data byte to be transmitted
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits of **UCBxTXBUF** register, bits of **UCBxCTLW0** register, bits of **UCBxIE** register and bits of **UCBxIFG** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.



**EUSCI\_B\_I2C\_masterSendStart()**

```
void EUSCI_B_I2C_masterSendStart (
    uint16_t baseAddress )
```

This function is used by the Master module to initiate START.

This function is used by the Master module to initiate START

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTT** of **UCBxCTLW0** register.

**Returns**

None

**EUSCI\_B\_I2C\_setMode()**

```
void EUSCI_B_I2C_setMode (
    uint16_t baseAddress,
    uint8_t mode )
```

Sets the mode of the I2C device.

When the mode parameter is set to **EUSCI\_B\_I2C\_TRANSMIT\_MODE**, the address will indicate that the I2C module is in send mode; otherwise, the I2C module is in receive mode.

**Parameters**

<i>baseAddress</i>	is the base address of the USC I2C module.
<i>mode</i>	Mode for the EUSCI_B_I2C module Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_I2C_TRANSMIT_MODE</b> [Default]</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_MODE</b></li> </ul>

Modified bits are **UCTR** of **UCBxCTLW0** register.

**Returns**

None

**EUSCI\_B\_I2C\_setSlaveAddress()**

```
void EUSCI_B_I2C_setSlaveAddress (
    uint16_t baseAddress,
    uint8_t slaveAddress )
```

Sets the address that the I2C Master will place on the bus.

This function will set the address that the I2C Master will place on the bus when initiating a transaction.

#### Parameters

<i>baseAddress</i>	is the base address of the USCI I2C module.
<i>slaveAddress</i>	7-bit slave address

Modified bits of **UCBxI2CSA** register.

#### Returns

None

### EUSCI\_B\_I2C\_setTimeout()

```
void EUSCI_B_I2C_setTimeout (
    uint16_t baseAddress,
    uint16_t timeout )
```

Enforces a timeout if the I2C clock is held low longer than a defined time.

By using this function, the UCCLTOIFG interrupt will trigger if the clock is held low longer than this defined time. It is possible to detect the situation, when a clock is stretched by a master or slave for too long. The user can then handle this issue by, for example, resetting the eUSCI\_B module. It is possible to select one of three predefined times for the clock low timeout.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C module.
<i>timeout</i>	how long the clock can be low before a timeout triggers. Enables generation of the UCCLTOIFG interrupt. Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_I2C_TIMEOUT_DISABLE</b> [Default]</li> <li>■ <b>EUSCI_B_I2C_TIMEOUT_28_MS</b></li> <li>■ <b>EUSCI_B_I2C_TIMEOUT_31_MS</b></li> <li>■ <b>EUSCI_B_I2C_TIMEOUT_34_MS</b></li> </ul>

Modified bits are **UCCLTO** of **UCBxCTLW1** register; bits **UCSWRST** of **UCBxCTLW0** register.

#### Returns

None

### EUSCI\_B\_I2C\_slaveGetData()

```
uint8_t EUSCI_B_I2C_slaveGetData (
    uint16_t baseAddress )
```

Receives a byte that has been sent to the I2C Module.

This function reads a byte of data from the I2C receive data Register.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Slave module.
--------------------	--

#### Returns

Returns the byte received from by the I2C module, cast as an `uint8_t`.

### EUSCI\_B\_I2C\_slavePutData()

```
void EUSCI_B_I2C_slavePutData (
    uint16_t baseAddress,
    uint8_t transmitData )
```

Transmits a byte from the I2C Module.

This function will place the supplied data into I2C transmit data register to start transmission.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Slave module.
<i>transmitData</i>	data to be transmitted from the I2C module

Modified bits of **UCBxTXBUF** register.

#### Returns

None

## 19.5 Programming Example

The following example shows how to use the I2C API to send data as a master.

```
//Initialize Slave
EUSCI_B_I2C_initSlaveParam param = {0};
param.slaveAddress = 0x48;
param.slaveAddressOffset = EUSCI_B_I2C.OWN_ADDRESS_OFFSET0;
param.slaveOwnAddressEnable = EUSCI_B_I2C.OWN_ADDRESS_ENABLE;
EUSCI_B_I2C_initSlave(EUSCI_B0_BASE, &param);

EUSCI_B_I2C_enable(EUSCI_B0_BASE);

EUSCI_B_I2C_enableInterrupt(EUSCI_B0_BASE,
    EUSCI_B_I2C_TRANSMIT_INTERRUPT0 +
    EUSCI_B_I2C_STOP_INTERRUPT);
```

## 20 FlashCtl - Flash Memory Controller

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### 20.1 Introduction

The flash memory is byte, word, and long-word addressable and programmable. The flash memory module has an integrated controller that controls programming and erase operations. The flash main memory is partitioned into 512-byte segments. Single bits, bytes, or words can be written to flash memory, but a segment is the smallest size of the flash memory that can be erased. The flash memory is partitioned into main and information memory sections. There is no difference in the operation of the main and information memory sections. Code and data can be located in either section. The difference between the sections is the segment size. There are four information memory segments, A through D. Each information memory segment contains 128 bytes and can be erased individually. The bootstrap loader (BSL) memory consists of four segments, A through D. Each BSL memory segment contains 512 bytes and can be erased individually. The main memory segment size is 512 byte. See the device-specific data sheet for the start and end addresses of each bank, when available, and for the complete memory map of a device. This library provides the API for flash segment erase, flash writes and flash operation status check.

### 20.2 API Functions

#### Functions

- void [FlashCtl\\_eraseSegment](#) (uint8\_t \*flash\_ptr)  
*Erase a single segment of the flash memory.*
- void [FlashCtl\\_eraseBank](#) (uint8\_t \*flash\_ptr)  
*Erase a single bank of the flash memory.*
- void [FlashCtl\\_performMassErase](#) (uint8\_t \*flash\_ptr)  
*Erase all flash memory.*
- bool [FlashCtl\\_performEraseCheck](#) (uint8\_t \*flash\_ptr, uint16\_t numberOfBytes)  
*Erase check of the flash memory.*
- void [FlashCtl\\_write8](#) (uint8\_t \*data\_ptr, uint8\_t \*flash\_ptr, uint16\_t count)  
*Write data into the flash memory in byte format, pass by reference.*
- void [FlashCtl\\_write16](#) (uint16\_t \*data\_ptr, uint16\_t \*flash\_ptr, uint16\_t count)  
*Write data into the flash memory in 16-bit word format, pass by reference.*
- void [FlashCtl\\_write32](#) (uint32\_t \*data\_ptr, uint32\_t \*flash\_ptr, uint16\_t count)  
*Write data into the flash memory in 32-bit word format, pass by reference.*
- void [FlashCtl\\_fillMemory32](#) (uint32\_t value, uint32\_t \*flash\_ptr, uint16\_t count)  
*Write data into the flash memory in 32-bit word format, pass by value.*
- uint8\_t [FlashCtl\\_getStatus](#) (uint8\_t mask)  
*Check FlashCtl status to see if it is currently busy erasing or programming.*
- void [FlashCtl\\_lockInfoA](#) (void)  
*Locks the information flash memory segment A.*

- void `FlashCtl_unlockInfoA` (void)  
*Unlocks the information flash memory segment A.*

## 20.2.1 Detailed Description

`FlashCtl_eraseSegment` helps erase a single segment of the flash memory. A pointer to the flash segment being erased is passed on to this function.

`FlashCtl_performEraseCheck` helps check if a specific number of bytes in flash are currently erased. A pointer to the starting location of the erase check and the number of bytes to be checked is passed into this function.

Depending on the kind of writes being performed to the flash, this library provides APIs for flash writes.

`FlashCtl_write8` facilitates writing into the flash memory in byte format. `FlashCtl_write16` facilitates writing into the flash memory in word format. `FlashCtl_write32` facilitates writing into the flash memory in long format, pass by reference. `FlashCtl_fillMemory32` facilitates writing into the flash memory in long format, pass by value. `FlashCtl_getStatus` checks if the flash is currently busy erasing or programming. `FlashCtl_lockInfoA` locks segment A of information memory. `FlashCtl_unlockInfoA` unlocks segment A of information memory.

The Flash API is broken into 4 groups of functions: those that deal with flash erase, those that write into flash, those that give status of flash, and those that lock/unlock segment A of information memory.

The flash erase operations are managed by

- `FlashCtl_eraseSegment()`
- `FlashCtl_eraseBank()`

Flash writes are managed by

- `FlashCtl_write8()`
- `FlashCtl_write16()`
- `FlashCtl_write32()`
- `FlashCtl_fillMemory32()`

The status is given by

- `FlashCtl_getStatus()`
- `FlashCtl_performEraseCheck()`

The segment A of information memory lock/unlock operations are managed by

- `FlashCtl_lockInfoA()`
- `FlashCtl_unlockInfoA()`

## 20.2.2 Function Documentation

### `FlashCtl_eraseBank()`

```
void FlashCtl_eraseBank (
```

```
uint8_t * flash_ptr )
```

Erase a single bank of the flash memory.

This function erases a single bank of the flash memory. This API will erase the entire flash if device contains only one flash bank.

#### Parameters

<i>flash_ptr</i>	is a pointer into the bank to be erased
------------------	---

#### Returns

None

### FlashCtl\_eraseSegment()

```
void FlashCtl_eraseSegment (
    uint8_t * flash_ptr )
```

Erase a single segment of the flash memory.

For devices like MSP430i204x, if the specified segment is the information flash segment, the FLASH\_unlockInfo API must be called prior to calling this API.

#### Parameters

<i>flash_ptr</i>	is the pointer into the flash segment to be erased
------------------	--

#### Returns

None

### FlashCtl\_fillMemory32()

```
void FlashCtl_fillMemory32 (
    uint32_t value,
    uint32_t * flash_ptr,
    uint16_t count )
```

Write data into the flash memory in 32-bit word format, pass by value.

This function writes a 32-bit data value into flash memory, count times. Assumes the flash memory is already erased and unlocked. FlashCtl\_eraseSegment can be used to erase a segment.

#### Parameters

<i>value</i>	value to fill memory with
<i>flash_ptr</i>	is the pointer into which to write the data
<i>count</i>	number of times to write the value

**Returns**

None

**FlashCtl\_getStatus()**

```
uint8_t FlashCtl_getStatus (
    uint8_t mask )
```

Check FlashCtl status to see if it is currently busy erasing or programming.

This function checks the status register to determine if the flash memory is ready for writing.

**Parameters**

<i>mask</i>	<p>FLASHCTL status to read Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>FLASHCTL_READY_FOR_NEXT_WRITE</b></li> <li>■ <b>FLASHCTL_ACCESS_VIOLATION_INTERRUPT_FLAG</b></li> <li>■ <b>FLASHCTL_PASSWORD_WRITTEN_INCORRECTLY</b></li> <li>■ <b>FLASHCTL_BUSY</b></li> </ul>
-------------	--

**Returns**

Logical OR of any of the following:

- **FLASHCTL\_READY\_FOR\_NEXT\_WRITE**
- **FLASHCTL\_ACCESS\_VIOLATION\_INTERRUPT\_FLAG**
- **FLASHCTL\_PASSWORD\_WRITTEN\_INCORRECTLY**
- **FLASHCTL\_BUSY**

indicating the status of the FlashCtl

**FlashCtl\_lockInfoA()**

```
void FlashCtl_lockInfoA (
    void )
```

Locks the information flash memory segment A.

This function is typically called after an erase or write operation on the information flash segment is performed by any of the other API functions in order to re-lock the information flash segment.

**Returns**

None

**FlashCtl\_performEraseCheck()**

```
bool FlashCtl_performEraseCheck (
    uint8_t * flash_ptr,
```

```
uint16_t numberOfBytes )
```

Erase check of the flash memory.

This function checks bytes in flash memory to make sure that they are in an erased state (are set to 0xFF).

#### Parameters

<i>flash_ptr</i>	is the pointer to the starting location of the erase check
<i>numberOfBytes</i>	is the number of bytes to be checked

#### Returns

STATUS\_SUCCESS or STATUS\_FAIL

### FlashCtl\_performMassErase()

```
void FlashCtl_performMassErase (
    uint8_t * flash_ptr )
```

Erase all flash memory.

This function erases all the flash memory banks. For devices like MSP430i204x, this API erases main memory and information flash memory if the FLASH\_unlockInfo API was previously executed (otherwise the information flash is not erased). Also note that erasing information flash memory in the MSP430i204x impacts the TLV calibration constants located at the information memory.

#### Parameters

<i>flash_ptr</i>	is a pointer into the bank to be erased
------------------	---

#### Returns

None

### FlashCtl\_unlockInfoA()

```
void FlashCtl_unlockInfoA (
    void )
```

Unlocks the information flash memory segment A.

This function must be called before an erase or write operation on the information flash segment is performed by any of the other API functions.

#### Returns

None



**FlashCtl\_write16()**

```
void FlashCtl_write16 (
    uint16_t * data_ptr,
    uint16_t * flash_ptr,
    uint16_t count )
```

Write data into the flash memory in 16-bit word format, pass by reference.

This function writes a 16-bit word array of size count into flash memory. Assumes the flash memory is already erased and unlocked. FlashCtl\_eraseSegment can be used to erase a segment.

**Parameters**

<i>data_ptr</i>	is the pointer to the data to be written
<i>flash_ptr</i>	is the pointer into which to write the data
<i>count</i>	number of times to write the value

**Returns**

None

**FlashCtl\_write32()**

```
void FlashCtl_write32 (
    uint32_t * data_ptr,
    uint32_t * flash_ptr,
    uint16_t count )
```

Write data into the flash memory in 32-bit word format, pass by reference.

This function writes a 32-bit array of size count into flash memory. Assumes the flash memory is already erased and unlocked. FlashCtl\_eraseSegment can be used to erase a segment.

**Parameters**

<i>data_ptr</i>	is the pointer to the data to be written
<i>flash_ptr</i>	is the pointer into which to write the data
<i>count</i>	number of times to write the value

**Returns**

None

**FlashCtl\_write8()**

```
void FlashCtl_write8 (
    uint8_t * data_ptr,
    uint8_t * flash_ptr,
    uint16_t count )
```

Write data into the flash memory in byte format, pass by reference.

This function writes a byte array of size count into flash memory. Assumes the flash memory is already erased and unlocked. FlashCtl\_eraseSegment can be used to erase a segment.

#### Parameters

<i>data_ptr</i>	is the pointer to the data to be written
<i>flash_ptr</i>	is the pointer into which to write the data
<i>count</i>	number of times to write the value

#### Returns

None

## 20.3 Programming Example

The following example shows some flash operations using the APIs

```
do{
    FlashCtl_eraseSegment(FlashCtl_BASE,
        (unsigned char *)INFOD_START
    );
    status = FlashCtl_performEraseCheck(FlashCtl_BASE,
        (unsigned char *)INFOD_START,
        128
    );
}while(status == STATUS_FAIL);

//Flash write
FlashCtl_write32(FlashCtl_BASE,
    calibration_data,
    (unsigned long *) (INFOD_START), 1);
```

# 21 GPIO

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## 21.1 Introduction

The Digital I/O (GPIO) API provides a set of functions for using the MSP430Ware GPIO modules. Functions are provided to setup and enable use of input/output pins, setting them up with or without interrupts and those that access the pin value. The digital I/O features include:

- Independently programmable individual I/Os
- Any combination of input or output
- Individually configurable P1 and P2 interrupts. Some devices may include additional port interrupts.
- Independent input and output data registers
- Individually configurable pullup or pulldown resistors

Devices within the family may have up to twelve digital I/O ports implemented (P1 to P11 and PJ). Most ports contain eight I/O lines; however, some ports may contain less (see the device-specific data sheet for ports available). Each I/O line is individually configurable for input or output direction, and each can be individually read or written. Each I/O line is individually configurable for pullup or pulldown resistors, as well as, configurable drive strength, full or reduced. PJ contains only four I/O lines.

Ports P1 and P2 always have interrupt capability. Each interrupt for the P1 and P2 I/O lines can be individually enabled and configured to provide an interrupt on a rising or falling edge of an input signal. All P1 I/O lines source a single interrupt vector P1IV, and all P2 I/O lines source a different, single interrupt vector P2IV. On some devices, additional ports with interrupt capability may be available (see the device-specific data sheet for details) and contain their own respective interrupt vectors. Individual ports can be accessed as byte-wide ports or can be combined into word-wide ports and accessed via word formats. Port pairs P1/P2, P3/P4, P5/P6, P7/P8, etc., are associated with the names PA, PB, PC, PD, etc., respectively. All port registers are handled in this manner with this naming convention except for the interrupt vector registers, P1IV and P2IV; that is, PAIV does not exist. When writing to port PA with word operations, all 16 bits are written to the port. When writing to the lower byte of the PA port using byte operations, the upper byte remains unchanged. Similarly, writing to the upper byte of the PA port using byte instructions leaves the lower byte unchanged. When writing to a port that contains less than the maximum number of bits possible, the unused bits are a "don't care". Ports PB, PC, PD, PE, and PF behave similarly.

Reading of the PA port using word operations causes all 16 bits to be transferred to the destination. Reading the lower or upper byte of the PA port (P1 or P2) and storing to memory using byte operations causes only the lower or upper byte to be transferred to the destination, respectively. Reading of the PA port and storing to a general-purpose register using byte operations causes the byte transferred to be written to the least significant byte of the register. The upper significant byte of the destination register is cleared automatically. Ports PB, PC, PD, PE, and PF behave similarly. When reading from ports that contain less than the maximum bits possible, unused bits are read as zeros (similarly for port PJ).

The GPIO pin may be configured as an I/O pin with `GPIO_setAsOutputPin()`, `GPIO_setAsInputPin()`, `GPIO_setAsInputPinWithPullDownResistor()` or `GPIO_setAsInputPinWithPullUpResistor()`. The GPIO pin may instead be configured to operate in the Peripheral Module assigned function by configuring the GPIO using `GPIO_setAsPeripheralModuleFunctionOutputPin()` or `GPIO_setAsPeripheralModuleFunctionInputPin()`.

## 21.2 API Functions

### Functions

- void `GPIO_setAsOutputPin` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function configures the selected Pin as output pin.*
- void `GPIO_setAsInputPin` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function configures the selected Pin as input pin.*
- void `GPIO_setAsPeripheralModuleFunctionOutputPin` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function configures the peripheral module function in the output direction for the selected pin.*
- void `GPIO_setAsPeripheralModuleFunctionInputPin` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function configures the peripheral module function in the input direction for the selected pin.*
- void `GPIO_setOutputHighOnPin` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function sets output HIGH on the selected Pin.*
- void `GPIO_setOutputLowOnPin` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function sets output LOW on the selected Pin.*
- void `GPIO_toggleOutputOnPin` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function toggles the output on the selected Pin.*
- void `GPIO_setAsInputPinWithPullDownResistor` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function sets the selected Pin in input Mode with Pull Down resistor.*
- void `GPIO_setAsInputPinWithPullUpResistor` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function sets the selected Pin in input Mode with Pull Up resistor.*
- uint8\_t `GPIO_getInputPinValue` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function gets the input value on the selected pin.*
- void `GPIO_enableInterrupt` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function enables the port interrupt on the selected pin.*
- void `GPIO_disableInterrupt` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function disables the port interrupt on the selected pin.*
- uint16\_t `GPIO_getInterruptStatus` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function gets the interrupt status of the selected pin.*
- void `GPIO_clearInterrupt` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function clears the interrupt flag on the selected pin.*
- void `GPIO_selectInterruptEdge` (uint8\_t selectedPort, uint16\_t selectedPins, uint8\_t edgeSelect)  
*This function selects on what edge the port interrupt flag should be set for a transition.*
- void `GPIO_setDriveStrength` (uint8\_t selectedPort, uint16\_t selectedPins, uint8\_t driveStrength)  
*This function sets the drive strength for the selected port pin.*

## 21.2.1 Detailed Description

The GPIO API is broken into three groups of functions: those that deal with configuring the GPIO pins, those that deal with interrupts, and those that access the pin value.

The GPIO pins are configured with

- `GPIO_setAsOutputPin()`
- `GPIO_setAsInputPin()`
- `GPIO_setAsInputPinWithPullDownResistor()`
- `GPIO_setAsInputPinWithPullUpResistor()`
- `GPIO_setDriveStrength()`
- `GPIO_setAsPeripheralModuleFunctionOutputPin()`
- `GPIO_setAsPeripheralModuleFunctionInputPin()`

The GPIO interrupts are handled with

- `GPIO_enableInterrupt()`
- `GPIO_disableInterrupt()`
- `GPIO_clearInterrupt()`
- `GPIO_getInterruptStatus()`
- `GPIO_selectInterruptEdge()`

The GPIO pin state is accessed with

- `GPIO_setOutputHighOnPin()`
- `GPIO_setOutputLowOnPin()`
- `GPIO_toggleOutputOnPin()`
- `GPIO_getInputPinValue()`

## 21.2.2 Function Documentation

### GPIO\_clearInterrupt()

```
void GPIO_clearInterrupt (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function clears the interrupt flag on the selected pin.

This function clears the interrupt flag on the selected pin. Please refer to family user's guide for available ports with interrupt capability.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
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## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
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Modified bits of **PxIFG** register.

## Returns

None

## GPIO\_disableInterrupt()

```
void GPIO_disableInterrupt (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function disables the port interrupt on the selected pin.

This function disables the port interrupt on the selected pin. Please refer to family user's guide for available ports with interrupt capability.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
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## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
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Modified bits of **PxIE** register.

## Returns

None

## GPIO\_enableInterrupt()

```
void GPIO_enableInterrupt (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function enables the port interrupt on the selected pin.

This function enables the port interrupt on the selected pin. Please refer to family user's guide for available ports with interrupt capability.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
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## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
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Modified bits of **PxIE** register.

## Returns

None

## GPIO\_getInputPinValue()

```
uint8_t GPIO_getInputPinValue (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function gets the input value on the selected pin.

This function gets the input value on the selected pin.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
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## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>GPIO_PIN0</b></li> <li>■ <b>GPIO_PIN1</b></li> <li>■ <b>GPIO_PIN2</b></li> <li>■ <b>GPIO_PIN3</b></li> <li>■ <b>GPIO_PIN4</b></li> <li>■ <b>GPIO_PIN5</b></li> <li>■ <b>GPIO_PIN6</b></li> <li>■ <b>GPIO_PIN7</b></li> <li>■ <b>GPIO_PIN8</b></li> <li>■ <b>GPIO_PIN9</b></li> <li>■ <b>GPIO_PIN10</b></li> <li>■ <b>GPIO_PIN11</b></li> <li>■ <b>GPIO_PIN12</b></li> <li>■ <b>GPIO_PIN13</b></li> <li>■ <b>GPIO_PIN14</b></li> <li>■ <b>GPIO_PIN15</b></li> <li>■ <b>GPIO_PIN_ALL8</b></li> <li>■ <b>GPIO_PIN_ALL16</b></li> </ul>
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## Returns

One of the following:

- **GPIO\_INPUT\_PIN\_HIGH**
  - **GPIO\_INPUT\_PIN\_LOW**
- indicating the status of the pin

## GPIO\_getInterruptStatus()

```
uint16_t GPIO_getInterruptStatus (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function gets the interrupt status of the selected pin.

This function gets the interrupt status of the selected pin. Please refer to family user's guide for available ports with interrupt capability.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
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## Parameters

<i>selectedPins</i>	is the specified pin in the selected port. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"><li>■ GPIO_PIN0</li><li>■ GPIO_PIN1</li><li>■ GPIO_PIN2</li><li>■ GPIO_PIN3</li><li>■ GPIO_PIN4</li><li>■ GPIO_PIN5</li><li>■ GPIO_PIN6</li><li>■ GPIO_PIN7</li><li>■ GPIO_PIN8</li><li>■ GPIO_PIN9</li><li>■ GPIO_PIN10</li><li>■ GPIO_PIN11</li><li>■ GPIO_PIN12</li><li>■ GPIO_PIN13</li><li>■ GPIO_PIN14</li><li>■ GPIO_PIN15</li><li>■ GPIO_PIN_ALL8</li><li>■ GPIO_PIN_ALL16</li></ul>
---------------------	---

## Returns

Logical OR of any of the following:

- GPIO\_PIN0
- GPIO\_PIN1
- GPIO\_PIN2
- GPIO\_PIN3
- GPIO\_PIN4
- GPIO\_PIN5
- GPIO\_PIN6
- GPIO\_PIN7
- GPIO\_PIN8
- GPIO\_PIN9
- GPIO\_PIN10
- GPIO\_PIN11
- GPIO\_PIN12
- GPIO\_PIN13
- GPIO\_PIN14
- GPIO\_PIN15
- GPIO\_PIN\_ALL8

- **GPIO\_PIN\_ALL16**

indicating the interrupt status of the selected pins [Default: 0]

### GPIO\_selectInterruptEdge()

```
void GPIO_selectInterruptEdge (
    uint8_t selectedPort,
    uint16_t selectedPins,
    uint8_t edgeSelect )
```

This function selects on what edge the port interrupt flag should be set for a transition.

This function selects on what edge the port interrupt flag should be set for a transition. Values for edgeSelect should be GPIO\_LOW\_TO\_HIGH\_TRANSITION or GPIO\_HIGH\_TO\_LOW\_TRANSITION. Please refer to family user's guide for available ports with interrupt capability.

#### Parameters

<i>selectedPort</i>	<p>is the selected port. Valid values are:</p> <ul style="list-style-type: none"> <li>■ GPIO_PORT_P1</li> <li>■ GPIO_PORT_P2</li> <li>■ GPIO_PORT_P3</li> <li>■ GPIO_PORT_P4</li> <li>■ GPIO_PORT_P5</li> <li>■ GPIO_PORT_P6</li> <li>■ GPIO_PORT_P7</li> <li>■ GPIO_PORT_P8</li> <li>■ GPIO_PORT_P9</li> <li>■ GPIO_PORT_P10</li> <li>■ GPIO_PORT_P11</li> <li>■ GPIO_PORT_PA</li> <li>■ GPIO_PORT_PB</li> <li>■ GPIO_PORT_PC</li> <li>■ GPIO_PORT_PD</li> <li>■ GPIO_PORT_PE</li> <li>■ GPIO_PORT_PF</li> <li>■ GPIO_PORT_PJ</li> </ul>
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## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>GPIO_PIN0</b></li> <li>■ <b>GPIO_PIN1</b></li> <li>■ <b>GPIO_PIN2</b></li> <li>■ <b>GPIO_PIN3</b></li> <li>■ <b>GPIO_PIN4</b></li> <li>■ <b>GPIO_PIN5</b></li> <li>■ <b>GPIO_PIN6</b></li> <li>■ <b>GPIO_PIN7</b></li> <li>■ <b>GPIO_PIN8</b></li> <li>■ <b>GPIO_PIN9</b></li> <li>■ <b>GPIO_PIN10</b></li> <li>■ <b>GPIO_PIN11</b></li> <li>■ <b>GPIO_PIN12</b></li> <li>■ <b>GPIO_PIN13</b></li> <li>■ <b>GPIO_PIN14</b></li> <li>■ <b>GPIO_PIN15</b></li> <li>■ <b>GPIO_PIN_ALL8</b></li> <li>■ <b>GPIO_PIN_ALL16</b></li> </ul>
<i>edgeSelect</i>	<p>specifies what transition sets the interrupt flag Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>GPIO_HIGH_TO_LOW_TRANSITION</b></li> <li>■ <b>GPIO_LOW_TO_HIGH_TRANSITION</b></li> </ul>

Modified bits of **PxIES** register.

## Returns

None

## GPIO\_setAsInputPin()

```
void GPIO_setAsInputPin (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function configures the selected Pin as input pin.

This function selected pins on a selected port as input pins.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
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## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
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Modified bits of **PxDIR** register, bits of **PxREN** register and bits of **PxSEL** register.

## Returns

None

## GPIO\_setAsInputPinWithPullDownResistor()

```
void GPIO_setAsInputPinWithPullDownResistor (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function sets the selected Pin in input Mode with Pull Down resistor.

This function sets the selected Pin in input Mode with Pull Down resistor.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
---------------------	---

## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>GPIO_PIN0</b></li> <li>■ <b>GPIO_PIN1</b></li> <li>■ <b>GPIO_PIN2</b></li> <li>■ <b>GPIO_PIN3</b></li> <li>■ <b>GPIO_PIN4</b></li> <li>■ <b>GPIO_PIN5</b></li> <li>■ <b>GPIO_PIN6</b></li> <li>■ <b>GPIO_PIN7</b></li> <li>■ <b>GPIO_PIN8</b></li> <li>■ <b>GPIO_PIN9</b></li> <li>■ <b>GPIO_PIN10</b></li> <li>■ <b>GPIO_PIN11</b></li> <li>■ <b>GPIO_PIN12</b></li> <li>■ <b>GPIO_PIN13</b></li> <li>■ <b>GPIO_PIN14</b></li> <li>■ <b>GPIO_PIN15</b></li> <li>■ <b>GPIO_PIN_ALL8</b></li> <li>■ <b>GPIO_PIN_ALL16</b></li> </ul>
---------------------	---

Modified bits of **PxDIR** register, bits of **PxOUT** register and bits of **PxREN** register.

## Returns

None

## GPIO\_setAsInputPinWithPullUpResistor()

```
void GPIO_setAsInputPinWithPullUpResistor (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function sets the selected Pin in input Mode with Pull Up resistor.

This function sets the selected Pin in input Mode with Pull Up resistor.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
---------------------	---

## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>GPIO_PIN0</b></li> <li>■ <b>GPIO_PIN1</b></li> <li>■ <b>GPIO_PIN2</b></li> <li>■ <b>GPIO_PIN3</b></li> <li>■ <b>GPIO_PIN4</b></li> <li>■ <b>GPIO_PIN5</b></li> <li>■ <b>GPIO_PIN6</b></li> <li>■ <b>GPIO_PIN7</b></li> <li>■ <b>GPIO_PIN8</b></li> <li>■ <b>GPIO_PIN9</b></li> <li>■ <b>GPIO_PIN10</b></li> <li>■ <b>GPIO_PIN11</b></li> <li>■ <b>GPIO_PIN12</b></li> <li>■ <b>GPIO_PIN13</b></li> <li>■ <b>GPIO_PIN14</b></li> <li>■ <b>GPIO_PIN15</b></li> <li>■ <b>GPIO_PIN_ALL8</b></li> <li>■ <b>GPIO_PIN_ALL16</b></li> </ul>
---------------------	---

Modified bits of **PxDIR** register, bits of **PxOUT** register and bits of **PxREN** register.

## Returns

None

## GPIO\_setAsOutputPin()

```
void GPIO_setAsOutputPin (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function configures the selected Pin as output pin.

This function selected pins on a selected port as output pins.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
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## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>GPIO_PIN0</b></li> <li>■ <b>GPIO_PIN1</b></li> <li>■ <b>GPIO_PIN2</b></li> <li>■ <b>GPIO_PIN3</b></li> <li>■ <b>GPIO_PIN4</b></li> <li>■ <b>GPIO_PIN5</b></li> <li>■ <b>GPIO_PIN6</b></li> <li>■ <b>GPIO_PIN7</b></li> <li>■ <b>GPIO_PIN8</b></li> <li>■ <b>GPIO_PIN9</b></li> <li>■ <b>GPIO_PIN10</b></li> <li>■ <b>GPIO_PIN11</b></li> <li>■ <b>GPIO_PIN12</b></li> <li>■ <b>GPIO_PIN13</b></li> <li>■ <b>GPIO_PIN14</b></li> <li>■ <b>GPIO_PIN15</b></li> <li>■ <b>GPIO_PIN_ALL8</b></li> <li>■ <b>GPIO_PIN_ALL16</b></li> </ul>
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Modified bits of **PxDIR** register and bits of **PxSEL** register.

## Returns

None

## GPIO\_setAsPeripheralModuleFunctionInputPin()

```
void GPIO_setAsPeripheralModuleFunctionInputPin (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function configures the peripheral module function in the input direction for the selected pin.

This function configures the peripheral module function in the input direction for the selected pin for either primary, secondary or ternary module function modes. Note that MSP430F5xx/6xx family doesn't support these function modes.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
---------------------	---

## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>GPIO_PIN0</b></li> <li>■ <b>GPIO_PIN1</b></li> <li>■ <b>GPIO_PIN2</b></li> <li>■ <b>GPIO_PIN3</b></li> <li>■ <b>GPIO_PIN4</b></li> <li>■ <b>GPIO_PIN5</b></li> <li>■ <b>GPIO_PIN6</b></li> <li>■ <b>GPIO_PIN7</b></li> <li>■ <b>GPIO_PIN8</b></li> <li>■ <b>GPIO_PIN9</b></li> <li>■ <b>GPIO_PIN10</b></li> <li>■ <b>GPIO_PIN11</b></li> <li>■ <b>GPIO_PIN12</b></li> <li>■ <b>GPIO_PIN13</b></li> <li>■ <b>GPIO_PIN14</b></li> <li>■ <b>GPIO_PIN15</b></li> <li>■ <b>GPIO_PIN_ALL8</b></li> <li>■ <b>GPIO_PIN_ALL16</b></li> </ul>
---------------------	---

Modified bits of **PxDIR** register and bits of **PxSEL** register.

## Returns

None

## GPIO\_setAsPeripheralModuleFunctionOutputPin()

```
void GPIO_setAsPeripheralModuleFunctionOutputPin (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function configures the peripheral module function in the output direction for the selected pin.

This function configures the peripheral module function in the output direction for the selected pin for either primary, secondary or ternary module function modes. Note that MSP430F5xx/6xx family doesn't support these function modes.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
---------------------	---

## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
---------------------	---

Modified bits of **PxDIR** register and bits of **PxSEL** register.

## Returns

None

## GPIO\_setDriveStrength()

```
void GPIO_setDriveStrength (
    uint8_t selectedPort,
    uint16_t selectedPins,
    uint8_t driveStrength )
```

This function sets the drive strength for the selected port pin.

his function sets the drive strength for the selected port pin. Acceptable values for driveStrength are GPIO\_REDUCED\_OUTPUT\_DRIVE\_STRENGTH and GPIO\_FULL\_OUTPUT\_DRIVE\_STRENGTH.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
---------------------	---

## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
<i>driveStrength</i>	<p>specifies the drive strength of the pin Valid values are:</p> <ul style="list-style-type: none"> <li>■ GPIO_REDUCED_OUTPUT_DRIVE_STRENGTH</li> <li>■ GPIO_FULL_OUTPUT_DRIVE_STRENGTH</li> </ul>

Modified bits of **PxDS** register.

## Returns

None

## GPIO\_setOutputHighOnPin()

```
void GPIO_setOutputHighOnPin (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function sets output HIGH on the selected Pin.

This function sets output HIGH on the selected port's pin.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
---------------------	---



## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
---------------------	---

Modified bits of **PxOUT** register.

## Returns

None

## GPIO\_setOutputLowOnPin()

```
void GPIO_setOutputLowOnPin (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function sets output LOW on the selected Pin.

This function sets output LOW on the selected port's pin.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
---------------------	---

## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
---------------------	---

Modified bits of **PxOUT** register.

## Returns

None

## GPIO\_toggleOutputOnPin()

```
void GPIO_toggleOutputOnPin (
    uint8_t selectedPort,
    uint16_t selectedPins )
```

This function toggles the output on the selected Pin.

This function toggles the output on the selected port's pin.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
---------------------	---

## Parameters

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
---------------------	---

Modified bits of **PxOUT** register.

## Returns

None

## 21.3 Programming Example

The following example shows how to use the GPIO API.

```

// Set P1.0 to output direction
GPIO_setAsOutputPin(GPIO_PORT_P1,
                    GPIO_PIN0
                    );

// Set P1.4 to input direction
GPIO_setAsInputPin(GPIO_PORT_P1,
                  GPIO_PIN4
                  );

while (1)
{
    // Test P1.4
    if(GPIO_INPUT_PIN_HIGH == GPIO_getInputPinValue(
        GPIO_PORT_P1,

```

```
GPIO_PIN4
))
{
    // if P1.4 set, set P1.0
    GPIO_setOutputHighOnPin(
        GPIO_PORT_P1,
        GPIO_PIN0
    );
}
else
{
    // else reset
    GPIO_setOutputLowOnPin(
        GPIO_PORT_P1,
        GPIO_PIN0
    );
}
}
```

## 22 LCD<sub>B</sub>Controller

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### 22.1 Introduction

The LCD<sub>B</sub> Controller APIs provides a set of functions for using the LCD<sub>B</sub> module. Main functions include initialization, LCD enable/disable, charge pump config, voltage settings and memory/blinking memory writing.

LCD<sub>B</sub> only supports static/2-mux/3-mux/4-mux and no low-power waveform feature.

### 22.2 API Functions

The LCD<sub>B</sub> API is broken into four groups of functions: those that deal with the basic setup and pin config, those that handle charge pump, VLCD voltage and source, those that set memory and blinking memory, and those auxiliary functions.

The LCD<sub>B</sub> setup and pin config functions are

- LCD<sub>B</sub>.init()
- LCD<sub>B</sub>.on()
- LCD<sub>B</sub>.off()
- LCD<sub>B</sub>.setPinAsLCDFunction()
- LCD<sub>B</sub>.setPinAsPortFunction()
- LCD<sub>B</sub>.setPinAsLCDFunctionEx()

The LCD<sub>B</sub> charge pump, VLCD voltage/source functions are

- LCD<sub>B</sub>.enableChargePump()
- LCD<sub>B</sub>.disableChargePump()
- LCD<sub>B</sub>.configureChargePump()
- LCD<sub>B</sub>.selectBias()
- LCD<sub>B</sub>.selectChargePumpReference()
- LCD<sub>B</sub>.setVLCDSource()
- LCD<sub>B</sub>.setVLCDVoltage()

The LCD<sub>B</sub> memory/blinking memory setting functions are

- LCD<sub>B</sub>.clearAllMemory()
- LCD<sub>B</sub>.clearAllBlinkingMemory()
- LCD<sub>B</sub>.selectDisplayMemory()
- LCD<sub>B</sub>.setBlinkingControl()

- LCD.B.setMemory()
- LCD.B.updateMemory()
- LCD.B.toggleMemory()
- LCD.B.clearMemory()
- LCD.B.setBlinkingMemory()
- LCD.B.updateBlinkingMemory()
- LCD.B.toggleBlinkingMemory()
- LCD.B.clearBlinkingMemory()

The LCD.B auxiliary functions are

- LCD.B.clearInterrupt()
- LCD.B.getInterruptStatus()
- LCD.B.enableInterrupt()
- LCD.B.disableInterrupt()

## 22.3 Programming Example

The following example shows how to initialize a 4-mux LCD and display "09" on the LCD screen.

```
// Set pin to LCD function
LCD.B.setPinAsLCDFunctionEx(LCD.B.BASE, LCD.B.SEGMENT.LINE.0, LCD.B.SEGMENT.LINE.21);
LCD.B.setPinAsLCDFunctionEx(LCD.B.BASE, LCD.B.SEGMENT.LINE.26, LCD.B.SEGMENT.LINE.43);

LCD.B.InitParam initParams = {0};
initParams.clockSource = LCD.B.CLOCKSOURCE.ACLK;
initParams.clockDivider = LCD.B.CLOCKDIVIDER.1;
initParams.clockPrescaler = LCD.B.CLOCKPRESCALAR.16;
initParams.muxRate = LCD.B.4MUX;
initParams.waveforms = LCD.B.LOW_POWER.WAVEFORMS;
initParams.segments = LCD.B.SEGMENTS.ENABLED;

LCD.B.init(LCD.B.BASE, &initParams);

// LCD Operation - VLCD generated internally, V2-V4 generated internally, v5 to ground
LCD.B.setVLCDSource(LCD.B.BASE, LCD.B.VLCD.GENERATED.INTERNALLY,
    LCD.B.V2V3V4.GENERATED.INTERNALLY_NOT_SWITCHED_TO_PINS,
    LCD.B.V5.VSS);

// Set VLCD voltage to 2.60v
LCD.B.setVLCDVoltage(LCD.B.BASE, LCD.B.CHARGE_PUMP.VOLTAGE.2.60V_OR_2.17VREF);

// Enable charge pump and select internal reference for it
LCD.B.enableChargePump(LCD.B.BASE);
LCD.B.selectChargePumpReference(LCD.B.BASE, LCD.B.INTERNAL.REFERENCE.VOLTAGE);

LCD.B.configChargePump(LCD.B.BASE, LCD.B.SYNCHRONIZATION.ENABLED, 0);

// Clear LCD memory
LCD.B.clearMemory(LCD.B.BASE);

// Display "09"
LCD.B.setMemory(LCD.B.BASE, LCD.B.SEGMENT.LINE.8, 0xC);
LCD.B.setMemory(LCD.B.BASE, LCD.B.SEGMENT.LINE.9, 0xF);

LCD.B.setMemory(LCD.B.BASE, LCD.B.SEGMENT.LINE.12, 0x7);
LCD.B.setMemory(LCD.B.BASE, LCD.B.SEGMENT.LINE.13, 0xF);

//Turn LCD on
LCD.B.on(LCD.B.BASE);
```



## 23 LDO-PWR

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### 23.1 Introduction

The features of the LDO-PWR module include:

- Integrated 3.3-V LDO regulator with sufficient output to power the entire MSP430? microcontroller and system circuitry from 5-V external supply
- Current-limiting capability on 3.3-V LDO output with detection flag and interrupt generation
- LDO input voltage detection flag and interrupt generation

The LDO-PWR power system incorporates an integrated 3.3-V LDO regulator that allows the entire MSP430 microcontroller to be powered from nominal 5-V LDOI when it is made available from the system. Alternatively, the power system can supply power only to other components within the system, or it can be unused altogether.

### 23.2 API Functions

#### Functions

- void [LDOPWR\\_unlockConfiguration](#) (uint16\_t baseAddress)  
*Unlocks the configuration registers and enables write access.*
- void [LDOPWR\\_lockConfiguration](#) (uint16\_t baseAddress)  
*Locks the configuration registers and disables write access.*
- void [LDOPWR\\_enablePort\\_U\\_inputs](#) (uint16\_t baseAddress)  
*Enables Port U inputs.*
- void [LDOPWR\\_disablePort\\_U\\_inputs](#) (uint16\_t baseAddress)  
*Disables Port U inputs.*
- void [LDOPWR\\_enablePort\\_U\\_outputs](#) (uint16\_t baseAddress)  
*Enables Port U outputs.*
- void [LDOPWR\\_disablePort\\_U\\_outputs](#) (uint16\_t baseAddress)  
*Disables Port U outputs.*
- uint8\_t [LDOPWR\\_getPort\\_U1\\_inputData](#) (uint16\_t baseAddress)  
*Returns PU.1 input data.*
- uint8\_t [LDOPWR\\_getPort\\_U0\\_inputData](#) (uint16\_t baseAddress)  
*Returns PU.0 input data.*
- uint8\_t [LDOPWR\\_getPort\\_U1\\_outputData](#) (uint16\_t baseAddress)  
*Returns PU.1 output data.*
- uint8\_t [LDOPWR\\_getPort\\_U0\\_outputData](#) (uint16\_t baseAddress)  
*Returns PU.0 output data.*
- void [LDOPWR\\_setPort\\_U1\\_outputData](#) (uint16\_t baseAddress, uint8\_t value)  
*Sets PU.1 output data.*
- void [LDOPWR\\_setPort\\_U0\\_outputData](#) (uint16\_t baseAddress, uint8\_t value)

- Sets PU.0 output data.*
- void `LDOPWR_togglePort_U1_outputData` (uint16\_t baseAddress)
- Toggles PU.1 output data.*
- void `LDOPWR_togglePort_U0_outputData` (uint16\_t baseAddress)
- Toggles PU.0 output data.*
- void `LDOPWR_enableInterrupt` (uint16\_t baseAddress, uint16\_t mask)
- Enables LDO-PWR module interrupts.*
- void `LDOPWR_disableInterrupt` (uint16\_t baseAddress, uint16\_t mask)
- Disables LDO-PWR module interrupts.*
- void `LDOPWR_enable` (uint16\_t baseAddress)
- Enables LDO-PWR module.*
- void `LDOPWR_disable` (uint16\_t baseAddress)
- Disables LDO-PWR module.*
- uint8\_t `LDOPWR_getInterruptStatus` (uint16\_t baseAddress, uint16\_t mask)
- Returns the interrupt status of LDO-PWR module interrupts.*
- void `LDOPWR_clearInterrupt` (uint16\_t baseAddress, uint16\_t mask)
- Clears the interrupt status of LDO-PWR module interrupts.*
- uint8\_t `LDOPWR_isLDOInputValid` (uint16\_t baseAddress)
- Returns if the the LDOI is valid and within bounds.*
- void `LDOPWR_enableOverloadAutoOff` (uint16\_t baseAddress)
- Enables the LDO overload auto-off.*
- void `LDOPWR_disableOverloadAutoOff` (uint16\_t baseAddress)
- Disables the LDO overload auto-off.*
- uint8\_t `LDOPWR_getOverloadAutoOffStatus` (uint16\_t baseAddress)
- Returns if the LDOI overload auto-off is enabled or disabled.*

## 23.2.1 Detailed Description

The LDOPWR configuration is handled by

- `LDOPWR_unlockConfiguration()`
- `LDOPWR_lockConfiguration()`
- `LDOPWR_enablePort_U_inputs()`
- `LDOPWR_disablePort_U_inputs()`
- `LDOPWR_enablePort_U_outputs()`
- `LDOPWR_disablePort_U_outputs()`
- `LDOPWR_enable()`
- `LDOPWR_disable()`
- `LDOPWR_enableOverloadAutoOff()`
- `LDOPWR_disableOverloadAutoOff()`

Handling the read/write of output data is handled by

- `LDOPWR_getPort_U1_inputData()`
- `LDOPWR_getPort_U0_inputData()`
- `LDOPWR_getPort_U1_outputData()`
- `LDOPWR_getPort_U0_outputData()`
- `LDOPWR_getOverloadAutoOffStatus()`

- LDOPWR\_setPort\_U0\_outputData()
- LDOPWR\_togglePort\_U1\_outputData()
- LDOPWR\_togglePort\_U0\_outputData()
- LDOPWR\_setPort\_U1\_outputData()

The interrupt and status operations are handled by

- LDOPWR\_enableInterrupt()
- LDOPWR\_disableInterrupt()
- LDOPWR\_getInterruptStatus()
- LDOPWR\_clearInterrupt()
- LDOPWR\_isLDOInputValid()
- LDOPWR\_getOverloadAutoOffStatus()

## 23.2.2 Function Documentation

### LDOPWR\_clearInterrupt()

```
void LDOPWR_clearInterrupt (
    uint16_t baseAddress,
    uint16_t mask )
```

Clears the interrupt status of LDO-PWR module interrupts.

#### Parameters

<i>baseAddress</i>	is the base address of the LDOPWR module.
<i>mask</i>	mask of interrupts to clear the status of Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ LDOPWR_LDO_I_VOLTAGE_GOING_OFF_INTERRUPT</li> <li>■ LDOPWR_LDO_I_VOLTAGE_COMING_ON_INTERRUPT</li> <li>■ LDOPWR_LDO_OVERLOAD_INDICATION_INTERRUPT</li> </ul>

Modified bits of **LDOPWRCTL** register.

#### Returns

None

### LDOPWR\_disable()

```
void LDOPWR_disable (
    uint16_t baseAddress )
```

Disables LDO-PWR module.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

Modified bits of **LDOPWRCTL** register.

**Returns**

None

**LDOPWR\_disableInterrupt()**

```
void LDOPWR_disableInterrupt (
    uint16_t baseAddress,
    uint16_t mask )
```

Disables LDO-PWR module interrupts.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
<i>mask</i>	mask of interrupts to disable Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>LDOPWR_LDO1_VOLTAGE_GOING_OFF_INTERRUPT</b></li> <li>■ <b>LDOPWR_LDO1_VOLTAGE_COMING_ON_INTERRUPT</b></li> <li>■ <b>LDOPWR_LDO_OVERLOAD_INDICATION_INTERRUPT</b></li> </ul>

Modified bits of **LDOPWRCTL** register.

**Returns**

None

**LDOPWR\_disableOverloadAutoOff()**

```
void LDOPWR_disableOverloadAutoOff (
    uint16_t baseAddress )
```

Disables the LDO overload auto-off.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

Modified bits of **LDOPWRCTL** register.

**Returns**

None

**LDOPWR\_disablePort\_U\_inputs()**

```
void LDOPWR_disablePort_U_inputs (
    uint16_t baseAddress )
```

Disables Port U inputs.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

Modified bits of **PUCTL** register.**Returns**

None

**LDOPWR\_disablePort\_U\_outputs()**

```
void LDOPWR_disablePort_U_outputs (
    uint16_t baseAddress )
```

Disables Port U inputs.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

Modified bits of **PUCTL** register.**Returns**

None

**LDOPWR\_enable()**

```
void LDOPWR_enable (
    uint16_t baseAddress )
```

Enables LDO-PWR module.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

Modified bits of **LDOPWRCTL** register.

**Returns**

None

### LDOPWR\_enableInterrupt()

```
void LDOPWR_enableInterrupt (
    uint16_t baseAddress,
    uint16_t mask )
```

Enables LDO-PWR module interrupts.

Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
<i>mask</i>	mask of interrupts to enable Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>LDOPWR_LDO1_VOLTAGE_GOING_OFF_INTERRUPT</b></li> <li>■ <b>LDOPWR_LDO1_VOLTAGE_COMING_ON_INTERRUPT</b></li> <li>■ <b>LDOPWR_LDO_OVERLOAD_INDICATION_INTERRUPT</b></li> </ul>

Modified bits of **LDOPWRCTL** register.

**Returns**

None

### LDOPWR\_enableOverloadAutoOff()

```
void LDOPWR_enableOverloadAutoOff (
    uint16_t baseAddress )
```

Enables the LDO overload auto-off.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

Modified bits of **LDOPWRCTL** register.

**Returns**

None

**LDOPWR\_enablePort\_U\_inputs()**

```
void LDOPWR_enablePort_U_inputs (
    uint16_t baseAddress )
```

Enables Port U inputs.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

Modified bits of **PUCTL** register.**Returns**

None

**LDOPWR\_enablePort\_U\_outputs()**

```
void LDOPWR_enablePort_U_outputs (
    uint16_t baseAddress )
```

Enables Port U outputs.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

Modified bits of **PUCTL** register.**Returns**

None

**LDOPWR\_getInterruptStatus()**

```
uint8_t LDOPWR_getInterruptStatus (
    uint16_t baseAddress,
    uint16_t mask )
```

Returns the interrupt status of LDO-PWR module interrupts.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

**Parameters**

<i>mask</i>	mask of interrupts to get the status of Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ LDOPWR_LDO_I_VOLTAGE_GOING_OFF_INTERRUPT</li> <li>■ LDOPWR_LDO_I_VOLTAGE_COMING_ON_INTERRUPT</li> <li>■ LDOPWR_LDO_OVERLOAD_INDICATION_INTERRUPT</li> </ul>
-------------	--

**Returns**

Logical OR of any of the following:

- LDOPWR\_LDO\_I\_VOLTAGE\_GOING\_OFF\_INTERRUPT
  - LDOPWR\_LDO\_I\_VOLTAGE\_COMING\_ON\_INTERRUPT
  - LDOPWR\_LDO\_OVERLOAD\_INDICATION\_INTERRUPT
- indicating the status of the masked interrupts

**LDOPWR\_getOverloadAutoOffStatus()**

```
uint8_t LDOPWR_getOverloadAutoOffStatus (
    uint16_t baseAddress )
```

Returns if the LDO I overload auto-off is enabled or disabled.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

**Returns**

One of the following:

- LDOPWR\_AUTOOFF\_ENABLED
- LDOPWR\_AUTOOFF\_DISABLED

**LDOPWR\_getPort\_U0\_inputData()**

```
uint8_t LDOPWR_getPort_U0_inputData (
    uint16_t baseAddress )
```

Returns PU.0 input data.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---



**Returns**

One of the following:

- **LDOPWR\_PORTU\_PIN\_HIGH**
- **LDOPWR\_PORTU\_PIN\_LOW**

**LDOPWR\_getPort\_U0\_outputData()**

```
uint8_t LDOPWR_getPort_U0_outputData (
    uint16_t baseAddress )
```

Returns PU.0 output data.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

**Returns**

One of the following:

- **LDOPWR\_PORTU\_PIN\_HIGH**
- **LDOPWR\_PORTU\_PIN\_LOW**

**LDOPWR\_getPort\_U1\_inputData()**

```
uint8_t LDOPWR_getPort_U1_inputData (
    uint16_t baseAddress )
```

Returns PU.1 input data.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

**Returns**

One of the following:

- **LDOPWR\_PORTU\_PIN\_HIGH**
- **LDOPWR\_PORTU\_PIN\_LOW**

**LDOPWR\_getPort\_U1\_outputData()**

```
uint8_t LDOPWR_getPort_U1_outputData (
    uint16_t baseAddress )
```

Returns PU.1 output data.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

**Returns**

One of the following:

- **LDOPWR\_PORTU\_PIN\_HIGH**
- **LDOPWR\_PORTU\_PIN\_LOW**

**LDOPWR\_isLDOInputValid()**

```
uint8_t LDOPWR_isLDOInputValid (
    uint16_t baseAddress )
```

Returns if the the LDOI is valid and within bounds.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

**Returns**

One of the following:

- **LDOPWR\_LDO\_INPUT\_VALID**
- **LDOPWR\_LDO\_INPUT\_INVALID**

**LDOPWR\_lockConfiguration()**

```
void LDOPWR_lockConfiguration (
    uint16_t baseAddress )
```

Locks the configuration registers and disables write access.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

Modified bits of **LDOKEYPID** register.

**Returns**

None

**LDOPWR\_setPort\_U0\_outputData()**

```
void LDOPWR_setPort_U0_outputData (
```

```
uint16_t baseAddress,
uint8_t value )
```

Sets PU.0 output data.

#### Parameters

<i>baseAddress</i>	is the base address of the LDOPWR module.
<i>value</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>LDOPWR_PORTU_PIN_HIGH</b></li> <li>■ <b>LDOPWR_PORTU_PIN_LOW</b></li> </ul>

Modified bits of **PUCTL** register.

#### Returns

None

### LDOPWR\_setPort\_U1\_outputData()

```
void LDOPWR_setPort_U1_outputData (
uint16_t baseAddress,
uint8_t value )
```

Sets PU.1 output data.

#### Parameters

<i>baseAddress</i>	is the base address of the LDOPWR module.
<i>value</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>LDOPWR_PORTU_PIN_HIGH</b></li> <li>■ <b>LDOPWR_PORTU_PIN_LOW</b></li> </ul>

Modified bits of **PUCTL** register.

#### Returns

None

### LDOPWR\_togglePort\_U0\_outputData()

```
void LDOPWR_togglePort_U0_outputData (
uint16_t baseAddress )
```

Toggles PU.0 output data.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

Modified bits of **PUCTL** register.

**Returns**

None

**LDOPWR\_togglePort\_U1\_outputData()**

```
void LDOPWR_togglePort_U1_outputData (
    uint16_t baseAddress )
```

Toggles PU.1 output data.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

Modified bits of **PUCTL** register.

**Returns**

None

**LDOPWR\_unlockConfiguration()**

```
void LDOPWR_unlockConfiguration (
    uint16_t baseAddress )
```

Unlocks the configuration registers and enables write access.

**Parameters**

<i>baseAddress</i>	is the base address of the LDOPWR module.
--------------------	---

Modified bits of **LDOKEYPID** register.

**Returns**

None

## 23.3 Programming Example

The following example shows how to use the LDO-PWR API.

```

{
// Enable access to config registers
LDOPWR_unlockConfiguration(LDOPWR_BASE);

// Configure PU.0 as output pins
LDOPWR_enablePort_U_outputs(LDOPWR_BASE);

//Set PU.1 = high
LDOPWR_setPort_U1_outputData(LDOPWR_BASE,
                             LDOPWR_PORTU_PIN_HIGH
                             );

//Set PU.0 = low
LDOPWR_setPort_U0_outputData(LDOPWR_BASE,
                             LDOPWR_PORTU_PIN_LOW
                             );

// Enable LDO overload indication interrupt
LDOPWR_enableInterrupt(LDOPWR_BASE,
                       LDOPWR_LDO_OVERLOAD_INDICATION_INTERRUPT
                       );

// Disable access to config registers
LDOPWR_lockConfiguration(LDOPWR_BASE);

// continuous loop
while(1)
{
// Delay
for(i=50000;i>0;i--);

// Enable access to config registers
LDOPWR_unlockConfiguration(LDOPWR_BASE);

// XOR PU.0/1
LDOPWR_togglePort_U1_outputData(LDOPWR_BASE);
LDOPWR_togglePort_U0_outputData(LDOPWR_BASE);

// Disable access to config registers
LDOPWR_lockConfiguration(LDOPWR_BASE);
}
}

//*****
//
// This is the LDO_PWR_VECTOR interrupt vector service routine.
//
//*****
__interrupt void LDOInterruptHandler(void)
{
if(LDOPWR_getInterruptStatus(LDOPWR_BASE,
                             LDOPWR_LDO_OVERLOAD_INDICATION_INTERRUPT
                             ))

{
// Enable access to config registers
LDOPWR_unlockConfiguration(LDOPWR_BASE);

// Software clear IFG
LDOPWR_clearInterrupt(LDOPWR_BASE,
                      LDOPWR_LDO_OVERLOAD_INDICATION_INTERRUPT
                      );

// Disable access to config registers
LDOPWR_lockConfiguration(LDOPWR_BASE);

// Over load indication; take necessary steps in application firmware
while(1);
}
}

```

## 24 32-Bit Hardware Multiplier (MPY32)

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### 24.1 Introduction

The 32-Bit Hardware Multiplier (MPY32) API provides a set of functions for using the MSP430Ware MPY32 modules. Functions are provided to setup the MPY32 modules, set the operand registers, and obtain the results.

The MPY32 Modules does not generate any interrupts.

### 24.2 API Functions

#### Functions

- void [MPY32\\_setWriteDelay](#) (uint16\_t writeDelaySelect)  
*Sets the write delay setting for the MPY32 module.*
- void [MPY32\\_enableSaturationMode](#) (void)  
*Enables Saturation Mode.*
- void [MPY32\\_disableSaturationMode](#) (void)  
*Disables Saturation Mode.*
- uint8\_t [MPY32\\_getSaturationMode](#) (void)  
*Gets the Saturation Mode.*
- void [MPY32\\_enableFractionalMode](#) (void)  
*Enables Fraction Mode.*
- void [MPY32\\_disableFractionalMode](#) (void)  
*Disables Fraction Mode.*
- uint8\_t [MPY32\\_getFractionalMode](#) (void)  
*Gets the Fractional Mode.*
- void [MPY32\\_setOperandOne8Bit](#) (uint8\_t multiplicationType, uint8\_t operand)  
*Sets an 8-bit value into operand 1.*
- void [MPY32\\_setOperandOne16Bit](#) (uint8\_t multiplicationType, uint16\_t operand)  
*Sets an 16-bit value into operand 1.*
- void [MPY32\\_setOperandOne24Bit](#) (uint8\_t multiplicationType, uint32\_t operand)  
*Sets an 24-bit value into operand 1.*
- void [MPY32\\_setOperandOne32Bit](#) (uint8\_t multiplicationType, uint32\_t operand)  
*Sets an 32-bit value into operand 1.*
- void [MPY32\\_setOperandTwo8Bit](#) (uint8\_t operand)  
*Sets an 8-bit value into operand 2, which starts the multiplication.*
- void [MPY32\\_setOperandTwo16Bit](#) (uint16\_t operand)  
*Sets an 16-bit value into operand 2, which starts the multiplication.*
- void [MPY32\\_setOperandTwo24Bit](#) (uint32\_t operand)  
*Sets an 24-bit value into operand 2, which starts the multiplication.*
- void [MPY32\\_setOperandTwo32Bit](#) (uint32\_t operand)

- Sets an 32-bit value into operand 2, which starts the multiplication.*
- `uint64_t MPY32_getResult` (void)
  - Returns an 64-bit result of the last multiplication operation.*
- `uint16_t MPY32_getSumExtension` (void)
  - Returns the Sum Extension of the last multiplication operation.*
- `uint16_t MPY32_getCarryBitValue` (void)
  - Returns the Carry Bit of the last multiplication operation.*
- `void MPY32_clearCarryBitValue` (void)
  - Clears the Carry Bit of the last multiplication operation.*
- `void MPY32_preloadResult` (`uint64_t` result)
  - Preloads the result register.*

## 24.2.1 Detailed Description

The MPY32 API is broken into three groups of functions: those that control the settings, those that set the operand registers, and those that return the results, sum extension, and carry bit value.

The settings are handled by

- `MPY32_setWriteDelay()`
- `MPY32_enableSaturationMode()`
- `MPY32_disableSaturationMode()`
- `MPY32_enableFractionalMode()`
- `MPY32_disableFractionalMode()`
- `MPY32_preloadResult()`

The operand registers are set by

- `MPY32_setOperandOne8Bit()`
- `MPY32_setOperandOne16Bit()`
- `MPY32_setOperandOne24Bit()`
- `MPY32_setOperandOne32Bit()`
- `MPY32_setOperandTwo8Bit()`
- `MPY32_setOperandTwo16Bit()`
- `MPY32_setOperandTwo24Bit()`
- `MPY32_setOperandTwo32Bit()`

The results can be returned by

- `MPY32_getResult()`
- `MPY32_getSumExtension()`
- `MPY32_getCarryBitValue()`
- `MPY32_getSaturationMode()`
- `MPY32_getFractionalMode()`

## 24.2.2 Function Documentation

### MPY32\_clearCarryBitValue()

```
void MPY32_clearCarryBitValue (  
    void )
```

Clears the Carry Bit of the last multiplication operation.

This function clears the Carry Bit of the MPY module

#### Returns

The value of the MPY32 module Carry Bit 0x0 or 0x1.

### MPY32\_disableFractionalMode()

```
void MPY32_disableFractionalMode (  
    void )
```

Disables Fraction Mode.

This function disables fraction mode.

#### Returns

None

### MPY32\_disableSaturationMode()

```
void MPY32_disableSaturationMode (  
    void )
```

Disables Saturation Mode.

This function disables saturation mode, which allows the raw result of the MPY result registers to be returned.

#### Returns

None

### MPY32\_enableFractionalMode()

```
void MPY32_enableFractionalMode (  
    void )
```

Enables Fraction Mode.

This function enables fraction mode.

#### Returns

None



**MPY32\_enableSaturationMode()**

```
void MPY32_enableSaturationMode (
    void )
```

Enables Saturation Mode.

This function enables saturation mode. When this is enabled, the result read out from the MPY result registers is converted to the most-positive number in the case of an overflow, or the most-negative number in the case of an underflow. Please note, that the raw value in the registers does not reflect the result returned, and if the saturation mode is disabled, then the raw value of the registers will be returned instead.

**Returns**

None

**MPY32\_getCarryBitValue()**

```
uint16_t MPY32_getCarryBitValue (
    void )
```

Returns the Carry Bit of the last multiplication operation.

This function returns the Carry Bit of the MPY module, which either gives the sign after a signed operation or shows a carry after a multiply- and- accumulate operation.

**Returns**

The value of the MPY32 module Carry Bit 0x0 or 0x1.

**MPY32\_getFractionalMode()**

```
uint8_t MPY32_getFractionalMode (
    void )
```

Gets the Fractional Mode.

This function gets the current fractional mode.

**Returns**

Gets the fractional mode Return one of the following:

- **MPY32\_FRACTIONAL\_MODE\_DISABLED**
- **MPY32\_FRACTIONAL\_MODE\_ENABLED**

Gets the Fractional Mode

**MPY32\_getResult()**

```
uint64_t MPY32_getResult (
    void )
```

Returns an 64-bit result of the last multiplication operation.

This function returns all 64 bits of the result registers

**Returns**

The 64-bit result is returned as a `uint64_t` type

**MPY32\_getSaturationMode()**

```
uint8_t MPY32_getSaturationMode (
    void )
```

Gets the Saturation Mode.

This function gets the current saturation mode.

**Returns**

Gets the Saturation Mode Return one of the following:

- **MPY32\_SATURATION\_MODE\_DISABLED**
- **MPY32\_SATURATION\_MODE\_ENABLED**

Gets the Saturation Mode

**MPY32\_getSumExtension()**

```
uint16_t MPY32_getSumExtension (
    void )
```

Returns the Sum Extension of the last multiplication operation.

This function returns the Sum Extension of the MPY module, which either gives the sign after a signed operation or shows a carry after a multiply- and-accumulate operation. The Sum Extension acts as a check for overflows or underflows.

**Returns**

The value of the MPY32 module Sum Extension.

**MPY32\_preloadResult()**

```
void MPY32_preloadResult (
    uint64_t result )
```

Preloads the result register.

This function Preloads the result register

**Parameters**

<i>result</i>	value to preload the result register to
---------------	---

**Returns**

None

**MPY32\_setOperandOne16Bit()**

```
void MPY32_setOperandOne16Bit (
    uint8_t multiplicationType,
    uint16_t operand )
```

Sets an 16-bit value into operand 1.

This function sets the first operand for multiplication and determines what type of operation should be performed. Once the second operand is set, then the operation will begin.

**Parameters**

<i>multiplicationType</i>	is the type of multiplication to perform once the second operand is set. Valid values are: <ul style="list-style-type: none"> <li>■ <b>MPY32_MULTIPLY_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLY_SIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_SIGNED</b></li> </ul>
<i>operand</i>	is the 16-bit value to load into the 1st operand.

**Returns**

None

**MPY32\_setOperandOne24Bit()**

```
void MPY32_setOperandOne24Bit (
    uint8_t multiplicationType,
    uint32_t operand )
```

Sets an 24-bit value into operand 1.

This function sets the first operand for multiplication and determines what type of operation should be performed. Once the second operand is set, then the operation will begin.

**Parameters**

<i>multiplicationType</i>	is the type of multiplication to perform once the second operand is set. Valid values are: <ul style="list-style-type: none"> <li>■ <b>MPY32_MULTIPLY_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLY_SIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_SIGNED</b></li> </ul>
<i>operand</i>	is the 24-bit value to load into the 1st operand.

**Returns**

None

**MPY32\_setOperandOne32Bit()**

```
void MPY32_setOperandOne32Bit (
    uint8_t multiplicationType,
    uint32_t operand )
```

Sets an 32-bit value into operand 1.

This function sets the first operand for multiplication and determines what type of operation should be performed. Once the second operand is set, then the operation will begin.

**Parameters**

<i>multiplicationType</i>	is the type of multiplication to perform once the second operand is set. Valid values are: <ul style="list-style-type: none"> <li>■ <b>MPY32_MULTIPLY_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLY_SIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_SIGNED</b></li> </ul>
<i>operand</i>	is the 32-bit value to load into the 1st operand.

**Returns**

None

**MPY32\_setOperandOne8Bit()**

```
void MPY32_setOperandOne8Bit (
    uint8_t multiplicationType,
    uint8_t operand )
```

Sets an 8-bit value into operand 1.

This function sets the first operand for multiplication and determines what type of operation should be performed. Once the second operand is set, then the operation will begin.

**Parameters**

<i>multiplicationType</i>	is the type of multiplication to perform once the second operand is set. Valid values are: <ul style="list-style-type: none"> <li>■ <b>MPY32_MULTIPLY_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLY_SIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_SIGNED</b></li> </ul>
---------------------------	---

**Parameters**

<i>operand</i>	is the 8-bit value to load into the 1st operand.
----------------	--

**Returns**

None

**MPY32\_setOperandTwo16Bit()**

```
void MPY32_setOperandTwo16Bit (
    uint16_t operand )
```

Sets an 16-bit value into operand 2, which starts the multiplication.

This function sets the second operand of the multiplication operation and starts the operation.

**Parameters**

<i>operand</i>	is the 16-bit value to load into the 2nd operand.
----------------	---

**Returns**

None

**MPY32\_setOperandTwo24Bit()**

```
void MPY32_setOperandTwo24Bit (
    uint32_t operand )
```

Sets an 24-bit value into operand 2, which starts the multiplication.

This function sets the second operand of the multiplication operation and starts the operation.

**Parameters**

<i>operand</i>	is the 24-bit value to load into the 2nd operand.
----------------	---

**Returns**

None

**MPY32\_setOperandTwo32Bit()**

```
void MPY32_setOperandTwo32Bit (
    uint32_t operand )
```

Sets an 32-bit value into operand 2, which starts the multiplication.

This function sets the second operand of the multiplication operation and starts the operation.

**Parameters**

<i>operand</i>	is the 32-bit value to load into the 2nd operand.
----------------	---

**Returns**

None

**MPY32\_setOperandTwo8Bit()**

```
void MPY32_setOperandTwo8Bit (
    uint8_t operand )
```

Sets an 8-bit value into operand 2, which starts the multiplication.

This function sets the second operand of the multiplication operation and starts the operation.

**Parameters**

<i>operand</i>	is the 8-bit value to load into the 2nd operand.
----------------	--

**Returns**

None

**MPY32\_setWriteDelay()**

```
void MPY32_setWriteDelay (
    uint16_t writeDelaySelect )
```

Sets the write delay setting for the MPY32 module.

This function sets up a write delay to the MPY module's registers, which holds any writes to the registers until all calculations are complete. There are two different settings, one which waits for 32-bit results to be ready, and one which waits for 64-bit results to be ready. This prevents unpredictable results if registers are changed before the results are ready.



## 25 Operational Amplifier (OA)

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### 25.1 Introduction

The OA operational amplifiers can be used to support front-end analog signal conditioning prior to analog-to-digital conversion, as well as, other general purpose applications.

Features of the OA include

- Single-supply, low-current operation
- Software selectable rail-to-rail input
- Rail-to-rail output
- Input switches on positive and negative inputs individually software selectable
- Internal voltage follower setting
- Low impedance ground switches individually software selectable (not available on all devices)

### 25.2 API Functions

The OA API is broken into two groups of functions: those that deal with initialization and those that are used to obtain the status of the OA

The OA initialization functions are

- `OA_openSwitch()`
- `OA_closeSwitch()`
- `OA_enableRailToRailInput()`
- `OA_disableRailToRailInput()`
- `OA_disableAmplifierMode()`
- `OA_enableAmplifierMode()`

OA status can be obtained by

- `OA_getSwitchStatus()`
- `OA_getRailToRailInputReadyStatus()`
- `OA_getRailToRailInputStatus()`
- `OA_getAmplifierModeStatus()`



## 25.3 Programming Example

The following example shows how to initialize and use the OA API

```
// Select OA0IP0 as "+" input
// Select OA0IN0 as "-" input
OA_closeSwitch(OA_BASE,
               OA_POSITIVE_INPUT_TERMINAL_SWITCH0,
               OA_NEGATIVE_INPUT_TERMINAL_SWITCH0,
               OA_GROUND_NONE
               );

// Enable OA0 amplifier
OA_enableAmplifierMode(OA_BASE);
```

## 26 Port Mapping Controller

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### 26.1 Introduction

The port mapping controller allows the flexible and re-configurable mapping of digital functions to port pins. The port mapping controller features are:

- Configuration protected by write access key.
- Default mapping provided for each port pin (device-dependent, the device pinout in the device-specific data sheet).
- Mapping can be reconfigured during runtime.
- Each output signal can be mapped to several output pins.

### 26.2 API Functions

#### Functions

- void [PMAP\\_initPorts](#) (uint16\_t baseAddress, [PMAP\\_initPortsParam](#) \*param)  
*This function configures the MSP430 Port Mapper.*

#### 26.2.1 Detailed Description

The MSP430ware API that configures Port Mapping is [PMAP\\_initPorts\(\)](#)

It needs the following data to configure port mapping. portMapping - pointer to init Data PxMAPy - pointer start of first Port Mapper to initialize numberOfPorts - number of Ports to initialize portMapReconfigure - to enable/disable reconfiguration

#### 26.2.2 Function Documentation

##### PMPMAP\_initPorts()

```
void PMPMAP_initPorts (
    uint16_t baseAddress,
    PMPMAP_initPortsParam * param )
```

This function configures the MSP430 Port Mapper.

This function port maps a set of pins to a new set.

Modified bits of **PMPMAPKETID** register and bits of **PMPMAPCTL** register.

**Returns**

None

References PMAP\_initPortsParam::numberOfPorts, PMAP\_initPortsParam::portMapping, PMAP\_initPortsParam::portMapReconfigure, and PMAP\_initPortsParam::PxMAPy.

## 26.3 Programming Example

The following example shows some Port Mapping Controller operations using the APIs

```
const unsigned char port_mapping[] = {
    //Port P4:
    PM.TB0CCR0A,
    PM.TB0CCR1A,
    PM.TB0CCR2A,
    PM.TB0CCR3A,
    PM.TB0CCR4A,
    PM.TB0CCR5A,
    PM.TB0CCR6A,
    PM.NONE
};

//CONFIGURE PORTS- pass the port_mapping array, start @ P4MAP01, initialize
//a single port, do not allow run-time reconfiguration of port mapping

PMAP_initPortsParam initPortsParams = {0};
initPortsParam.portMapping = port_mapping;
initPortsParam.PxMAPy = (uint8_t *)&P4MAP01;
initPortsParam.numberOfPorts = 1;
initPortsParam.portMapReconfigure = PMAP_DISABLE_RECONFIGURATION;
PMAP_initPorts(PMAP_CTRLBASE, &initPortsParam);
```

## 27 Power Management Module (PMM)

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### 27.1 Introduction

The PMM manages the following internal circuitry:

- An integrated low-dropout voltage regulator (LDO) that produces a secondary core voltage (VCORE) from the primary voltage that is applied to the device (DVCC)
- Supply voltage supervisors (SVS) and supply voltage monitors (SVM) for the primary voltage (DVCC) and the secondary voltage (VCORE). The SVS and SVM include programmable threshold levels and power-fail indicators. Therefore, the PMM plays a crucial role in defining the maximum performance, valid voltage conditions, and current consumption for an application running on an MSP430x5xx or MSP430x6xx device. The secondary voltage that is generated by the integrated LDO, VCore, is programmable to one of four core voltage levels, shown as 0, 1, 2, and 3. Each increase in VCore allows the CPU to operate at a higher maximum frequency. The values of these frequencies are specified in the device-specific data sheet. This feature allows the user the flexibility to trade power consumption in active and low-power modes for different degrees of maximum performance and minimum supply voltage.

NOTE: To align with the nomenclature in the MSP430x5xx/MSP430x6xx Family User's Guide, the primary voltage domain (DVCC) is referred to as the high-side voltage (SvsH/SVMH) and the secondary voltage domain (VCORE) is referred to as the low-side voltage (SvsL/SvmL).

Moving between the different VCore voltages requires a specific sequence of events and can be done only one level at a time; for example, to change from level 0 to level 3, the application code must step through level 1 and level 2.

VCore increase:

1. SvmL monitor level is incremented.
2. VCore level is incremented.
3. The SvmL Level Reached Interrupt Flag (SVSMLVLRIFG) in the PMMIFG register is polled. When asserted, SVSMLVLRIFG indicates that the VCore voltage has reached its next level.
4. SvsL is increased. SvsL is changed last, because if SVSL were incremented prior to VCore, it would potentially cause a reset.

VCore decrease:

1. Decrement SvmL and SVSL levels.
2. Decrement VCore. The `PMM_setVCore()` function appropriately handles an increase or decrease of the core voltage. NOTE: The procedure recommended above provides a workaround for the erratum FLASH37. See the device-specific erratasheet to determine if a device is affected by FLASH37. The workaround is also highlighted in the source code for the PMM library

**Recommended SVS and SVM Settings** The SVS and SVM on both the high side and the low side are enabled in normal performance mode following a brown-out reset condition. The device is held in reset until the SVS and SVM verify that the external and core voltages meet the minimum requirements of the default core voltage, which is level zero. The SVS and SVM remain enabled unless disabled by the firmware. The low-side SVS and SVM are useful for verifying the startup conditions and for verifying any modification to the core voltage. However, in their default mode, they prevent the CPU from executing code on wake-up from low-power modes 2, 3, and 4 for a full 150  $\mu$ s, not 5  $\mu$ s. This is because, in their default states, the SVSL and SvmL are powered down in the low-power mode of the PMM and need time for their comparators to wake and stabilize before they can verify the voltage condition and release the CPU for execution. Note that the high-side SVS and SVM do not influence the wake time from low-power modes. If the wake-up from low-power modes needs to be shortened to 5  $\mu$ s, the SVSL and SvmL should be disabled after the initialization of the core voltage at the beginning of the application. Disabling SVSL and SvmL prevents them from gating the CPU on wake-up from LPM2, LPM3, and LPM4. The application is still protected on the high side with SvsH and SVMH. The `PMM_setVCore()` function automatically enables and disables the SVS and SVM as necessary if a non-zero core voltage level is required. If the application does not require a change in the core voltage (that is, when the target MCLK is less than 8 MHz), the `PMM_disableSVLSvmL()` and `PMM_enableSvsHReset()` macros can be used to disable the low-side SVS and SVM circuitry and enable only the high-side SVS POR reset, respectively.

**Setting SVS/SVM Threshold Levels** The voltage thresholds for the SVS and SVM modules are programmable. On the high side, there are two bit fields that control these threshold levels – the SvsHRVL and SVSMHRRL. The SvsHRVL field defines the voltage threshold at which the SvsH triggers a reset (also known as the SvsH ON voltage level). The SVSMHRRL field defines the voltage threshold at which the SvsH releases the device from a reset (also known as SvsH OFF voltage level). The MSP430x5xx/MSP430x6xx Family User's Guide (SLAU208) [1] recommends the settings shown in Table 1 when setting these bits. The `PMM_setVCore()` function follows these recommendations and ensures that the SVS levels match the core voltage levels that are used.

**Advanced SVS Controls and Trade-offs** In addition to the default SVS settings that are provided with the `PMM_setVCore()` function, the SVS/SVM modules can be optimized for wake-up speed, response time (propagation delay), and current consumption, as needed. The following controls can be optimized for the SVS/SVM modules:

- Protection in low power modes - LPM2, LPM3, and LPM4
- Wake-up time from LPM2, LPM3, and LPM4
- Response time to react to an SVS event Selecting the LPM option, wake-up time, and response time that is best suited for the application is left to the user. A few typical examples illustrate the trade-offs: Case A: The most robust protection that stays on in LPMs and has the fastest response and wake-up time consumes the most power. Case B: With SVS high side active only in AM, no protection in LPMs, slow wake-up, and slow response time has SVS protection with the least current consumption. Case C: An optimized case is described - turn off the low-side monitor and supervisor, thereby saving power while keeping response time fast on the high side to help with timing critical applications. The user can call the `PMM_setVCore()` function, which configures SVS/SVM high side and low side with the recommended or default configurations, or can call the APIs provided to control the parameters as the application demands.

Any writes to the SVSMLCTL and SVSMHCTL registers require a delay time for these registers to settle before the new settings take effect. This delay time is dependent on whether the SVS and SVM modules are configured for normal or full performance. See device-specific data sheet for exact delay times.

## 27.2 API Functions

### Functions

- void `PMM_enableSvsL` (void)  
*Enables the low-side SVS circuitry.*
- void `PMM_disableSvsL` (void)  
*Disables the low-side SVS circuitry.*
- void `PMM_enableSvmL` (void)  
*Enables the low-side SVM circuitry.*
- void `PMM_disableSvmL` (void)  
*Disables the low-side SVM circuitry.*
- void `PMM_enableSvsH` (void)  
*Enables the high-side SVS circuitry.*
- void `PMM_disableSvsH` (void)  
*Disables the high-side SVS circuitry.*
- void `PMM_enableSvmH` (void)  
*Enables the high-side SVM circuitry.*
- void `PMM_disableSvmH` (void)  
*Disables the high-side SVM circuitry.*
- void `PMM_enableSvsLSvmL` (void)  
*Enables the low-side SVS and SVM circuitry.*
- void `PMM_disableSvsLSvmL` (void)  
*Disables the low-side SVS and SVM circuitry.*
- void `PMM_enableSvsHSvmH` (void)  
*Enables the high-side SVS and SVM circuitry.*
- void `PMM_disableSvsHSvmH` (void)  
*Disables the high-side SVS and SVM circuitry.*
- void `PMM_enableSvsLReset` (void)  
*Enables the POR signal generation when a low-voltage event is registered by the low-side SVS.*
- void `PMM_disableSvsLReset` (void)  
*Disables the POR signal generation when a low-voltage event is registered by the low-side SVS.*
- void `PMM_enableSvmLInterrupt` (void)  
*Enables the interrupt generation when a low-voltage event is registered by the low-side SVM.*
- void `PMM_disableSvmLInterrupt` (void)  
*Disables the interrupt generation when a low-voltage event is registered by the low-side SVM.*
- void `PMM_enableSvsHReset` (void)  
*Enables the POR signal generation when a low-voltage event is registered by the high-side SVS.*
- void `PMM_disableSvsHReset` (void)  
*Disables the POR signal generation when a low-voltage event is registered by the high-side SVS.*
- void `PMM_enableSvmHInterrupt` (void)  
*Enables the interrupt generation when a low-voltage event is registered by the high-side SVM.*
- void `PMM_disableSvmHInterrupt` (void)  
*Disables the interrupt generation when a low-voltage event is registered by the high-side SVM.*
- void `PMM_clearPMMIFGS` (void)  
*Clear all interrupt flags for the PMM.*
- void `PMM_enableSvsLInLPMFastWake` (void)  
*Enables supervisor low side in LPM with twake-up-fast from LPM2, LPM3, and LPM4.*
- void `PMM_enableSvsLInLPMSlowWake` (void)  
*Enables supervisor low side in LPM with twake-up-slow from LPM2, LPM3, and LPM4.*
- void `PMM_disableSvsLInLPMFastWake` (void)  
*Disables supervisor low side in LPM with twake-up-fast from LPM2, LPM3, and LPM4.*

- void `PMM_disableSvsLInLPMslowWake` (void)  
*Disables supervisor low side in LPM with twake-up-slow from LPM2, LPM3, and LPM4.*
- void `PMM_enableSvsHInLPMnormPerf` (void)  
*Enables supervisor high side in LPM with  $tpd = 20 \mu s(1)$*
- void `PMM_enableSvsHInLPMfullPerf` (void)  
*Enables supervisor high side in LPM with  $tpd = 2.5 \mu s(1)$*
- void `PMM_disableSvsHInLPMnormPerf` (void)  
*Disables supervisor high side in LPM with  $tpd = 20 \mu s(1)$*
- void `PMM_disableSvsHInLPMfullPerf` (void)  
*Disables supervisor high side in LPM with  $tpd = 2.5 \mu s(1)$*
- void `PMM_optimizeSvsLInLPMfastWake` (void)  
*Optimized to provide twake-up-fast from LPM2, LPM3, and LPM4 with least power.*
- void `PMM_optimizeSvsHInLPMfullPerf` (void)  
*Optimized to provide  $tpd = 2.5 \mu s(1)$  in LPM with least power.*
- uint16\_t `PMM_setVcoreUp` (uint8\_t level)  
*Increase Vcore by one level.*
- uint16\_t `PMM_setVcoreDown` (uint8\_t level)  
*Decrease Vcore by one level.*
- bool `PMM_setVcore` (uint8\_t level)  
*Set Vcore to expected level.*
- uint16\_t `PMM_getInterruptStatus` (uint16\_t mask)  
*Returns interrupt status.*

## 27.2.1 Detailed Description

`PMM_enableSvsL()` / `PMM_disableSvsL()` Enables or disables the low-side SVS circuitry

`PMM_enableSvmL()` / `PMM_disableSvmL()` Enables or disables the low-side SVM circuitry

`PMM_enableSvsH()` / `PMM_disableSvsH()` Enables or disables the high-side SVS circuitry

`PMM_enableSVMH()` / `PMM_disableSVMH()` Enables or disables the high-side SVM circuitry

`PMM_enableSvsLSvmL()` / `PMM_disableSvsLSvmL()` Enables or disables the low-side SVS and SVM circuitry

`PMM_enableSvsHSvmH()` / `PMM_disableSvsHSvmH()` Enables or disables the high-side SVS and SVM circuitry

`PMM_enableSvsLReset()` / `PMM_disableSvsLReset()` Enables or disables the POR signal generation when a low-voltage event is registered by the low-side SVS

`PMM_enableSvmLInterrupt()` / `PMM_disableSvmLInterrupt()` Enables or disables the interrupt generation when a low-voltage event is registered by the low-side SVM

`PMM_enableSvsHReset()` / `PMM_disableSvsHReset()` Enables or disables the POR signal generation when a low-voltage event is registered by the high-side SVS

`PMM_enableSVMHInterrupt()` / `PMM_disableSVMHInterrupt()` Enables or disables the interrupt generation when a low-voltage event is registered by the high-side SVM

`PMM_clearPMMIFGS()` Clear all interrupt flags for the PMM

`PMM_enableSvsLInLPMfastWake()` Enables supervisor low side in LPM with twake-up-fast from LPM2, LPM3, and LPM4

`PMM_enableSvsLInLPMslowWake()` Enables supervisor low side in LPM with twake-up-slow from LPM2, LPM3, and LPM4

**PMM\_disableSvsLinLPMFastWake()** Disables supervisor low side in LPM with twake-up-fast from LPM2, LPM3, and LPM4

**PMM\_disableSvsLinLPMSlowWake()** Disables supervisor low side in LPM with twake-up-slow from LPM2, LPM3, and LPM4

**PMM\_enableSvsHInLPMNormPerf()** Enables supervisor high side in LPM with tpd = 20 ?s(1)

**PMM\_enableSvsHInLPMFullPerf()** Enables supervisor high side in LPM with tpd = 2.5 ?s(1)

**PMM\_disableSvsHInLPMNormPerf()** Disables supervisor high side in LPM with tpd = 20 ?s(1)

**PMM\_disableSvsHInLPMFullPerf()** Disables supervisor high side in LPM with tpd = 2.5 ?s(1)

**PMM\_optimizeSvsLinLPMFastWake()** Optimized to provide twake-up-fast from LPM2, LPM3, and LPM4 with least power

**PMM\_optimizeSvsHInLPMFullPerf()** Optimized to provide tpd = 2.5 ?s(1) in LPM with least power

**PMM\_getInterruptStatus()** Returns interrupt status of the PMM module

**PMM\_setVCore()** Sets the appropriate VCORE level. Calls the **PMM\_setVCoreUp()** or **PMM\_setVCoreDown()** function the required number of times depending on the current VCORE level, because the levels must be stepped through individually. A status indicator equal to STATUS\_SUCCESS or STATUS\_FAIL that indicates a valid or invalid VCORE transition, respectively. An invalid VCORE transition exists if DVCC is less than the minimum required voltage for the target VCORE voltage.

## 27.2.2 Function Documentation

### PMM\_clearPMMIFGS()

```
void PMM_clearPMMIFGS (
    void )
```

Clear all interrupt flags for the PMM.

Modified bits of **PMMCTL0** register and bits of **PMMIFG** register.

#### Returns

None

### PMM\_disableSvmH()

```
void PMM_disableSvmH (
    void )
```

Disables the high-side SVM circuitry.

Modified bits of **PMMCTL0** register and bits of **SVSMHCTL** register.

#### Returns

None



### PMM\_disableSvmHInterrupt()

```
void PMM_disableSvmHInterrupt (  
    void )
```

Disables the interrupt generation when a low-voltage event is registered by the high-side SVM.  
Modified bits of **PMMCTLO** register and bits of **PMMIE** register.

#### Returns

None

### PMM\_disableSvmL()

```
void PMM_disableSvmL (  
    void )
```

Disables the low-side SVM circuitry.  
Modified bits of **PMMCTLO** register and bits of **SVSMLCTL** register.

#### Returns

None

### PMM\_disableSvmLInterrupt()

```
void PMM_disableSvmLInterrupt (  
    void )
```

Disables the interrupt generation when a low-voltage event is registered by the low-side SVM.  
Modified bits of **PMMCTLO** register and bits of **PMMIE** register.

#### Returns

None

### PMM\_disableSvsH()

```
void PMM_disableSvsH (  
    void )
```

Disables the high-side SVS circuitry.  
Modified bits of **PMMCTLO** register and bits of **SVSMHCTL** register.

#### Returns

None

### PMM\_disableSvsHInLPMFullPerf()

```
void PMM_disableSvsHInLPMFullPerf (  
    void )
```

Disables supervisor high side in LPM with  $t_{pd} = 2.5 \mu s(1)$

Modified bits of **PMMCTLO** register and bits of **SVSMHCTL** register.

#### Returns

None

### PMM\_disableSvsHInLPMNormPerf()

```
void PMM_disableSvsHInLPMNormPerf (  
    void )
```

Disables supervisor high side in LPM with  $t_{pd} = 20 \mu s(1)$

Modified bits of **PMMCTLO** register and bits of **SVSMHCTL** register.

#### Returns

None

### PMM\_disableSvsHReset()

```
void PMM_disableSvsHReset (  
    void )
```

Disables the POR signal generation when a low-voltage event is registered by the high-side SVS.

Modified bits of **PMMCTLO** register and bits of **PMMIE** register.

#### Returns

None

### PMM\_disableSvsHSvmH()

```
void PMM_disableSvsHSvmH (  
    void )
```

Disables the high-side SVS and SVM circuitry.

Modified bits of **PMMCTLO** register and bits of **SVSMHCTL** register.

#### Returns

None

### PMM\_disableSvsL()

```
void PMM_disableSvsL (  
    void )
```

Disables the low-side SVS circuitry.

Modified bits of **PMMCTLO** register and bits of **SVSMLCTL** register.

#### Returns

None

### PMM\_disableSvsLInLPMFastWake()

```
void PMM_disableSvsLInLPMFastWake (  
    void )
```

Disables supervisor low side in LPM with twake-up-fast from LPM2, LPM3, and LPM4.

Modified bits of **PMMCTLO** register and bits of **SVSMLCTL** register.

#### Returns

None

### PMM\_disableSvsLInLPMSlowWake()

```
void PMM_disableSvsLInLPMSlowWake (  
    void )
```

Disables supervisor low side in LPM with twake-up-slow from LPM2, LPM3, and LPM4.

Modified bits of **PMMCTLO** register and bits of **SVSMLCTL** register.

#### Returns

None

### PMM\_disableSvsLReset()

```
void PMM_disableSvsLReset (  
    void )
```

Disables the POR signal generation when a low-voltage event is registered by the low-side SVS.

Modified bits of **PMMCTLO** register and bits of **PMMIE** register.

#### Returns

None

### PMM\_disableSvsLSvmL()

```
void PMM_disableSvsLSvmL (  
    void )
```

Disables the low-side SVS and SVM circuitry.

Modified bits of **PMMCTLO** register and bits of **SVSMLCTL** register.

#### Returns

None

### PMM\_enableSvmH()

```
void PMM_enableSvmH (  
    void )
```

Enables the high-side SVM circuitry.

Modified bits of **PMMCTLO** register and bits of **SVSMHCTL** register.

#### Returns

None

### PMM\_enableSvmHInterrupt()

```
void PMM_enableSvmHInterrupt (  
    void )
```

Enables the interrupt generation when a low-voltage event is registered by the high-side SVM.

Modified bits of **PMMCTLO** register and bits of **PMMIE** register.

#### Returns

None

### PMM\_enableSvmL()

```
void PMM_enableSvmL (  
    void )
```

Enables the low-side SVM circuitry.

Modified bits of **PMMCTLO** register and bits of **SVSMLCTL** register.

#### Returns

None

### PMM\_enableSvmLInterrupt()

```
void PMM_enableSvmLInterrupt (  
    void )
```

Enables the interrupt generation when a low-voltage event is registered by the low-side SVM.  
Modified bits of **PMMCTLO** register and bits of **PMMIE** register.

#### Returns

None

### PMM\_enableSvsH()

```
void PMM_enableSvsH (  
    void )
```

Enables the high-side SVS circuitry.  
Modified bits of **PMMCTLO** register and bits of **SVSMHCTL** register.

#### Returns

None

### PMM\_enableSvsHInLPMFullPerf()

```
void PMM_enableSvsHInLPMFullPerf (  
    void )
```

Enables supervisor high side in LPM with  $t_{pd} = 2.5 \mu s(1)$   
Modified bits of **PMMCTLO** register and bits of **SVSMHCTL** register.

#### Returns

None

### PMM\_enableSvsHInLPMNormPerf()

```
void PMM_enableSvsHInLPMNormPerf (  
    void )
```

Enables supervisor high side in LPM with  $t_{pd} = 20 \mu s(1)$   
Modified bits of **PMMCTLO** register and bits of **SVSMHCTL** register.

#### Returns

None

### PMM\_enableSvsHReset()

```
void PMM_enableSvsHReset (  
    void )
```

Enables the POR signal generation when a low-voltage event is registered by the high-side SVS.  
Modified bits of **PMMCTLO** register and bits of **PMMIE** register.

#### Returns

None

### PMM\_enableSvsHSvmH()

```
void PMM_enableSvsHSvmH (  
    void )
```

Enables the high-side SVS and SVM circuitry.  
Modified bits of **PMMCTLO** register and bits of **SVSMHCTL** register.

#### Returns

None

### PMM\_enableSvsL()

```
void PMM_enableSvsL (  
    void )
```

Enables the low-side SVS circuitry.  
Modified bits of **PMMCTLO** register and bits of **SVSMLCTL** register.

#### Returns

None

### PMM\_enableSvsLInLPMFastWake()

```
void PMM_enableSvsLInLPMFastWake (  
    void )
```

Enables supervisor low side in LPM with twake-up-fast from LPM2, LPM3, and LPM4.  
Modified bits of **PMMCTLO** register and bits of **SVSMLCTL** register.

#### Returns

None

### PMM\_enableSvsLInLPMSlowWake()

```
void PMM_enableSvsLInLPMSlowWake (  
    void )
```

Enables supervisor low side in LPM with twake-up-slow from LPM2, LPM3, and LPM4.  
Modified bits of **PMMCTLO** register and bits of **SVSMLCTL** register.

#### Returns

None

### PMM\_enableSvsLReset()

```
void PMM_enableSvsLReset (  
    void )
```

Enables the POR signal generation when a low-voltage event is registered by the low-side SVS.  
Modified bits of **PMMCTLO** register and bits of **PMMIE** register.

#### Returns

None

### PMM\_enableSvsLSvmL()

```
void PMM_enableSvsLSvmL (  
    void )
```

Enables the low-side SVS and SVM circuitry.  
Modified bits of **PMMCTLO** register and bits of **SVSMLCTL** register.

#### Returns

None

### PMM\_getInterruptStatus()

```
uint16_t PMM_getInterruptStatus (  
    uint16_t mask )
```

Returns interrupt status.

## Parameters

<i>mask</i>	<p>is the mask for specifying the required flag Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>PMM_SVSMLDLYIFG</b></li> <li>■ <b>PMM_SVMLIFG</b></li> <li>■ <b>PMM_SVMLVLRIFG</b></li> <li>■ <b>PMM_SVSMHDLYIFG</b></li> <li>■ <b>PMM_SVMHIFG</b></li> <li>■ <b>PMM_SVMHVLRFIFG</b></li> <li>■ <b>PMM_PMMBORIFG</b></li> <li>■ <b>PMM_PMMRSTIFG</b></li> <li>■ <b>PMM_PMMPORIFG</b></li> <li>■ <b>PMM_SVSHIFG</b></li> <li>■ <b>PMM_SVSLIFG</b></li> <li>■ <b>PMM_PMMLPM5IFG</b></li> </ul>
-------------	--

## Returns

Logical OR of any of the following:

- **PMM\_SVSMLDLYIFG**
- **PMM\_SVMLIFG**
- **PMM\_SVMLVLRIFG**
- **PMM\_SVSMHDLYIFG**
- **PMM\_SVMHIFG**
- **PMM\_SVMHVLRFIFG**
- **PMM\_PMMBORIFG**
- **PMM\_PMMRSTIFG**
- **PMM\_PMMPORIFG**
- **PMM\_SVSHIFG**
- **PMM\_SVSLIFG**
- **PMM\_PMMLPM5IFG**

indicating the status of the masked interrupts

## PMM\_optimizeSvsHInLPMFullPerf()

```
void PMM_optimizeSvsHInLPMFullPerf (
    void )
```

Optimized to provide  $t_{pd} = 2.5 \mu s(1)$  in LPM with least power.

Modified bits of **PMMCTL0** register and bits of **SVSMLCTL** register.

## Returns

None



**PMM\_optimizeSvsLInLPMFastWake()**

```
void PMM_optimizeSvsLInLPMFastWake (
    void )
```

Optimized to provide twake-up-fast from LPM2, LPM3, and LPM4 with least power.  
Modified bits of **PMMCTL0** register and bits of **SVSMLCTL** register.

**Returns**

None

**PMM\_setVCore()**

```
bool PMM_setVCore (
    uint8_t level )
```

Set Vcore to expected level.

**Parameters**

<i>level</i>	level to which Vcore needs to be decreased/increased Valid values are: <ul style="list-style-type: none"> <li>■ <b>PMM_CORE_LEVEL_0</b> [Default]</li> <li>■ <b>PMM_CORE_LEVEL_1</b></li> <li>■ <b>PMM_CORE_LEVEL_2</b></li> <li>■ <b>PMM_CORE_LEVEL_3</b></li> </ul>
--------------	---

Modified bits of **PMMCTL0** register, bits of **PMMIFG** register, bits of **PMMRIE** register, bits of **SVSMHCTL** register and bits of **SVSMLCTL** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAIL

References PMM\_setVCoreDown(), and PMM\_setVCoreUp().

**PMM\_setVCoreDown()**

```
uint16_t PMM_setVCoreDown (
    uint8_t level )
```

Decrease Vcore by one level.

**Parameters**

<i>level</i>	level to which Vcore needs to be decreased Valid values are: <ul style="list-style-type: none"> <li>■ <b>PMM_CORE_LEVEL_0</b> [Default]</li> <li>■ <b>PMM_CORE_LEVEL_1</b></li> <li>■ <b>PMM_CORE_LEVEL_2</b></li> <li>■ <b>PMM_CORE_LEVEL_3</b></li> </ul>
--------------	---

Modified bits of **PMMCTLO** register, bits of **PMMIFG** register, bits of **PMMRIE** register, bits of **SVSMHCTL** register and bits of **SVSMLCTL** register.

**Returns**

STATUS\_SUCCESS

Referenced by PMM.setVCore().

**PMM.setVCoreUp()**

```
uint16_t PMM.setVCoreUp (
    uint8_t level )
```

Increase Vcore by one level.

**Parameters**

<i>level</i>	level to which Vcore needs to be increased Valid values are: <ul style="list-style-type: none"> <li>■ <b>PMM_CORE_LEVEL_0</b> [Default]</li> <li>■ <b>PMM_CORE_LEVEL_1</b></li> <li>■ <b>PMM_CORE_LEVEL_2</b></li> <li>■ <b>PMM_CORE_LEVEL_3</b></li> </ul>
--------------	---

Modified bits of **PMMCTLO** register, bits of **PMMIFG** register, bits of **PMMRIE** register, bits of **SVSMHCTL** register and bits of **SVSMLCTL** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAIL

Referenced by PMM.setVCore().

## 27.3 Programming Example

The following example shows some pmm operations using the APIs

```
//Use the line below to bring the level back to 0
```

```
status = PMM.setVCore(PMM.CORE.LEVEL_0);

//Set P1.0 to output direction
GPIO.setAsOutputPin(
    GPIO.PORT_P1,
    GPIO.PIN0
);

//continuous loop
while (1)
{
    //Toggle P1.0
    GPIO.toggleOutputOnPin(
        GPIO.PORT_P1,
        GPIO.PIN0
    );
    //Delay
    _delay_cycles(20000);
}
```

## 28 RAM Controller

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### 28.1 Introduction

The RAMCTL provides access to the different power modes of the RAM. The RAMCTL allows the ability to reduce the leakage current while the CPU is off. The RAM can also be switched off. In retention mode, the RAM content is saved while the RAM content is lost in off mode. The RAM is partitioned in sectors, typically of 4KB (sector) size. See the device-specific data sheet for actual block allocation and size. Each sector is controlled by the RAM controller RAM Sector Off control bit (RCRSyOFF) of the RAMCTL Control 0 register (RCCTL0). The RCCTL0 register is protected with a key. Only if the correct key is written during a word write, the RCCTL0 register content can be modified. Byte write accesses or write accesses with a wrong key are ignored.

### 28.2 API Functions

#### Functions

- void [RAM.setSectorOff](#) (uint8\_t sector)  
*Set specified RAM sector off.*
- uint8\_t [RAM.getSectorState](#) (uint8\_t sector)  
*Get RAM sector ON/OFF status.*

#### 28.2.1 Detailed Description

The MSP430ware API that configure the RAM controller are:

[RAM.setSectorOff\(\)](#) - Set specified RAM sector off [RAM.getSectorState\(\)](#) - Get RAM sector ON/OFF status

#### 28.2.2 Function Documentation

##### RAM\_getSectorState()

```
uint8_t RAM_getSectorState (
    uint8_t sector )
```

Get RAM sector ON/OFF status.

**Parameters**

<i>sector</i>	<p>is specified sector Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>RAM_SECTOR0</b></li> <li>■ <b>RAM_SECTOR1</b></li> <li>■ <b>RAM_SECTOR2</b></li> <li>■ <b>RAM_SECTOR3</b></li> <li>■ <b>RAM_SECTOR4</b></li> <li>■ <b>RAM_SECTOR5</b></li> <li>■ <b>RAM_SECTOR6</b></li> <li>■ <b>RAM_SECTOR7</b></li> </ul>
---------------	---

Modified bits of **RCCTL0** register.

**Returns**

Logical OR of any of the following:

- **RAM\_SECTOR0**
- **RAM\_SECTOR1**
- **RAM\_SECTOR2**
- **RAM\_SECTOR3**
- **RAM\_SECTOR4**
- **RAM\_SECTOR5**
- **RAM\_SECTOR6**
- **RAM\_SECTOR7**

indicating the status of the masked sectors

**RAM\_setSectorOff()**

```
void RAM_setSectorOff (
    uint8_t sector )
```

Set specified RAM sector off.

## Parameters

<i>sector</i>	<p>is specified sector to be set off. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>RAM_SECTOR0</b></li> <li>■ <b>RAM_SECTOR1</b></li> <li>■ <b>RAM_SECTOR2</b></li> <li>■ <b>RAM_SECTOR3</b></li> <li>■ <b>RAM_SECTOR4</b></li> <li>■ <b>RAM_SECTOR5</b></li> <li>■ <b>RAM_SECTOR6</b></li> <li>■ <b>RAM_SECTOR7</b></li> </ul>
---------------	--

Modified bits of **RCCTL0** register.

## Returns

None

## 28.3 Programming Example

The following example shows some RAM Controller operations using the APIs

```

//Start timer
Timer_A_clearTimerInterrupt(TIMER_A0_BASE);

Timer_A_initUpModeParam param = {0};
param.clockSource = TIMER_A_CLOCKSOURCE_ACLK;
param.clockSourceDivider = TIMER_A_CLOCKSOURCE_DIVIDER_1;
param.timerPeriod = 25000;
param.timerInterruptEnable_TAIE = TIMER_A_TAIE_INTERRUPT_DISABLE;
param.captureCompareInterruptEnable_CCR0_CCIE =
    TIMER_A_CAPTURECOMPARE_INTERRUPT_ENABLE;
param.timerClear = TIMER_A_DO_CLEAR;
param.startTimer = true;
Timer_A_initUpMode(TIMER_A0_BASE, &param);

//RAM controller sector off
RAM_setSectorOff(RAM_SECTOR2);

//Enter LPM0, enable interrupts
_bis_SR_register(LPM3_bits + GIE);

//For debugger
_no_operation();
}

//*****
//
//This is the Timer B0 interrupt vector service routine.
//
//*****
#pragma vector=TIMERB0_VECTOR
__interrupt void TIMERB0_ISR(void)
{
    returnValue = RAM_getSectorState(RAM_BASE,
        RAM_SECTOR0 +
        RAM_SECTOR1 +
        RAM_SECTOR2 +
        RAM_SECTOR3);
}

```

}

## 29 Internal Reference (REF)

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### 29.1 Introduction

The Internal Reference (REF) API provides a set of functions for using the MSP430Ware REF modules. Functions are provided to setup and enable use of the Reference voltage, enable or disable the internal temperature sensor, and view the status of the inner workings of the REF module.

The reference module (REF) is responsible for generation of all critical reference voltages that can be used by various analog peripherals in a given device. These include, but are not necessarily limited to, the ADC10\_A, ADC12\_A, DAC12\_A, LCD\_B, and COMP\_B modules dependent upon the particular device. The heart of the reference system is the bandgap from which all other references are derived by unity or non-inverting gain stages. The REFGEN sub-system consists of the bandgap, the bandgap bias, and the non-inverting buffer stage which generates the three primary voltage reference available in the system, namely 1.5 V, 2.0 V, and 2.5 V. In addition, when enabled, a buffered bandgap voltage is also available.

### 29.2 API Functions

#### Functions

- void [Ref\\_setReferenceVoltage](#) (uint16\_t baseAddress, uint8\_t referenceVoltageSelect)  
*Sets the reference voltage for the voltage generator.*
- void [Ref\\_disableTempSensor](#) (uint16\_t baseAddress)  
*Disables the internal temperature sensor to save power consumption.*
- void [Ref\\_enableTempSensor](#) (uint16\_t baseAddress)  
*Enables the internal temperature sensor.*
- void [Ref\\_enableReferenceVoltageOutput](#) (uint16\_t baseAddress)  
*Outputs the reference voltage to an output pin.*
- void [Ref\\_disableReferenceVoltageOutput](#) (uint16\_t baseAddress)  
*Disables the reference voltage as an output to a pin.*
- void [Ref\\_enableReferenceVoltage](#) (uint16\_t baseAddress)  
*Enables the reference voltage to be used by peripherals.*
- void [Ref\\_disableReferenceVoltage](#) (uint16\_t baseAddress)  
*Disables the reference voltage.*
- uint16\_t [Ref\\_getBandgapMode](#) (uint16\_t baseAddress)  
*Returns the bandgap mode of the Ref module.*
- bool [Ref\\_isBandgapActive](#) (uint16\_t baseAddress)  
*Returns the active status of the bandgap in the Ref module.*
- uint16\_t [Ref\\_isRefGenBusy](#) (uint16\_t baseAddress)  
*Returns the busy status of the reference generator in the Ref module.*
- bool [Ref\\_isRefGenActive](#) (uint16\_t baseAddress)  
*Returns the active status of the reference generator in the Ref module.*



## 29.2.1 Detailed Description

The DMA API is broken into three groups of functions: those that deal with the reference voltage, those that handle the internal temperature sensor, and those that return the status of the REF module.

The reference voltage of the REF module is handled by

- [Ref\\_setReferenceVoltage\(\)](#)
- [Ref\\_enableReferenceVoltageOutput\(\)](#)
- [Ref\\_disableReferenceVoltageOutput\(\)](#)
- [Ref\\_enableReferenceVoltage\(\)](#)
- [Ref\\_disableReferenceVoltage\(\)](#)

The internal temperature sensor is handled by

- [Ref\\_disableTempSensor\(\)](#)
- [Ref\\_enableTempSensor\(\)](#)

The status of the REF module is handled by

- [Ref\\_getBandgapMode\(\)](#)
- [Ref\\_isBandgapActive\(\)](#)
- [Ref\\_isRefGenBusy\(\)](#)
- [Ref\\_isRefGen\(\)](#)

## 29.2.2 Function Documentation

### Ref\_disableReferenceVoltage()

```
void Ref_disableReferenceVoltage (
    uint16_t baseAddress )
```

Disables the reference voltage.

This function is used to disable the generated reference voltage. Please note, if the [Ref\\_isRefGenBusy\(\)](#) returns Ref\_BUSY, this function will have no effect.

#### Parameters

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

Modified bits are **REFON** of **REFCTL0** register.

**Returns**

None

**Ref\_disableReferenceVoltageOutput()**

```
void Ref_disableReferenceVoltageOutput (
    uint16_t baseAddress )
```

Disables the reference voltage as an output to a pin.

This function is used to disables the reference voltage being generated to be given to an output pin. Please note, if the [Ref\\_isRefGenBusy\(\)](#) returns Ref\_BUSY, this function will have no effect.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

Modified bits are **REFOUT** of **REFCTL0** register.

**Returns**

None

**Ref\_disableTempSensor()**

```
void Ref_disableTempSensor (
    uint16_t baseAddress )
```

Disables the internal temperature sensor to save power consumption.

This function is used to turn off the internal temperature sensor to save on power consumption. The temperature sensor is enabled by default. Please note, that giving ADC12 module control over the Ref module, the state of the temperature sensor is dependent on the controls of the ADC12 module. Please note, if the [Ref\\_isRefGenBusy\(\)](#) returns Ref\_BUSY, this function will have no effect.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

Modified bits are **REFTCOFF** of **REFCTL0** register.

**Returns**

None

**Ref\_enableReferenceVoltage()**

```
void Ref_enableReferenceVoltage (
```

```
uint16_t baseAddress )
```

Enables the reference voltage to be used by peripherals.

This function is used to enable the generated reference voltage to be used other peripherals or by an output pin, if enabled. Please note, that giving ADC12 module control over the Ref module, the state of the reference voltage is dependent on the controls of the ADC12 module. Please note, ADC10\_A does not support the reference request. If the [Ref\\_isRefGenBusy\(\)](#) returns Ref\_BUSY, this function will have no effect.

#### Parameters

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

Modified bits are **REFON** of **REFCTL0** register.

#### Returns

None

### Ref\_enableReferenceVoltageOutput()

```
void Ref_enableReferenceVoltageOutput (
    uint16_t baseAddress )
```

Outputs the reference voltage to an output pin.

This function is used to output the reference voltage being generated to an output pin. Please note, the output pin is device specific. Please note, that giving ADC12 module control over the Ref module, the state of the reference voltage as an output to a pin is dependent on the controls of the ADC12 module. If ADC12\_A reference burst is disabled or DAC12\_A is enabled, this output is available continuously. If ADC12\_A reference burst is enabled, this output is available only during an ADC12\_A conversion. For devices with CTSD16, [Ref\\_enableReferenceVoltage\(\)](#) needs to be invoked to get VREFBG available continuously. Otherwise, VREFBG is only available externally when a module requests it. Please note, if the [Ref\\_isRefGenBusy\(\)](#) returns Ref\_BUSY, this function will have no effect.

#### Parameters

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

Modified bits are **REFOUT** of **REFCTL0** register.

#### Returns

None

### Ref\_enableTempSensor()

```
void Ref_enableTempSensor (
    uint16_t baseAddress )
```

Enables the internal temperature sensor.

This function is used to turn on the internal temperature sensor to use by other peripherals. The temperature sensor is enabled by default. Please note, if the [Ref\\_isRefGenBusy\(\)](#) returns Ref\_BUSY, this function will have no effect.

#### Parameters

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

Modified bits are **REFTCOFF** of **REFCTL0** register.

#### Returns

None

### Ref\_getBandgapMode()

```
uint16_t Ref_getBandgapMode (
    uint16_t baseAddress )
```

Returns the bandgap mode of the Ref module.

This function is used to return the bandgap mode of the Ref module, requested by the peripherals using the bandgap. If a peripheral requests static mode, then the bandgap mode will be static for all modules, whereas if all of the peripherals using the bandgap request sample mode, then that will be the mode returned. Sample mode allows the bandgap to be active only when necessary to save on power consumption, static mode requires the bandgap to be active until no peripherals are using it anymore.

#### Parameters

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

#### Returns

One of the following:

- **REF\_STATICMODE** if the bandgap is operating in static mode
- **REF\_SAMPLEMODE** if the bandgap is operating in sample mode indicating the bandgap mode of the module

### Ref\_isBandgapActive()

```
bool Ref_isBandgapActive (
    uint16_t baseAddress )
```

Returns the active status of the bandgap in the Ref module.

This function is used to return the active status of the bandgap in the Ref module. If the bandgap is in use by a peripheral, then the status will be seen as active.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

**Returns**

One of the following:

- **REF\_ACTIVE** if active
- **REF\_INACTIVE** if not active  
indicating the bandgap active status of the module

**Ref\_isRefGenActive()**

```
bool Ref_isRefGenActive (  
    uint16_t baseAddress )
```

Returns the active status of the reference generator in the Ref module.

This function is used to return the active status of the reference generator in the Ref module. If the ref generator is on and ready to use, then the status will be seen as active.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

**Returns**

One of the following:

- **REF\_ACTIVE** if active
- **REF\_INACTIVE** if not active  
indicating the reference generator active status of the module

**Ref\_isRefGenBusy()**

```
uint16_t Ref_isRefGenBusy (  
    uint16_t baseAddress )
```

Returns the busy status of the reference generator in the Ref module.

This function is used to return the busy status of the reference generator in the Ref module. If the ref generator is in use by a peripheral, then the status will be seen as busy.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

**Returns**

One of the following:

- **REF\_NOTBUSY** if the reference generator is not being used
- **REF\_BUSY** if the reference generator is being used, disallowing changes to be made to the Ref module controls indicating the reference generator busy status of the module

**Ref\_setReferenceVoltage()**

```
void Ref_setReferenceVoltage (
    uint16_t baseAddress,
    uint8_t referenceVoltageSelect )
```

Sets the reference voltage for the voltage generator.

This function sets the reference voltage generated by the voltage generator to be used by other peripherals. This reference voltage will only be valid while the Ref module is in control. Please note, if the [Ref\\_isRefGenBusy\(\)](#) returns Ref\_BUSY, this function will have no effect.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
<i>referenceVoltageSelect</i>	is the desired voltage to generate for a reference voltage. Valid values are: <ul style="list-style-type: none"> <li>■ <b>REF_VREF1_5V</b> [Default]</li> <li>■ <b>REF_VREF2_0V</b></li> <li>■ <b>REF_VREF2_5V</b></li> </ul> Modified bits are <b>REFVSEL</b> of <b>REFCTL0</b> register.

**Returns**

None

## 29.3 Programming Example

The following example shows how to initialize and use the REF API with the ADC12.A module to use as a positive reference to the analog signal input.

```
// By default, REFMSTR=1 => REFCTL is used to configure the internal reference
// If ref generator busy, WAIT
while(Ref_refGenBusyStatus(REF_BASE));
// Select internal ref = 2.5V
Ref_setReferenceVoltage(REF_BASE,
    REF_VREF2_5V);
// Internal Reference ON
Ref_enableReferenceVoltage(REF_BASE);

__delay_cycles(75); // Delay (~75us) for Ref to settle

// Initialize the ADC12.A Module
```

```

/*
 * Base address of ADC12.A Module
 * Use internal ADC12.A bit as sample/hold signal to start conversion
 * USE MODOSC 5MHZ Digital Oscillator as clock source
 * Use default clock divider of 1
 */
ADC12.A.init(ADC12.A.BASE,
            ADC12.A.SAMPLEHOLDSOURCE.SC,
            ADC12.A.CLOCKSOURCE.ADC12OSC,
            ADC12.A.CLOCKDIVIDEBY.1);

/*
 * Base address of ADC12 Module
 * For memory buffers 0-7 sample/hold for 64 clock cycles
 * For memory buffers 8-15 sample/hold for 4 clock cycles (default)
 * Disable Multiple Sampling
 */
ADC12.A.setupSamplingTimer(ADC12.A.BASE,
                          ADC12.A.CYCLEHOLD.64.CYCLES,
                          ADC12.A.CYCLEHOLD.4.CYCLES,
                          ADC12.A.MULTIPLESAMPLESENABLE);

// Configure Memory Buffer
/*
 * Base address of the ADC12 Module
 * Configure memory buffer 0
 * Map input A0 to memory buffer 0
 * Vref+ = Vref+ (INT)
 * Vref- = AVss
 */
ADC12.A.memoryConfigure(ADC12.A.BASE,
                       ADC12.A.MEMORY_0,
                       ADC12.A.INPUT_A0,
                       ADC12.A.VREFPOS.INT,
                       ADC12.A.VREFNEG.AVSS,
                       ADC12.A.NOTENDOFSEQUENCE);

while (1)
{
    // Enable/Start sampling and conversion
    /*
     * Base address of ADC12 Module
     * Start the conversion into memory buffer 0
     * Use the single-channel, single-conversion mode
     */
    ADC12.A.startConversion(ADC12.A.BASE,
                          ADC12.A.MEMORY_0,
                          ADC12.A.SINGLECHANNEL);

    // Poll for interrupt on memory buffer 0
    while(!ADC12.A.interruptStatus(ADC12.A.BASE, ADC12.IFG0));

    __no_operation(); // SET BREAKPOINT HERE
}

```

## 30 Real-Time Clock (RTC\_A)

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### 30.1 Introduction

The Real Time Clock (RTC\_A) API provides a set of functions for using the MSP430Ware RTC\_A modules. Functions are provided to calibrate the clock, initialize the RTC\_A modules in calendar mode/counter mode and setup conditions for, and enable, interrupts for the RTC\_A modules. If an RTC\_A module is used, then counter mode may also be initialized, as well as prescale counters.

The RTC\_A module provides the ability to keep track of the current time and date in calendar mode, or can be setup as a 32-bit counter (RTC\_A Only).

The RTC\_A module generates multiple interrupts. There are 2 interrupts that can be defined in calendar mode, and 1 interrupt in counter mode for counter overflow, as well as an interrupt for each prescaler.

### 30.2 API Functions

#### Functions

- void [RTC\\_A\\_startClock](#) (uint16\_t baseAddress)  
*Starts the RTC.*
- void [RTC\\_A\\_holdClock](#) (uint16\_t baseAddress)  
*Holds the RTC.*
- void [RTC\\_A\\_setCalibrationFrequency](#) (uint16\_t baseAddress, uint16\_t frequencySelect)  
*Allows and Sets the frequency output to RTCCLK pin for calibration measurement.*
- void [RTC\\_A\\_setCalibrationData](#) (uint16\_t baseAddress, uint8\_t offsetDirection, uint8\_t offsetValue)  
*Sets the specified calibration for the RTC.*
- void [RTC\\_A\\_initCounter](#) (uint16\_t baseAddress, uint16\_t clockSelect, uint16\_t counterSizeSelect)  
*Initializes the settings to operate the RTC in Counter mode.*
- void [RTC\\_A\\_initCalendar](#) (uint16\_t baseAddress, [Calendar](#) \*CalendarTime, uint16\_t formatSelect)  
*Initializes the settings to operate the RTC in calendar mode.*
- [Calendar](#) [RTC\\_A\\_getCalendarTime](#) (uint16\_t baseAddress)  
*Returns the [Calendar](#) Time stored in the [Calendar](#) registers of the RTC.*
- void [RTC\\_A\\_configureCalendarAlarm](#) (uint16\_t baseAddress, [RTC\\_A\\_configureCalendarAlarmParam](#) \*param)  
*Sets and Enables the desired [Calendar](#) Alarm settings.*
- void [RTC\\_A\\_setCalendarEvent](#) (uint16\_t baseAddress, uint16\_t eventSelect)  
*Sets a single specified [Calendar](#) interrupt condition.*
- uint32\_t [RTC\\_A\\_getCounterValue](#) (uint16\_t baseAddress)  
*Returns the value of the Counter register.*



- void `RTC_A_setCounterValue` (uint16\_t baseAddress, uint32\_t counterValue)  
*Sets the value of the Counter register.*
- void `RTC_A_initCounterPrescale` (uint16\_t baseAddress, uint8\_t prescaleSelect, uint16\_t prescaleClockSelect, uint16\_t prescaleDivider)  
*Initializes the Prescaler for Counter mode.*
- void `RTC_A_holdCounterPrescale` (uint16\_t baseAddress, uint8\_t prescaleSelect)  
*Holds the selected Prescaler.*
- void `RTC_A_startCounterPrescale` (uint16\_t baseAddress, uint8\_t prescaleSelect)  
*Starts the selected Prescaler.*
- void `RTC_A_definePrescaleEvent` (uint16\_t baseAddress, uint8\_t prescaleSelect, uint8\_t prescaleEventDivider)  
*Sets up an interrupt condition for the selected Prescaler.*
- uint8\_t `RTC_A_getPrescaleValue` (uint16\_t baseAddress, uint8\_t prescaleSelect)  
*Returns the selected prescaler value.*
- void `RTC_A_setPrescaleValue` (uint16\_t baseAddress, uint8\_t prescaleSelect, uint8\_t prescaleCounterValue)  
*Sets the selected prescaler value.*
- void `RTC_A_enableInterrupt` (uint16\_t baseAddress, uint8\_t interruptMask)  
*Enables selected RTC interrupt sources.*
- void `RTC_A_disableInterrupt` (uint16\_t baseAddress, uint8\_t interruptMask)  
*Disables selected RTC interrupt sources.*
- uint8\_t `RTC_A_getInterruptStatus` (uint16\_t baseAddress, uint8\_t interruptFlagMask)  
*Returns the status of the selected interrupts flags.*
- void `RTC_A_clearInterrupt` (uint16\_t baseAddress, uint8\_t interruptFlagMask)  
*Clears selected RTC interrupt flags.*

### 30.2.1 Detailed Description

The RTC\_A API is broken into 5 groups of functions: clock settings, calender mode, counter mode, prescale counter, and interrupt condition setup/enable functions and data conversion.

The RTC\_A clock settings are handled by

- `RTC_A.startClock()`
- `RTC_A.holdClock()`
- `RTC_A.setCalibrationFrequency()`
- `RTC_A.setCalibrationData()`

The RTC\_A calender mode is initialized and setup by

- `RTC_A.initCalender()`
- `RTC_A.getCalenderTime()`

The RTC\_A counter mode is initialized and setup by

- `RTC_A.initCounter()`
- `RTC_A.getCounterValue()`
- `RTC_A.setCounterValue()`
- `RTC_A.initCounterPrescale()`
- `RTC_A.holdCounterPrescale()`

- [RTC\\_A.startCounterPrescale\(\)](#)

The RTC\_A prescale counter is handled by

- [RTC\\_A.getPrescaleValue\(\)](#)
- [RTC\\_A.setPrescaleValue\(\)](#)

The RTC\_A interrupts are handled by

- [RTC\\_A.configureCalendarAlarm\(\)](#)
- [RTC\\_A.setCalenderEvent\(\)](#)
- [RTC\\_A.definePrescaleEvent\(\)](#)
- [RTC\\_A.enableInterrupt\(\)](#)
- [RTC\\_A.disableInterrupt\(\)](#)
- [RTC\\_A.getInterruptStatus\(\)](#)
- [RTC\\_A.clearInterrupt\(\)](#)

## 30.2.2 Function Documentation

### RTC\_A\_clearInterrupt()

```
void RTC_A_clearInterrupt (
    uint16_t baseAddress,
    uint8_t interruptFlagMask )
```

Clears selected RTC interrupt flags.

This function clears the RTC interrupt flag is cleared, so that it no longer asserts.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>interruptFlagMask</i>	is a bit mask of the interrupt flags to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>RTC_A_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <a href="#">defineCalendarEvent()</a> is met.</li> <li>■ <b>RTC_A_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_A_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_A_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_A_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> </ul>

**Returns**

None

**RTC\_A\_configureCalendarAlarm()**

```
void RTC_A_configureCalendarAlarm (
    uint16_t baseAddress,
    RTC_A_configureCalendarAlarmParam * param )
```

Sets and Enables the desired **Calendar** Alarm settings.

This function sets a **Calendar** interrupt condition to assert the RTCAIFG interrupt flag. The condition is a logical and of all of the parameters. For example if the minutes and hours alarm is set, then the interrupt will only assert when the minutes AND the hours change to the specified setting. Use the RTC\_A\_ALARM\_OFF for any alarm settings that should not be apart of the alarm condition.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>param</i>	is the pointer to struct for calendar alarm configuration.

**Returns**

None

References RTC\_A\_configureCalendarAlarmParam::dayOfMonthAlarm, RTC\_A\_configureCalendarAlarmParam::dayOfWeekAlarm, RTC\_A\_configureCalendarAlarmParam::hoursAlarm, and RTC\_A\_configureCalendarAlarmParam::minutesAlarm.

**RTC\_A\_definePrescaleEvent()**

```
void RTC_A_definePrescaleEvent (
    uint16_t baseAddress,
    uint8_t prescaleSelect,
    uint8_t prescaleEventDivider )
```

Sets up an interrupt condition for the selected Prescaler.

This function sets the condition for an interrupt to assert based on the individual prescalers.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>prescaleSelect</i>	is the prescaler to define an interrupt for. Valid values are: <ul style="list-style-type: none"> <li>■ RTC_A_PRESCALE_0</li> <li>■ RTC_A_PRESCALE_1</li> </ul>

## Parameters

<i>prescaleEventDivider</i>	<p>is a divider to specify when an interrupt can occur based on the clock source of the selected prescaler. (Does not affect timer of the selected prescaler). Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_A_PSEVENTDIVIDER_2</b> [Default]</li> <li>■ <b>RTC_A_PSEVENTDIVIDER_4</b></li> <li>■ <b>RTC_A_PSEVENTDIVIDER_8</b></li> <li>■ <b>RTC_A_PSEVENTDIVIDER_16</b></li> <li>■ <b>RTC_A_PSEVENTDIVIDER_32</b></li> <li>■ <b>RTC_A_PSEVENTDIVIDER_64</b></li> <li>■ <b>RTC_A_PSEVENTDIVIDER_128</b></li> <li>■ <b>RTC_A_PSEVENTDIVIDER_256</b></li> </ul> <p>Modified bits are <b>RTxIP</b> of <b>RTCPSxCTL</b> register.</p>
-----------------------------	--

## Returns

None

## RTC\_A\_disableInterrupt()

```
void RTC_A_disableInterrupt (
    uint16_t baseAddress,
    uint8_t interruptMask )
```

Disables selected RTC interrupt sources.

This function disables the selected RTC interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

## Parameters

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>interruptMask</i>	<p>is a bit mask of the interrupts to disable. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_A_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <b>Calendar</b> event condition defined by <b>defineCalendarEvent()</b> is met.</li> <li>■ <b>RTC_A_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <b>Calendar</b> mode is met.</li> <li>■ <b>RTC_A_CLOCK_READ_READY_INTERRUPT</b> - asserts when <b>Calendar</b> registers are settled.</li> <li>■ <b>RTC_A_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_A_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> </ul>

**Returns**

None

**RTC\_A\_enableInterrupt()**

```
void RTC_A.enableInterrupt (
    uint16_t baseAddress,
    uint8_t interruptMask )
```

Enables selected RTC interrupt sources.

This function enables the selected RTC interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>interruptMask</i>	is a bit mask of the interrupts to enable. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>RTC_A_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_A_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_A_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_A_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_A_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> </ul>

**Returns**

None

**RTC\_A\_getCalendarTime()**

```
Calendar RTC_A.getCalendarTime (
    uint16_t baseAddress )
```

Returns the [Calendar](#) Time stored in the [Calendar](#) registers of the RTC.

This function returns the current [Calendar](#) time in the form of a [Calendar](#) structure. The RTCRDY polling is used in this function to prevent reading invalid time.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
--------------------	--

**Returns**

A [Calendar](#) structure containing the current time.

References [Calendar::DayOfMonth](#), [Calendar::DayOfWeek](#), [Calendar::Hours](#), [Calendar::Minutes](#), [Calendar::Month](#), [Calendar::Seconds](#), and [Calendar::Year](#).

**RTC\_A\_getCounterValue()**

```
uint32_t RTC_A_getCounterValue (
    uint16_t baseAddress )
```

Returns the value of the Counter register.

This function returns the value of the counter register for the RTC\_A module. It will return the 32-bit value no matter the size set during initialization. The RTC should be held before trying to use this function.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
--------------------	--

**Returns**

The raw value of the full 32-bit Counter Register.

**RTC\_A\_getInterruptStatus()**

```
uint8_t RTC_A_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t interruptFlagMask )
```

Returns the status of the selected interrupts flags.

This function returns the status of the interrupt flag for the selected channel.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
--------------------	--

## Parameters

<i>interruptFlagMask</i>	<p>is a bit mask of the interrupt flags to return the status of. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_A_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_A_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_A_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_A_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_A_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> </ul>
--------------------------	--

## Returns

Logical OR of any of the following:

- **RTC\_A\_TIME\_EVENT\_INTERRUPT** asserts when counter overflows in counter mode or when [Calendar](#) event condition defined by `defineCalendarEvent()` is met.
  - **RTC\_A\_CLOCK\_ALARM\_INTERRUPT** asserts when alarm condition in [Calendar](#) mode is met.
  - **RTC\_A\_CLOCK\_READ\_READY\_INTERRUPT** asserts when [Calendar](#) registers are settled.
  - **RTC\_A\_PRESCALE\_TIMER0\_INTERRUPT** asserts when Prescaler 0 event condition is met.
  - **RTC\_A\_PRESCALE\_TIMER1\_INTERRUPT** asserts when Prescaler 1 event condition is met.
- indicating the status of the masked interrupts

## RTC\_A\_getPrescaleValue()

```
uint8_t RTC_A_getPrescaleValue (
    uint16_t baseAddress,
    uint8_t prescaleSelect )
```

Returns the selected prescaler value.

This function returns the value of the selected prescale counter register. Note that the counter value should be held by calling [RTC\\_A\\_holdClock\(\)](#) before calling this API.

## Parameters

<i>baseAddress</i>	is the base address of the RTC_A module.
--------------------	--

**Parameters**

<i>prescaleSelect</i>	is the prescaler to obtain the value of. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_A_PRESCALE_0</b></li> <li>■ <b>RTC_A_PRESCALE_1</b></li> </ul>
-----------------------	---

**Returns**

The value of the specified prescaler count register

**RTC\_A\_holdClock()**

```
void RTC_A_holdClock (
    uint16_t baseAddress )
```

Holds the RTC.

This function sets the RTC main hold bit to disable RTC functionality.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
--------------------	--

**Returns**

None

**RTC\_A\_holdCounterPrescale()**

```
void RTC_A_holdCounterPrescale (
    uint16_t baseAddress,
    uint8_t prescaleSelect )
```

Holds the selected Prescaler.

This function holds the prescale counter from continuing. This will only work in counter mode, in [Calendar](#) mode, the [RTC\\_A\\_holdClock\(\)](#) must be used. In counter mode, if using both prescalers in conjunction with the main RTC counter, then stopping RT0PS will stop RT1PS, but stopping RT1PS will not stop RT0PS.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>prescaleSelect</i>	is the prescaler to hold. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_A_PRESCALE_0</b></li> <li>■ <b>RTC_A_PRESCALE_1</b></li> </ul>



**Returns**

None

**RTC\_A\_initCalendar()**

```
void RTC_A_initCalendar (
    uint16_t baseAddress,
    Calendar * CalendarTime,
    uint16_t formatSelect )
```

Initializes the settings to operate the RTC in calendar mode.

This function initializes the [Calendar](#) mode of the RTC module. To prevent potential erroneous alarm conditions from occurring, the alarm should be disabled by clearing the RTCAIE, RTCAIFG and AE bits with APIs: [RTC\\_A\\_disableInterrupt\(\)](#), [RTC\\_A\\_clearInterrupt\(\)](#) and [RTC\\_A\\_configureCalendarAlarm\(\)](#) before calendar initialization.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>CalendarTime</i>	is the pointer to the structure containing the values for the <a href="#">Calendar</a> to be initialized to. Valid values should be of type pointer to <a href="#">Calendar</a> and should contain the following members and corresponding values: <b>Seconds</b> between 0-59 <b>Minutes</b> between 0-59 <b>Hours</b> between 0-23 <b>DayOfWeek</b> between 0-6 <b>DayOfMonth</b> between 1-31 <b>Year</b> between 0-4095 NOTE: Values beyond the ones specified may result in erratic behavior.
<i>formatSelect</i>	is the format for the <a href="#">Calendar</a> registers to use. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_A_FORMAT_BINARY</b> [Default]</li> <li>■ <b>RTC_A_FORMAT_BCD</b> Modified bits are <b>RTCBCD</b> of <b>RTCCTL1</b> register.</li> </ul>

**Returns**

None

References [Calendar::DayOfMonth](#), [Calendar::DayOfWeek](#), [Calendar::Hours](#), [Calendar::Minutes](#), [Calendar::Month](#), [Calendar::Seconds](#), and [Calendar::Year](#).

**RTC\_A\_initCounter()**

```
void RTC_A_initCounter (
    uint16_t baseAddress,
    uint16_t clockSelect,
    uint16_t counterSizeSelect )
```

Initializes the settings to operate the RTC in Counter mode.

This function initializes the Counter mode of the RTC.A. Setting the clock source and counter size will allow an interrupt from the RTCTEVIFG once an overflow to the counter register occurs.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>clockSelect</i>	is the selected clock for the counter mode to use. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_A_CLOCKSELECT_ACLK</b> [Default]</li> <li>■ <b>RTC_A_CLOCKSELECT_SMCLK</b></li> <li>■ <b>RTC_A_CLOCKSELECT_RT1PS</b> - use Prescaler 1 as source to RTC Modified bits are <b>RTCSEL</b> of <b>RTCCTL1</b> register.</li> </ul>
<i>counterSizeSelect</i>	is the size of the counter. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_A_COUNTERSIZE_8BIT</b> [Default]</li> <li>■ <b>RTC_A_COUNTERSIZE_16BIT</b></li> <li>■ <b>RTC_A_COUNTERSIZE_24BIT</b></li> <li>■ <b>RTC_A_COUNTERSIZE_32BIT</b> Modified bits are <b>RTCCTEV</b> of <b>RTCCTL1</b> register.</li> </ul>

**Returns**

None

**RTC\_A\_initCounterPrescale()**

```
void RTC_A_initCounterPrescale (
    uint16_t baseAddress,
    uint8_t prescaleSelect,
    uint16_t prescaleClockSelect,
    uint16_t prescaleDivider )
```

Initializes the Prescaler for Counter mode.

This function initializes the selected prescaler for the counter mode in the RTC\_A module. If the RTC is initialized in [Calendar](#) mode, then these are automatically initialized. The Prescalers can be used to divide a clock source additionally before it gets to the main RTC clock.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>prescaleSelect</i>	is the prescaler to initialize. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_A_PRESCALE_0</b></li> <li>■ <b>RTC_A_PRESCALE_1</b></li> </ul>

## Parameters

<i>prescaleClockSelect</i>	<p>is the clock to drive the selected prescaler. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_A_PSCLOCKSELECT_ACLK</b></li> <li>■ <b>RTC_A_PSCLOCKSELECT_SMCLK</b></li> <li>■ <b>RTC_A_PSCLOCKSELECT_RT0PS</b> - use Prescaler 0 as source to Prescaler 1 (May only be used if prescaleSelect is RTC_A_PRESCALE_1)</li> </ul> <p>Modified bits are <b>RTxSSEL</b> of <b>RTCPSxCTL</b> register.</p>
<i>prescaleDivider</i>	<p>is the divider for the selected clock source. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_A_PSDIVIDER_2</b> [Default]</li> <li>■ <b>RTC_A_PSDIVIDER_4</b></li> <li>■ <b>RTC_A_PSDIVIDER_8</b></li> <li>■ <b>RTC_A_PSDIVIDER_16</b></li> <li>■ <b>RTC_A_PSDIVIDER_32</b></li> <li>■ <b>RTC_A_PSDIVIDER_64</b></li> <li>■ <b>RTC_A_PSDIVIDER_128</b></li> <li>■ <b>RTC_A_PSDIVIDER_256</b></li> </ul> <p>Modified bits are <b>RTxPSDIV</b> of <b>RTCPSxCTL</b> register.</p>

## Returns

None

## RTC\_A\_setCalendarEvent()

```
void RTC_A_setCalendarEvent (
    uint16_t baseAddress,
    uint16_t eventSelect )
```

Sets a single specified [Calendar](#) interrupt condition.

This function sets a specified event to assert the RTCTEVIFG interrupt. This interrupt is independent from the [Calendar](#) alarm interrupt.

## Parameters

<i>baseAddress</i>	is the base address of the RTC_A module.
--------------------	--

## Parameters

<i>eventSelect</i>	<p>is the condition selected. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_A_CALENDAREVENT_MINUTECHANGE</b> - assert interrupt on every minute</li> <li>■ <b>RTC_A_CALENDAREVENT_HOURLCHANGE</b> - assert interrupt on every hour</li> <li>■ <b>RTC_A_CALENDAREVENT_NOON</b> - assert interrupt when hour is 12</li> <li>■ <b>RTC_A_CALENDAREVENT_MIDNIGHT</b> - assert interrupt when hour is 0</li> </ul> <p>Modified bits are <b>RTCCTEV</b> of <b>RTCCTL</b> register.</p>
--------------------	---

## Returns

None

## RTC\_A\_setCalibrationData()

```
void RTC_A_setCalibrationData (
    uint16_t baseAddress,
    uint8_t offsetDirection,
    uint8_t offsetValue )
```

Sets the specified calibration for the RTC.

This function sets the calibration offset to make the RTC as accurate as possible. The *offsetDirection* can be either +4-ppm or -2-ppm, and the *offsetValue* should be from 1-63 and is multiplied by the direction setting (i.e. +4-ppm \* 8 (*offsetValue*) = +32-ppm). Please note, when measuring the frequency after setting the calibration, you will only see a change on the 1Hz frequency.

## Parameters

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>offsetDirection</i>	<p>is the direction that the calibration offset will go. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_A_CALIBRATION_DOWN2PPM</b> - calibrate at steps of -2</li> <li>■ <b>RTC_A_CALIBRATION_UP4PPM</b> - calibrate at steps of +4</li> </ul> <p>Modified bits are <b>RTCCALS</b> of <b>RTCCTL2</b> register.</p>
<i>offsetValue</i>	<p>is the value that the offset will be a factor of; a valid value is any integer from 1-63.</p> <p>Modified bits are <b>RTCCAL</b> of <b>RTCCTL2</b> register.</p>

**Returns**

None

**RTC\_A\_setCalibrationFrequency()**

```
void RTC_A_setCalibrationFrequency (
    uint16_t baseAddress,
    uint16_t frequencySelect )
```

Allows and Sets the frequency output to RTCCLK pin for calibration measurement.

This function sets a frequency to measure at the RTCCLK output pin. After testing the set frequency, the calibration could be set accordingly.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>frequencySelect</i>	is the frequency output to RTCCLK. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_A_CALIBRATIONFREQ_OFF</b> [Default] - turn off calibration output</li> <li>■ <b>RTC_A_CALIBRATIONFREQ_512HZ</b> - output signal at 512Hz for calibration</li> <li>■ <b>RTC_A_CALIBRATIONFREQ_256HZ</b> - output signal at 256Hz for calibration</li> <li>■ <b>RTC_A_CALIBRATIONFREQ_1HZ</b> - output signal at 1Hz for calibration</li> </ul> Modified bits are <b>RTCCALF</b> of <b>RTCCTL3</b> register.

**Returns**

None

**RTC\_A\_setCounterValue()**

```
void RTC_A_setCounterValue (
    uint16_t baseAddress,
    uint32_t counterValue )
```

Sets the value of the Counter register.

This function sets the counter register of the RTC\_A module.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>counterValue</i>	is the value to set the Counter register to; a valid value may be any 32-bit integer.

**Returns**

None

**RTC\_A\_setPrescaleValue()**

```
void RTC_A_setPrescaleValue (
    uint16_t baseAddress,
    uint8_t prescaleSelect,
    uint8_t prescaleCounterValue )
```

Sets the selected prescaler value.

This function sets the prescale counter value. Before setting the prescale counter, it should be held by calling [RTC\\_A\\_holdClock\(\)](#).

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>prescaleSelect</i>	is the prescaler to set the value for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_A_PRESCALE_0</b></li> <li>■ <b>RTC_A_PRESCALE_1</b></li> </ul>
<i>prescaleCounterValue</i>	is the specified value to set the prescaler to. Valid values are any integer between 0-255 Modified bits are <b>RTxPS</b> of <b>RTxPS</b> register.

**Returns**

None

**RTC\_A\_startClock()**

```
void RTC_A_startClock (
    uint16_t baseAddress )
```

Starts the RTC.

This function clears the RTC main hold bit to allow the RTC to function.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
--------------------	--

**Returns**

None

**RTC\_A\_startCounterPrescale()**

```
void RTC_A_startCounterPrescale (
    uint16_t baseAddress,
    uint8_t prescaleSelect )
```

Starts the selected Prescaler.

This function starts the selected prescale counter. This function will only work if the RTC is in counter mode.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_A module.
<i>prescaleSelect</i>	is the prescaler to start. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_A_PRESCALE_0</b></li> <li>■ <b>RTC_A_PRESCALE_1</b></li> </ul>

**Returns**

None

## 30.3 Programming Example

The following example shows how to initialize and use the RTC API to setup Calendar Mode with the current time and various interrupts.

```
//Initialize calendar struct
Calendar currentTime;
currentTime.Seconds = 0x00;
currentTime.Minutes = 0x26;
currentTime.Hours = 0x13;
currentTime.DayOfWeek = 0x03;
currentTime.DayOfMonth = 0x20;
currentTime.Month = 0x07;
currentTime.Year = 0x2011;

//Initialize alarm struct
RTC_A_configureCalendarAlarmParam alarmParam;
alarmParam.minutesAlarm = 0x00;
alarmParam.hoursAlarm = 0x17;
alarmParam.dayOfWeekAlarm = RTC_A_ALARMCONDITION_OFF;
alarmParam.dayOfMonthAlarm = 0x05;

//Initialize Calendar Mode of RTC_A
/*
 * Base Address of the RTC_A
 * Pass in current time, initialized above
 * Use BCD as Calendar Register Format
 */
RTC_A_initCalendar(RTC_A_BASE,
    &currentTime,
    RTC_A_FORMAT_BCD);
```

```
//Setup Calendar Alarm for 5:00pm on the 5th day of the month.
//Note: Does not specify day of the week.
RTC_C.configureCalendarAlarm(RTC_A.BASE, &alarmParam);

//Specify an interrupt to assert every minute
RTC_A.setCalendarEvent(RTC_A.BASE,
    RTC_A.CALENDAREVENT_MINUTECHANGE);

//Enable interrupt for RTC_A Ready Status, which asserts when the RTC_A
//Calendar registers are ready to read.
//Also, enable interrupts for the Calendar alarm and Calendar event.
RTC_A.enableInterrupt(RTC_A.BASE,
    RTC_A.CLOCK_READ_READY_INTERRUPT +
    RTC_A.TIME_EVENT_INTERRUPT +
    RTC_A.CLOCK_ALARM_INTERRUPT);

//Start RTC_A Clock
RTC_A.startClock(RTC_A.BASE);

//Enter LPM3 mode with interrupts enabled
_bis_SR_register(LPM3_bits + GIE);
__no_operation();
```



## 31 Real-Time Clock (RTC\_B)

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### 31.1 Introduction

The Real Time Clock (RTC\_B) API provides a set of functions for using the MSP430Ware RTC\_B modules. Functions are provided to calibrate the clock, initialize the RTC modules in calendar mode, and setup conditions for, and enable, interrupts for the RTC modules. If an RTC\_B module is used, then prescale counters are also initialized.

The RTC\_B module provides the ability to keep track of the current time and date in calendar mode.

The RTC\_B module generates multiple interrupts. There are 2 interrupts that can be defined in calendar mode, and 1 interrupt for user-configured event, as well as an interrupt for each prescaler.

### 31.2 API Functions

#### Functions

- void [RTC\\_B\\_startClock](#) (uint16\_t baseAddress)  
*Starts the RTC.*
- void [RTC\\_B\\_holdClock](#) (uint16\_t baseAddress)  
*Holds the RTC.*
- void [RTC\\_B\\_setCalibrationFrequency](#) (uint16\_t baseAddress, uint16\_t frequencySelect)  
*Allows and Sets the frequency output to RTCCLK pin for calibration measurement.*
- void [RTC\\_B\\_setCalibrationData](#) (uint16\_t baseAddress, uint8\_t offsetDirection, uint8\_t offsetValue)  
*Sets the specified calibration for the RTC.*
- void [RTC\\_B\\_initCalendar](#) (uint16\_t baseAddress, [Calendar](#) \*CalendarTime, uint16\_t formatSelect)  
*Initializes the settings to operate the RTC in calendar mode.*
- [Calendar](#) [RTC\\_B\\_getCalendarTime](#) (uint16\_t baseAddress)  
*Returns the [Calendar](#) Time stored in the [Calendar](#) registers of the RTC.*
- void [RTC\\_B\\_configureCalendarAlarm](#) (uint16\_t baseAddress, [RTC\\_B\\_configureCalendarAlarmParam](#) \*param)  
*Sets and Enables the desired [Calendar](#) Alarm settings.*
- void [RTC\\_B\\_setCalendarEvent](#) (uint16\_t baseAddress, uint16\_t eventSelect)  
*Sets a single specified [Calendar](#) interrupt condition.*
- void [RTC\\_B\\_definePrescaleEvent](#) (uint16\_t baseAddress, uint8\_t prescaleSelect, uint8\_t prescaleEventDivider)  
*Sets up an interrupt condition for the selected Prescaler.*
- uint8\_t [RTC\\_B\\_getPrescaleValue](#) (uint16\_t baseAddress, uint8\_t prescaleSelect)  
*Returns the selected prescaler value.*

- void `RTC_B_setPrescaleValue` (uint16\_t baseAddress, uint8\_t prescaleSelect, uint8\_t prescaleCounterValue)  
*Sets the selected prescaler value.*
- void `RTC_B_enableInterrupt` (uint16\_t baseAddress, uint8\_t interruptMask)  
*Enables selected RTC interrupt sources.*
- void `RTC_B_disableInterrupt` (uint16\_t baseAddress, uint8\_t interruptMask)  
*Disables selected RTC interrupt sources.*
- uint8\_t `RTC_B_getInterruptStatus` (uint16\_t baseAddress, uint8\_t interruptFlagMask)  
*Returns the status of the selected interrupts flags.*
- void `RTC_B_clearInterrupt` (uint16\_t baseAddress, uint8\_t interruptFlagMask)  
*Clears selected RTC interrupt flags.*
- uint16\_t `RTC_B_convertBCDToBinary` (uint16\_t baseAddress, uint16\_t valueToConvert)  
*Convert the given BCD value to binary format.*
- uint16\_t `RTC_B_convertBinaryToBCD` (uint16\_t baseAddress, uint16\_t valueToConvert)  
*Convert the given binary value to BCD format.*

### 31.2.1 Detailed Description

The RTC\_B API is broken into 5 groups of functions: clock settings, calendar mode, prescale counter, interrupt condition setup/enable functions and data conversion.

The RTC\_B clock settings are handled by

- `RTC_B_startClock()`
- `RTC_B_holdClock()`
- `RTC_B_setCalibrationFrequency()`
- `RTC_B_setCalibrationData()`

The RTC\_B calendar mode is initialized and handled by

- `RTC_B_initCalendar()`
- `RTC_B_configureCalendarAlarm()`
- `RTC_B_getCalendarTime()`

The RTC\_B prescale counter is handled by

- `RTC_B_getPrescaleValue()`
- `RTC_B_setPrescaleValue()`

The RTC\_B interrupts are handled by

- `RTC_B_definePrescaleEvent()`
- `RTC_B_setCalendarEvent()`
- `RTC_B_enableInterrupt()`
- `RTC_B_disableInterrupt()`
- `RTC_B_getInterruptStatus()`
- `RTC_B_clearInterrupt()`

The RTC\_B conversions are handled by

- `RTC_B_convertBCDToBinary()`
- `RTC_B_convertBinaryToBCD()`

## 31.2.2 Function Documentation

### RTC\_B\_clearInterrupt()

```
void RTC_B_clearInterrupt (
    uint16_t baseAddress,
    uint8_t interruptFlagMask )
```

Clears selected RTC interrupt flags.

This function clears the RTC interrupt flag is cleared, so that it no longer asserts.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>interruptFlagMask</i>	<p>is a bit mask of the interrupt flags to be cleared. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_B_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_B_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_B_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_B_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_B_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> <li>■ <b>RTC_B_OSCILLATOR_FAULT_INTERRUPT</b> - asserts if there is a problem with the 32kHz oscillator, while the RTC is running.</li> </ul>

#### Returns

None

### RTC\_B\_configureCalendarAlarm()

```
void RTC_B_configureCalendarAlarm (
    uint16_t baseAddress,
    RTC_B_configureCalendarAlarmParam * param )
```

Sets and Enables the desired [Calendar](#) Alarm settings.

This function sets a [Calendar](#) interrupt condition to assert the RTCAIFG interrupt flag. The condition is a logical and of all of the parameters. For example if the minutes and hours alarm is set, then the interrupt will only assert when the minutes AND the hours change to the specified setting. Use the `RTC_B_ALARM_OFF` for any alarm settings that should not be apart of the alarm condition.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC.B module.
<i>param</i>	is the pointer to struct for calendar alarm configuration.

**Returns**

None

References RTC\_B\_configureCalendarAlarmParam::dayOfMonthAlarm, RTC\_B\_configureCalendarAlarmParam::dayOfWeekAlarm, RTC\_B\_configureCalendarAlarmParam::hoursAlarm, and RTC\_B\_configureCalendarAlarmParam::minutesAlarm.

**RTC\_B\_convertBCDToBinary()**

```
uint16_t RTC_B_convertBCDToBinary (
    uint16_t baseAddress,
    uint16_t valueToConvert )
```

Convert the given BCD value to binary format.

This function converts BCD values to binary format. This API uses the hardware registers to perform the conversion rather than a software method.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>valueToConvert</i>	is the raw value in BCD format to convert to Binary. Modified bits are <b>BCD2BIN</b> of <b>BCD2BIN</b> register.

**Returns**

The binary version of the input parameter

**RTC\_B\_convertBinaryToBCD()**

```
uint16_t RTC_B_convertBinaryToBCD (
    uint16_t baseAddress,
    uint16_t valueToConvert )
```

Convert the given binary value to BCD format.

This function converts binary values to BCD format. This API uses the hardware registers to perform the conversion rather than a software method.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>valueToConvert</i>	is the raw value in Binary format to convert to BCD. Modified bits are <b>BIN2BCD</b> of <b>BIN2BCD</b> register.

**Returns**

The BCD version of the valueToConvert parameter

**RTC\_B\_definePrescaleEvent()**

```
void RTC_B.definePrescaleEvent (
    uint16_t baseAddress,
    uint8_t prescaleSelect,
    uint8_t prescaleEventDivider )
```

Sets up an interrupt condition for the selected Prescaler.

This function sets the condition for an interrupt to assert based on the individual prescalers.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>prescaleSelect</i>	is the prescaler to define an interrupt for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B.PRESCALE_0</b></li> <li>■ <b>RTC_B.PRESCALE_1</b></li> </ul>
<i>prescaleEventDivider</i>	is a divider to specify when an interrupt can occur based on the clock source of the selected prescaler. (Does not affect timer of the selected prescaler). Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B.PSEVENTDIVIDER_2</b> [Default]</li> <li>■ <b>RTC_B.PSEVENTDIVIDER_4</b></li> <li>■ <b>RTC_B.PSEVENTDIVIDER_8</b></li> <li>■ <b>RTC_B.PSEVENTDIVIDER_16</b></li> <li>■ <b>RTC_B.PSEVENTDIVIDER_32</b></li> <li>■ <b>RTC_B.PSEVENTDIVIDER_64</b></li> <li>■ <b>RTC_B.PSEVENTDIVIDER_128</b></li> <li>■ <b>RTC_B.PSEVENTDIVIDER_256</b></li> </ul> Modified bits are <b>RTxIP</b> of <b>RTCPSxCTL</b> register.

**Returns**

None

**RTC\_B\_disableInterrupt()**

```
void RTC_B.disableInterrupt (
    uint16_t baseAddress,
    uint8_t interruptMask )
```

Disables selected RTC interrupt sources.

This function disables the selected RTC interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>interruptMask</i>	is a bit mask of the interrupts to disable. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>RTC_B_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_B_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_B_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_B_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_B_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> <li>■ <b>RTC_B_OSCILLATOR_FAULT_INTERRUPT</b> - asserts if there is a problem with the 32kHz oscillator, while the RTC is running.</li> </ul>

**Returns**

None

**RTC\_B\_enableInterrupt()**

```
void RTC_B_enableInterrupt (
    uint16_t baseAddress,
    uint8_t interruptMask )
```

Enables selected RTC interrupt sources.

This function enables the selected RTC interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
--------------------	--

**Parameters**

<i>interruptMask</i>	<p>is a bit mask of the interrupts to enable. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_B_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_B_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_B_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_B_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_B_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> <li>■ <b>RTC_B_OSCILLATOR_FAULT_INTERRUPT</b> - asserts if there is a problem with the 32kHz oscillator, while the RTC is running.</li> </ul>
----------------------	---

**Returns**

None

**RTC\_B\_getCalendarTime()**

```
Calendar RTC_B_getCalendarTime (
    uint16_t baseAddress )
```

Returns the [Calendar](#) Time stored in the [Calendar](#) registers of the RTC.

This function returns the current [Calendar](#) time in the form of a [Calendar](#) structure. The RTCRDY polling is used in this function to prevent reading invalid time.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
--------------------	--

**Returns**

A [Calendar](#) structure containing the current time.

References [Calendar::DayOfMonth](#), [Calendar::DayOfWeek](#), [Calendar::Hours](#), [Calendar::Minutes](#), [Calendar::Month](#), [Calendar::Seconds](#), and [Calendar::Year](#).

**RTC\_B\_getInterruptStatus()**

```
uint8_t RTC_B_getInterruptStatus (
    uint16_t baseAddress,
```

```
uint8_t interruptFlagMask )
```

Returns the status of the selected interrupts flags.

This function returns the status of the interrupt flag for the selected channel.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>interruptFlagMask</i>	is a bit mask of the interrupt flags to return the status of. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>RTC_B_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_B_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_B_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_B_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_B_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> <li>■ <b>RTC_B_OSCILLATOR_FAULT_INTERRUPT</b> - asserts if there is a problem with the 32kHz oscillator, while the RTC is running.</li> </ul>

#### Returns

Logical OR of any of the following:

- **RTC\_B\_TIME\_EVENT\_INTERRUPT** asserts when counter overflows in counter mode or when [Calendar](#) event condition defined by `defineCalendarEvent()` is met.
  - **RTC\_B\_CLOCK\_ALARM\_INTERRUPT** asserts when alarm condition in [Calendar](#) mode is met.
  - **RTC\_B\_CLOCK\_READ\_READY\_INTERRUPT** asserts when [Calendar](#) registers are settled.
  - **RTC\_B\_PRESCALE\_TIMER0\_INTERRUPT** asserts when Prescaler 0 event condition is met.
  - **RTC\_B\_PRESCALE\_TIMER1\_INTERRUPT** asserts when Prescaler 1 event condition is met.
  - **RTC\_B\_OSCILLATOR\_FAULT\_INTERRUPT** asserts if there is a problem with the 32kHz oscillator, while the RTC is running.
- indicating the status of the masked interrupts

#### RTC\_B\_getPrescaleValue()

```
uint8_t RTC_B_getPrescaleValue (
    uint16_t baseAddress,
    uint8_t prescaleSelect )
```

Returns the selected prescaler value.



This function returns the value of the selected prescale counter register. Note that the counter value should be held by calling [RTC\\_B\\_holdClock\(\)](#) before calling this API.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>prescaleSelect</i>	is the prescaler to obtain the value of. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B_PRESCALE_0</b></li> <li>■ <b>RTC_B_PRESCALE_1</b></li> </ul>

#### Returns

The value of the specified prescaler count register

### RTC\_B\_holdClock()

```
void RTC_B_holdClock (
    uint16_t baseAddress )
```

Holds the RTC.

This function sets the RTC main hold bit to disable RTC functionality.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
--------------------	--

#### Returns

None

### RTC\_B\_initCalendar()

```
void RTC_B_initCalendar (
    uint16_t baseAddress,
    Calendar * CalendarTime,
    uint16_t formatSelect )
```

Initializes the settings to operate the RTC in calendar mode.

This function initializes the [Calendar](#) mode of the RTC module. To prevent potential erroneous alarm conditions from occurring, the alarm should be disabled by clearing the RTCAIE, RTCAIFG and AE bits with APIs: [RTC\\_B\\_disableInterrupt\(\)](#), [RTC\\_B\\_clearInterrupt\(\)](#) and [RTC\\_B\\_configureCalendarAlarm\(\)](#) before calendar initialization.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
--------------------	--

**Parameters**

<i>CalendarTime</i>	is the pointer to the structure containing the values for the <a href="#">Calendar</a> to be initialized to. Valid values should be of type pointer to <a href="#">Calendar</a> and should contain the following members and corresponding values: <b>Seconds</b> between 0-59 <b>Minutes</b> between 0-59 <b>Hours</b> between 0-23 <b>DayOfWeek</b> between 0-6 <b>DayOfMonth</b> between 1-31 <b>Year</b> between 0-4095 NOTE: Values beyond the ones specified may result in erratic behavior.
<i>formatSelect</i>	is the format for the <a href="#">Calendar</a> registers to use. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B_FORMAT_BINARY</b> [Default]</li> <li>■ <b>RTC_B_FORMAT_BCD</b> Modified bits are <b>RTCB</b>CD of <b>RTCCTL1</b> register.</li> </ul>

**Returns**

None

References [Calendar::DayOfMonth](#), [Calendar::DayOfWeek](#), [Calendar::Hours](#), [Calendar::Minutes](#), [Calendar::Month](#), [Calendar::Seconds](#), and [Calendar::Year](#).

**RTC\_B\_setCalendarEvent()**

```
void RTC_B.setCalendarEvent (
    uint16_t baseAddress,
    uint16_t eventSelect )
```

Sets a single specified [Calendar](#) interrupt condition.

This function sets a specified event to assert the **RTCCTEVIFG** interrupt. This interrupt is independent from the [Calendar](#) alarm interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the <b>RTC.B</b> module.
<i>eventSelect</i>	is the condition selected. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B_CALENDAREVENT_MINUTECHANGE</b> - assert interrupt on every minute</li> <li>■ <b>RTC_B_CALENDAREVENT_HOURLCHANGE</b> - assert interrupt on every hour</li> <li>■ <b>RTC_B_CALENDAREVENT_NOON</b> - assert interrupt when hour is 12</li> <li>■ <b>RTC_B_CALENDAREVENT_MIDNIGHT</b> - assert interrupt when hour is 0</li> </ul> Modified bits are <b>RTCCTEV</b> of <b>RTCCTL</b> register.

**Returns**

None

**RTC\_B\_setCalibrationData()**

```
void RTC_B.setCalibrationData (
    uint16_t baseAddress,
    uint8_t  offsetDirection,
    uint8_t  offsetValue )
```

Sets the specified calibration for the RTC.

This function sets the calibration offset to make the RTC as accurate as possible. The `offsetDirection` can be either +4-ppm or -2-ppm, and the `offsetValue` should be from 1-63 and is multiplied by the direction setting (i.e. +4-ppm \* 8 (offsetValue) = +32-ppm). Please note, when measuring the frequency after setting the calibration, you will only see a change on the 1Hz frequency.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC.B module.
<i>offsetDirection</i>	is the direction that the calibration offset will go. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B_CALIBRATION_DOWN2PPM</b> - calibrate at steps of -2</li> <li>■ <b>RTC_B_CALIBRATION_UP4PPM</b> - calibrate at steps of +4</li> </ul> Modified bits are <b>RTCCALS</b> of <b>RTCCTL2</b> register.
<i>offsetValue</i>	is the value that the offset will be a factor of; a valid value is any integer from 1-63. Modified bits are <b>RTCCAL</b> of <b>RTCCTL2</b> register.

**Returns**

None

**RTC\_B\_setCalibrationFrequency()**

```
void RTC_B.setCalibrationFrequency (
    uint16_t baseAddress,
    uint16_t frequencySelect )
```

Allows and Sets the frequency output to RTCCLK pin for calibration measurement.

This function sets a frequency to measure at the RTCCLK output pin. After testing the set frequency, the calibration could be set accordingly.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC.B module.
--------------------	--

## Parameters

<i>frequencySelect</i>	<p>is the frequency output to RTCCLK. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_B_CALIBRATIONFREQ_OFF</b> [Default] - turn off calibration output</li> <li>■ <b>RTC_B_CALIBRATIONFREQ_512HZ</b> - output signal at 512Hz for calibration</li> <li>■ <b>RTC_B_CALIBRATIONFREQ_256HZ</b> - output signal at 256Hz for calibration</li> <li>■ <b>RTC_B_CALIBRATIONFREQ_1HZ</b> - output signal at 1Hz for calibration</li> </ul> <p>Modified bits are <b>RTCCALF</b> of <b>RTCCTL3</b> register.</p>
------------------------	---

## Returns

None

## RTC\_B\_setPrescaleValue()

```
void RTC_B_setPrescaleValue (
    uint16_t baseAddress,
    uint8_t prescaleSelect,
    uint8_t prescaleCounterValue )
```

Sets the selected prescaler value.

This function sets the prescale counter value. Before setting the prescale counter, it should be held by calling [RTC\\_B\\_holdClock\(\)](#).

## Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>prescaleSelect</i>	<p>is the prescaler to set the value for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_B_PRESCALE_0</b></li> <li>■ <b>RTC_B_PRESCALE_1</b></li> </ul>
<i>prescaleCounterValue</i>	<p>is the specified value to set the prescaler to. Valid values are any integer between 0-255</p> <p>Modified bits are <b>RTxPS</b> of <b>RTxPS</b> register.</p>

## Returns

None

## RTC\_B\_startClock()

```
void RTC_B_startClock (
    uint16_t baseAddress )
```

Starts the RTC.

This function clears the RTC main hold bit to allow the RTC to function.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
--------------------	--

#### Returns

None

## 31.3 Programming Example

The following example shows how to initialize and use the RTC API to setup Calendar Mode with the current time and various interrupts.

```
//Initialize calendar struct
Calendar currentTime;
currentTime.Seconds    = 0x00;
currentTime.Minutes   = 0x26;
currentTime.Hours     = 0x13;
currentTime.DayOfWeek = 0x03;
currentTime.DayOfMonth = 0x20;
currentTime.Month     = 0x07;
currentTime.Year      = 0x2011;

//Initialize alarm struct
RTC_B_configureCalendarAlarmParam alarmParam;
alarmParam.minutesAlarm = 0x00;
alarmParam.hoursAlarm  = 0x17;
alarmParam.dayOfWeekAlarm = RTC_B_ALARMCONDITION_OFF;
alarmParam.dayOfMonthAlarm = 0x05;

//Initialize Calendar Mode of RTC_B
/*
 * Base Address of the RTC_B
 * Pass in current time, initialized above
 * Use BCD as Calendar Register Format
 */
RTC_B_initCalendar(RTC_B_BASE,
                  &currentTime,
                  RTC_B_FORMAT_BCD);

//Setup Calendar Alarm for 5:00pm on the 5th day of the month.
//Note: Does not specify day of the week.
RTC_B_setCalendarAlarm(RTC_B_BASE, &alarmParam);

//Specify an interrupt to assert every minute
RTC_B_setCalendarEvent(RTC_B_BASE,
                      RTC_B_CALENDAREVENT_MINUTECHANGE);

//Enable interrupt for RTC_B Ready Status, which asserts when the RTC_B
//Calendar registers are ready to read.
//Also, enable interrupts for the Calendar alarm and Calendar event.
RTC_B_enableInterrupt(RTC_B_BASE,
                     RTC_B_CLOCK_READ_READY_INTERRUPT +
                     RTC_B_TIME_EVENT_INTERRUPT +
                     RTC_B_CLOCK_ALARM_INTERRUPT);

//Start RTC_B Clock
RTC_B_startClock(RTC_B_BASE);

//Enter LPM3 mode with interrupts enabled
__bis_SR_register(LPM3_bits + GIE);
__no_operation();
```

## 32 Real-Time Clock (RTC\_C)

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### 32.1 Introduction

The Real Time Clock (RTC\_C) API provides a set of functions for using the MSP430Ware RTC\_C modules. Functions are provided to calibrate the clock, initialize the RTC\_C modules in [Calendar](#) mode, and setup conditions for, and enable, interrupts for the RTC\_C modules.

The RTC\_C module provides the ability to keep track of the current time and date in calendar mode. The counter mode (device-dependent) provides a 32-bit counter.

The RTC\_C module generates multiple interrupts. There are 2 interrupts that can be defined in calendar mode, and 1 interrupt in counter mode for counter overflow, as well as an interrupt for each prescaler.

If the device header file defines the baseaddress as RTC\_C.BASE, pass in RTC\_C.BASE as the baseaddress parameter. If the device header file defines the baseaddress as RTC\_CE.BASE, pass in RTC\_CE.BASE as the baseaddress parameter.

### 32.2 API Functions

#### Functions

- void [RTC\\_C.startClock](#) (uint16\_t baseAddress)  
*Starts the RTC.*
- void [RTC\\_C.holdClock](#) (uint16\_t baseAddress)  
*Holds the RTC.*
- void [RTC\\_C.setCalibrationFrequency](#) (uint16\_t baseAddress, uint16\_t frequencySelect)  
*Allows and Sets the frequency output to RTCCLK pin for calibration measurement.*
- void [RTC\\_C.setCalibrationData](#) (uint16\_t baseAddress, uint8\_t offsetDirection, uint8\_t offsetValue)  
*Sets the specified calibration for the RTC.*
- void [RTC\\_C.initCounter](#) (uint16\_t baseAddress, uint16\_t clockSelect, uint16\_t counterSizeSelect)  
*Initializes the settings to operate the RTC in Counter mode.*
- bool [RTC\\_C.setTemperatureCompensation](#) (uint16\_t baseAddress, uint16\_t offsetDirection, uint8\_t offsetValue)  
*Sets the specified temperature compensation for the RTC.*
- void [RTC\\_C.initCalendar](#) (uint16\_t baseAddress, [Calendar](#) \*CalendarTime, uint16\_t formatSelect)  
*Initializes the settings to operate the RTC in calendar mode.*
- [Calendar](#) [RTC\\_C.getCalendarTime](#) (uint16\_t baseAddress)  
*Returns the Calendar Time stored in the Calendar registers of the RTC.*
- void [RTC\\_C.configureCalendarAlarm](#) (uint16\_t baseAddress, [RTC\\_C.configureCalendarAlarmParam](#) \*param)

- Sets and Enables the desired [Calendar Alarm](#) settings.*
- void [RTC\\_C\\_setCalendarEvent](#) (uint16\_t baseAddress, uint16\_t eventSelect)
  - Sets a single specified [Calendar](#) interrupt condition.*
- uint32\_t [RTC\\_C\\_getCounterValue](#) (uint16\_t baseAddress)
  - Returns the value of the Counter register.*
- void [RTC\\_C\\_setCounterValue](#) (uint16\_t baseAddress, uint32\_t counterValue)
  - Sets the value of the Counter register.*
- void [RTC\\_C\\_initCounterPrescale](#) (uint16\_t baseAddress, uint8\_t prescaleSelect, uint16\_t prescaleClockSelect, uint16\_t prescaleDivider)
  - Initializes the Prescaler for Counter mode.*
- void [RTC\\_C\\_holdCounterPrescale](#) (uint16\_t baseAddress, uint8\_t prescaleSelect)
  - Holds the selected Prescaler.*
- void [RTC\\_C\\_startCounterPrescale](#) (uint16\_t baseAddress, uint8\_t prescaleSelect)
  - Starts the selected Prescaler.*
- void [RTC\\_C\\_definePrescaleEvent](#) (uint16\_t baseAddress, uint8\_t prescaleSelect, uint8\_t prescaleEventDivider)
  - Sets up an interrupt condition for the selected Prescaler.*
- uint8\_t [RTC\\_C\\_getPrescaleValue](#) (uint16\_t baseAddress, uint8\_t prescaleSelect)
  - Returns the selected prescaler value.*
- void [RTC\\_C\\_setPrescaleValue](#) (uint16\_t baseAddress, uint8\_t prescaleSelect, uint8\_t prescaleCounterValue)
  - Sets the selected Prescaler value.*
- void [RTC\\_C\\_enableInterrupt](#) (uint16\_t baseAddress, uint8\_t interruptMask)
  - Enables selected RTC interrupt sources.*
- void [RTC\\_C\\_disableInterrupt](#) (uint16\_t baseAddress, uint8\_t interruptMask)
  - Disables selected RTC interrupt sources.*
- uint8\_t [RTC\\_C\\_getInterruptStatus](#) (uint16\_t baseAddress, uint8\_t interruptFlagMask)
  - Returns the status of the selected interrupts flags.*
- void [RTC\\_C\\_clearInterrupt](#) (uint16\_t baseAddress, uint8\_t interruptFlagMask)
  - Clears selected RTC interrupt flags.*
- uint16\_t [RTC\\_C\\_convertBCDToBinary](#) (uint16\_t baseAddress, uint16\_t valueToConvert)
  - Convert the given BCD value to binary format.*
- uint16\_t [RTC\\_C\\_convertBinaryToBCD](#) (uint16\_t baseAddress, uint16\_t valueToConvert)
  - Convert the given binary value to BCD format.*

## 32.2.1 Detailed Description

The RTC\_C API is broken into 6 groups of functions: clock settings, calendar mode, counter mode, prescale counter, interrupt condition setup/enable functions and data conversion.

The RTC\_C clock settings are handled by

- [RTC\\_C.startClock\(\)](#)
- [RTC\\_C.holdClock\(\)](#)
- [RTC\\_C.setCalibrationFrequency\(\)](#)
- [RTC\\_C.setCalibrationData\(\)](#)
- [RTC\\_C.setTemperatureCompensation\(\)](#)

The RTC\_C calendar mode is initialized and setup by

- [RTC\\_C.initCalendar\(\)](#)

- `RTC_C_getCalenderTime()`

The `RTC_C` counter mode is initialized and handled by

- `RTC_C_initCounter()`
- `RTC_C_setCounterValue()`
- `RTC_C_getCounterValue()`
- `RTC_C_initCounterPrescale()`
- `RTC_C_holdCounterPrescale()`
- `RTC_C_startCounterPrescale()`

The `RTC_C` prescale counter is handled by

- `RTC_C_getPrescaleValue()`
- `RTC_C_setPrescaleValue()`

The `RTC_C` interrupts are handled by

- `RTC_C_configureCalendarAlarm()`
- `RTC_C_setCalenderEvent()`
- `RTC_C_definePrescaleEvent()`
- `RTC_C_enableInterrupt()`
- `RTC_C.disableInterrupt()`
- `RTC_C_getInterruptStatus()`
- `RTC_C.clearInterrupt()`

The `RTC_C` data conversion is handled by

- `RTC_C.convertBCDToBinary()`
- `RTC_C.convertBinaryToBCD()`

## 32.2.2 Function Documentation

### `RTC_C_clearInterrupt()`

```
void RTC_C_clearInterrupt (
    uint16_t baseAddress,
    uint8_t interruptFlagMask )
```

Clears selected RTC interrupt flags.

This function clears the RTC interrupt flag is cleared, so that it no longer asserts.

#### Parameters

<i>baseAddress</i>	is the base address of the <code>RTC_C</code> module.
--------------------	---



**Parameters**

<i>interruptFlagMask</i>	<p>is a bit mask of the interrupt flags to be cleared. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_C_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_C_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_C_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_C_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_C_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> <li>■ <b>RTC_C_OSCILLATOR_FAULT_INTERRUPT</b> - asserts if there is a problem with the 32kHz oscillator, while the RTC is running.</li> </ul>
--------------------------	--

**Returns**

None

**RTC\_C\_configureCalendarAlarm()**

```
void RTC_C_configureCalendarAlarm (
    uint16_t baseAddress,
    RTC_C_configureCalendarAlarmParam * param )
```

Sets and Enables the desired [Calendar](#) Alarm settings.

This function sets a [Calendar](#) interrupt condition to assert the RTCAIFG interrupt flag. The condition is a logical and of all of the parameters. For example if the minutes and hours alarm is set, then the interrupt will only assert when the minutes AND the hours change to the specified setting. Use the `RTC_C_ALARM_OFF` for any alarm settings that should not be apart of the alarm condition.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>param</i>	is the pointer to struct for calendar alarm configuration.

**Returns**

None

References `RTC_C_configureCalendarAlarmParam::dayOfMonthAlarm`, `RTC_C_configureCalendarAlarmParam::dayOfWeekAlarm`, `RTC_C_configureCalendarAlarmParam::hoursAlarm`, and `RTC_C_configureCalendarAlarmParam::minutesAlarm`.

## RTC\_C\_convertBCDToBinary()

```
uint16_t RTC_C_convertBCDToBinary (
    uint16_t baseAddress,
    uint16_t valueToConvert )
```

Convert the given BCD value to binary format.

This function converts BCD values to binary format. This API uses the hardware registers to perform the conversion rather than a software method.

### Parameters

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>valueToConvert</i>	is the raw value in BCD format to convert to Binary. Modified bits are <b>BCD2BIN</b> of <b>BCD2BIN</b> register.

### Returns

The binary version of the input parameter

## RTC\_C\_convertBinaryToBCD()

```
uint16_t RTC_C_convertBinaryToBCD (
    uint16_t baseAddress,
    uint16_t valueToConvert )
```

Convert the given binary value to BCD format.

This function converts binary values to BCD format. This API uses the hardware registers to perform the conversion rather than a software method.

### Parameters

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>valueToConvert</i>	is the raw value in Binary format to convert to BCD. Modified bits are <b>BIN2BCD</b> of <b>BIN2BCD</b> register.

### Returns

The BCD version of the valueToConvert parameter

## RTC\_C\_definePrescaleEvent()

```
void RTC_C_definePrescaleEvent (
    uint16_t baseAddress,
    uint8_t prescaleSelect,
    uint8_t prescaleEventDivider )
```

Sets up an interrupt condition for the selected Prescaler.

This function sets the condition for an interrupt to assert based on the individual prescalers.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>prescaleSelect</i>	is the prescaler to define an interrupt for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_C.PRESCALE_0</b></li> <li>■ <b>RTC_C.PRESCALE_1</b></li> </ul>
<i>prescaleEventDivider</i>	is a divider to specify when an interrupt can occur based on the clock source of the selected prescaler. (Does not affect timer of the selected prescaler). Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_C.PSEVENTDIVIDER_2</b> [Default]</li> <li>■ <b>RTC_C.PSEVENTDIVIDER_4</b></li> <li>■ <b>RTC_C.PSEVENTDIVIDER_8</b></li> <li>■ <b>RTC_C.PSEVENTDIVIDER_16</b></li> <li>■ <b>RTC_C.PSEVENTDIVIDER_32</b></li> <li>■ <b>RTC_C.PSEVENTDIVIDER_64</b></li> <li>■ <b>RTC_C.PSEVENTDIVIDER_128</b></li> <li>■ <b>RTC_C.PSEVENTDIVIDER_256</b></li> </ul> Modified bits are <b>RTxIP</b> of <b>RTCPSxCTL</b> register.

**Returns**

None

**RTC\_C\_disableInterrupt()**

```
void RTC_C_disableInterrupt (
    uint16_t baseAddress,
    uint8_t interruptMask )
```

Disables selected RTC interrupt sources.

This function disables the selected RTC interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_C module.
--------------------	--

**Parameters**

<i>interruptMask</i>	<p>is a bit mask of the interrupts to disable. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_C_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_C_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_C_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_C_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_C_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> <li>■ <b>RTC_C_OSCILLATOR_FAULT_INTERRUPT</b> - asserts if there is a problem with the 32kHz oscillator, while the RTC is running.</li> </ul>
----------------------	--

**Returns**

None

**RTC\_C\_enableInterrupt()**

```
void RTC_C.enableInterrupt (
    uint16_t baseAddress,
    uint8_t interruptMask )
```

Enables selected RTC interrupt sources.

This function enables the selected RTC interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_C module.
--------------------	--

## Parameters

<i>interruptMask</i>	<p>is a bit mask of the interrupts to enable. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_C_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_C_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_C_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_C_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_C_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> <li>■ <b>RTC_C_OSCILLATOR_FAULT_INTERRUPT</b> - asserts if there is a problem with the 32kHz oscillator, while the RTC is running.</li> </ul>
----------------------	---

## Returns

None

## RTC\_C\_getCalendarTime()

```
Calendar RTC_C_getCalendarTime (
    uint16_t baseAddress )
```

Returns the [Calendar](#) Time stored in the [Calendar](#) registers of the RTC.

This function returns the current [Calendar](#) time in the form of a [Calendar](#) structure. The RTCRDY polling is used in this function to prevent reading invalid time.

## Parameters

<i>baseAddress</i>	is the base address of the RTC_C module.
--------------------	--

## Returns

A [Calendar](#) structure containing the current time.

References [Calendar::DayOfMonth](#), [Calendar::DayOfWeek](#), [Calendar::Hours](#), [Calendar::Minutes](#), [Calendar::Month](#), [Calendar::Seconds](#), and [Calendar::Year](#).

## RTC\_C\_getCounterValue()

```
uint32_t RTC_C_getCounterValue (
    uint16_t baseAddress )
```

Returns the value of the Counter register.

This function returns the value of the counter register for the RTC\_C module. It will return the 32-bit value no matter the size set during initialization. The RTC should be held before trying to use this function.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_C module.
--------------------	--

#### Returns

The raw value of the full 32-bit Counter Register.

### RTC\_C\_getInterruptStatus()

```
uint8_t RTC_C_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t interruptFlagMask )
```

Returns the status of the selected interrupts flags.

This function returns the status of the interrupt flag for the selected channel.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>interruptFlagMask</i>	<p>is a bit mask of the interrupt flags to return the status of. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_C_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_C_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_C_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_C_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_C_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> <li>■ <b>RTC_C_OSCILLATOR_FAULT_INTERRUPT</b> - asserts if there is a problem with the 32kHz oscillator, while the RTC is running.</li> </ul>

#### Returns

Logical OR of any of the following:

- **RTC\_C\_TIME\_EVENT\_INTERRUPT** asserts when counter overflows in counter mode or when [Calendar](#) event condition defined by `defineCalendarEvent()` is met.

- **RTC\_C\_CLOCK\_ALARM\_INTERRUPT** asserts when alarm condition in [Calendar](#) mode is met.
- **RTC\_C\_CLOCK\_READ\_READY\_INTERRUPT** asserts when [Calendar](#) registers are settled.
- **RTC\_C\_PRESCALE\_TIMER0\_INTERRUPT** asserts when Prescaler 0 event condition is met.
- **RTC\_C\_PRESCALE\_TIMER1\_INTERRUPT** asserts when Prescaler 1 event condition is met.
- **RTC\_C\_OSCILLATOR\_FAULT\_INTERRUPT** asserts if there is a problem with the 32kHz oscillator, while the RTC is running, indicating the status of the masked interrupts

### RTC\_C\_getPrescaleValue()

```
uint8_t RTC_C_getPrescaleValue (
    uint16_t baseAddress,
    uint8_t prescaleSelect )
```

Returns the selected prescaler value.

This function returns the value of the selected prescale counter register. Note that the counter value should be held by calling [RTC\\_C\\_holdClock\(\)](#) before calling this API.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>prescaleSelect</i>	is the prescaler to obtain the value of. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_C_PRESCALE_0</b></li> <li>■ <b>RTC_C_PRESCALE_1</b></li> </ul>

#### Returns

The value of the specified prescaler count register

### RTC\_C\_holdClock()

```
void RTC_C_holdClock (
    uint16_t baseAddress )
```

Holds the RTC.

This function sets the RTC main hold bit to disable RTC functionality.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_C module.
--------------------	--

**Returns**

None

**RTC\_C\_holdCounterPrescale()**

```
void RTC_C_holdCounterPrescale (
    uint16_t baseAddress,
    uint8_t prescaleSelect )
```

Holds the selected Prescaler.

This function holds the prescale counter from continuing. This will only work in counter mode, in [Calendar](#) mode, the [RTC\\_C\\_holdClock\(\)](#) must be used. In counter mode, if using both prescalers in conjunction with the main RTC counter, then stopping RT0PS will stop RT1PS, but stopping RT1PS will not stop RT0PS.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>prescaleSelect</i>	is the prescaler to hold. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_C.PRESCALE_0</b></li> <li>■ <b>RTC_C.PRESCALE_1</b></li> </ul>

**Returns**

None

**RTC\_C\_initCalendar()**

```
void RTC_C_initCalendar (
    uint16_t baseAddress,
    Calendar * CalendarTime,
    uint16_t formatSelect )
```

Initializes the settings to operate the RTC in calendar mode.

This function initializes the [Calendar](#) mode of the RTC module. To prevent potential erroneous alarm conditions from occurring, the alarm should be disabled by clearing the RTCAIE, RTCAIFG and AE bits with APIs: [RTC\\_C\\_disableInterrupt\(\)](#), [RTC\\_C\\_clearInterrupt\(\)](#) and [RTC\\_C\\_configureCalendarAlarm\(\)](#) before calendar initialization.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_C module.
--------------------	--



## Parameters

<i>CalendarTime</i>	is the pointer to the structure containing the values for the <a href="#">Calendar</a> to be initialized to. Valid values should be of type pointer to <a href="#">Calendar</a> and should contain the following members and corresponding values: <b>Seconds</b> between 0-59 <b>Minutes</b> between 0-59 <b>Hours</b> between 0-23 <b>DayOfWeek</b> between 0-6 <b>DayOfMonth</b> between 1-31 <b>Month</b> between 1-12 <b>Year</b> between 0-4095 <b>NOTE:</b> Values beyond the ones specified may result in erratic behavior.
<i>formatSelect</i>	is the format for the <a href="#">Calendar</a> registers to use. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_C_FORMAT_BINARY</b> [Default]</li> <li>■ <b>RTC_C_FORMAT_BCD</b> Modified bits are <b>RTCBDC</b> of <b>RTCCTL1</b> register.</li> </ul>

## Returns

None

References [Calendar::DayOfMonth](#), [Calendar::DayOfWeek](#), [Calendar::Hours](#), [Calendar::Minutes](#), [Calendar::Month](#), [Calendar::Seconds](#), and [Calendar::Year](#).

## RTC\_C\_initCounter()

```
void RTC_C_initCounter (
    uint16_t baseAddress,
    uint16_t clockSelect,
    uint16_t counterSizeSelect )
```

Initializes the settings to operate the RTC in Counter mode.

This function initializes the Counter mode of the RTC.C. Setting the clock source and counter size will allow an interrupt from the **RTCTEVIFG** once an overflow to the counter register occurs.

## Parameters

<i>baseAddress</i>	is the base address of the <b>RTC_C</b> module.
<i>clockSelect</i>	is the selected clock for the counter mode to use. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_C_CLOCKSELECT_32KHZ_OSC</b></li> <li>■ <b>RTC_C_CLOCKSELECT_RT1PS</b> Modified bits are <b>RTCSSEL</b> of <b>RTCCTL1</b> register.</li> </ul>

## Parameters

<i>counterSizeSelect</i>	<p>is the size of the counter. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_C_COUNTERSIZE_8BIT</b> [Default]</li> <li>■ <b>RTC_C_COUNTERSIZE_16BIT</b></li> <li>■ <b>RTC_C_COUNTERSIZE_24BIT</b></li> <li>■ <b>RTC_C_COUNTERSIZE_32BIT</b></li> </ul> <p>Modified bits are <b>RTCCTEV</b> of <b>RTCCTL1</b> register.</p>
--------------------------	--

## Returns

None

## RTC\_C\_initCounterPrescale()

```
void RTC_C_initCounterPrescale (
    uint16_t baseAddress,
    uint8_t prescaleSelect,
    uint16_t prescaleClockSelect,
    uint16_t prescaleDivider )
```

Initializes the Prescaler for Counter mode.

This function initializes the selected prescaler for the counter mode in the RTC\_C module. If the RTC is initialized in [Calendar](#) mode, then these are automatically initialized. The Prescalers can be used to divide a clock source additionally before it gets to the main RTC clock.

## Parameters

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>prescaleSelect</i>	<p>is the prescaler to initialize. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_C_PRESCALE_0</b></li> <li>■ <b>RTC_C_PRESCALE_1</b></li> </ul>
<i>prescaleClockSelect</i>	<p>is the clock to drive the selected prescaler. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_C_PSCLOCKSELECT_ACLK</b></li> <li>■ <b>RTC_C_PSCLOCKSELECT_SMCLK</b></li> <li>■ <b>RTC_C_PSCLOCKSELECT_RT0PS</b> - use Prescaler 0 as source to Prescaler 1 (May only be used if prescaleSelect is <b>RTC_C_PRESCALE_1</b>)</li> </ul> <p>Modified bits are <b>RTxSSEL</b> of <b>RTCPSxCTL</b> register.</p>

## Parameters

<i>prescaleDivider</i>	<p>is the divider for the selected clock source. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_C.PSDIVIDER_2</b> [Default]</li> <li>■ <b>RTC_C.PSDIVIDER_4</b></li> <li>■ <b>RTC_C.PSDIVIDER_8</b></li> <li>■ <b>RTC_C.PSDIVIDER_16</b></li> <li>■ <b>RTC_C.PSDIVIDER_32</b></li> <li>■ <b>RTC_C.PSDIVIDER_64</b></li> <li>■ <b>RTC_C.PSDIVIDER_128</b></li> <li>■ <b>RTC_C.PSDIVIDER_256</b></li> </ul> <p>Modified bits are <b>RTxPSDIV</b> of <b>RTCPSxCTL</b> register.</p>
------------------------	---

## Returns

None

## RTC\_C\_setCalendarEvent()

```
void RTC_C_setCalendarEvent (
    uint16_t baseAddress,
    uint16_t eventSelect )
```

Sets a single specified [Calendar](#) interrupt condition.

This function sets a specified event to assert the **RTCCTEVIFG** interrupt. This interrupt is independent from the [Calendar](#) alarm interrupt.

## Parameters

<i>baseAddress</i>	is the base address of the <b>RTC_C</b> module.
<i>eventSelect</i>	<p>is the condition selected. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_C.CALENDAREVENT_MINUTECHANGE</b> - assert interrupt on every minute</li> <li>■ <b>RTC_C.CALENDAREVENT_HOURLCHANGE</b> - assert interrupt on every hour</li> <li>■ <b>RTC_C.CALENDAREVENT_NOON</b> - assert interrupt when hour is 12</li> <li>■ <b>RTC_C.CALENDAREVENT_MIDNIGHT</b> - assert interrupt when hour is 0</li> </ul> <p>Modified bits are <b>RTCCTEV</b> of <b>RTCCTL</b> register.</p>

**Returns**

None

**RTC\_C\_setCalibrationData()**

```
void RTC_C.setCalibrationData (
    uint16_t baseAddress,
    uint8_t  offsetDirection,
    uint8_t  offsetValue )
```

Sets the specified calibration for the RTC.

This function sets the calibration offset to make the RTC as accurate as possible. The `offsetDirection` can be either +1-ppm or -1-ppm, and the `offsetValue` should be from 1-240 and is multiplied by the direction setting (i.e. +1-ppm \* 8 (offsetValue) = +8-ppm).

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>offsetDirection</i>	is the direction that the calibration offset will go. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_C_CALIBRATION_DOWN1PPM</b> - calibrate at steps of -1</li> <li>■ <b>RTC_C_CALIBRATION_UP1PPM</b> - calibrate at steps of +1</li> </ul> Modified bits are <b>RTC0CAL5</b> of <b>RTC0CAL</b> register.
<i>offsetValue</i>	is the value that the offset will be a factor of; a valid value is any integer from 1-240. Modified bits are <b>RTC0CALx</b> of <b>RTC0CAL</b> register.

**Returns**

None

**RTC\_C\_setCalibrationFrequency()**

```
void RTC_C.setCalibrationFrequency (
    uint16_t baseAddress,
    uint16_t frequencySelect )
```

Allows and Sets the frequency output to RTCCLK pin for calibration measurement.

This function sets a frequency to measure at the RTCCLK output pin. After testing the set frequency, the calibration could be set accordingly.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_C module.
--------------------	--

**Parameters**

<i>frequencySelect</i>	<p>is the frequency output to RTCCLK. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_C_CALIBRATIONFREQ_OFF</b> [Default] - turn off calibration output</li> <li>■ <b>RTC_C_CALIBRATIONFREQ_512HZ</b> - output signal at 512Hz for calibration</li> <li>■ <b>RTC_C_CALIBRATIONFREQ_256HZ</b> - output signal at 256Hz for calibration</li> <li>■ <b>RTC_C_CALIBRATIONFREQ_1HZ</b> - output signal at 1Hz for calibration</li> </ul> <p>Modified bits are <b>RTCCALF</b> of <b>RTCCTL3</b> register.</p>
------------------------	---

**Returns**

None

**RTC\_C\_setCounterValue()**

```
void RTC_C_setCounterValue (
    uint16_t baseAddress,
    uint32_t counterValue )
```

Sets the value of the Counter register.

This function sets the counter register of the RTC\_C module.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>counterValue</i>	is the value to set the Counter register to; a valid value may be any 32-bit integer.

**Returns**

None

**RTC\_C\_setPrescaleValue()**

```
void RTC_C_setPrescaleValue (
    uint16_t baseAddress,
    uint8_t prescaleSelect,
    uint8_t prescaleCounterValue )
```

Sets the selected Prescaler value.

This function sets the prescale counter value. Before setting the prescale counter, it should be held by calling [RTC\\_C\\_holdClock\(\)](#).

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>prescaleSelect</i>	is the prescaler to set the value for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_C_PRESCALE_0</b></li> <li>■ <b>RTC_C_PRESCALE_1</b></li> </ul>
<i>prescaleCounterValue</i>	is the specified value to set the prescaler to. Valid values are any integer between 0-255 Modified bits are <b>RTxPS</b> of <b>RTxPS</b> register.

**Returns**

None

**RTC\_C\_setTemperatureCompensation()**

```
bool RTC_C_setTemperatureCompensation (
    uint16_t baseAddress,
    uint16_t offsetDirection,
    uint8_t offsetValue )
```

Sets the specified temperature compensation for the RTC.

This function sets the calibration offset to make the RTC as accurate as possible. The *offsetDirection* can be either +1-ppm or -1-ppm, and the *offsetValue* should be from 1-240 and is multiplied by the direction setting (i.e. +1-ppm \* 8 (*offsetValue*) = +8-ppm).

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>offsetDirection</i>	is the direction that the calibration offset will go Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_C_COMPENSATION_DOWN1PPM</b></li> <li>■ <b>RTC_C_COMPENSATION_UP1PPM</b></li> </ul> Modified bits are <b>RTCTCMPS</b> of <b>RTCTCMP</b> register.
<i>offsetValue</i>	is the value that the offset will be a factor of; a valid value is any integer from 1-240. Modified bits are <b>RTCTCMPx</b> of <b>RTCTCMP</b> register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of setting the temperature compensation

**RTC\_C\_startClock()**

```
void RTC_C_startClock (
    uint16_t baseAddress )
```

Starts the RTC.

This function clears the RTC main hold bit to allow the RTC to function.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_C module.
--------------------	--

#### Returns

None

### RTC\_C\_startCounterPrescale()

```
void RTC_C_startCounterPrescale (
    uint16_t baseAddress,
    uint8_t prescaleSelect )
```

Starts the selected Prescaler.

This function starts the selected prescale counter. This function will only work if the RTC is in counter mode.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_C module.
<i>prescaleSelect</i>	is the prescaler to start. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_C.PRESCALE_0</b></li> <li>■ <b>RTC_C.PRESCALE_1</b></li> </ul>

#### Returns

None

## 32.3 Programming Example

The following example shows how to initialize and use the RTC\_C API to setup Calendar Mode with the current time and various interrupts.

```
//Initialize calendar struct
Calendar currentTime;
currentTime.Seconds = 0x00;
currentTime.Minutes = 0x26;
currentTime.Hours = 0x13;
currentTime.DayOfWeek = 0x03;
currentTime.DayOfMonth = 0x20;
currentTime.Month = 0x07;
currentTime.Year = 0x2011;

//Initialize alarm struct
RTC_C_configureCalendarAlarmParam alarmParam;
alarmParam.minutesAlarm = 0x00;
```

```
alarmParam.hoursAlarm = 0x17;
alarmParam.dayOfWeekAlarm = RTC_C.ALARMCONDITION_OFF;
alarmParam.dayOfMonthAlarm = 0x05;

//Initialize Calendar Mode of RTC_C
/*
 * Base Address of the RTC_C.A
 * Pass in current time, initialized above
 * Use BCD as Calendar Register Format
 */
RTC_C_initCalendar(RTC_C.BASE,
                  &currentTime,
                  RTC_C.FORMAT_BCD);

//Setup Calendar Alarm for 5:00pm on the 5th day of the month.
//Note: Does not specify day of the week.
RTC_C_setCalendarAlarm(RTC_C.BASE, &alarmParam);

//Specify an interrupt to assert every minute
RTC_C_setCalendarEvent(RTC_C.BASE,
                      RTC_C.CALENDAREVENT_MINUTECHANGE);

//Enable interrupt for RTC_C Ready Status, which asserts when the RTC_C
//Calendar registers are ready to read.
//Also, enable interrupts for the Calendar alarm and Calendar event.
RTC_C_enableInterrupt(RTC_C.BASE,
                     RTC_C.CLOCK_READ_READY_INTERRUPT +
                     RTC_C.TIME_EVENT_INTERRUPT +
                     RTC_C.CLOCK_ALARM_INTERRUPT);

//Start RTC_C Clock
RTC_C_startClock(RTC_C.BASE);

//Enter LPM3 mode with interrupts enabled
__bis_SR_register(LPM3_bits + GIE);
__no_operation();
```



## 33 24-Bit Sigma Delta Converter (SD24\_B)

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### 33.1 Introduction

The SD24.B module consists of up to eight independent sigma-delta analog-to-digital converters. The converters are based on second-order oversampling sigma-delta modulators and digital decimation filters. The decimation filters are comb type filters with selectable oversampling ratios of up to 1024. Additional filtering can be done in software.

A sigma-delta analog-to-digital converter basically consists of two parts: the analog part

- called modulator - and the digital part - a decimation filter. The modulator of the SD24.B provides a bit stream of zeros and ones to the digital decimation filter. The digital filter averages the bitstream from the modulator over a given number of bits (specified by the oversampling rate) and provides samples at a reduced rate for further processing to the CPU.

As commonly known averaging can be used to increase the signal-to-noise performance of a conversion. With a conventional ADC each factor-of-4 oversampling can improve the SNR by about 6 dB or 1 bit. To achieve a 16-bit resolution out of a simple 1-bit ADC would require an impractical oversampling rate of  $415 = 1.073.741.824$ . To overcome this limitation the sigma-delta modulator implements a technique called noise-shaping - due to an implemented feedback-loop and integrators the quantization noise is pushed to higher frequencies and thus much lower oversampling rates are sufficient to achieve high resolutions.

### 33.2 API Functions

#### Functions

- void `SD24_B_init` (uint16\_t baseAddress, `SD24_B_initParam` \*param)  
*Initializes the SD24.B Module.*
- void `SD24_B_initConverter` (uint16\_t baseAddress, `SD24_B_initConverterParam` \*param)  
*Configure SD24.B converter.*
- void `SD24_B_initConverterAdvanced` (uint16\_t baseAddress, `SD24_B_initConverterAdvancedParam` \*param)  
*Configure SD24.B converter - Advanced Configure.*
- void `SD24_B_setConverterDataFormat` (uint16\_t baseAddress, uint8\_t converter, uint8\_t dataFormat)  
*Set SD24.B converter data format.*
- void `SD24_B_startGroupConversion` (uint16\_t baseAddress, uint8\_t group)  
*Start Conversion Group.*
- void `SD24_B_stopGroupConversion` (uint16\_t baseAddress, uint8\_t group)  
*Stop Conversion Group.*
- void `SD24_B_startConverterConversion` (uint16\_t baseAddress, uint8\_t converter)  
*Start Conversion for Converter.*

- void [SD24\\_B\\_stopConverterConversion](#) (uint16\_t baseAddress, uint8\_t converter)  
*Stop Conversion for Converter.*
- void [SD24\\_B\\_configureDMATrigger](#) (uint16\_t baseAddress, uint16\_t interruptFlag)  
*Configures the converter that triggers a DMA transfer.*
- void [SD24\\_B\\_setInterruptDelay](#) (uint16\_t baseAddress, uint8\_t converter, uint8\_t sampleDelay)  
*Configures the delay for an interrupt to trigger.*
- void [SD24\\_B\\_setConversionDelay](#) (uint16\_t baseAddress, uint8\_t converter, uint16\_t cycleDelay)  
*Configures the delay for the conversion start.*
- void [SD24\\_B\\_setOversampling](#) (uint16\_t baseAddress, uint8\_t converter, uint16\_t oversampleRatio)  
*Configures the oversampling ratio for a converter.*
- void [SD24\\_B\\_setGain](#) (uint16\_t baseAddress, uint8\_t converter, uint8\_t gain)  
*Configures the gain for the converter.*
- uint32\_t [SD24\\_B\\_getResults](#) (uint16\_t baseAddress, uint8\_t converter)  
*Returns the results for a converter.*
- uint16\_t [SD24\\_B\\_getHighWordResults](#) (uint16\_t baseAddress, uint8\_t converter)  
*Returns the high word results for a converter.*
- void [SD24\\_B\\_enableInterrupt](#) (uint16\_t baseAddress, uint8\_t converter, uint16\_t mask)  
*Enables interrupts for the SD24.B Module.*
- void [SD24\\_B\\_disableInterrupt](#) (uint16\_t baseAddress, uint8\_t converter, uint16\_t mask)  
*Disables interrupts for the SD24.B Module.*
- void [SD24\\_B\\_clearInterrupt](#) (uint16\_t baseAddress, uint8\_t converter, uint16\_t mask)  
*Clears interrupts for the SD24.B Module.*
- uint16\_t [SD24\\_B\\_getInterruptStatus](#) (uint16\_t baseAddress, uint8\_t converter, uint16\_t mask)  
*Returns the interrupt status for the SD24.B Module.*

### 33.2.1 Detailed Description

The SD24.B API is broken into three groups of functions: those that deal with initialization and conversions, those that handle interrupts, and those that handle auxiliary features of the SD24.B.

The SD24.B initialization and conversion functions are

- [SD24\\_B\\_init\(\)](#)
- [SD24\\_B\\_configureConverter\(\)](#)
- [SD24\\_B\\_configureConverterAdvanced\(\)](#)
- [SD24\\_B\\_startGroupConversion\(\)](#)
- [SD24\\_B\\_stopGroupConversion\(\)](#)
- [SD24\\_B\\_stopConverterConversion\(\)](#)
- [SD24\\_B\\_startConverterConversion\(\)](#)
- [SD24\\_B\\_configureDMATrigger\(\)](#)
- [SD24\\_B\\_getResults\(\)](#)
- [SD24\\_B\\_getHighWordResults\(\)](#)

The SD24.B interrupts are handled by

- [SD24\\_B\\_enableInterrupt\(\)](#)
- [SD24\\_B\\_disableInterrupt\(\)](#)

- [SD24.B\\_clearInterrupt\(\)](#)
- [SD24.B\\_getInterruptStatus\(\)](#)

Auxiliary features of the SD24.B are handled by

- [SD24.B\\_setConverterDataFormat\(\)](#)
- [SD24.B\\_setInterruptDelay\(\)](#)
- [SD24.B\\_setOversampling\(\)](#)
- [SD24.B\\_setGain\(\)](#)

## 33.2.2 Function Documentation

### SD24.B\_clearInterrupt()

```
void SD24.B_clearInterrupt (
    uint16_t baseAddress,
    uint8_t converter,
    uint16_t mask )
```

Clears interrupts for the SD24.B Module.

This function clears interrupt flags for the SD24.B module.

#### Parameters

<i>baseAddress</i>	is the base address of the SD24.B module.
<i>converter</i>	is the selected converter. Valid values are: <ul style="list-style-type: none"> <li>■ <b>SD24.B_CONVERTER_0</b></li> <li>■ <b>SD24.B_CONVERTER_1</b></li> <li>■ <b>SD24.B_CONVERTER_2</b></li> <li>■ <b>SD24.B_CONVERTER_3</b></li> <li>■ <b>SD24.B_CONVERTER_4</b></li> <li>■ <b>SD24.B_CONVERTER_5</b></li> <li>■ <b>SD24.B_CONVERTER_6</b></li> <li>■ <b>SD24.B_CONVERTER_7</b></li> </ul>
<i>mask</i>	is the bit mask of the converter interrupt sources to clear. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>SD24.B_CONVERTER_INTERRUPT</b></li> <li>■ <b>SD24.B_CONVERTER_OVERFLOW_INTERRUPT</b></li> </ul> Modified bits are <b>SD24OVIFGx</b> of <b>SD24BIFG</b> register.

**Returns**

None

**SD24\_B\_configureDMATrigger()**

```
void SD24_B_configureDMATrigger (
    uint16_t baseAddress,
    uint16_t interruptFlag )
```

Configures the converter that triggers a DMA transfer.

This function chooses which interrupt will trigger a DMA transfer.

**Parameters**

<i>baseAddress</i>	is the base address of the SD24_B module.
<i>interruptFlag</i>	<p>selects the converter interrupt that triggers a DMA transfer. Valid values are:</p> <ul style="list-style-type: none"> <li>■ SD24_B_DMA_TRIGGER_IFG0</li> <li>■ SD24_B_DMA_TRIGGER_IFG1</li> <li>■ SD24_B_DMA_TRIGGER_IFG2</li> <li>■ SD24_B_DMA_TRIGGER_IFG3</li> <li>■ SD24_B_DMA_TRIGGER_IFG4</li> <li>■ SD24_B_DMA_TRIGGER_IFG5</li> <li>■ SD24_B_DMA_TRIGGER_IFG6</li> <li>■ SD24_B_DMA_TRIGGER_IFG7</li> <li>■ SD24_B_DMA_TRIGGER_TRGIFG</li> </ul> <p>Modified bits are <b>SD24DMAx</b> of <b>SD24BCTL1</b> register.</p>

**Returns**

None

**SD24\_B\_disableInterrupt()**

```
void SD24_B_disableInterrupt (
    uint16_t baseAddress,
    uint8_t converter,
    uint16_t mask )
```

Disables interrupts for the SD24\_B Module.

This function disables interrupts for the SD24\_B module.

**Parameters**

<i>baseAddress</i>	is the base address of the SD24_B module.
--------------------	---

## Parameters

<i>converter</i>	is the selected converter. Valid values are: <ul style="list-style-type: none"> <li>■ <b>SD24.B_CONVERTER_0</b></li> <li>■ <b>SD24.B_CONVERTER_1</b></li> <li>■ <b>SD24.B_CONVERTER_2</b></li> <li>■ <b>SD24.B_CONVERTER_3</b></li> <li>■ <b>SD24.B_CONVERTER_4</b></li> <li>■ <b>SD24.B_CONVERTER_5</b></li> <li>■ <b>SD24.B_CONVERTER_6</b></li> <li>■ <b>SD24.B_CONVERTER_7</b></li> </ul>
<i>mask</i>	is the bit mask of the converter interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>SD24.B_CONVERTER_INTERRUPT</b></li> <li>■ <b>SD24.B_CONVERTER_OVERFLOW_INTERRUPT</b></li> </ul> Modified bits are <b>SD24OVIE<sub>x</sub></b> of <b>SD24BIE</b> register.

Modified bits of **SD24BIE** register.

## Returns

None

## SD24.B\_enableInterrupt()

```
void SD24.B_enableInterrupt (
    uint16_t baseAddress,
    uint8_t converter,
    uint16_t mask )
```

Enables interrupts for the SD24.B Module.

This function enables interrupts for the SD24.B module. Does not clear interrupt flags.

## Parameters

<i>baseAddress</i>	is the base address of the SD24.B module.
--------------------	---

## Parameters

<i>converter</i>	is the selected converter. Valid values are: <ul style="list-style-type: none"> <li>■ SD24_B_CONVERTER_0</li> <li>■ SD24_B_CONVERTER_1</li> <li>■ SD24_B_CONVERTER_2</li> <li>■ SD24_B_CONVERTER_3</li> <li>■ SD24_B_CONVERTER_4</li> <li>■ SD24_B_CONVERTER_5</li> <li>■ SD24_B_CONVERTER_6</li> <li>■ SD24_B_CONVERTER_7</li> </ul>
<i>mask</i>	is the bit mask of the converter interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ SD24_B_CONVERTER_INTERRUPT</li> <li>■ SD24_B_CONVERTER_OVERFLOW_INTERRUPT</li> </ul> Modified bits are <b>SD24OVIE<sub>x</sub></b> of <b>SD24BIE</b> register.

## Returns

None

## SD24\_B\_getHighWordResults()

```
uint16_t SD24_B_getHighWordResults (
    uint16_t baseAddress,
    uint8_t converter )
```

Returns the high word results for a converter.

This function gets the results from the SD24MEMH<sub>x</sub> register and returns it.

## Parameters

<i>baseAddress</i>	is the base address of the SD24_B module.
<i>converter</i>	selects the converter who's results will be returned Valid values are: <ul style="list-style-type: none"> <li>■ SD24_B_CONVERTER_0</li> <li>■ SD24_B_CONVERTER_1</li> <li>■ SD24_B_CONVERTER_2</li> <li>■ SD24_B_CONVERTER_3</li> <li>■ SD24_B_CONVERTER_4</li> <li>■ SD24_B_CONVERTER_5</li> <li>■ SD24_B_CONVERTER_6</li> <li>■ SD24_B_CONVERTER_7</li> </ul>

**Returns**

Result of conversion

**SD24\_B\_getInterruptStatus()**

```
uint16_t SD24_B_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t converter,
    uint16_t mask )
```

Returns the interrupt status for the SD24.B Module.

This function returns interrupt flag statuses for the SD24\_B module.

**Parameters**

<i>baseAddress</i>	is the base address of the SD24_B module.
<i>converter</i>	is the selected converter. Valid values are: <ul style="list-style-type: none"> <li>■ <b>SD24_B_CONVERTER_0</b></li> <li>■ <b>SD24_B_CONVERTER_1</b></li> <li>■ <b>SD24_B_CONVERTER_2</b></li> <li>■ <b>SD24_B_CONVERTER_3</b></li> <li>■ <b>SD24_B_CONVERTER_4</b></li> <li>■ <b>SD24_B_CONVERTER_5</b></li> <li>■ <b>SD24_B_CONVERTER_6</b></li> <li>■ <b>SD24_B_CONVERTER_7</b></li> </ul>
<i>mask</i>	is the bit mask of the converter interrupt sources to return. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>SD24_B_CONVERTER_INTERRUPT</b></li> <li>■ <b>SD24_B_CONVERTER_OVERFLOW_INTERRUPT</b></li> </ul>

**Returns**

Logical OR of any of the following:

- **SD24\_B\_CONVERTER\_INTERRUPT**
  - **SD24\_B\_CONVERTER\_OVERFLOW\_INTERRUPT**
- indicating the status of the masked interrupts

**SD24\_B\_getResults()**

```
uint32_t SD24_B_getResults (
    uint16_t baseAddress,
    uint8_t converter )
```

Returns the results for a converter.

This function gets the results from the SD24BMEMLx and SD24MEMHx registers and concatenates them to form a long. The actual result is a maximum 24 bits.

#### Parameters

<i>baseAddress</i>	is the base address of the SD24_B module.
<i>converter</i>	selects the converter who's results will be returned Valid values are: <ul style="list-style-type: none"> <li>■ SD24_B_CONVERTER_0</li> <li>■ SD24_B_CONVERTER_1</li> <li>■ SD24_B_CONVERTER_2</li> <li>■ SD24_B_CONVERTER_3</li> <li>■ SD24_B_CONVERTER_4</li> <li>■ SD24_B_CONVERTER_5</li> <li>■ SD24_B_CONVERTER_6</li> <li>■ SD24_B_CONVERTER_7</li> </ul>

#### Returns

Result of conversion

#### SD24\_B\_init()

```
void SD24_B_init (
    uint16_t baseAddress,
    SD24_B_initParam * param )
```

Initializes the SD24\_B Module.

This function initializes the SD24\_B module sigma-delta analog-to-digital conversions. Specifically the function sets up the clock source for the SD24\_B core to use for conversions. Upon completion of the initialization the SD24\_B interrupt registers will be reset and the given parameters will be set. The converter configuration settings are independent of this function. The values you choose for the clock divider and predivider are used to determine the effective clock frequency. The formula used is:  $f_{sd24} = f_{clk} / (divider * predivider)$

#### Parameters

<i>baseAddress</i>	is the base address of the SD24_B module.
<i>param</i>	is the pointer to struct for initialization.

#### Returns

None

References SD24\_B\_initParam::clockDivider, SD24\_B\_initParam::clockPreDivider, SD24\_B\_initParam::clockSourceSelect, and SD24\_B\_initParam::referenceSelect.



## SD24\_B\_initConverter()

```
void SD24_B_initConverter (
    uint16_t baseAddress,
    SD24_B_initConverterParam * param )
```

Configure SD24.B converter.

This function initializes a converter of the SD24.B module. Upon completion the converter will be ready for a conversion and can be started with the [SD24\\_B\\_startGroupConversion\(\)](#) or [SD24\\_B\\_startConverterConversion\(\)](#) depending on the startSelect parameter. Additional configuration such as data format can be configured in [SD24\\_B\\_setConverterDataFormat\(\)](#).

### Parameters

<i>baseAddress</i>	is the base address of the SD24_B module.
<i>param</i>	is the pointer to struct for converter configuration.

### Returns

None

References [SD24\\_B\\_initConverterParam::alignment](#), [SD24\\_B\\_initConverterParam::conversionMode](#), [SD24\\_B\\_initConverterParam::converter](#), and [SD24\\_B\\_initConverterParam::startSelect](#).

## SD24\_B\_initConverterAdvanced()

```
void SD24_B_initConverterAdvanced (
    uint16_t baseAddress,
    SD24_B_initConverterAdvancedParam * param )
```

Configure SD24.B converter - Advanced Configure.

This function initializes a converter of the SD24.B module. Upon completion the converter will be ready for a conversion and can be started with the [SD24\\_B\\_startGroupConversion\(\)](#) or [SD24\\_B\\_startConverterConversion\(\)](#) depending on the startSelect parameter.

### Parameters

<i>baseAddress</i>	is the base address of the SD24_B module.
<i>param</i>	is the pointer to struct for converter advanced configuration.

### Returns

None

References [SD24\\_B\\_initConverterAdvancedParam::alignment](#), [SD24\\_B\\_initConverterAdvancedParam::conversionMode](#), [SD24\\_B\\_initConverterAdvancedParam::converter](#), [SD24\\_B\\_initConverterAdvancedParam::dataFormat](#), [SD24\\_B\\_initConverterAdvancedParam::gain](#), and [SD24\\_B\\_initConverterAdvancedParam::oversampleRatio](#).

SD24\_B\_initConverterAdvancedParam::sampleDelay, and  
SD24\_B\_initConverterAdvancedParam::startSelect.

### SD24\_B\_setConversionDelay()

```
void SD24_B_setConversionDelay (
    uint16_t baseAddress,
    uint8_t converter,
    uint16_t cycleDelay )
```

Configures the delay for the conversion start.

This function configures the delay for the specified converter start. Please note the delay should be written before conversion or after corresponding conversion is completed. If no delay at start of conversion is desired, a previously written non-zero value must be changed to zero before starting the conversion.

#### Parameters

<i>baseAddress</i>	is the base address of the SD24_B module.
<i>converter</i>	selects the converter that will be delayed Valid values are: <ul style="list-style-type: none"> <li>■ SD24_B_CONVERTER_0</li> <li>■ SD24_B_CONVERTER_1</li> <li>■ SD24_B_CONVERTER_2</li> <li>■ SD24_B_CONVERTER_3</li> <li>■ SD24_B_CONVERTER_4</li> <li>■ SD24_B_CONVERTER_5</li> <li>■ SD24_B_CONVERTER_6</li> <li>■ SD24_B_CONVERTER_7</li> </ul>
<i>cycleDelay</i>	is the clock cycles to delay ranging from 0 to 1023. Modified bits are <b>SD24PREx</b> of <b>SD24BPREx</b> register.

#### Returns

None

### SD24\_B\_setConverterDataFormat()

```
void SD24_B_setConverterDataFormat (
    uint16_t baseAddress,
    uint8_t converter,
    uint8_t dataFormat )
```

Set SD24\_B converter data format.

This function sets the converter format so that the resulting data can be viewed in either binary or 2's complement.

**Parameters**

<i>baseAddress</i>	is the base address of the SD24_B module.
<i>converter</i>	<p>selects the converter that will be configured. Check datasheet for available converters on device. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SD24_B_CONVERTER_0</b></li> <li>■ <b>SD24_B_CONVERTER_1</b></li> <li>■ <b>SD24_B_CONVERTER_2</b></li> <li>■ <b>SD24_B_CONVERTER_3</b></li> <li>■ <b>SD24_B_CONVERTER_4</b></li> <li>■ <b>SD24_B_CONVERTER_5</b></li> <li>■ <b>SD24_B_CONVERTER_6</b></li> <li>■ <b>SD24_B_CONVERTER_7</b></li> </ul>
<i>dataFormat</i>	<p>selects how the data format of the results Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SD24_B_DATA_FORMAT_BINARY</b> [Default]</li> <li>■ <b>SD24_B_DATA_FORMAT_2COMPLEMENT</b> Modified bits are <b>SD24DFx</b> of <b>SD24BCCTLx</b> register.</li> </ul>

**Returns**

None

**SD24\_B\_setGain()**

```
void SD24_B_setGain (
    uint16_t baseAddress,
    uint8_t converter,
    uint8_t gain )
```

Configures the gain for the converter.

This function configures the gain for a single converter.

**Parameters**

<i>baseAddress</i>	is the base address of the SD24_B module.
--------------------	---

## Parameters

<i>converter</i>	selects the converter that will be configured Valid values are: <ul style="list-style-type: none"> <li>■ SD24.B_CONVERTER_0</li> <li>■ SD24.B_CONVERTER_1</li> <li>■ SD24.B_CONVERTER_2</li> <li>■ SD24.B_CONVERTER_3</li> <li>■ SD24.B_CONVERTER_4</li> <li>■ SD24.B_CONVERTER_5</li> <li>■ SD24.B_CONVERTER_6</li> <li>■ SD24.B_CONVERTER_7</li> </ul>
<i>gain</i>	selects the gain for the converter Valid values are: <ul style="list-style-type: none"> <li>■ SD24.B_GAIN_1 [Default]</li> <li>■ SD24.B_GAIN_2</li> <li>■ SD24.B_GAIN_4</li> <li>■ SD24.B_GAIN_8</li> <li>■ SD24.B_GAIN_16</li> <li>■ SD24.B_GAIN_32</li> <li>■ SD24.B_GAIN_64</li> <li>■ SD24.B_GAIN_128</li> </ul> Modified bits are <b>SD24GAINx</b> of <b>SD24BINCTLx</b> register.

## Returns

None

## SD24\_B\_setInterruptDelay()

```
void SD24_B_setInterruptDelay (
    uint16_t baseAddress,
    uint8_t converter,
    uint8_t sampleDelay )
```

Configures the delay for an interrupt to trigger.

This function configures the delay for the first interrupt service request for the corresponding converter. This feature delays the interrupt request for a completed conversion by up to four conversion cycles allowing the digital filter to settle prior to generating an interrupt request.

## Parameters

<i>baseAddress</i>	is the base address of the SD24_B module.
--------------------	---

## Parameters

<i>converter</i>	selects the converter that will be stopped Valid values are: <ul style="list-style-type: none"> <li>■ SD24.B_CONVERTER_0</li> <li>■ SD24.B_CONVERTER_1</li> <li>■ SD24.B_CONVERTER_2</li> <li>■ SD24.B_CONVERTER_3</li> <li>■ SD24.B_CONVERTER_4</li> <li>■ SD24.B_CONVERTER_5</li> <li>■ SD24.B_CONVERTER_6</li> <li>■ SD24.B_CONVERTER_7</li> </ul>
<i>sampleDelay</i>	selects the delay for the interrupt Valid values are: <ul style="list-style-type: none"> <li>■ SD24.B_FOURTH_SAMPLE_INTERRUPT [Default]</li> <li>■ SD24.B_THIRD_SAMPLE_INTERRUPT</li> <li>■ SD24.B_SECOND_SAMPLE_INTERRUPT</li> <li>■ SD24.B_FIRST_SAMPLE_INTERRUPT</li> </ul> Modified bits are SD24INTDLYx of SD24INCTLx register.

## Returns

None

## SD24.B\_setOversampling()

```
void SD24.B_setOversampling (
    uint16_t baseAddress,
    uint8_t converter,
    uint16_t oversampleRatio )
```

Configures the oversampling ratio for a converter.

This function configures the oversampling ratio for a given converter.

## Parameters

<i>baseAddress</i>	is the base address of the SD24.B module.
--------------------	---

## Parameters

<i>converter</i>	selects the converter that will be configured Valid values are: <ul style="list-style-type: none"> <li>■ SD24_B_CONVERTER_0</li> <li>■ SD24_B_CONVERTER_1</li> <li>■ SD24_B_CONVERTER_2</li> <li>■ SD24_B_CONVERTER_3</li> <li>■ SD24_B_CONVERTER_4</li> <li>■ SD24_B_CONVERTER_5</li> <li>■ SD24_B_CONVERTER_6</li> <li>■ SD24_B_CONVERTER_7</li> </ul>
<i>oversampleRatio</i>	selects oversampling ratio for the converter Valid values are: <ul style="list-style-type: none"> <li>■ SD24_B_OVERSAMPLE_32</li> <li>■ SD24_B_OVERSAMPLE_64</li> <li>■ SD24_B_OVERSAMPLE_128</li> <li>■ SD24_B_OVERSAMPLE_256</li> <li>■ SD24_B_OVERSAMPLE_512</li> <li>■ SD24_B_OVERSAMPLE_1024</li> </ul> Modified bits are <b>SD24OSRx</b> of <b>SD24BOSRx</b> register.

## Returns

None

## SD24\_B\_startConverterConversion()

```
void SD24_B_startConverterConversion (
    uint16_t baseAddress,
    uint8_t converter )
```

Start Conversion for Converter.

This function starts a single converter.

## Parameters

<i>baseAddress</i>	is the base address of the SD24_B module.
--------------------	---

## Parameters

<i>converter</i>	<p>selects the converter that will be started Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SD24.B_CONVERTER_0</b></li> <li>■ <b>SD24.B_CONVERTER_1</b></li> <li>■ <b>SD24.B_CONVERTER_2</b></li> <li>■ <b>SD24.B_CONVERTER_3</b></li> <li>■ <b>SD24.B_CONVERTER_4</b></li> <li>■ <b>SD24.B_CONVERTER_5</b></li> <li>■ <b>SD24.B_CONVERTER_6</b></li> <li>■ <b>SD24.B_CONVERTER_7</b></li> </ul> <p>Modified bits are <b>SD24SC</b> of <b>SD24BCCTLx</b> register.</p>
------------------	--

## Returns

None

## SD24.B\_startGroupConversion()

```
void SD24.B_startGroupConversion (
    uint16_t baseAddress,
    uint8_t group )
```

Start Conversion Group.

This function starts all the converters that are associated with a group. To set a converter to a group use the `SD24.B_configureConverter()` function.

## Parameters

<i>baseAddress</i>	is the base address of the SD24.B module.
<i>group</i>	<p>selects the group that will be started Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SD24.B_GROUP0</b></li> <li>■ <b>SD24.B_GROUP1</b></li> <li>■ <b>SD24.B_GROUP2</b></li> <li>■ <b>SD24.B_GROUP3</b></li> </ul> <p>Modified bits are <b>SD24DGRP<sub>x</sub>SC</b> of <b>SD24BCTL1</b> register.</p>

**Returns**

None

**SD24\_B\_stopConverterConversion()**

```
void SD24_B_stopConverterConversion (
    uint16_t baseAddress,
    uint8_t converter )
```

Stop Conversion for Converter.

This function stops a single converter.

**Parameters**

<i>baseAddress</i>	is the base address of the SD24_B module.
<i>converter</i>	<p>selects the converter that will be stopped Valid values are:</p> <ul style="list-style-type: none"> <li>■ SD24_B_CONVERTER_0</li> <li>■ SD24_B_CONVERTER_1</li> <li>■ SD24_B_CONVERTER_2</li> <li>■ SD24_B_CONVERTER_3</li> <li>■ SD24_B_CONVERTER_4</li> <li>■ SD24_B_CONVERTER_5</li> <li>■ SD24_B_CONVERTER_6</li> <li>■ SD24_B_CONVERTER_7</li> </ul> <p>Modified bits are <b>SD24SC</b> of <b>SD24BCCTLx</b> register.</p>

**Returns**

None

**SD24\_B\_stopGroupConversion()**

```
void SD24_B_stopGroupConversion (
    uint16_t baseAddress,
    uint8_t group )
```

Stop Conversion Group.

This function stops all the converters that are associated with a group. To set a converter to a group use the `SD24_B_configureConverter()` function.

**Parameters**

<i>baseAddress</i>	is the base address of the SD24_B module.
--------------------	---



## Parameters

<i>group</i>	selects the group that will be stopped Valid values are: <ul style="list-style-type: none"> <li>■ SD24_B_GROUP0</li> <li>■ SD24_B_GROUP1</li> <li>■ SD24_B_GROUP2</li> <li>■ SD24_B_GROUP3</li> </ul> Modified bits are <b>SD24DGRP<sub>x</sub>SC</b> of <b>SD24BCTL1</b> register.
--------------	---

## Returns

None

### 33.3 Programming Example

The following example shows how to initialize and use the SD24.B API to start a single channel, single conversion.

```

unsigned long results;

SD24_B_initParam initParam = {0};
initParam.clockSourceSelect = SD24_B_CLOCKSOURCE_SMCLK; // Select SMCLK as SD24.B clock
source
initParam.clockPreDivider = SD24_B_PRECLOCKDIVIDER_1;
initParam.clockDivider = SD24_B_CLOCKDIVIDER_1;
initParam.referenceSelect = SD24_B_REF_INTERNAL; // Select internal REF
SD24_B_init(SD24_BASE, &initParam);

SD24_B_configureConverter(SD24_BASE,
    SD24_B_CONVERTER_2,
    SD24_B_ALIGN_RIGHT,
    SD24_B_CONVERSION_SELECT_SD24SC,
    SD24_B_SINGLE_MODE);

__delay_cycles(0x3600); // Delay for 1.5V REF startup

while (1)
{
    SD24_B_startConverterConversion(SD24_BASE,
        SD24_B_CONVERTER_2); // Set bit to start conversion

    // Poll interrupt flag for channel 2
    while( SD24_B_getInterruptStatus(SD24_BASE,
        SD24_B_CONVERTER_2
        SD24_CONVERTER_INTERRUPT) == 0 );

    results = SD24_B_getResults(SD24_BASE,
        SD24_B_CONVERTER_2); // Save CH2 results (clears IFG)

    __no_operation(); // SET BREAKPOINT HERE
}

```

## 34 SFR Module

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### 34.1 Introduction

The Special Function Registers API provides a set of functions for using the MSP430Ware SFR module. Functions are provided to enable and disable interrupts and control the  $\sim$ RST/NMI pin

The SFR module can enable interrupts to be generated from other peripherals of the device.

### 34.2 API Functions

#### Functions

- void [SFR\\_enableInterrupt](#) (uint8\_t interruptMask)  
*Enables selected SFR interrupt sources.*
- void [SFR\\_disableInterrupt](#) (uint8\_t interruptMask)  
*Disables selected SFR interrupt sources.*
- uint8\_t [SFR\\_getInterruptStatus](#) (uint8\_t interruptFlagMask)  
*Returns the status of the selected SFR interrupt flags.*
- void [SFR\\_clearInterrupt](#) (uint8\_t interruptFlagMask)  
*Clears the selected SFR interrupt flags.*
- void [SFR\\_setResetPinPullResistor](#) (uint16\_t pullResistorSetup)  
*Sets the pull-up/down resistor on the  $\sim$ RST/NMI pin.*
- void [SFR\\_setNMIEdge](#) (uint16\_t edgeDirection)  
*Sets the edge direction that will assert an NMI from a signal on the  $\sim$ RST/NMI pin if NMI function is active.*
- void [SFR\\_setResetNMIPinFunction](#) (uint8\_t resetPinFunction)  
*Sets the function of the  $\sim$ RST/NMI pin.*

#### 34.2.1 Detailed Description

The SFR API is broken into 2 groups: the SFR interrupts and the SFR  $\sim$ RST/NMI pin control

The SFR interrupts are handled by

- [SFR\\_enableInterrupt\(\)](#)
- [SFR\\_disableInterrupt\(\)](#)
- [SFR\\_getInterruptStatus\(\)](#)
- [SFR\\_clearInterrupt\(\)](#)

The SFR  $\sim$ RST/NMI pin is controlled by

- [SFR\\_setResetPinPullResistor\(\)](#)
- [SFR\\_setNMIEdge\(\)](#)
- [SFR\\_setResetNMIPinFunction\(\)](#)

## 34.2.2 Function Documentation

### SFR\_clearInterrupt()

```
void SFR_clearInterrupt (
    uint8_t interruptFlagMask )
```

Clears the selected SFR interrupt flags.

This function clears the status of the selected SFR interrupt flags.

#### Parameters

<i>interruptFlagMask</i>	<p>is the bit mask of interrupt flags that should be cleared Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>SFR_JTAG_OUTBOX_INTERRUPT</b> - JTAG outbox interrupt enable</li> <li>■ <b>SFR_JTAG_INBOX_INTERRUPT</b> - JTAG inbox interrupt enable</li> <li>■ <b>SFR_NMI_PIN_INTERRUPT</b> - NMI pin interrupt enable, if NMI function is chosen</li> <li>■ <b>SFR_VACANT_MEMORY_ACCESS_INTERRUPT</b> - Vacant memory access interrupt enable</li> <li>■ <b>SFR_OSCILLATOR_FAULT_INTERRUPT</b> - Oscillator fault interrupt enable</li> <li>■ <b>SFR_WATCHDOG_INTERVAL_TIMER_INTERRUPT</b> - Watchdog interval timer interrupt enable</li> <li>■ <b>SFR_FLASH_CONTROLLER_ACCESS_VIOLATION_INTERRUPT</b> - Flash controller access violation interrupt enable</li> </ul>
--------------------------	---

#### Returns

None

### SFR\_disableInterrupt()

```
void SFR_disableInterrupt (
    uint8_t interruptMask )
```

Disables selected SFR interrupt sources.

This function disables the selected SFR interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

## Parameters

<i>interruptMask</i>	<p>is the bit mask of interrupts that will be disabled. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>SFR_JTAG_OUTBOX_INTERRUPT</b> - JTAG outbox interrupt enable</li> <li>■ <b>SFR_JTAG_INBOX_INTERRUPT</b> - JTAG inbox interrupt enable</li> <li>■ <b>SFR_NMI_PIN_INTERRUPT</b> - NMI pin interrupt enable, if NMI function is chosen</li> <li>■ <b>SFR_VACANT_MEMORY_ACCESS_INTERRUPT</b> - Vacant memory access interrupt enable</li> <li>■ <b>SFR_OSCILLATOR_FAULT_INTERRUPT</b> - Oscillator fault interrupt enable</li> <li>■ <b>SFR_WATCHDOG_INTERVAL_TIMER_INTERRUPT</b> - Watchdog interval timer interrupt enable</li> <li>■ <b>SFR_FLASH_CONTROLLER_ACCESS_VIOLATION_INTERRUPT</b> - Flash controller access violation interrupt enable</li> </ul>
----------------------	--

## Returns

None

## SFR\_enableInterrupt()

```
void SFR_enableInterrupt (
    uint8_t interruptMask )
```

Enables selected SFR interrupt sources.

This function enables the selected SFR interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>interruptMask</i>	<p>is the bit mask of interrupts that will be enabled. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>SFR_JTAG_OUTBOX_INTERRUPT</b> - JTAG outbox interrupt enable</li> <li>■ <b>SFR_JTAG_INBOX_INTERRUPT</b> - JTAG inbox interrupt enable</li> <li>■ <b>SFR_NMI_PIN_INTERRUPT</b> - NMI pin interrupt enable, if NMI function is chosen</li> <li>■ <b>SFR_VACANT_MEMORY_ACCESS_INTERRUPT</b> - Vacant memory access interrupt enable</li> <li>■ <b>SFR_OSCILLATOR_FAULT_INTERRUPT</b> - Oscillator fault interrupt enable</li> <li>■ <b>SFR_WATCHDOG_INTERVAL_TIMER_INTERRUPT</b> - Watchdog interval timer interrupt enable</li> <li>■ <b>SFR_FLASH_CONTROLLER_ACCESS_VIOLATION_INTERRUPT</b> - Flash controller access violation interrupt enable</li> </ul>
----------------------	---

**Returns**

None

**SFR\_getInterruptStatus()**

```
uint8_t SFR_getInterruptStatus (
    uint8_t interruptFlagMask )
```

Returns the status of the selected SFR interrupt flags.

This function returns the status of the selected SFR interrupt flags in a bit mask format matching that passed into the interruptFlagMask parameter.

## Parameters

<i>interruptFlagMask</i>	<p>is the bit mask of interrupt flags that the status of should be returned. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>SFR_JTAG_OUTBOX_INTERRUPT</b> - JTAG outbox interrupt enable</li> <li>■ <b>SFR_JTAG_INBOX_INTERRUPT</b> - JTAG inbox interrupt enable</li> <li>■ <b>SFR_NMI_PIN_INTERRUPT</b> - NMI pin interrupt enable, if NMI function is chosen</li> <li>■ <b>SFR_VACANT_MEMORY_ACCESS_INTERRUPT</b> - Vacant memory access interrupt enable</li> <li>■ <b>SFR_OSCILLATOR_FAULT_INTERRUPT</b> - Oscillator fault interrupt enable</li> <li>■ <b>SFR_WATCHDOG_INTERVAL_TIMER_INTERRUPT</b> - Watchdog interval timer interrupt enable</li> <li>■ <b>SFR_FLASH_CONTROLLER_ACCESS_VIOLATION_INTERRUPT</b> - Flash controller access violation interrupt enable</li> </ul>
--------------------------	---

## Returns

Logical OR of any of the following:

- **SFR\_JTAG\_OUTBOX\_INTERRUPT** JTAG outbox interrupt enable
  - **SFR\_JTAG\_INBOX\_INTERRUPT** JTAG inbox interrupt enable
  - **SFR\_NMI\_PIN\_INTERRUPT** NMI pin interrupt enable, if NMI function is chosen
  - **SFR\_VACANT\_MEMORY\_ACCESS\_INTERRUPT** Vacant memory access interrupt enable
  - **SFR\_OSCILLATOR\_FAULT\_INTERRUPT** Oscillator fault interrupt enable
  - **SFR\_WATCHDOG\_INTERVAL\_TIMER\_INTERRUPT** Watchdog interval timer interrupt enable
  - **SFR\_FLASH\_CONTROLLER\_ACCESS\_VIOLATION\_INTERRUPT** Flash controller access violation interrupt enable
- indicating the status of the masked interrupts

## SFR\_setNMIEdge()

```
void SFR_setNMIEdge (
    uint16_t edgeDirection )
```

Sets the edge direction that will assert an NMI from a signal on the  $\sim$ RST/NMI pin if NMI function is active.

This function sets the edge direction that will assert an NMI from a signal on the  $\sim$ RST/NMI pin if the NMI function is active. To activate the NMI function of the  $\sim$ RST/NMI use the [SFR\\_setResetNMIPinFunction\(\)](#) passing SFR\_RESETPINFUNC\_NMI into the resetPinFunction parameter.

**Parameters**

<i>edgeDirection</i>	<p>is the direction that the signal on the <math>\sim</math>RST/NMI pin should go to signal an interrupt, if enabled. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SFR_NMI_RISINGEDGE</b> [Default]</li> <li>■ <b>SFR_NMI_FALLINGEDGE</b></li> </ul> <p>Modified bits are <b>SYSNMIIES</b> of <b>SFRRPCR</b> register.</p>
----------------------	---

**Returns**

None

**SFR\_setResetNMIPinFunction()**

```
void SFR_setResetNMIPinFunction (
    uint8_t resetPinFunction )
```

Sets the function of the  $\sim$ RST/NMI pin.

This function sets the functionality of the  $\sim$ RST/NMI pin, whether in reset mode which will assert a reset if a low signal is observed on that pin, or an NMI which will assert an interrupt from an edge of the signal dependent on the setting of the *edgeDirection* parameter in [SFR\\_setNMIEdge\(\)](#).

**Parameters**

<i>resetPinFunction</i>	<p>is the function that the <math>\sim</math>RST/NMI pin should take on. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SFR_RESETPINFUNC_RESET</b> [Default]</li> <li>■ <b>SFR_RESETPINFUNC_NMI</b></li> </ul> <p>Modified bits are <b>SYSNMI</b> of <b>SFRRPCR</b> register.</p>
-------------------------	--

**Returns**

None

**SFR\_setResetPinPullResistor()**

```
void SFR_setResetPinPullResistor (
    uint16_t pullResistorSetup )
```

Sets the pull-up/down resistor on the  $\sim$ RST/NMI pin.

This function sets the pull-up/down resistors on the  $\sim$ RST/NMI pin to the settings from the *pullResistorSetup* parameter.

**Parameters**

<i>pullResistorSetup</i>	is the selection of how the pull-up/down resistor on the $\sim$ RST/NMI pin should be setup or disabled. Valid values are: <ul style="list-style-type: none"><li>■ <b>SFR_RESISTORDISABLE</b></li><li>■ <b>SFR_RESISTORENABLE_PULLUP</b> [Default]</li><li>■ <b>SFR_RESISTORENABLE_PULLDOWN</b></li></ul> Modified bits are <b>SYRSTUP</b> of <b>SFRRPCR</b> register.
--------------------------	--

**Returns**

None

### 34.3 Programming Example

The following example shows how to initialize and use the SFR API

```
do
{
    // Clear SFR Fault Flag
    SFR_clearInterrupt (SFR_BASE,
                      OFIFG);

    // Test oscillator fault flag
}while (SFR_getInterruptStatus (SFR_BASE, OFIFG));
```



# 35 System Control Module

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## 35.1 Introduction

The System Control (SYS) API provides a set of functions for using the MSP430Ware SYS module. Functions are provided to control various SYS controls, setup the BSL, and control the JTAG Mailbox.

## 35.2 API Functions

### Functions

- void [SysCtl.enableDedicatedJTAGPins](#) (void)  
*Sets the JTAG pins to be exclusively for JTAG until a BOR occurs.*
- uint8\_t [SysCtl.getBSLEntryIndication](#) (void)  
*Returns the indication of a BSL entry sequence from the Spy-Bi-Wire.*
- void [SysCtl.enablePMMAccessProtect](#) (void)  
*Enables PMM Access Protection.*
- void [SysCtl.enableRAMBasedInterruptVectors](#) (void)  
*Enables RAM-based Interrupt Vectors.*
- void [SysCtl.disableRAMBasedInterruptVectors](#) (void)  
*Disables RAM-based Interrupt Vectors.*
- void [SysCtl.enableBSLProtect](#) (void)  
*Enables BSL memory protection.*
- void [SysCtl.disableBSLProtect](#) (void)  
*Disables BSL memory protection.*
- void [SysCtl.enableBSLMemory](#) (void)  
*Enables BSL memory.*
- void [SysCtl.disableBSLMemory](#) (void)  
*Disables BSL memory.*
- void [SysCtl.setRAMAssignedToBSL](#) (uint8\_t BSLRAMAssignment)  
*Sets RAM assignment to BSL area.*
- void [SysCtl.setBSLSize](#) (uint8\_t BSLSizeSelect)  
*Sets the size of the BSL in Flash.*
- void [SysCtl.initJTAGMailbox](#) (uint8\_t mailboxSizeSelect, uint8\_t autoClearInboxFlagSelect)  
*Initializes JTAG Mailbox with selected properties.*
- uint8\_t [SysCtl.getJTAGMailboxFlagStatus](#) (uint8\_t mailboxFlagMask)  
*Returns the status of the selected JTAG Mailbox flags.*
- void [SysCtl.clearJTAGMailboxFlagStatus](#) (uint8\_t mailboxFlagMask)  
*Clears the status of the selected JTAG Mailbox flags.*
- uint16\_t [SysCtl.getJTAGInboxMessage16Bit](#) (uint8\_t inboxSelect)  
*Returns the contents of the selected JTAG Inbox in a 16 bit format.*
- uint32\_t [SysCtl.getJTAGInboxMessage32Bit](#) (void)

- Returns the contents of JTAG Inboxes in a 32 bit format.*

  - void `SysCtl_setJTAGOutgoingMessage16Bit` (uint8\_t outboxSelect, uint16\_t outgoingMessage)

*Sets a 16 bit outgoing message in to the selected JTAG Outbox.*

  - void `SysCtl_setJTAGOutgoingMessage32Bit` (uint32\_t outgoingMessage)

*Sets a 32 bit message in to both JTAG Outboxes.*

## 35.2.1 Detailed Description

The SYS API is broken into 3 groups: the various SYS controls, the BSL controls, and the JTAG mailbox controls.

The various SYS controls are handled by

- `SysCtl_enableDedicatedJTAGPins()`
- `SysCtl_getBSLEntryIndication()`
- `SysCtl_enablePMMAccessProtect()`
- `SysCtl_enableRAMBasedInterruptVectors()`
- `SysCtl_disableRAMBasedInterruptVectors()`

The BSL controls are handled by

- `SysCtl_enableBSLProtect()`
- `SysCtl_disableBSLProtect()`
- `SysCtl_disableBSLMemory()`
- `SysCtl_enableBSLMemory()`
- `SysCtl_setRAMAssignedToBSL()`
- `SysCtl_setBSLSize()`

The JTAG Mailbox controls are handled by

- `SysCtl_initJTAGMailbox()`
- `SysCtl_getJTAGMailboxFlagStatus()`
- `SysCtl_getJTAGInboxMessage16Bit()`
- `SysCtl_getJTAGInboxMessage32Bit()`
- `SysCtl_setJTAGOutgoingMessage16Bit()`
- `SysCtl_setJTAGOutgoingMessage32Bit()`
- `SysCtl_clearJTAGMailboxFlagStatus()`

## 35.2.2 Function Documentation

### `SysCtl_clearJTAGMailboxFlagStatus()`

```
void SysCtl_clearJTAGMailboxFlagStatus (
    uint8_t mailboxFlagMask )
```

Clears the status of the selected JTAG Mailbox flags.

This function clears the selected JTAG Mailbox flags.

**Parameters**

<i>mailboxFlagMask</i>	<p>is the bit mask of JTAG mailbox flags that the status of should be cleared. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>SYSCTL_JTAGOUTBOX_FLAG0</b> - flag for JTAG outbox 0</li> <li>■ <b>SYSCTL_JTAGOUTBOX_FLAG1</b> - flag for JTAG outbox 1</li> <li>■ <b>SYSCTL_JTAGINBOX_FLAG0</b> - flag for JTAG inbox 0</li> <li>■ <b>SYSCTL_JTAGINBOX_FLAG1</b> - flag for JTAG inbox 1</li> </ul>
------------------------	---

**Returns**

None

**SysCtl\_disableBSLMemory()**

```
void SysCtl_disableBSLMemory (
    void )
```

Disables BSL memory.

This function disables BSL memory, which makes BSL memory act like vacant memory.

**Returns**

None

**SysCtl\_disableBSLProtect()**

```
void SysCtl_disableBSLProtect (
    void )
```

Disables BSL memory protection.

This function disables protection on the BSL memory.

**Returns**

None

**SysCtl\_disableRAMBasedInterruptVectors()**

```
void SysCtl_disableRAMBasedInterruptVectors (
    void )
```

Disables RAM-based Interrupt Vectors.

This function disables the interrupt vectors from being generated at the top of the RAM.

**Returns**

None

### SysCtl\_enableBSLMemory()

```
void SysCtl_enableBSLMemory (  
    void )
```

Enables BSL memory.

This function enables BSL memory, which allows BSL memory to be addressed

**Returns**

None

### SysCtl\_enableBSLProtect()

```
void SysCtl_enableBSLProtect (  
    void )
```

Enables BSL memory protection.

This function enables protection on the BSL memory, which prevents any reading, programming, or erasing of the BSL memory.

**Returns**

None

### SysCtl\_enableDedicatedJTAGPins()

```
void SysCtl_enableDedicatedJTAGPins (  
    void )
```

Sets the JTAG pins to be exclusively for JTAG until a BOR occurs.

This function sets the JTAG pins to be exclusively used for the JTAG, and not to be shared with the GPIO pins. This setting can only be cleared when a BOR occurs.

**Returns**

None

### SysCtl\_enablePMMAccessProtect()

```
void SysCtl_enablePMMAccessProtect (  
    void )
```

Enables PMM Access Protection.

This function enables the PMM Access Protection, which will lock any changes on the PMM control registers until a BOR occurs.

**Returns**

None

## SysCtl\_enableRAMBasedInterruptVectors()

```
void SysCtl_enableRAMBasedInterruptVectors (
    void )
```

Enables RAM-based Interrupt Vectors.

This function enables RAM-base Interrupt Vectors, which means that interrupt vectors are generated with the end address at the top of RAM, instead of the top of the lower 64kB of flash.

### Returns

None

## SysCtl\_getBSLEntryIndication()

```
uint8_t SysCtl_getBSLEntryIndication (
    void )
```

Returns the indication of a BSL entry sequence from the Spy-Bi-Wire.

This function returns the indication of a BSL entry sequence from the Spy- Bi-Wire.

### Returns

One of the following:

- **SYSCTL\_BSENTRY\_INDICATED**
- **SYSCTL\_BSENTRY\_NOTINDICATED**  
indicating if a BSL entry sequence was detected

## SysCtl\_getJTAGInboxMessage16Bit()

```
uint16_t SysCtl_getJTAGInboxMessage16Bit (
    uint8_t inboxSelect )
```

Returns the contents of the selected JTAG Inbox in a 16 bit format.

This function returns the message contents of the selected JTAG inbox. If the auto clear settings for the Inbox flags were set, then using this function will automatically clear the corresponding JTAG inbox flag.

### Parameters

<i>inboxSelect</i>	<p>is the chosen JTAG inbox that the contents of should be returned Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SYSCTL_JTAGINBOX_0</b> - return contents of JTAG inbox 0</li> <li>■ <b>SYSCTL_JTAGINBOX_1</b> - return contents of JTAG inbox 1</li> </ul>
--------------------	--

### Returns

The contents of the selected JTAG inbox in a 16 bit format.

## SysCtl\_getJTAGInboxMessage32Bit()

```
uint32_t SysCtl_getJTAGInboxMessage32Bit (
    void )
```

Returns the contents of JTAG Inboxes in a 32 bit format.

This function returns the message contents of both JTAG inboxes in a 32 bit format. This function should be used if 32-bit messaging has been set in the `SYS_initJTAGMailbox()` function. If the auto clear settings for the Inbox flags were set, then using this function will automatically clear both JTAG inbox flags.

### Returns

The contents of both JTAG messages in a 32 bit format.

## SysCtl\_getJTAGMailboxFlagStatus()

```
uint8_t SysCtl_getJTAGMailboxFlagStatus (
    uint8_t mailboxFlagMask )
```

Returns the status of the selected JTAG Mailbox flags.

This function will return the status of the selected JTAG Mailbox flags in bit mask format matching that passed into the `mailboxFlagMask` parameter.

### Parameters

<i>mailboxFlagMask</i>	<p>is the bit mask of JTAG mailbox flags that the status of should be returned. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>SYSCTL_JTAGOUTBOX_FLAG0</b> - flag for JTAG outbox 0</li> <li>■ <b>SYSCTL_JTAGOUTBOX_FLAG1</b> - flag for JTAG outbox 1</li> <li>■ <b>SYSCTL_JTAGINBOX_FLAG0</b> - flag for JTAG inbox 0</li> <li>■ <b>SYSCTL_JTAGINBOX_FLAG1</b> - flag for JTAG inbox 1</li> </ul>
------------------------	--

### Returns

A bit mask of the status of the selected mailbox flags.

## SysCtl\_initJTAGMailbox()

```
void SysCtl_initJTAGMailbox (
    uint8_t mailboxSizeSelect,
    uint8_t autoClearInboxFlagSelect )
```

Initializes JTAG Mailbox with selected properties.

This function sets the specified settings for the JTAG Mailbox system. The settings that can be set are the size of the JTAG messages, and the auto- clearing of the inbox flags. If the inbox flags are set to auto-clear, then the inbox flags will be cleared upon reading of the inbox message buffer,

otherwise they will have to be reset by software using the `SYS_clearJTAGMailboxFlagStatus()` function.

#### Parameters

<i>mailboxSizeSelect</i>	<p>is the size of the JTAG Mailboxes, whether 16- or 32-bits. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SYSCTL_JTAGMBSIZE_16BIT</b> [Default] - the JTAG messages will take up only one JTAG mailbox (i. e. an outgoing message will take up only 1 outbox of the JTAG mailboxes)</li> <li>■ <b>SYSCTL_JTAGMBSIZE_32BIT</b> - the JTAG messages will be contained within both JTAG mailboxes (i. e. an outgoing message will take up both Outboxes of the JTAG mailboxes) Modified bits are <b>JMBMODE</b> of <b>SYSJMBC</b> register.</li> </ul>
<i>autoClearInboxFlagSelect</i>	<p>decides how the JTAG inbox flags should be cleared, whether automatically after the corresponding outbox has been written to, or manually by software. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SYSCTL_JTAGINBOX0AUTO_JTAGINBOX1AUTO</b> [Default] - both JTAG inbox flags will be reset automatically when the corresponding inbox is read from.</li> <li>■ <b>SYSCTL_JTAGINBOX0AUTO_JTAGINBOX1SW</b> - only JTAG inbox 0 flag is reset automatically, while JTAG inbox 1 is reset with the</li> <li>■ <b>SYSCTL_JTAGINBOX0SW_JTAGINBOX1AUTO</b> - only JTAG inbox 1 flag is reset automatically, while JTAG inbox 0 is reset with the</li> <li>■ <b>SYSCTL_JTAGINBOX0SW_JTAGINBOX1SW</b> - both JTAG inbox flags will need to be reset manually by the Modified bits are <b>JMBCLR0OFF</b> and <b>JMBCLR1OFF</b> of <b>SYSJMBC</b> register.</li> </ul>

#### Returns

None

#### SysCtl\_setBSLSize()

```
void SysCtl_setBSLSize (
    uint8_t BSLSizeSelect )
```

Sets the size of the BSL in Flash.

This function sets the size of the BSL in Flash memory.

**Parameters**

<i>BSLSizeSelect</i>	<p>is the amount of segments the BSL should take. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SYSCTL_BSLSIZE_SEG3</b></li> <li>■ <b>SYSCTL_BSLSIZE_SEGS23</b></li> <li>■ <b>SYSCTL_BSLSIZE_SEGS123</b></li> <li>■ <b>SYSCTL_BSLSIZE_SEGS1234</b> [Default]</li> </ul> <p>Modified bits are <b>SYSBSLSIZE</b> of <b>SYSBSLC</b> register.</p>
----------------------	--

**Returns**

None

**SysCtl.setJTAGOutgoingMessage16Bit()**

```
void SysCtl_setJTAGOutgoingMessage16Bit (
    uint8_t outboxSelect,
    uint16_t outgoingMessage )
```

Sets a 16 bit outgoing message in to the selected JTAG Outbox.

This function sets the outgoing message in the selected JTAG outbox. The corresponding JTAG outbox flag is cleared after this function, and set after the JTAG has read the message.

**Parameters**

<i>outboxSelect</i>	<p>is the chosen JTAG outbox that the message should be set it. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SYSCTL_JTAGOUTBOX_0</b> - set the contents of JTAG outbox 0</li> <li>■ <b>SYSCTL_JTAGOUTBOX_1</b> - set the contents of JTAG outbox 1</li> </ul>
<i>outgoingMessage</i>	<p>is the message to send to the JTAG. Modified bits are <b>MSGHI</b> and <b>MSGLO</b> of <b>SYSJMBOX</b> register.</p>

**Returns**

None

**SysCtl.setJTAGOutgoingMessage32Bit()**

```
void SysCtl_setJTAGOutgoingMessage32Bit (
    uint32_t outgoingMessage )
```

Sets a 32 bit message in to both JTAG Outboxes.

This function sets the 32-bit outgoing message in both JTAG outboxes. The JTAG outbox flags are cleared after this function, and set after the JTAG has read the message.



**Parameters**

<i>outgoingMessage</i>	is the message to send to the JTAG. Modified bits are <b>MSGHI</b> and <b>MSGLO</b> of <b>SYSJMBOX</b> register.
------------------------	---

**Returns**

None

**SysCtl.setRAMAssignedToBSL()**

```
void SysCtl.setRAMAssignedToBSL (
    uint8_t BSLRAMAssignment )
```

Sets RAM assignment to BSL area.

This function allows RAM to be assigned to BSL, based on the selection of the BSLRAMAssignment parameter.

**Parameters**

<i>BSLRAMAssignment</i>	is the selection of if the BSL should be placed in RAM or not. Valid values are: <ul style="list-style-type: none"> <li>■ <b>SYSCTL.BSLRAMASSIGN_NORAM</b> [Default]</li> <li>■ <b>SYSCTL.BSLRAMASSIGN_LOWEST16BYTES</b> Modified bits are <b>SYSBSLR</b> of <b>SYSBSLC</b> register.</li> </ul>
-------------------------	--

**Returns**

None

## 35.3 Programming Example

The following example shows how to initialize and use the SYS API

```
SysCtl.enableBSLProtect();
```

## 36 Timer Event Control (TEC)

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### 36.1 Introduction

Timer Event Control (TEC) module is the interface between Timer modules and the external events. This chapter describes the TEC Module.

TEC is a module that connects different Timer modules to each other and routes the external signals to the Timer modules. TEC contains the control registers to configure the routing between the Timer modules, and it also has the enable register bits and the interrupt enable and interrupt flags for external event inputs. TEC features include:

- Enabling of internal and external clear signals
- Routing of internal signals (between Timer\_D instances) and external clear signals
- Support of external fault input signals
- Interrupt vector generation of external fault and clear signals.
- Generating feedback signals to the Timer capture/compare channels to affect the timer outputs

### 36.2 API Functions

#### Functions

- void [TEC\\_initExternalClearInput](#) (uint16\_t baseAddress, uint8\_t signalType, uint8\_t signalHold, uint8\_t polarityBit)  
*Configures the Timer Event Control External Clear Input.*
- void [TEC\\_initExternalFaultInput](#) (uint16\_t baseAddress, [TEC\\_initExternalFaultInputParam](#) \*param)  
*Configures the Timer Event Control External Fault Input.*
- void [TEC\\_enableExternalFaultInput](#) (uint16\_t baseAddress, uint8\_t channelEventBlock)  
*Enable the Timer Event Control External fault input.*
- void [TEC\\_disableExternalFaultInput](#) (uint16\_t baseAddress, uint8\_t channelEventBlock)  
*Disable the Timer Event Control External fault input.*
- void [TEC\\_enableExternalClearInput](#) (uint16\_t baseAddress)  
*Enable the Timer Event Control External Clear Input.*
- void [TEC\\_disableExternalClearInput](#) (uint16\_t baseAddress)  
*Disable the Timer Event Control External Clear Input.*
- void [TEC\\_enableAuxiliaryClearSignal](#) (uint16\_t baseAddress)  
*Enable the Timer Event Control Auxiliary Clear Signal.*
- void [TEC\\_disableAuxiliaryClearSignal](#) (uint16\_t baseAddress)  
*Disable the Timer Event Control Auxiliary Clear Signal.*
- void [TEC\\_clearInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)

- Clears the Timer Event Control Interrupt flag.*
- uint8\_t [TEC\\_getInterruptStatus](#) (uint16\_t baseAddress, uint8\_t mask)
  - Gets the current Timer Event Control interrupt status.*
- void [TEC\\_enableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)
  - Enables individual Timer Event Control interrupt sources.*
- void [TEC\\_disableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)
  - Disables individual Timer Event Control interrupt sources.*
- uint8\_t [TEC\\_getExternalFaultStatus](#) (uint16\_t baseAddress, uint8\_t mask)
  - Gets the current Timer Event Control External Fault Status.*
- void [TEC\\_clearExternalFaultStatus](#) (uint16\_t baseAddress, uint8\_t mask)
  - Clears the Timer Event Control External Fault Status.*
- uint8\_t [TEC\\_getExternalClearStatus](#) (uint16\_t baseAddress)
  - Gets the current Timer Event Control External Clear Status.*
- void [TEC\\_clearExternalClearStatus](#) (uint16\_t baseAddress)
  - Clears the Timer Event Control External Clear Status.*

## 36.2.1 Detailed Description

The tec configuration is handled by

- [TEC\\_configureExternalClearInput\(\)](#)
- [TEC\\_initExternalFaultInput\(\)](#)
- [TEC\\_enableExternalFaultInput\(\)](#)
- [TEC\\_disableExternalFaultInput\(\)](#)
- [TEC\\_enableExternalClearInput\(\)](#)
- [TEC\\_disableExternalClearInput\(\)](#)
- [TEC\\_enableAuxiliaryClearSignal\(\)](#)
- [TEC\\_disableAuxiliaryClearSignal\(\)](#)

The interrupt and status operations are handled by

- [TEC\\_enableExternalFaultInput\(\)](#)
- [TEC\\_disableExternalFaultInput\(\)](#)
- [TEC\\_clearInterrupt\(\)](#)
- [TEC\\_getInterruptStatus\(\)](#)
- [TEC\\_enableInterrupt\(\)](#)
- [TEC\\_disableInterrupt\(\)](#)
- [TEC\\_getExternalFaultStatus\(\)](#)
- [TEC\\_clearExternalFaultStatus\(\)](#)
- [TEC\\_getExternalClearStatus\(\)](#)
- [TEC\\_clearExternalClearStatus\(\)](#)

## 36.2.2 Function Documentation

### TEC\_clearExternalClearStatus()

```
void TEC_clearExternalClearStatus (
    uint16_t baseAddress )
```

Clears the Timer Event Control External Clear Status.

#### Parameters

<i>baseAddress</i>	is the base address of the TEC module.
--------------------	--

Modified bits of **TECxINT** register.

#### Returns

None

### TEC\_clearExternalFaultStatus()

```
void TEC_clearExternalFaultStatus (
    uint16_t baseAddress,
    uint8_t mask )
```

Clears the Timer Event Control External Fault Status.

#### Parameters

<i>baseAddress</i>	is the base address of the TEC module.
<i>mask</i>	is the masked status flag be cleared Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TEC.CE0</b></li> <li>■ <b>TEC.CE1</b></li> <li>■ <b>TEC.CE2</b></li> <li>■ <b>TEC.CE3</b> - (available on TEC5 TEC7)</li> <li>■ <b>TEC.CE4</b> - (available on TEC5 TEC7)</li> <li>■ <b>TEC.CE5</b> - (only available on TEC7)</li> <li>■ <b>TEC.CE6</b> - (only available on TEC7)</li> </ul>

Modified bits of **TECxINT** register.

**Returns**

None

**TEC\_clearInterrupt()**

```
void TEC_clearInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Clears the Timer Event Control Interrupt flag.

**Parameters**

<i>baseAddress</i>	is the base address of the TEC module.
<i>mask</i>	is the masked interrupt flag to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TEC.EXTERNAL_FAULT_INTERRUPT</b> - External fault interrupt flag</li> <li>■ <b>TEC.EXTERNAL_CLEAR_INTERRUPT</b> - External clear interrupt flag</li> <li>■ <b>TEC.AUXILIARY_CLEAR_INTERRUPT</b> - Auxiliary clear interrupt flag</li> </ul>

Modified bits of **TECxINT** register.

**Returns**

None

**TEC\_disableAuxiliaryClearSignal()**

```
void TEC_disableAuxiliaryClearSignal (
    uint16_t baseAddress )
```

Disable the Timer Event Control Auxiliary Clear Signal.

**Parameters**

<i>baseAddress</i>	is the base address of the TEC module.
--------------------	--

Modified bits of **TECxCTL2** register.

**Returns**

None

**TEC\_disableExternalClearInput()**

```
void TEC_disableExternalClearInput (
```

```
uint16_t baseAddress )
```

Disable the Timer Event Control External Clear Input.

#### Parameters

<i>baseAddress</i>	is the base address of the TEC module.
--------------------	--

Modified bits of **TECxCTL2** register.

#### Returns

None

### TEC\_disableExternalFaultInput()

```
void TEC_disableExternalFaultInput (
    uint16_t baseAddress,
    uint8_t channelEventBlock )
```

Disable the Timer Event Control External fault input.

#### Parameters

<i>baseAddress</i>	is the base address of the TEC module.
<i>channelEventBlock</i>	selects the channel event block Valid values are: <ul style="list-style-type: none"> <li>■ <b>TEC_CE0</b></li> <li>■ <b>TEC_CE1</b></li> <li>■ <b>TEC_CE2</b></li> <li>■ <b>TEC_CE3</b> - (available on TEC5 TEC7)</li> <li>■ <b>TEC_CE4</b> - (available on TEC5 TEC7)</li> <li>■ <b>TEC_CE5</b> - (only available on TEC7)</li> <li>■ <b>TEC_CE6</b> - (only available on TEC7)</li> </ul>

Modified bits of **TECxCTL0** register.

#### Returns

None

### TEC\_disableInterrupt()

```
void TEC_disableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Disables individual Timer Event Control interrupt sources.

Disables the indicated Timer Event Control interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

#### Parameters

<i>baseAddress</i>	is the base address of the TEC module.
<i>mask</i>	is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TEC_EXTERNAL_FAULT_INTERRUPT</b> - External fault interrupt flag</li> <li>■ <b>TEC_EXTERNAL_CLEAR_INTERRUPT</b> - External clear interrupt flag</li> <li>■ <b>TEC_AUXILIARY_CLEAR_INTERRUPT</b> - Auxiliary clear interrupt flag</li> </ul>

Modified bits of **TECxINT** register.

#### Returns

None

### TEC\_enableAuxiliaryClearSignal()

```
void TEC_enableAuxiliaryClearSignal (
    uint16_t baseAddress )
```

Enable the Timer Event Control Auxiliary Clear Signal.

#### Parameters

<i>baseAddress</i>	is the base address of the TEC module.
--------------------	--

Modified bits of **TECxCTL2** register.

#### Returns

None

### TEC\_enableExternalClearInput()

```
void TEC_enableExternalClearInput (
    uint16_t baseAddress )
```

Enable the Timer Event Control External Clear Input.

#### Parameters

<i>baseAddress</i>	is the base address of the TEC module.
--------------------	--

Modified bits of **TECxCTL2** register.

**Returns**

None

**TEC\_enableExternalFaultInput()**

```
void TEC_enableExternalFaultInput (
    uint16_t baseAddress,
    uint8_t channelEventBlock )
```

Enable the Timer Event Control External fault input.

**Parameters**

<i>baseAddress</i>	is the base address of the TEC module.
<i>channelEventBlock</i>	selects the channel event block Valid values are: <ul style="list-style-type: none"> <li>■ <b>TEC_CE0</b></li> <li>■ <b>TEC_CE1</b></li> <li>■ <b>TEC_CE2</b></li> <li>■ <b>TEC_CE3</b> - (available on TEC5 TEC7)</li> <li>■ <b>TEC_CE4</b> - (available on TEC5 TEC7)</li> <li>■ <b>TEC_CE5</b> - (only available on TEC7)</li> <li>■ <b>TEC_CE6</b> - (only available on TEC7)</li> </ul>

Modified bits of **TECxCTL0** register.

**Returns**

None

**TEC\_enableInterrupt()**

```
void TEC_enableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Enables individual Timer Event Control interrupt sources.

Enables the indicated Timer Event Control interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the TEC module.
--------------------	--



**Parameters**

<i>mask</i>	is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TEC_EXTERNAL_FAULT_INTERRUPT</b> - External fault interrupt flag</li> <li>■ <b>TEC_EXTERNAL_CLEAR_INTERRUPT</b> - External clear interrupt flag</li> <li>■ <b>TEC_AUXILIARY_CLEAR_INTERRUPT</b> - Auxiliary clear interrupt flag</li> </ul>
-------------	---

Modified bits of **TECxINT** register.

**Returns**

None

**TEC\_getExternalClearStatus()**

```
uint8_t TEC_getExternalClearStatus (
    uint16_t baseAddress )
```

Gets the current Timer Event Control External Clear Status.

**Parameters**

<i>baseAddress</i>	is the base address of the TEC module.
--------------------	--

**Returns**

One of the following:

- **TEC\_EXTERNAL\_CLEAR\_DETECTED**
- **TEC\_EXTERNAL\_CLEAR\_NOT\_DETECTED**  
indicating the status of the external clear

**TEC\_getExternalFaultStatus()**

```
uint8_t TEC_getExternalFaultStatus (
    uint16_t baseAddress,
    uint8_t mask )
```

Gets the current Timer Event Control External Fault Status.

This returns the Timer Event Control fault status for the module.

**Parameters**

<i>baseAddress</i>	is the base address of the TEC module.
--------------------	--

**Parameters**

<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TEC_CE0</b></li> <li>■ <b>TEC_CE1</b></li> <li>■ <b>TEC_CE2</b></li> <li>■ <b>TEC_CE3</b> - (available on TEC5 TEC7)</li> <li>■ <b>TEC_CE4</b> - (available on TEC5 TEC7)</li> <li>■ <b>TEC_CE5</b> - (only available on TEC7)</li> <li>■ <b>TEC_CE6</b> - (only available on TEC7)</li> </ul>
-------------	--

**Returns**

Logical OR of any of the following:

- **TEC\_CE0**
  - **TEC\_CE1**
  - **TEC\_CE2**
  - **TEC\_CE3** (available on TEC5 TEC7)
  - **TEC\_CE4** (available on TEC5 TEC7)
  - **TEC\_CE5** (only available on TEC7)
  - **TEC\_CE6** (only available on TEC7)
- indicating the external fault status of the masked channel event blocks

**TEC\_getInterruptStatus()**

```
uint8_t TEC_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t mask )
```

Gets the current Timer Event Control interrupt status.

This returns the interrupt status for the module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the TEC module.
<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TEC_EXTERNAL_FAULT_INTERRUPT</b> - External fault interrupt flag</li> <li>■ <b>TEC_EXTERNAL_CLEAR_INTERRUPT</b> - External clear interrupt flag</li> <li>■ <b>TEC_AUXILIARY_CLEAR_INTERRUPT</b> - Auxiliary clear interrupt flag</li> </ul>

**Returns**

Logical OR of any of the following:

- **TEC\_EXTERNAL\_FAULT\_INTERRUPT** External fault interrupt flag
- **TEC\_EXTERNAL\_CLEAR\_INTERRUPT** External clear interrupt flag
- **TEC\_AUXILIARY\_CLEAR\_INTERRUPT** Auxiliary clear interrupt flag indicating the status of the masked interrupts

**TEC\_initExternalClearInput()**

```
void TEC_initExternalClearInput (
    uint16_t baseAddress,
    uint8_t signalType,
    uint8_t signalHold,
    uint8_t polarityBit )
```

Configures the Timer Event Control External Clear Input.

**Parameters**

<i>baseAddress</i>	is the base address of the TEC module.
<i>signalType</i>	is the selected signal type Valid values are: <ul style="list-style-type: none"> <li>■ <b>TEC_EXTERNAL_CLEAR_SIGNALTYPE_EDGE_SENSITIVE</b> [Default]</li> <li>■ <b>TEC_EXTERNAL_CLEAR_SIGNALTYPE_LEVEL_SENSITIVE</b></li> </ul>
<i>signalHold</i>	is the selected signal hold Valid values are: <ul style="list-style-type: none"> <li>■ <b>TEC_EXTERNAL_CLEAR_SIGNAL_NOT_HELD</b> [Default]</li> <li>■ <b>TEC_EXTERNAL_CLEAR_SIGNAL_HELD</b></li> </ul>
<i>polarityBit</i>	is the selected signal type Valid values are: <ul style="list-style-type: none"> <li>■ <b>TEC_EXTERNAL_CLEAR_POLARITY_FALLING_EDGE_OR_LOW_LEVEL</b> [Default]</li> <li>■ <b>TEC_EXTERNAL_CLEAR_POLARITY_RISING_EDGE_OR_HIGH_LEVEL</b></li> </ul>

Modified bits of **TECxCTL2** register.

**Returns**

None

**TEC\_initExternalFaultInput()**

```
void TEC_initExternalFaultInput (
    uint16_t baseAddress,
    TEC_initExternalFaultInputParam * param )
```

Configures the Timer Event Control External Fault Input.

**Parameters**

<i>baseAddress</i>	is the base address of the TEC module.
<i>param</i>	is the pointer to struct for external fault input initialization.

Modified bits of **TECxCTL2** register.

**Returns**

None

References `TEC_initExternalFaultInputParam::polarityBit`, `TEC_initExternalFaultInputParam::selectedExternalFault`, `TEC_initExternalFaultInputParam::signalHold`, and `TEC_initExternalFaultInputParam::signalType`.

## 36.3 Programming Example

The following example shows how to use the TEC API.

```
{
    TIMER_D.startCounter(TIMER_D1.BASE,
        TIMERD.UP.MODE);

    // Configure TD1 TEC External Clear
    // Need to physically connect P2.0/TD0.2 to P2.7/TEC1CLR
    GPIO_setAsPeripheralModuleFunctionInputPin(
        GPIO_PORT_P2,
        GPIO_PIN7
    );

    // High Level trigger, ext clear enable
    TEC_configureExternalClearInput(TEC1.BASE,
        TEC_EXTERNAL_CLEAR_SIGNALTYPE_LEVEL_SENSITIVE,
        TEC_EXTERNAL_CLEAR_SIGNAL_NOT_HELD,
        TEC_EXTERNAL_CLEAR_POLARITY_RISING_EDGE_OR_HIGH_LEVEL
    );
    TEC_enableExternalClearInput(TEC1.BASE);
}
```

## 37 16-Bit Timer\_A (TIMER\_A)

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### 37.1 Introduction

TIMER\_A is a 16-bit timer/counter with multiple capture/compare registers. TIMER\_A can support multiple capture/compares, PWM outputs, and interval timing. TIMER\_A also has extensive interrupt capabilities. Interrupts may be generated from the counter on overflow conditions and from each of the capture/compare registers.

This peripheral API handles Timer A hardware peripheral.

TIMER\_A features include:

- Asynchronous 16-bit timer/counter with four operating modes
- Selectable and configurable clock source
- Up to seven configurable capture/compare registers
- Configurable outputs with pulse width modulation (PWM) capability
- Asynchronous input and output latching
- Interrupt vector register for fast decoding of all Timer interrupts

TIMER\_A can operate in 3 modes

- Continuous Mode
- Up Mode
- Down Mode

TIMER\_A Interrupts may be generated on counter overflow conditions and during capture compare events.

The TIMER\_A may also be used to generate PWM outputs. PWM outputs can be generated by initializing the compare mode with `TIMER_A_initCompare()` and the necessary parameters. The PWM may be customized by selecting a desired timer mode (continuous/up/upDown), duty cycle, output mode, timer period etc. The library also provides a simpler way to generate PWM using `Timer_A_generatePWM()` API. However the level of customization and the kinds of PWM generated are limited in this API. Depending on how complex the PWM is and what level of customization is required, the user can use `Timer_A_generatePWM()` or a combination of `Timer_initCompare()` and timer start APIs

The TIMER\_A API provides a set of functions for dealing with the TIMER\_A module. Functions are provided to configure and control the timer, along with functions to modify timer/counter values, and to manage interrupt handling for the timer.

Control is also provided over interrupt sources and events. Interrupts can be generated to indicate that an event has been captured.

## 37.2 API Functions

### Functions

- void `Timer_A_startCounter` (uint16\_t baseAddress, uint16\_t timerMode)  
*Starts Timer\_A counter.*
- void `Timer_A_initContinuousMode` (uint16\_t baseAddress, `Timer_A_initContinuousModeParam` \*param)  
*Configures Timer\_A in continuous mode.*
- void `Timer_A_initUpMode` (uint16\_t baseAddress, `Timer_A_initUpModeParam` \*param)  
*Configures Timer\_A in up mode.*
- void `Timer_A_initUpDownMode` (uint16\_t baseAddress, `Timer_A_initUpDownModeParam` \*param)  
*Configures Timer\_A in up down mode.*
- void `Timer_A_initCaptureMode` (uint16\_t baseAddress, `Timer_A_initCaptureModeParam` \*param)  
*Initializes Capture Mode.*
- void `Timer_A_initCompareMode` (uint16\_t baseAddress, `Timer_A_initCompareModeParam` \*param)  
*Initializes Compare Mode.*
- void `Timer_A_enableInterrupt` (uint16\_t baseAddress)  
*Enable timer interrupt.*
- void `Timer_A_disableInterrupt` (uint16\_t baseAddress)  
*Disable timer interrupt.*
- uint32\_t `Timer_A_getInterruptStatus` (uint16\_t baseAddress)  
*Get timer interrupt status.*
- void `Timer_A_enableCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Enable capture compare interrupt.*
- void `Timer_A_disableCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Disable capture compare interrupt.*
- uint32\_t `Timer_A_getCaptureCompareInterruptStatus` (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint16\_t mask)  
*Return capture compare interrupt status.*
- void `Timer_A_clear` (uint16\_t baseAddress)  
*Reset/Clear the timer clock divider, count direction, count.*
- uint8\_t `Timer_A_getSynchronizedCaptureCompareInput` (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint16\_t synchronized)  
*Get synchronized capturecompare input.*
- uint8\_t `Timer_A_getOutputForOutputModeOutBitValue` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Get output bit for output mode.*
- uint16\_t `Timer_A_getCaptureCompareCount` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Get current capturecompare count.*
- void `Timer_A_setOutputForOutputModeOutBitValue` (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint8\_t outputModeOutBitValue)  
*Set output bit for output mode.*
- void `Timer_A_outputPWM` (uint16\_t baseAddress, `Timer_A_outputPWMPParam` \*param)  
*Generate a PWM with timer running in up mode.*
- void `Timer_A_stop` (uint16\_t baseAddress)

- Stops the timer.*
- void `Timer_A_setCompareValue` (uint16\_t baseAddress, uint16\_t compareRegister, uint16\_t compareValue)
- Sets the value of the capture-compare register.*
- void `Timer_A_setOutputMode` (uint16\_t baseAddress, uint16\_t compareRegister, uint16\_t compareOutputMode)
- Sets the output mode.*
- void `Timer_A_clearTimerInterrupt` (uint16\_t baseAddress)
- Clears the Timer TAIFG interrupt flag.*
- void `Timer_A_clearCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)
- Clears the capture-compare interrupt flag.*
- uint16\_t `Timer_A_getCounterValue` (uint16\_t baseAddress)
- Reads the current timer count value.*

### 37.2.1 Detailed Description

The TIMER\_A API is broken into three groups of functions: those that deal with timer configuration and control, those that deal with timer contents, and those that deal with interrupt handling.

TIMER\_A configuration and initialization is handled by

- `Timer_A_startCounter()`
- `Timer_A_initUpMode()`
- `Timer_A_initUpDownMode()`
- `Timer_A_initContinuousMode()`
- `Timer_A_initCaptureMode()`
- `Timer_A_initCompareMode()`
- `Timer_A_clear()`
- `Timer_A.stop()`

TIMER\_A outputs are handled by

- `Timer_A_getSynchronizedCaptureCompareInput()`
- `Timer_A_getOutputForOutputModeOutBitValue()`
- `Timer_A_setOutputForOutputModeOutBitValue()`
- `Timer_A.outputPWM()`
- `Timer_A.getCaptureCompareCount()`
- `Timer_A.setCompareValue()`
- `Timer_A.getCounterValue()`

The interrupt handler for the TIMER\_A interrupt is managed with

- `Timer_A.enableInterrupt()`
- `Timer_A.disableInterrupt()`
- `Timer_A.getInterruptStatus()`
- `Timer_A.enableCaptureCompareInterrupt()`

- [Timer\\_A\\_disableCaptureCompareInterrupt\(\)](#)
- [Timer\\_A\\_getCaptureCompareInterruptStatus\(\)](#)
- [Timer\\_A\\_clearCaptureCompareInterrupt\(\)](#)
- [Timer\\_A\\_clearTimerInterrupt\(\)](#)

## 37.2.2 Function Documentation

### Timer\_A\_clear()

```
void Timer_A_clear (
    uint16_t baseAddress )
```

Reset/Clear the timer clock divider, count direction, count.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

Modified bits of **TAxCTL** register.

#### Returns

None

References [Timer\\_A\\_getSynchronizedCaptureCompareInput\(\)](#).

### Timer\_A\_clearCaptureCompareInterrupt()

```
void Timer_A_clearCaptureCompareInterrupt (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Clears the capture-compare interrupt flag.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>captureCompareRegister</i>	selects the Capture-compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>



Modified bits are **CCIFG** of **TAxCTLn** register.

**Returns**

None

### Timer\_A\_clearTimerInterrupt()

```
void Timer_A_clearTimerInterrupt (
    uint16_t baseAddress )
```

Clears the Timer TAIFG interrupt flag.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

Modified bits are **TAIFG** of **TAxCTL** register.

**Returns**

None

### Timer\_A\_disableCaptureCompareInterrupt()

```
void Timer_A_disableCaptureCompareInterrupt (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Disable capture compare interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>captureCompareRegister</i>	is the selected capture compare register Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

Modified bits of **TAxCTLn** register.

**Returns**

None

**Timer\_A\_disableInterrupt()**

```
void Timer_A_disableInterrupt (
    uint16_t baseAddress )
```

Disable timer interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

Modified bits of **TAxCTL** register.**Returns**

None

**Timer\_A\_enableCaptureCompareInterrupt()**

```
void Timer_A_enableCaptureCompareInterrupt (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Enable capture compare interrupt.

Does not clear interrupt flags

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>captureCompareRegister</i>	is the selected capture compare register Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

Modified bits of **TAxCCTLn** register.

**Returns**

None

**Timer\_A\_enableInterrupt()**

```
void Timer_A_enableInterrupt (
    uint16_t baseAddress )
```

Enable timer interrupt.

Does not clear interrupt flags

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

Modified bits of **TAxCTL** register.**Returns**

None

**Timer\_A\_getCaptureCompareCount()**

```
uint16_t Timer_A_getCaptureCompareCount (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Get current capturecompare count.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>captureCompareRegister</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b>↔</li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b>↔</li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b>↔</li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b>↔</li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b>↔</li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b>↔</li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b>↔</li> </ul>

**Returns**

Current count as an uint16\_t

References Timer\_A.setOutputForOutputModeOutBitValue().

Referenced by Timer\_A.getOutputForOutputModeOutBitValue().

**Timer\_A.getCaptureCompareInterruptStatus()**

```
uint32_t Timer_A.getCaptureCompareInterruptStatus (
    uint16_t baseAddress,
    uint16_t captureCompareRegister,
    uint16_t mask )
```

Return capture compare interrupt status.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>captureCompareRegister</i>	is the selected capture compare register Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>mask</i>	is the mask for the interrupt status Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURE_OVERFLOW</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_INTERRUPT_FLAG</b></li> </ul>

**Returns**

Logical OR of any of the following:

- **TIMER\_A\_CAPTURE\_OVERFLOW**
  - **TIMER\_A\_CAPTURECOMPARE\_INTERRUPT\_FLAG**
- indicating the status of the masked interrupts

**Timer\_A.getCounterValue()**

```
uint16_t Timer_A.getCounterValue (
    uint16_t baseAddress )
```

Reads the current timer count value.

Reads the current count value of the timer. There is a majority vote system in place to confirm an accurate value is returned. The `TIMER_A_THRESHOLD` #define in the corresponding header file can be modified so that the votes must be closer together for a consensus to occur.

#### Parameters

<i>baseAddress</i>	is the base address of the <code>TIMER_A</code> module.
--------------------	---

#### Returns

Majority vote of timer count value

### Timer\_A\_getInterruptStatus()

```
uint32_t Timer_A_getInterruptStatus (
    uint16_t baseAddress )
```

Get timer interrupt status.

#### Parameters

<i>baseAddress</i>	is the base address of the <code>TIMER_A</code> module.
--------------------	---

#### Returns

One of the following:

- `TIMER_A_INTERRUPT_NOT_PENDING`
- `TIMER_A_INTERRUPT_PENDING`  
indicating the `Timer_A` interrupt status

### Timer\_A\_getOutputForOutputModeOutBitValue()

```
uint8_t Timer_A_getOutputForOutputModeOutBitValue (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Get output bit for output mode.

#### Parameters

<i>baseAddress</i>	is the base address of the <code>TIMER_A</code> module.
--------------------	---

## Parameters

<i>captureCompareRegister</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b>↔</li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b>↔</li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b>↔</li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b>↔</li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b>↔</li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b>↔</li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b>↔</li> </ul>
-------------------------------	---

## Returns

One of the following:

- **TIMER\_A\_OUTPUTMODE\_OUTBITVALUE\_HIGH**
- **TIMER\_A\_OUTPUTMODE\_OUTBITVALUE\_LOW**

References `Timer_A.getCaptureCompareCount()`.

Referenced by `Timer_A.getSynchronizedCaptureCompareInput()`.

### Timer\_A.getSynchronizedCaptureCompareInput()

```
uint8_t Timer_A.getSynchronizedCaptureCompareInput (
    uint16_t baseAddress,
    uint16_t captureCompareRegister,
    uint16_t synchronized )
```

Get synchronized capturecompare input.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

## Parameters

<i>captureCompareRegister</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>synchronized</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_READ_SYNCHRONIZED_CAPTURECOMPAREI↔ NPUT</b></li> <li>■ <b>TIMER_A_READ_CAPTURE_COMPARE_INPUT</b></li> </ul>

## Returns

One of the following:

- **TIMER\_A\_CAPTURECOMPARE\_INPUT\_HIGH**
- **TIMER\_A\_CAPTURECOMPARE\_INPUT\_LOW**

References `Timer_A_getOutputForOutputModeOutBitValue()`.

Referenced by `Timer_A_clear()`.

## Timer\_A\_initCaptureMode()

```
void Timer_A_initCaptureMode (
    uint16_t baseAddress,
    Timer_A_initCaptureModeParam * param )
```

Initializes Capture Mode.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>param</i>	is the pointer to struct for capture mode initialization.

Modified bits of **TAxCTLn** register.

## Returns

None

References `Timer_A_initCaptureModeParam::captureInputSelect`,  
`Timer_A_initCaptureModeParam::captureInterruptEnable`,  
`Timer_A_initCaptureModeParam::captureMode`,

Timer\_A\_initCaptureModeParam::captureOutputMode,  
 Timer\_A\_initCaptureModeParam::captureRegister, and  
 Timer\_A\_initCaptureModeParam::synchronizeCaptureSource.

## Timer\_A\_initCompareMode()

```
void Timer_A_initCompareMode (
    uint16_t baseAddress,
    Timer_A_initCompareModeParam * param )
```

Initializes Compare Mode.

### Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>param</i>	is the pointer to struct for compare mode initialization.

Modified bits of **TAxCCRn** register and bits of **TAxCTLn** register.

### Returns

None

References Timer\_A\_initCompareModeParam::compareInterruptEnable,  
 Timer\_A\_initCompareModeParam::compareOutputMode,  
 Timer\_A\_initCompareModeParam::compareRegister, and  
 Timer\_A\_initCompareModeParam::compareValue.

## Timer\_A\_initContinuousMode()

```
void Timer_A_initContinuousMode (
    uint16_t baseAddress,
    Timer_A_initContinuousModeParam * param )
```

Configures Timer\_A in continuous mode.

### Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>param</i>	is the pointer to struct for continuous mode initialization.

Modified bits of **TAxCTL** register.

### Returns

None

References Timer\_A\_initContinuousModeParam::clockSource,  
 Timer\_A\_initContinuousModeParam::clockSourceDivider,  
 Timer\_A\_initContinuousModeParam::startTimer, Timer\_A\_initContinuousModeParam::timerClear,  
 and Timer\_A\_initContinuousModeParam::timerInterruptEnable\_TAIE.



## Timer\_A\_initUpDownMode()

```
void Timer_A_initUpDownMode (
    uint16_t baseAddress,
    Timer_A_initUpDownModeParam * param )
```

Configures Timer\_A in up down mode.

### Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>param</i>	is the pointer to struct for up-down mode initialization.

Modified bits of **TAxCTL** register, bits of **TAxCTL0** register and bits of **TAxCCR0** register.

### Returns

None

References `Timer_A_initUpDownModeParam::captureCompareInterruptEnable_CCR0_CCIE`, `Timer_A_initUpDownModeParam::clockSource`, `Timer_A_initUpDownModeParam::clockSourceDivider`, `Timer_A_initUpDownModeParam::startTimer`, `Timer_A_initUpDownModeParam::timerClear`, `Timer_A_initUpDownModeParam::timerInterruptEnable_TAIE`, and `Timer_A_initUpDownModeParam::timerPeriod`.

## Timer\_A\_initUpMode()

```
void Timer_A_initUpMode (
    uint16_t baseAddress,
    Timer_A_initUpModeParam * param )
```

Configures Timer\_A in up mode.

### Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>param</i>	is the pointer to struct for up mode initialization.

Modified bits of **TAxCTL** register, bits of **TAxCTL0** register and bits of **TAxCCR0** register.

### Returns

None

References `Timer_A_initUpModeParam::captureCompareInterruptEnable_CCR0_CCIE`, `Timer_A_initUpModeParam::clockSource`, `Timer_A_initUpModeParam::clockSourceDivider`, `Timer_A_initUpModeParam::startTimer`, `Timer_A_initUpModeParam::timerClear`, `Timer_A_initUpModeParam::timerInterruptEnable_TAIE`, and `Timer_A_initUpModeParam::timerPeriod`.

## Timer\_A\_outputPWM()

```
void Timer_A_outputPWM (
    uint16_t baseAddress,
    Timer_A_outputPWMPParam * param )
```

Generate a PWM with timer running in up mode.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>param</i>	is the pointer to struct for PWM configuration.

Modified bits of **TAxCTL** register, bits of **TAxCCTL0** register, bits of **TAxCCR0** register and bits of **TAxCCTLn** register.

## Returns

None

References Timer\_A\_outputPWMPParam::clockSource, Timer\_A\_outputPWMPParam::clockSourceDivider, Timer\_A\_outputPWMPParam::compareOutputMode, Timer\_A\_outputPWMPParam::compareRegister, Timer\_A\_outputPWMPParam::dutyCycle, and Timer\_A\_outputPWMPParam::timerPeriod.

## Timer\_A\_setCompareValue()

```
void Timer_A_setCompareValue (
    uint16_t baseAddress,
    uint16_t compareRegister,
    uint16_t compareValue )
```

Sets the value of the capture-compare register.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>compareRegister</i>	selects the Capture register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>compareValue</i>	is the count to be compared with in compare mode

Modified bits of **TAxCCRn** register.

**Returns**

None

### Timer\_A\_setOutputForOutputModeOutBitValue()

```
void Timer_A_setOutputForOutputModeOutBitValue (
    uint16_t baseAddress,
    uint16_t captureCompareRegister,
    uint8_t outputModeOutBitValue )
```

Set output bit for output mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>captureCompareRegister</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>outputModeOutBitValue</i>	is the value to be set for out bit Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_OUTPUTMODE_OUTBITVALUE_HIGH</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_OUTBITVALUE_LOW</b></li> </ul>

Modified bits of **TAxCCTLn** register.

**Returns**

None

Referenced by `Timer_A_getCaptureCompareCount()`.

### Timer\_A\_setOutputMode()

```
void Timer_A_setOutputMode (
    uint16_t baseAddress,
    uint16_t compareRegister,
    uint16_t compareOutputMode )
```

Sets the output mode.

Sets the output mode for the timer even the timer is already running.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>compareRegister</i>	selects the compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>compareOutputMode</i>	specifies the output mode. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_OUTPUTMODE_OUTBITVALUE</b> [Default]</li> <li>■ <b>TIMER_A_OUTPUTMODE_SET</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_TOGGLE_RESET</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_SET_RESET</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_TOGGLE</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_RESET</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_TOGGLE_SET</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_RESET_SET</b></li> </ul>

Modified bits are **OUTMOD** of **TAxCTLn** register.

## Returns

None

## Timer\_A\_startCounter()

```
void Timer_A_startCounter (
    uint16_t baseAddress,
    uint16_t timerMode )
```

Starts Timer\_A counter.

This function assumes that the timer has been previously configured using `Timer_A_initContinuousMode`, `Timer_A_initUpMode` or `Timer_A_initUpDownMode`.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

## Parameters

<i>timerMode</i>	mode to put the timer in Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_STOP_MODE</b></li> <li>■ <b>TIMER_A_UP_MODE</b></li> <li>■ <b>TIMER_A_CONTINUOUS_MODE</b> [Default]</li> <li>■ <b>TIMER_A_UPDOWN_MODE</b></li> </ul>
------------------	---

Modified bits of **TAxCTL** register.

## Returns

None

## Timer\_A\_stop()

```
void Timer_A_stop (
    uint16_t baseAddress )
```

Stops the timer.

## Parameters

<i>baseAddress</i>	is the base address of the <b>TIMER_A</b> module.
--------------------	---

Modified bits of **TAxCTL** register.

## Returns

None

## 37.3 Programming Example

The following example shows some **TIMER\_A** operations using the APIs

```
{ //Start TIMER_A
  Timer_A_initContinuousModeParam initContParam = {0};
  initContParam.clockSource = TIMER_A_CLOCKSOURCE_SMCLK;
  initContParam.clockSourceDivider = TIMER_A_CLOCKSOURCE_DIVIDER_1;
  initContParam.timerInterruptEnable_TAIE = TIMER_A_TAIE_INTERRUPT_DISABLE;
  initContParam.timerClear = TIMER_A_DO_CLEAR;
  initContParam.startTimer = false;
  Timer_A_initContinuousMode(TIMER_A1_BASE, &initContParam);

  //Initiaze compare mode
  Timer_A_clearCaptureCompareInterrupt (TIMER_A1_BASE,
    TIMER_A_CAPTURECOMPARE_REGISTER_0
  );

  Timer_A_initCompareModeParam initCompParam = {0};
  initCompParam.compareRegister = TIMER_A_CAPTURECOMPARE_REGISTER_0;
  initCompParam.compareInterruptEnable = TIMER_A_CAPTURECOMPARE_INTERRUPT_ENABLE;
```

```
initCompParam.compareOutputMode = TIMER_A_OUTPUTMODE_OUTBITVALUE;
initCompParam.compareValue = COMPARE_VALUE;
Timer_A_initCompareMode(TIMER_A1_BASE, &initCompParam);

Timer_A_startCounter( TIMER_A1_BASE,
                      TIMER_A_CONTINUOUS_MODE
                      );

//Enter LPM0
__bis_SR_register(LPM0_bits);

//For debugger
__no_operation();
}
```

## 38 16-Bit Timer\_B (TIMER\_B)

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### 38.1 Introduction

TIMER\_B is a 16-bit timer/counter with multiple capture/compare registers. TIMER\_B can support multiple capture/compares, PWM outputs, and interval timing. TIMER\_B also has extensive interrupt capabilities. Interrupts may be generated from the counter on overflow conditions and from each of the capture/compare registers.

This peripheral API handles Timer B hardware peripheral.

TIMER\_B features include:

- Asynchronous 16-bit timer/counter with four operating modes
- Selectable and configurable clock source
- Up to seven configurable capture/compare registers
- Configurable outputs with pulse width modulation (PWM) capability
- Asynchronous input and output latching
- Interrupt vector register for fast decoding of all Timer\_B interrupts

Differences From Timer\_A Timer\_B is identical to Timer\_A with the following exceptions:

- The length of Timer\_B is programmable to be 8, 10, 12, or 16 bits
- Timer\_B TBxCCRn registers are double-buffered and can be grouped
- All Timer\_B outputs can be put into a high-impedance state
- The SCCI bit function is not implemented in Timer\_B

TIMER\_B can operate in 3 modes

- Continuous Mode
- Up Mode
- Down Mode

TIMER\_B Interrupts may be generated on counter overflow conditions and during capture compare events.

The TIMER\_B may also be used to generate PWM outputs. PWM outputs can be generated by initializing the compare mode with `TIMER_B_initCompare()` and the necessary parameters. The PWM may be customized by selecting a desired timer mode (continuous/up/upDown), duty cycle, output mode, timer period etc. The library also provides a simpler way to generate PWM using `TIMER_B_generatePWM()` API. However the level of customization and the kinds of PWM generated are limited in this API. Depending on how complex the PWM is and what level of customization is required, the user can use `TIMER_B_generatePWM()` or a combination of `Timer_initCompare()` and timer start APIs

The TIMER\_B API provides a set of functions for dealing with the TIMER\_B module. Functions are provided to configure and control the timer, along with functions to modify timer/counter values, and to manage interrupt handling for the timer.

Control is also provided over interrupt sources and events. Interrupts can be generated to indicate that an event has been captured.

## 38.2 API Functions

### Functions

- void `Timer_B_startCounter` (uint16\_t baseAddress, uint16\_t timerMode)  
*Starts Timer\_B counter.*
- void `Timer_B_initContinuousMode` (uint16\_t baseAddress, `Timer_B_initContinuousModeParam` \*param)  
*Configures Timer\_B in continuous mode.*
- void `Timer_B_initUpMode` (uint16\_t baseAddress, `Timer_B_initUpModeParam` \*param)  
*Configures Timer\_B in up mode.*
- void `Timer_B_initUpDownMode` (uint16\_t baseAddress, `Timer_B_initUpDownModeParam` \*param)  
*Configures Timer\_B in up down mode.*
- void `Timer_B_initCaptureMode` (uint16\_t baseAddress, `Timer_B_initCaptureModeParam` \*param)  
*Initializes Capture Mode.*
- void `Timer_B_initCompareMode` (uint16\_t baseAddress, `Timer_B_initCompareModeParam` \*param)  
*Initializes Compare Mode.*
- void `Timer_B_enableInterrupt` (uint16\_t baseAddress)  
*Enable Timer\_B interrupt.*
- void `Timer_B_disableInterrupt` (uint16\_t baseAddress)  
*Disable Timer\_B interrupt.*
- uint32\_t `Timer_B_getInterruptStatus` (uint16\_t baseAddress)  
*Get Timer\_B interrupt status.*
- void `Timer_B_enableCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Enable capture compare interrupt.*
- void `Timer_B_disableCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Disable capture compare interrupt.*
- uint32\_t `Timer_B_getCaptureCompareInterruptStatus` (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint16\_t mask)  
*Return capture compare interrupt status.*
- void `Timer_B_clear` (uint16\_t baseAddress)  
*Reset/Clear the Timer\_B clock divider, count direction, count.*
- uint8\_t `Timer_B_getSynchronizedCaptureCompareInput` (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint16\_t synchronized)  
*Get synchronized capturecompare input.*
- uint8\_t `Timer_B_getOutputForOutputModeOutBitValue` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Get output bit for output mode.*



- `uint16_t Timer_B.getCaptureCompareCount` (`uint16_t baseAddress`, `uint16_t captureCompareRegister`)  
*Get current capturecompare count.*
- `void Timer_B.setOutputForOutputModeOutBitValue` (`uint16_t baseAddress`, `uint16_t captureCompareRegister`, `uint8_t outputModeOutBitValue`)  
*Set output bit for output mode.*
- `void Timer_B.outputPWM` (`uint16_t baseAddress`, `Timer_B_outputPWMPParam *param`)  
*Generate a PWM with Timer\_B running in up mode.*
- `void Timer_B.stop` (`uint16_t baseAddress`)  
*Stops the Timer\_B.*
- `void Timer_B.setCompareValue` (`uint16_t baseAddress`, `uint16_t compareRegister`, `uint16_t compareValue`)  
*Sets the value of the capture-compare register.*
- `void Timer_B.clearTimerInterrupt` (`uint16_t baseAddress`)  
*Clears the Timer\_B TBIFG interrupt flag.*
- `void Timer_B.clearCaptureCompareInterrupt` (`uint16_t baseAddress`, `uint16_t captureCompareRegister`)  
*Clears the capture-compare interrupt flag.*
- `void Timer_B.selectCounterLength` (`uint16_t baseAddress`, `uint16_t counterLength`)  
*Selects Timer\_B counter length.*
- `void Timer_B.selectLatchingGroup` (`uint16_t baseAddress`, `uint16_t groupLatch`)  
*Selects Timer\_B Latching Group.*
- `void Timer_B.initCompareLatchLoadEvent` (`uint16_t baseAddress`, `uint16_t compareRegister`, `uint16_t compareLatchLoadEvent`)  
*Selects Compare Latch Load Event.*
- `uint16_t Timer_B.getCounterValue` (`uint16_t baseAddress`)  
*Reads the current timer count value.*
- `void Timer_B.setOutputMode` (`uint16_t baseAddress`, `uint16_t compareRegister`, `uint16_t compareOutputMode`)  
*Sets the output mode.*

## 38.2.1 Detailed Description

The TIMER\_B API is broken into three groups of functions: those that deal with timer configuration and control, those that deal with timer contents, and those that deal with interrupt handling.

TIMER\_B configuration and initialization is handled by

- `Timer_B.startCounter()`
- `Timer_B.initUpMode()`
- `Timer_B.initUpDownMode()`
- `Timer_B.initContinuousMode()`
- `Timer_B.initCapture()`
- `Timer_B.initCompare()`
- `Timer_B.clear()`
- `Timer_B.stop()`
- `Timer_B.initCompareLatchLoadEvent()`
- `Timer_B.selectLatchingGroup()`
- `Timer_B.selectCounterLength()`

TIMER\_B outputs are handled by

- `Timer_B_getSynchronizedCaptureCompareInput()`
- `Timer_B_getOutputForOutputModeOutBitValue()`
- `Timer_B_setOutputForOutputModeOutBitValue()`
- `Timer_B_generatePWM()`
- `Timer_B_getCaptureCompareCount()`
- `Timer_B_setCompareValue()`
- `Timer_B_getCounterValue()`

The interrupt handler for the TIMER\_B interrupt is managed with

- `Timer_B_enableInterrupt()`
- `Timer_B_disableInterrupt()`
- `Timer_B_getInterruptStatus()`
- `Timer_B_enableCaptureCompareInterrupt()`
- `Timer_B_disableCaptureCompareInterrupt()`
- `Timer_B_getCaptureCompareInterruptStatus()`
- `Timer_B_clearCaptureCompareInterrupt()`
- `Timer_B_clearTimerInterrupt()`

## 38.2.2 Function Documentation

### Timer\_B\_clear()

```
void Timer_B_clear (
    uint16_t baseAddress )
```

Reset/Clear the Timer\_B clock divider, count direction, count.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

Modified bits of **TBxCTL** register.

#### Returns

None

References `Timer_B_getSynchronizedCaptureCompareInput()`.

### Timer\_B\_clearCaptureCompareInterrupt()

```
void Timer_B_clearCaptureCompareInterrupt (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Clears the capture-compare interrupt flag.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>captureCompareRegister</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

Modified bits are **CCIFG** of **TBxCCTLn** register.

#### Returns

None

### Timer\_B\_clearTimerInterrupt()

```
void Timer_B_clearTimerInterrupt (
    uint16_t baseAddress )
```

Clears the Timer\_B TBIFG interrupt flag.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

Modified bits are **TBIFG** of **TBxCTL** register.

#### Returns

None

### Timer\_B\_disableCaptureCompareInterrupt()

```
void Timer_B_disableCaptureCompareInterrupt (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Disable capture compare interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>captureCompareRegister</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

Modified bits of **TBxCCTLn** register.

**Returns**

None

**Timer\_B\_disableInterrupt()**

```
void Timer_B_disableInterrupt (
    uint16_t baseAddress )
```

Disable Timer\_B interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

Modified bits of **TBxCTL** register.

**Returns**

None

**Timer\_B\_enableCaptureCompareInterrupt()**

```
void Timer_B_enableCaptureCompareInterrupt (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Enable capture compare interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

**Parameters**

<i>captureCompareRegister</i>	<p>selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
-------------------------------	---

Modified bits of **TBxCCTLn** register.

**Returns**

None

**Timer\_B\_enableInterrupt()**

```
void Timer_B_enableInterrupt (
    uint16_t baseAddress )
```

Enable Timer\_B interrupt.

Enables Timer\_B interrupt. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

Modified bits of **TBxCTL** register.

**Returns**

None

**Timer\_B\_getCaptureCompareCount()**

```
uint16_t Timer_B_getCaptureCompareCount (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Get current capturecompare count.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>captureCompareRegister</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

**Returns**

Current count as uint16\_t

References Timer\_B.setOutputForOutputModeOutBitValue().

Referenced by Timer\_B.getOutputForOutputModeOutBitValue().

**Timer\_B.getCaptureCompareInterruptStatus()**

```
uint32_t Timer_B.getCaptureCompareInterruptStatus (
    uint16_t baseAddress,
    uint16_t captureCompareRegister,
    uint16_t mask )
```

Return capture compare interrupt status.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>captureCompareRegister</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

**Parameters**

<i>mask</i>	is the mask for the interrupt status Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURE_OVERFLOW</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_INTERRUPT_FLAG</b></li> </ul>
-------------	--

**Returns**

Logical OR of any of the following:

- **TIMER\_B\_CAPTURE\_OVERFLOW**
  - **TIMER\_B\_CAPTURECOMPARE\_INTERRUPT\_FLAG**
- indicating the status of the masked interrupts

**Timer\_B\_getCounterValue()**

```
uint16_t Timer_B_getCounterValue (
    uint16_t baseAddress )
```

Reads the current timer count value.

Reads the current count value of the timer. There is a majority vote system in place to confirm an accurate value is returned. The `TIMER_B_THRESHOLD` #define in the associated header file can be modified so that the votes must be closer together for a consensus to occur.

**Parameters**

<i>baseAddress</i>	is the base address of the Timer module.
--------------------	--

**Returns**

Majority vote of timer count value

**Timer\_B\_getInterruptStatus()**

```
uint32_t Timer_B_getInterruptStatus (
    uint16_t baseAddress )
```

Get Timer\_B interrupt status.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

**Returns**

One of the following:

- **TIMER\_B\_INTERRUPT\_NOT\_PENDING**
- **TIMER\_B\_INTERRUPT\_PENDING**  
indicating the status of the Timer\_B interrupt

**Timer\_B\_getOutputForOutputModeOutBitValue()**

```
uint8_t Timer_B_getOutputForOutputModeOutBitValue (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Get output bit for output mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>captureCompareRegister</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

**Returns**

One of the following:

- **TIMER\_B\_OUTPUTMODE\_OUTBITVALUE\_HIGH**
- **TIMER\_B\_OUTPUTMODE\_OUTBITVALUE\_LOW**

References Timer\_B\_getCaptureCompareCount().

Referenced by Timer\_B\_getSynchronizedCaptureCompareInput().

**Timer\_B\_getSynchronizedCaptureCompareInput()**

```
uint8_t Timer_B_getSynchronizedCaptureCompareInput (
    uint16_t baseAddress,
    uint16_t captureCompareRegister,
    uint16_t synchronized )
```

Get synchronized capturecompare input.



## Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>captureCompareRegister</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>synchronized</i>	selects the type of capture compare input Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_READ_SYNCHRONIZED_CAPTURECOMPAREINPUT</b></li> <li>■ <b>TIMER_B_READ_CAPTURE_COMPARE_INPUT</b></li> </ul>

## Returns

One of the following:

- **TIMER\_B\_CAPTURECOMPARE\_INPUT\_HIGH**
- **TIMER\_B\_CAPTURECOMPARE\_INPUT\_LOW**

References `Timer_B_getOutputForOutputModeOutBitValue()`.

Referenced by `Timer_B_clear()`.

## Timer\_B\_initCaptureMode()

```
void Timer_B_initCaptureMode (
    uint16_t baseAddress,
    Timer_B_initCaptureModeParam * param )
```

Initializes Capture Mode.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>param</i>	is the pointer to struct for capture mode initialization.

Modified bits of **TBxCCTLn** register.

**Returns**

None

References `Timer_B_initCaptureModeParam::captureInputSelect`, `Timer_B_initCaptureModeParam::captureInterruptEnable`, `Timer_B_initCaptureModeParam::captureMode`, `Timer_B_initCaptureModeParam::captureOutputMode`, `Timer_B_initCaptureModeParam::captureRegister`, and `Timer_B_initCaptureModeParam::synchronizeCaptureSource`.

**Timer\_B\_initCompareLatchLoadEvent()**

```
void Timer_B_initCompareLatchLoadEvent (
    uint16_t baseAddress,
    uint16_t compareRegister,
    uint16_t compareLatchLoadEvent )
```

Selects Compare Latch Load Event.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>compareRegister</i>	<p>selects the compare register being used. Refer to datasheet to ensure the device has the compare register being used. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>compareLatchLoadEvent</i>	<p>selects the latch load event Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>TIMER_B_LATCH_ON_WRITE_TO_TBxCCRn_COMPARE_REGISTER</b> [Default]</li> <li>■ <b>TIMER_B_LATCH_WHEN_COUNTER_COUNTS_TO_0_IN_UP_OR_CONT_MODE</b></li> <li>■ <b>TIMER_B_LATCH_WHEN_COUNTER_COUNTS_TO_0_IN_UPDOWN_MODE</b></li> <li>■ <b>TIMER_B_LATCH_WHEN_COUNTER_COUNTS_TO_CURRENT_COMPARE_LATCH_VALUE</b></li> </ul>

Modified bits are **CLLD** of **TBxCCTLn** register.

**Returns**

None

**Timer\_B\_initCompareMode()**

```
void Timer_B_initCompareMode (
    uint16_t baseAddress,
    Timer_B_initCompareModeParam * param )
```

Initializes Compare Mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>param</i>	is the pointer to struct for compare mode initialization.

Modified bits of **TBxCCTLn** register and bits of **TBxCCRn** register.**Returns**

None

References `Timer_B_initCompareModeParam::compareInterruptEnable`, `Timer_B_initCompareModeParam::compareOutputMode`, `Timer_B_initCompareModeParam::compareRegister`, and `Timer_B_initCompareModeParam::compareValue`.**Timer\_B\_initContinuousMode()**

```
void Timer_B_initContinuousMode (
    uint16_t baseAddress,
    Timer_B_initContinuousModeParam * param )
```

Configures Timer\_B in continuous mode.

This API does not start the timer. Timer needs to be started when required using the `Timer_B_startCounter` API.**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>param</i>	is the pointer to struct for continuous mode initialization.

Modified bits of **TBxCTL** register.**Returns**

None

References `Timer_B_initContinuousModeParam::clockSource`, `Timer_B_initContinuousModeParam::clockSourceDivider`,

Timer\_B\_initContinuousModeParam::startTimer, Timer\_B\_initContinuousModeParam::timerClear, and Timer\_B\_initContinuousModeParam::timerInterruptEnable\_TBIE.

## Timer\_B\_initUpDownMode()

```
void Timer_B_initUpDownMode (
    uint16_t baseAddress,
    Timer_B_initUpDownModeParam * param )
```

Configures Timer\_B in up down mode.

This API does not start the timer. Timer needs to be started when required using the Timer\_B\_startCounter API.

### Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>param</i>	is the pointer to struct for up-down mode initialization.

Modified bits of **TBxCTL** register, bits of **TBxCCTL0** register and bits of **TBxCCR0** register.

### Returns

None

References Timer\_B\_initUpDownModeParam::captureCompareInterruptEnable\_CCR0\_CCIE, Timer\_B\_initUpDownModeParam::clockSource, Timer\_B\_initUpDownModeParam::clockSourceDivider, Timer\_B\_initUpDownModeParam::startTimer, Timer\_B\_initUpDownModeParam::timerClear, Timer\_B\_initUpDownModeParam::timerInterruptEnable\_TBIE, and Timer\_B\_initUpDownModeParam::timerPeriod.

## Timer\_B\_initUpMode()

```
void Timer_B_initUpMode (
    uint16_t baseAddress,
    Timer_B_initUpModeParam * param )
```

Configures Timer\_B in up mode.

This API does not start the timer. Timer needs to be started when required using the Timer\_B\_startCounter API.

### Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>param</i>	is the pointer to struct for up mode initialization.

Modified bits of **TBxCTL** register, bits of **TBxCCTL0** register and bits of **TBxCCR0** register.

**Returns**

None

References `Timer_B_initUpModeParam::captureCompareInterruptEnable_CCR0_CCIE`, `Timer_B_initUpModeParam::clockSource`, `Timer_B_initUpModeParam::clockSourceDivider`, `Timer_B_initUpModeParam::startTimer`, `Timer_B_initUpModeParam::timerClear`, `Timer_B_initUpModeParam::timerInterruptEnable_TBIE`, and `Timer_B_initUpModeParam::timerPeriod`.

**Timer\_B\_outputPWM()**

```
void Timer_B_outputPWM (
    uint16_t baseAddress,
    Timer_B_outputPWMPParam * param )
```

Generate a PWM with `Timer_B` running in up mode.

**Parameters**

<i>baseAddress</i>	is the base address of the <code>TIMER_B</code> module.
<i>param</i>	is the pointer to struct for PWM configuration.

Modified bits of **TBxCCTLn** register, bits of **TBxCTL** register, bits of **TBxCCTL0** register and bits of **TBxCCR0** register.

**Returns**

None

References `Timer_B_outputPWMPParam::clockSource`, `Timer_B_outputPWMPParam::clockSourceDivider`, `Timer_B_outputPWMPParam::compareOutputMode`, `Timer_B_outputPWMPParam::compareRegister`, `Timer_B_outputPWMPParam::dutyCycle`, and `Timer_B_outputPWMPParam::timerPeriod`.

**Timer\_B\_selectCounterLength()**

```
void Timer_B_selectCounterLength (
    uint16_t baseAddress,
    uint16_t counterLength )
```

Selects `Timer_B` counter length.

**Parameters**

<i>baseAddress</i>	is the base address of the <code>TIMER_B</code> module.
--------------------	---

**Parameters**

<i>counterLength</i>	selects the value of counter length. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_COUNTER_16BIT</b> [Default]</li> <li>■ <b>TIMER_B_COUNTER_12BIT</b></li> <li>■ <b>TIMER_B_COUNTER_10BIT</b></li> <li>■ <b>TIMER_B_COUNTER_8BIT</b></li> </ul>
----------------------	--

Modified bits are **CNTL** of **TBxCTL** register.

**Returns**

None

**Timer\_B\_selectLatchingGroup()**

```
void Timer_B_selectLatchingGroup (
    uint16_t baseAddress,
    uint16_t groupLatch )
```

Selects Timer\_B Latching Group.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>groupLatch</i>	selects the latching group. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_GROUP_NONE</b> [Default]</li> <li>■ <b>TIMER_B_GROUP_CL12_CL23_CL56</b></li> <li>■ <b>TIMER_B_GROUP_CL123_CL456</b></li> <li>■ <b>TIMER_B_GROUP_ALL</b></li> </ul>

Modified bits are **TBCLGRP** of **TBxCTL** register.

**Returns**

None

**Timer\_B\_setCompareValue()**

```
void Timer_B_setCompareValue (
    uint16_t baseAddress,
    uint16_t compareRegister,
    uint16_t compareValue )
```

Sets the value of the capture-compare register.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>compareRegister</i>	selects the compare register being used. Refer to datasheet to ensure the device has the compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>compareValue</i>	is the count to be compared with in compare mode

Modified bits of **TBxCCRn** register.

## Returns

None

## Timer\_B\_setOutputForOutputModeOutBitValue()

```
void Timer_B_setOutputForOutputModeOutBitValue (
    uint16_t baseAddress,
    uint16_t captureCompareRegister,
    uint8_t outputModeOutBitValue )
```

Set output bit for output mode.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>captureCompareRegister</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

**Parameters**

<i>outputModeOutBitValue</i>	the value to be set for out bit Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_OUTPUTMODE_OUTBITVALUE_HIGH</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_OUTBITVALUE_LOW</b></li> </ul>
------------------------------	---

Modified bits of **TBxCCTLn** register.

**Returns**

None

Referenced by `Timer_B_getCaptureCompareCount()`.

**Timer\_B\_setOutputMode()**

```
void Timer_B_setOutputMode (
    uint16_t baseAddress,
    uint16_t compareRegister,
    uint16_t compareOutputMode )
```

Sets the output mode.

Sets the output mode for the timer even the timer is already running.

**Parameters**

<i>baseAddress</i>	is the base address of the <b>TIMER_B</b> module.
<i>compareRegister</i>	selects the compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>



## Parameters

<i>compareOutputMode</i>	specifies the output mode. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_OUTPUTMODE_OUTBITVALUE</b> [Default]</li> <li>■ <b>TIMER_B_OUTPUTMODE_SET</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_TOGGLE_RESET</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_SET_RESET</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_TOGGLE</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_RESET</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_TOGGLE_SET</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_RESET_SET</b></li> </ul>
--------------------------	--

Modified bits are **OUTMOD** of **TBxCCTLn** register.

## Returns

None

## Timer\_B\_startCounter()

```
void Timer_B_startCounter (
    uint16_t baseAddress,
    uint16_t timerMode )
```

Starts Timer\_B counter.

This function assumes that the timer has been previously configured using `Timer_B_initContinuousMode`, `Timer_B_initUpMode` or `Timer_B_initUpDownMode`.

## Parameters

<i>baseAddress</i>	is the base address of the <b>TIMER_B</b> module.
<i>timerMode</i>	selects the mode of the timer Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_STOP_MODE</b></li> <li>■ <b>TIMER_B_UP_MODE</b></li> <li>■ <b>TIMER_B_CONTINUOUS_MODE</b> [Default]</li> <li>■ <b>TIMER_B_UPDOWN_MODE</b></li> </ul>

Modified bits of **TBxCTL** register.

**Returns**

None

**Timer\_B\_stop()**

```
void Timer_B_stop (
    uint16_t baseAddress )
```

Stops the Timer\_B.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

Modified bits of **TBxCTL** register.**Returns**

None

## 38.3 Programming Example

The following example shows some TIMER\_B operations using the APIs

```
{ //Start timer in continuous mode sourced by SMCLK
  Timer_B_initContinuousModeParam initContParam = {0};
  initContParam.clockSource = TIMER_B_CLOCKSOURCE_SMCLK;
  initContParam.clockSourceDivider = TIMER_B_CLOCKSOURCE_DIVIDER_1;
  initContParam.timerInterruptEnable_TBIE = TIMER_B_TBIE_INTERRUPT_DISABLE;
  initContParam.timerClear = TIMER_B_DO_CLEAR;
  initContParam.startTimer = false;
  Timer_B_initContinuousMode(TIMER_B0_BASE, &initContParam);

  //Initiaze compare mode
  Timer_B_clearCaptureCompareInterrupt(TIMER_B0_BASE,
    TIMER_B_CAPTURECOMPARE_REGISTER_0);

  Timer_B_initCompareModeParam initCompParam = {0};
  initCompParam.compareRegister = TIMER_B_CAPTURECOMPARE_REGISTER_0;
  initCompParam.compareInterruptEnable = TIMER_B_CAPTURECOMPARE_INTERRUPT_ENABLE;
  initCompParam.compareOutputMode = TIMER_B_OUTPUTMODE_OUTBITVALUE;
  initCompParam.compareValue = COMPARE_VALUE;
  Timer_B_initCompareMode(TIMER_B0_BASE, &initCompParam);

  Timer_B_startCounter( TIMER_B0_BASE,
    TIMER_B_CONTINUOUS_MODE
  );
}
```

## 39 TIMER\_D

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### 39.1 Introduction

Timer\_D is a 16-bit timer/counter with multiple capture/compare registers. Timer\_D can support multiple capture/compares, interval timing, and PWM outputs both in general and high resolution modes. Timer\_D also has extensive interrupt capabilities. Interrupts may be generated from the counter on overflow conditions, from each of the capture/compare registers.

This peripheral API handles Timer D hardware peripheral.

TIMER\_D features include:

- Asynchronous 16-bit timer/counter with four operating modes and four selectable lengths
- Selectable and configurable clock source
- Configurable capture/compare registers
- Controlling rising and falling PWM edges by combining two neighbor TDCCR registers in one compare channel output
- Configurable outputs with PWM capability
- High-resolution mode with a fine clock frequency up to 16 times the timer input clock frequency
- Double-buffered compare registers with synchronized loading
- Interrupt vector register for fast decoding of all Timer\_D interrupts

Differences From Timer\_B Timer\_D is identical to Timer\_B with the following exceptions:

- Timer\_D supports high-resolution mode.
- Timer\_D supports the combination of two adjacent TDCCR<sub>x</sub> registers in one capture/compare channel.
- Timer\_D supports the dual capture event mode.
- Timer\_D supports external fault input, external clear input, and signal. See the TEC chapter for detailed information.
- Timer\_D can synchronize with a second timer instance when available. See the TEC chapter for detailed information.

Timer\_D can operate in 3 modes

- Continuous Mode
- Up Mode
- Down Mode

Timer\_D Interrupts may be generated on counter overflow conditions and during capture compare events.

The Timer\_D may also be used to generate PWM outputs. PWM outputs can be generated by initializing the compare mode with `Timer_D_initCompare()` and the necessary parameters. The PWM may be customized by selecting a desired timer mode (continuous/up/upDown), duty cycle, output mode, timer period etc. The library also provides a simpler way to generate PWM using `Timer_D_generatePWM()` API. However the level of customization and the kinds of PWM generated are limited in this API. Depending on how complex the PWM is and what level of customization is required, the user can use `Timer_D_generatePWM()` or a combination of `Timer_D_initCompare()` and timer start APIs

The TimerD API provides a set of functions for dealing with the TimerD module. Functions are provided to configure and control the timer, along with functions to modify timer/counter values, and to manage interrupt handling for the timer.

Control is also provided over interrupt sources and events. Interrupts can be generated to indicate that an event has been captured.

## 39.2 API Functions

### Functions

- void `Timer_D_startCounter` (uint16\_t baseAddress, uint16\_t timerMode)  
*Starts Timer\_D counter.*
- void `Timer_D_initContinuousMode` (uint16\_t baseAddress, `Timer_D_initContinuousModeParam` \*param)  
*Configures timer in continuous mode.*
- void `Timer_D_initUpMode` (uint16\_t baseAddress, `Timer_D_initUpModeParam` \*param)  
*Configures timer in up mode.*
- void `Timer_D_initUpDownMode` (uint16\_t baseAddress, `Timer_D_initUpDownModeParam` \*param)  
*Configures timer in up down mode.*
- void `Timer_D_initCaptureMode` (uint16\_t baseAddress, `Timer_D_initCaptureModeParam` \*param)  
*Initializes Capture Mode.*
- void `Timer_D_initCompareMode` (uint16\_t baseAddress, `Timer_D_initCompareModeParam` \*param)  
*Initializes Compare Mode.*
- void `Timer_D_enableTimerInterrupt` (uint16\_t baseAddress)  
*Enable timer interrupt.*
- void `Timer_D_enableHighResInterrupt` (uint16\_t baseAddress, uint16\_t mask)  
*Enable High Resolution interrupt.*
- void `Timer_D_disableTimerInterrupt` (uint16\_t baseAddress)  
*Disable timer interrupt.*
- void `Timer_D_disableHighResInterrupt` (uint16\_t baseAddress, uint16\_t mask)  
*Disable High Resolution interrupt.*
- uint32\_t `Timer_D_getTimerInterruptStatus` (uint16\_t baseAddress)  
*Get timer interrupt status.*
- void `Timer_D_enableCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Enable capture compare interrupt.*
- void `Timer_D_disableCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)

- Disable capture compare interrupt.*

  - uint32\_t [Timer\\_D.getCaptureCompareInterruptStatus](#) (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint16\_t mask)

*Return capture compare interrupt status.*
- uint16\_t [Timer\\_D.getHighResInterruptStatus](#) (uint16\_t baseAddress, uint16\_t mask)

*Returns High Resolution interrupt status.*
- void [Timer\\_D.clear](#) (uint16\_t baseAddress)

*Reset/Clear the timer clock divider, count direction, count.*
- void [Timer\\_D.clearHighResInterrupt](#) (uint16\_t baseAddress, uint16\_t mask)

*Clears High Resolution interrupt status.*
- uint8\_t [Timer\\_D.getSynchronizedCaptureCompareInput](#) (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint16\_t synchronized)

*Get synchronized capturecompare input.*
- uint8\_t [Timer\\_D.getOutputForOutputModeOutBitValue](#) (uint16\_t baseAddress, uint16\_t captureCompareRegister)

*Get output bit for output mode.*
- uint16\_t [Timer\\_D.getCaptureCompareCount](#) (uint16\_t baseAddress, uint16\_t captureCompareRegister)

*Get current capturecompare count.*
- uint16\_t [Timer\\_D.getCaptureCompareLatchCount](#) (uint16\_t baseAddress, uint16\_t captureCompareRegister)

*Get current capture compare latch register count.*
- uint8\_t [Timer\\_D.getCaptureCompareInputSignal](#) (uint16\_t baseAddress, uint16\_t captureCompareRegister)

*Get current capturecompare input signal.*
- void [Timer\\_D.setOutputForOutputModeOutBitValue](#) (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint8\_t outputModeOutBitValue)

*Set output bit for output mode.*
- void [Timer\\_D.outputPWM](#) (uint16\_t baseAddress, [Timer\\_D.outputPWMPParam](#) \*param)

*Generate a PWM with timer running in up mode.*
- void [Timer\\_D.stop](#) (uint16\_t baseAddress)

*Stops the timer.*
- void [Timer\\_D.setCompareValue](#) (uint16\_t baseAddress, uint16\_t compareRegister, uint16\_t compareValue)

*Sets the value of the capture-compare register.*
- void [Timer\\_D.clearTimerInterrupt](#) (uint16\_t baseAddress)

*Clears the Timer TDIFG interrupt flag.*
- void [Timer\\_D.clearCaptureCompareInterrupt](#) (uint16\_t baseAddress, uint16\_t captureCompareRegister)

*Clears the capture-compare interrupt flag.*
- uint8\_t [Timer\\_D.initHighResGeneratorInFreeRunningMode](#) (uint16\_t baseAddress, uint8\_t desiredHighResFrequency)

*Configures Timer\_D in free running mode.*
- void [Timer\\_D.initHighResGeneratorInRegulatedMode](#) (uint16\_t baseAddress, [Timer\\_D.initHighResGeneratorInRegulatedModeParam](#) \*param)

*Configures Timer\_D in Regulated mode.*
- void [Timer\\_D.combineTDCCRToOutputPWM](#) (uint16\_t baseAddress, [Timer\\_D.combineTDCCRToOutputPWMPParam](#) \*param)

*Combine TDCCR to get PWM.*
- void [Timer\\_D.selectLatchingGroup](#) (uint16\_t baseAddress, uint16\_t groupLatch)

*Selects Timer\_D Latching Group.*
- void [Timer\\_D.selectCounterLength](#) (uint16\_t baseAddress, uint16\_t counterLength)

*Selects Timer\_D counter length.*

- void `Timer_D_initCompareLatchLoadEvent` (uint16\_t baseAddress, uint16\_t compareRegister, uint16\_t compareLatchLoadEvent)  
*Selects Compare Latch Load Event.*
- void `Timer_D_disableHighResFastWakeup` (uint16\_t baseAddress)  
*Disable High Resolution fast wakeup.*
- void `Timer_D_enableHighResFastWakeup` (uint16\_t baseAddress)  
*Enable High Resolution fast wakeup.*
- void `Timer_D_disableHighResClockEnhancedAccuracy` (uint16\_t baseAddress)  
*Disable High Resolution Clock Enhanced Accuracy.*
- void `Timer_D_enableHighResClockEnhancedAccuracy` (uint16\_t baseAddress)  
*Enable High Resolution Clock Enhanced Accuracy.*
- void `Timer_D_disableHighResGeneratorForceON` (uint16\_t baseAddress)  
*Disable High Resolution Clock Enhanced Accuracy.*
- void `Timer_D_enableHighResGeneratorForceON` (uint16\_t baseAddress)  
*Enable High Resolution Clock Enhanced Accuracy.*
- void `Timer_D_selectHighResCoarseClockRange` (uint16\_t baseAddress, uint16\_t highResCoarseClockRange)  
*Select High Resolution Coarse Clock Range.*
- void `Timer_D_selectHighResClockRange` (uint16\_t baseAddress, uint16\_t highResClockRange)  
*Select High Resolution Clock Range Selection.*
- uint16\_t `Timer_D_getCounterValue` (uint16\_t baseAddress)  
*Reads the current timer count value.*
- void `Timer_D_setOutputMode` (uint16\_t baseAddress, uint16\_t compareRegister, uint16\_t compareOutputMode)  
*Sets the output mode.*

## 39.2.1 Detailed Description

The `Timer_D` API is broken into three groups of functions: those that deal with timer configuration and control, those that deal with timer contents, and those that deal with interrupt handling.

TimerD configuration and initialization is handled by

- `Timer_D.startCounter()`,
- `Timer_D.initContinuousMode()`,
- `Timer_D.initUpMode()`,
- `Timer_D.initUpDownMode()`,
- `Timer_D.initCaptureMode()`,
- `Timer_D.initCompareMode()`,
- `Timer_D.clear()`,
- `Timer_D.stop()`,
- `Timer_D.configureHighResGeneratorInFreeRunningMode()`,
- `Timer_D.configureHighResGeneratorInRegulatedMode()`,
- `Timer_D.combineTDCCRTToGeneratePWM()`,
- `Timer_D.selectLatchingGroup()`,
- `Timer_D.selectCounterLength()`,
- `Timer_D.initCompareLatchLoadEvent()`,

- `Timer_D.disableHighResFastWakeup()`,
- `Timer_D.enableHighResFastWakeup()`,
- `Timer_D.disableHighResClockEnhancedAccuracy()`,
- `Timer_D.enableHighResClockEnhancedAccuracy()`,
- `Timer_D.DisableHighResGeneratorForceON()`,
- `Timer_D.EnableHighResGeneratorForceON()`,
- `Timer_D.selectHighResCoarseClockRange()`,
- `Timer_D.selectHighResClockRange()`

TimerD outputs are handled by

- `Timer_D.getSynchronizedCaptureCompareInput()`,
- `Timer_D.getOutputForOutputModeOutBitValue()`,
- `Timer_D.setOutputForOutputModeOutBitValue()`,
- `Timer_D.outputPWM()`,
- `Timer_D.getCaptureCompareCount()`,
- `Timer_D.setCompareValue()`,
- `Timer_D.getCaptureCompareLatchCount()`,
- `Timer_D.getCaptureCompareInputSignal()`,
- `Timer_D.getCounterValue()`

The interrupt handler for the TimerD interrupt is managed with

- `Timer_D.enableTimerInterrupt()`,
- `Timer_D.disableTimerInterrupt()`,
- `Timer_D.getTimerInterruptStatus()`,
- `Timer_D.enableCaptureCompareInterrupt()`,
- `Timer_D.disableCaptureCompareInterrupt()`,
- `Timer_D.getCaptureCompareInterruptStatus()`,
- `Timer_D.clearCaptureCompareInterrupt()`
- `Timer_D.clearTimerInterrupt()`,
- `Timer_D.enableHighResInterrupt()`,
- `Timer_D.disableHighResInterrupt()`,
- `Timer_D.getHighResInterruptStatus()`,
- `Timer_D.clearHighResInterrupt()`

Timer\_D High Resolution handling APIs

- `Timer_D.getHighResInterruptStatus()`,
- `Timer_D.clearHighResInterrupt()`,
- `Timer_D.disableHighResFastWakeup()`,
- `Timer_D.enableHighResFastWakeup()`,
- `Timer_D.disableHighResClockEnhancedAccuracy()`,
- `Timer_D.enableHighResClockEnhancedAccuracy()`,

- `Timer_D.DisableHighResGeneratorForceON()`,
- `Timer_D.EnableHighResGeneratorForceON()`,
- `Timer_D.selectHighResCoarseClockRange()`,
- `Timer_D.selectHighResClockRange()`,
- `Timer_D.configureHighResGeneratorInFreeRunningMode()`,
- `Timer_D.configureHighResGeneratorInRegulatedMode()`

## 39.2.2 Function Documentation

### Timer\_D\_clear()

```
void Timer_D_clear (
    uint16_t baseAddress )
```

Reset/Clear the timer clock divider, count direction, count.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

Modified bits of **TDxCTL0** register.

#### Returns

None

### Timer\_D\_clearCaptureCompareInterrupt()

```
void Timer_D_clearCaptureCompareInterrupt (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Clears the capture-compare interrupt flag.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>captureCompareRegister</i>	selects the Capture-compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <code>TIMER_D_CAPTURECOMPARE_REGISTER_0</code></li> <li>■ <code>TIMER_D_CAPTURECOMPARE_REGISTER_1</code></li> <li>■ <code>TIMER_D_CAPTURECOMPARE_REGISTER_2</code></li> <li>■ <code>TIMER_D_CAPTURECOMPARE_REGISTER_3</code></li> <li>■ <code>TIMER_D_CAPTURECOMPARE_REGISTER_4</code></li> <li>■ <code>TIMER_D_CAPTURECOMPARE_REGISTER_5</code></li> <li>■ <code>TIMER_D_CAPTURECOMPARE_REGISTER_6</code></li> </ul>



Modified bits are **CCIFG** of **TDxCCTLn** register.

**Returns**

None

References `Timer_D_initHighResGeneratorInFreeRunningMode()`.

### Timer\_D\_clearHighResInterrupt()

```
void Timer_D_clearHighResInterrupt (
    uint16_t baseAddress,
    uint16_t mask )
```

Clears High Resolution interrupt status.

**Parameters**

<i>baseAddress</i>	is the base address of the <b>TIMER_D</b> module.
<i>mask</i>	is the mask for the interrupts to clear Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_HIGH_RES_FREQUENCY_UNLOCK</b></li> <li>■ <b>TIMER_D_HIGH_RES_FREQUENCY_LOCK</b></li> <li>■ <b>TIMER_D_HIGH_RES_FAIL_HIGH</b></li> <li>■ <b>TIMER_D_HIGH_RES_FAIL_LOW</b></li> </ul>

Modified bits of **TDxHINT** register.

**Returns**

None

References `Timer_D_getSynchronizedCaptureCompareInput()`.

### Timer\_D\_clearTimerInterrupt()

```
void Timer_D_clearTimerInterrupt (
    uint16_t baseAddress )
```

Clears the Timer **TDIFG** interrupt flag.

**Parameters**

<i>baseAddress</i>	is the base address of the <b>TIMER_D</b> module.
--------------------	---

Modified bits are **TDIFG** of **TDxCTL0** register.

**Returns**

None

**Timer\_D\_combineTDCCRToOutputPWM()**

```
void Timer_D_combineTDCCRToOutputPWM (
    uint16_t baseAddress,
    Timer_D_combineTDCCRToOutputPWMParam * param )
```

Combine TDCCR to get PWM.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>param</i>	is the pointer to struct for PWM generation using two CCRs.

Modified bits of **TDxCCTLn** register, bits of **TDxCCR0** register, bits of **TDxCCTL0** register, bits of **TDxCTL0** register and bits of **TDxCTL1** register.

**Returns**

None

References `Timer_D_combineTDCCRToOutputPWMParam::clockingMode`, `Timer_D_combineTDCCRToOutputPWMParam::clockSource`, `Timer_D_combineTDCCRToOutputPWMParam::clockSourceDivider`, `Timer_D_combineTDCCRToOutputPWMParam::combineCCRRegistersCombination`, `Timer_D_combineTDCCRToOutputPWMParam::compareOutputMode`, `Timer_D_combineTDCCRToOutputPWMParam::dutyCycle1`, `Timer_D_combineTDCCRToOutputPWMParam::dutyCycle2`, and `Timer_D_combineTDCCRToOutputPWMParam::timerPeriod`.

**Timer\_D\_disableCaptureCompareInterrupt()**

```
void Timer_D_disableCaptureCompareInterrupt (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Disable capture compare interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

**Parameters**

<i>captureCompareRegister</i>	is the selected capture compare register Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
-------------------------------	---

Modified bits of **TDxCCTLn** register.

**Returns**

None

**Timer\_D\_disableHighResClockEnhancedAccuracy()**

```
void Timer_D_disableHighResClockEnhancedAccuracy (
    uint16_t baseAddress )
```

Disable High Resolution Clock Enhanced Accuracy.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

Modified bits are **TDHEAEN** of **TDxHCTL0** register.

**Returns**

None

**Timer\_D\_disableHighResFastWakeup()**

```
void Timer_D_disableHighResFastWakeup (
    uint16_t baseAddress )
```

Disable High Resolution fast wakeup.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

Modified bits are **TDHFW** of **TDxHCTL0** register.

**Returns**

None

### Timer\_D\_disableHighResGeneratorForceON()

```
void Timer_D_disableHighResGeneratorForceON (
    uint16_t baseAddress )
```

Disable High Resolution Clock Enhanced Accuracy.  
High-resolution generator is on if the Timer\_D counter

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

Modified bits are **TDHRON** of **TDxHCTL0** register.

**Returns**

None

### Timer\_D\_disableHighResInterrupt()

```
void Timer_D_disableHighResInterrupt (
    uint16_t baseAddress,
    uint16_t mask )
```

Disable High Resolution interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>mask</i>	is the mask of interrupts to disable Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_HIGH_RES_FREQUENCY_UNLOCK</b></li> <li>■ <b>TIMER_D_HIGH_RES_FREQUENCY_LOCK</b></li> <li>■ <b>TIMER_D_HIGH_RES_FAIL_HIGH</b></li> <li>■ <b>TIMER_D_HIGH_RES_FAIL_LOW</b></li> </ul>

Modified bits of **TDxHINT** register.

**Returns**

None

**Timer\_D\_disableTimerInterrupt()**

```
void Timer_D_disableTimerInterrupt (
    uint16_t baseAddress )
```

Disable timer interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

Modified bits of **TDxCTL0** register.

**Returns**

None

**Timer\_D\_enableCaptureCompareInterrupt()**

```
void Timer_D_enableCaptureCompareInterrupt (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Enable capture compare interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>captureCompareRegister</i>	is the selected capture compare register Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

Modified bits of **TDxCCTLn** register.

**Returns**

None

**Timer\_D\_enableHighResClockEnhancedAccuracy()**

```
void Timer_D_enableHighResClockEnhancedAccuracy (
```

```
uint16_t baseAddress )
```

Enable High Resolution Clock Enhanced Accuracy.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

Modified bits are **TDHEAEN** of **TDxHCTL0** register.

#### Returns

None

### Timer\_D\_enableHighResFastWakeup()

```
void Timer_D_enableHighResFastWakeup (
    uint16_t baseAddress )
```

Enable High Resolution fast wakeup.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

Modified bits are **TDHFW** of **TDxHCTL0** register.

#### Returns

None

### Timer\_D\_enableHighResGeneratorForceON()

```
void Timer_D_enableHighResGeneratorForceON (
    uint16_t baseAddress )
```

Enable High Resolution Clock Enhanced Accuracy.

High-resolution generator is on in all Timer\_D MCx modes. The PMM remains in high-current mode.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

Modified bits are **TDHRON** of **TDxHCTL0** register.

#### Returns

None

**Timer\_D\_enableHighResInterrupt()**

```
void Timer_D_enableHighResInterrupt (
    uint16_t baseAddress,
    uint16_t mask )
```

Enable High Resolution interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>mask</i>	is the mask of interrupts to enable Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_HIGH_RES_FREQUENCY_UNLOCK</b></li> <li>■ <b>TIMER_D_HIGH_RES_FREQUENCY_LOCK</b></li> <li>■ <b>TIMER_D_HIGH_RES_FAIL_HIGH</b></li> <li>■ <b>TIMER_D_HIGH_RES_FAIL_LOW</b></li> </ul>

Modified bits of **TDxHINT** register.

**Returns**

None

**Timer\_D\_enableTimerInterrupt()**

```
void Timer_D_enableTimerInterrupt (
    uint16_t baseAddress )
```

Enable timer interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

Modified bits of **TDxCTL0** register.

**Returns**

None

**Timer\_D\_getCaptureCompareCount()**

```
uint16_t Timer_D_getCaptureCompareCount (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Get current capturecompare count.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>captureCompareRegister</i>	selects the Capture register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

**Returns**

current count as uint16\_t

References Timer\_D\_getCaptureCompareLatchCount().

Referenced by Timer\_D\_getOutputForOutputModeOutBitValue().

**Timer\_D\_getCaptureCompareInputSignal()**

```
uint8_t Timer_D_getCaptureCompareInputSignal (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Get current capturecompare input signal.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>captureCompareRegister</i>	selects the Capture register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_6</b></li> </ul>



**Returns**

One of the following:

- **TIMER\_D\_CAPTURECOMPARE\_INPUT**
- **0x00**

indicating the current input signal

References `Timer_D_setOutputForOutputModeOutBitValue()`.

Referenced by `Timer_D_getCaptureCompareLatchCount()`.

**Timer\_D\_getCaptureCompareInterruptStatus()**

```
uint32_t Timer_D_getCaptureCompareInterruptStatus (
    uint16_t baseAddress,
    uint16_t captureCompareRegister,
    uint16_t mask )
```

Return capture compare interrupt status.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>captureCompareRegister</i>	is the selected capture compare register Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>mask</i>	is the mask for the interrupt status Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURE_OVERFLOW</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_INTERRUPT_FLAG</b></li> </ul>

**Returns**

Logical OR of any of the following:

- **TIMER\_D\_CAPTURE\_OVERFLOW**
- **TIMER\_D\_CAPTURECOMPARE\_INTERRUPT\_FLAG**

indicating the status of the masked flags

**Timer\_D\_getCaptureCompareLatchCount()**

```
uint16_t Timer_D_getCaptureCompareLatchCount (
```

```
uint16_t baseAddress,
uint16_t captureCompareRegister )
```

Get current capture compare latch register count.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>captureCompareRegister</i>	selects the Capture register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

#### Returns

current count as uint16\_t

References Timer\_D\_getCaptureCompareInputSignal().

Referenced by Timer\_D\_getCaptureCompareCount().

### Timer\_D\_getCounterValue()

```
uint16_t Timer_D_getCounterValue (
uint16_t baseAddress )
```

Reads the current timer count value.

Reads the current count value of the timer. There is a majority vote system in place to confirm an accurate value is returned. The Timer\_D\_THRESHOLD #define in the corresponding header file can be modified so that the votes must be closer together for a consensus to occur.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

#### Returns

Majority vote of timer count value

### Timer\_D\_getHighResInterruptStatus()

```
uint16_t Timer_D_getHighResInterruptStatus (
```

```
uint16_t baseAddress,
uint16_t mask )
```

Returns High Resolution interrupt status.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>mask</i>	is the mask for the interrupt status Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_HIGH_RES_FREQUENCY_UNLOCK</b></li> <li>■ <b>TIMER_D_HIGH_RES_FREQUENCY_LOCK</b></li> <li>■ <b>TIMER_D_HIGH_RES_FAIL_HIGH</b></li> <li>■ <b>TIMER_D_HIGH_RES_FAIL_LOW</b></li> </ul>

Modified bits of **TDxHINT** register.

#### Returns

Logical OR of any of the following:

- **TIMER\_D\_HIGH\_RES\_FREQUENCY\_UNLOCK**
- **TIMER\_D\_HIGH\_RES\_FREQUENCY\_LOCK**
- **TIMER\_D\_HIGH\_RES\_FAIL\_HIGH**
- **TIMER\_D\_HIGH\_RES\_FAIL\_LOW**

indicating the status of the masked interrupts

#### Timer\_D\_getOutputForOutputModeOutBitValue()

```
uint8_t Timer_D_getOutputForOutputModeOutBitValue (
    uint16_t baseAddress,
    uint16_t captureCompareRegister )
```

Get output bit for output mode.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>captureCompareRegister</i>	selects the Capture register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

**Returns**

One of the following:

- **TIMER\_D\_OUTPUTMODE\_OUTBITVALUE\_HIGH**
- **TIMER\_D\_OUTPUTMODE\_OUTBITVALUE\_LOW**

References Timer\_D\_getCaptureCompareCount().

Referenced by Timer\_D\_getSynchronizedCaptureCompareInput().

**Timer\_D\_getSynchronizedCaptureCompareInput()**

```
uint8_t Timer_D_getSynchronizedCaptureCompareInput (
    uint16_t baseAddress,
    uint16_t captureCompareRegister,
    uint16_t synchronized )
```

Get synchronized capturecompare input.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>captureCompareRegister</i>	selects the Capture register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>synchronized</i>	is to select type of capture compare input. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_READ_SYNCHRONIZED_CAPTURECOMPAREINPUT</b></li> <li>■ <b>TIMER_D_READ_CAPTURE_COMPARE_INPUT</b></li> </ul>

**Returns**

One of the following:

- **TIMER\_D\_CAPTURECOMPARE\_INPUT\_HIGH**
- **TIMER\_D\_CAPTURECOMPARE\_INPUT\_LOW**

References Timer\_D\_getOutputForOutputModeOutBitValue().

Referenced by Timer\_D\_clearHighResInterrupt().

**Timer\_D\_getTimerInterruptStatus()**

```
uint32_t Timer_D_getTimerInterruptStatus (
```

```
uint16_t baseAddress )
```

Get timer interrupt status.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

#### Returns

One of the following:

- **TIMER\_D\_INTERRUPT\_NOT\_PENDING**
- **TIMER\_D\_INTERRUPT\_PENDING**  
indicating the timer interrupt status

### Timer\_D\_initCaptureMode()

```
void Timer_D_initCaptureMode (
    uint16_t baseAddress,
    Timer_D_initCaptureModeParam * param )
```

Initializes Capture Mode.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>param</i>	is the pointer to struct for capture mode initialization.

Modified bits of **TDxCCTLn** register and bits of **TDxCTL2** register.

#### Returns

None

References `Timer_D_initCaptureModeParam::captureInputSelect`, `Timer_D_initCaptureModeParam::captureInterruptEnable`, `Timer_D_initCaptureModeParam::captureMode`, `Timer_D_initCaptureModeParam::captureOutputMode`, `Timer_D_initCaptureModeParam::captureRegister`, `Timer_D_initCaptureModeParam::channelCaptureMode`, and `Timer_D_initCaptureModeParam::synchronizeCaptureSource`.

### Timer\_D\_initCompareLatchLoadEvent()

```
void Timer_D_initCompareLatchLoadEvent (
    uint16_t baseAddress,
    uint16_t compareRegister,
    uint16_t compareLatchLoadEvent )
```

Selects Compare Latch Load Event.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>compareRegister</i>	selects the compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>compareLatchLoadEvent</i>	selects the latch load event Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_LATCH_ON_WRITE_TO_TDxCcRn_COMPARE_REGISTER</b> [Default]</li> <li>■ <b>TIMER_D_LATCH_WHEN_COUNTER_COUNTS_TO_0_IN_UP_OR_CONT_MODE</b></li> <li>■ <b>TIMER_D_LATCH_WHEN_COUNTER_COUNTS_TO_0_IN_UPDOWN_MODE</b></li> <li>■ <b>TIMER_D_LATCH_WHEN_COUNTER_COUNTS_TO_CURRENT_COMPARE_LATCH_VALUE</b></li> </ul>

Modified bits are **CLLD** of **TDxCCTLn** register.

## Returns

None

## Timer\_D\_initCompareMode()

```
void Timer_D_initCompareMode (
    uint16_t baseAddress,
    Timer_D_initCompareModeParam * param )
```

Initializes Compare Mode.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>param</i>	is the pointer to struct for compare mode initialization.

Modified bits of **TDxCCTLn** register and bits of **TDxCcRn** register.

**Returns**

None

References `Timer_D_initCompareModeParam::compareInterruptEnable`, `Timer_D_initCompareModeParam::compareOutputMode`, `Timer_D_initCompareModeParam::compareRegister`, and `Timer_D_initCompareModeParam::compareValue`.

**Timer\_D\_initContinuousMode()**

```
void Timer_D_initContinuousMode (
    uint16_t baseAddress,
    Timer_D_initContinuousModeParam * param )
```

Configures timer in continuous mode.

This API does not start the timer. Timer needs to be started when required using the `Timer_D_start` API.

**Parameters**

<i>baseAddress</i>	is the base address of the <code>TIMER_D</code> module.
<i>param</i>	is the pointer to struct for continuous mode initialization.

Modified bits of **TDxCTL0** register and bits of **TDxCTL1** register.

**Returns**

None

References `Timer_D_initContinuousModeParam::clockingMode`, `Timer_D_initContinuousModeParam::clockSource`, `Timer_D_initContinuousModeParam::clockSourceDivider`, `Timer_D_initContinuousModeParam::timerClear`, and `Timer_D_initContinuousModeParam::timerInterruptEnable_TDIE`.

**Timer\_D\_initHighResGeneratorInFreeRunningMode()**

```
uint8_t Timer_D_initHighResGeneratorInFreeRunningMode (
    uint16_t baseAddress,
    uint8_t desiredHighResFrequency )
```

Configures `Timer_D` in free running mode.

**Parameters**

<i>baseAddress</i>	is the base address of the <code>TIMER_D</code> module.
--------------------	---

**Parameters**

<i>desiredHighResFrequency</i>	selects the desired High Resolution frequency used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_HIGHRES_64MHZ</b></li> <li>■ <b>TIMER_D_HIGHRES_128MHZ</b></li> <li>■ <b>TIMER_D_HIGHRES_200MHZ</b></li> <li>■ <b>TIMER_D_HIGHRES_256MHZ</b></li> </ul>
--------------------------------	---

Modified bits of **TDxHCTL1** register, bits of **TDxHCTL0** register and bits of **TDxCTL1** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAIL

References TLV\_getInfo().

Referenced by Timer\_D\_clearCaptureCompareInterrupt().

**Timer\_D\_initHighResGeneratorInRegulatedMode()**

```
void Timer_D_initHighResGeneratorInRegulatedMode (
    uint16_t baseAddress,
    Timer_D_initHighResGeneratorInRegulatedModeParam * param )
```

Configures Timer\_D in Regulated mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>param</i>	is the pointer to struct for high resolution generator in regulated mode.

Modified bits of **TDxHCTL0** register, bits of **TDxCTL0** register and bits of **TDxCTL1** register.

**Returns**

None

References Timer\_D\_initHighResGeneratorInRegulatedModeParam::clockingMode, Timer\_D\_initHighResGeneratorInRegulatedModeParam::clockSource, Timer\_D\_initHighResGeneratorInRegulatedModeParam::clockSourceDivider, Timer\_D\_initHighResGeneratorInRegulatedModeParam::highResClockDivider, and Timer\_D\_initHighResGeneratorInRegulatedModeParam::highResClockMultiplyFactor.

**Timer\_D\_initUpDownMode()**

```
void Timer_D_initUpDownMode (
    uint16_t baseAddress,
    Timer_D_initUpDownModeParam * param )
```



Configures timer in up down mode.

This API does not start the timer. Timer needs to be started when required using the `Timer_D_start` API.

#### Parameters

<i>baseAddress</i>	is the base address of the <code>TIMER_D</code> module.
<i>param</i>	is the pointer to struct for up-down mode initialization.

Modified bits of **TDxCCR0** register, bits of **TDxCCTL0** register, bits of **TDxCTL0** register and bits of **TDxCTL1** register.

#### Returns

None

References `Timer_D_initUpDownModeParam::captureCompareInterruptEnable_CCR0_CCIE`, `Timer_D_initUpDownModeParam::clockingMode`, `Timer_D_initUpDownModeParam::clockSource`, `Timer_D_initUpDownModeParam::clockSourceDivider`, `Timer_D_initUpDownModeParam::timerClear`, `Timer_D_initUpDownModeParam::timerInterruptEnable_TDIE`, and `Timer_D_initUpDownModeParam::timerPeriod`.

### Timer\_D\_initUpMode()

```
void Timer_D_initUpMode (
    uint16_t baseAddress,
    Timer_D_initUpModeParam * param )
```

Configures timer in up mode.

This API does not start the timer. Timer needs to be started when required using the `Timer_D_start` API.

#### Parameters

<i>baseAddress</i>	is the base address of the <code>TIMER_D</code> module.
<i>param</i>	is the pointer to struct for up mode initialization.

Modified bits of **TDxCCR0** register, bits of **TDxCCTL0** register, bits of **TDxCTL0** register and bits of **TDxCTL1** register.

#### Returns

None

References `Timer_D_initUpModeParam::captureCompareInterruptEnable_CCR0_CCIE`, `Timer_D_initUpModeParam::clockingMode`, `Timer_D_initUpModeParam::clockSource`, `Timer_D_initUpModeParam::clockSourceDivider`, `Timer_D_initUpModeParam::timerClear`, `Timer_D_initUpModeParam::timerInterruptEnable_TDIE`, and `Timer_D_initUpModeParam::timerPeriod`.

## Timer\_D\_outputPWM()

```
void Timer_D_outputPWM (
    uint16_t baseAddress,
    Timer_D_outputPWMPParam * param )
```

Generate a PWM with timer running in up mode.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>param</i>	is the pointer to struct for PWM configuration.

Modified bits of **TDxCCTLn** register, bits of **TDxCCR0** register, bits of **TDxCCTL0** register, bits of **TDxCTL0** register and bits of **TDxCTL1** register.

## Returns

None

References `Timer_D_outputPWMPParam::clockingMode`, `Timer_D_outputPWMPParam::clockSource`, `Timer_D_outputPWMPParam::clockSourceDivider`, `Timer_D_outputPWMPParam::compareOutputMode`, `Timer_D_outputPWMPParam::compareRegister`, `Timer_D_outputPWMPParam::dutyCycle`, and `Timer_D_outputPWMPParam::timerPeriod`.

## Timer\_D\_selectCounterLength()

```
void Timer_D_selectCounterLength (
    uint16_t baseAddress,
    uint16_t counterLength )
```

Selects Timer\_D counter length.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>counterLength</i>	selects the value of counter length. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_COUNTER_16BIT</b> [Default]</li> <li>■ <b>TIMER_D_COUNTER_12BIT</b></li> <li>■ <b>TIMER_D_COUNTER_10BIT</b></li> <li>■ <b>TIMER_D_COUNTER_8BIT</b></li> </ul>

Modified bits are **CNTL** of **TDxCTL0** register.

**Returns**

None

**Timer\_D\_selectHighResClockRange()**

```
void Timer_D_selectHighResClockRange (
    uint16_t baseAddress,
    uint16_t highResClockRange )
```

Select High Resolution Clock Range Selection.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>highResClockRange</i>	selects the High Resolution Clock Range. Refer to datasheet for frequency details Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CLOCK_RANGE0</b> [Default]</li> <li>■ <b>TIMER_D_CLOCK_RANGE1</b></li> <li>■ <b>TIMER_D_CLOCK_RANGE2</b></li> </ul>

**Returns**

None

**Timer\_D\_selectHighResCoarseClockRange()**

```
void Timer_D_selectHighResCoarseClockRange (
    uint16_t baseAddress,
    uint16_t highResCoarseClockRange )
```

Select High Resolution Coarse Clock Range.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>highResCoarseClockRange</i>	selects the High Resolution Coarse Clock Range Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_HIGHRES_BELOW_15MHz</b> [Default]</li> <li>■ <b>TIMER_D_HIGHRES_ABOVE_15MHz</b></li> </ul>

Modified bits are **TDHCLKCR** of **TDxHCTL1** register.**Returns**

None

## Timer\_D\_selectLatchingGroup()

```
void Timer_D_selectLatchingGroup (
    uint16_t baseAddress,
    uint16_t groupLatch )
```

Selects Timer\_D Latching Group.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>groupLatch</i>	selects the group latch Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_GROUP_NONE</b> [Default]</li> <li>■ <b>TIMER_D_GROUP_CL12.CL23.CL56</b></li> <li>■ <b>TIMER_D_GROUP_CL123.CL456</b></li> <li>■ <b>TIMER_D_GROUP_ALL</b></li> </ul>

Modified bits are **TDCLGRP** of **TDxCTL0** register.

## Returns

None

## Timer\_D\_setCompareValue()

```
void Timer_D_setCompareValue (
    uint16_t baseAddress,
    uint16_t compareRegister,
    uint16_t compareValue )
```

Sets the value of the capture-compare register.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>compareRegister</i>	selects the Capture register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>compareValue</i>	is the count to be compared with in compare mode

Modified bits of **TDxCCRn** register.

**Returns**

None

### Timer\_D\_setOutputForOutputModeOutBitValue()

```
void Timer_D_setOutputForOutputModeOutBitValue (
    uint16_t baseAddress,
    uint16_t captureCompareRegister,
    uint8_t outputModeOutBitValue )
```

Set output bit for output mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>captureCompareRegister</i>	selects the Capture register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>outputModeOutBitValue</i>	the value to be set for out bit Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_OUTPUTMODE_OUTBITVALUE_HIGH</b></li> <li>■ <b>TIMER_D_OUTPUTMODE_OUTBITVALUE_LOW</b></li> </ul>

Modified bits of **TDxCCTLn** register.

**Returns**

None

Referenced by Timer\_D\_getCaptureCompareInputSignal().

### Timer\_D\_setOutputMode()

```
void Timer_D_setOutputMode (
    uint16_t baseAddress,
    uint16_t compareRegister,
    uint16_t compareOutputMode )
```

Sets the output mode.

Sets the output mode for the timer even the timer is already running.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
<i>compareRegister</i>	selects the compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_D_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>compareOutputMode</i>	specifies the output mode. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_OUTPUTMODE_OUTBITVALUE</b> [Default]</li> <li>■ <b>TIMER_D_OUTPUTMODE_SET</b></li> <li>■ <b>TIMER_D_OUTPUTMODE_TOGGLE_RESET</b></li> <li>■ <b>TIMER_D_OUTPUTMODE_SET_RESET</b></li> <li>■ <b>TIMER_D_OUTPUTMODE_TOGGLE</b></li> <li>■ <b>TIMER_D_OUTPUTMODE_RESET</b></li> <li>■ <b>TIMER_D_OUTPUTMODE_TOGGLE_SET</b></li> <li>■ <b>TIMER_D_OUTPUTMODE_RESET_SET</b></li> </ul>

Modified bits are **OUTMOD** of **TDxCCTLn** register.

#### Returns

None

#### Timer\_D\_startCounter()

```
void Timer_D_startCounter (
    uint16_t baseAddress,
    uint16_t timerMode )
```

Starts Timer\_D counter.

NOTE: This function assumes that the timer has been previously configured using `Timer_D_initContinuousMode`, `Timer_D_initUpMode` or `Timer_D_initUpDownMode`.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_DA module.
--------------------	---

## Parameters

<i>timerMode</i>	selects the mode of the timer Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_D_STOP_MODE</b></li> <li>■ <b>TIMER_D_UP_MODE</b></li> <li>■ <b>TIMER_D_CONTINUOUS_MODE</b> [Default]</li> <li>■ <b>TIMER_D_UPDOWN_MODE</b></li> </ul>
------------------	--

Modified bits of **TDxCTL0** register.

## Returns

None

## Timer\_D\_stop()

```
void Timer_D_stop (
    uint16_t baseAddress )
```

Stops the timer.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_D module.
--------------------	--

Modified bits of **TDxCTL0** register.

## Returns

None

## 39.3 Programming Example

The following example shows some TimerD operations using the APIs

```
{ //Start TimerD
  //Start timer in continuous mode sourced by SMCLK
  Timer_D_initContinuousModeParam initContparam = {0};
  initContparam.clockSource = TIMER_D_CLOCKSOURCE_SMCLK;
  initContparam.clockSourceDivider = TIMER_D_CLOCKSOURCE_DIVIDER_1;
  initContparam.clockingMode = TIMER_D_CLOCKINGMODE_EXTERNAL_CLOCK;
  initContparam.timerInterruptEnable_TDIE = TIMER_D_TDIE_INTERRUPT_DISABLE;
  initContparam.timerClear = TIMER_D_DO_CLEAR;
  Timer_D_initContinuousMode(TIMER_D0_BASE, &initContparam);

  Timer_D_startCounter(TIMER_D0_BASE,
    TIMER_D_CONTINUOUS_MODE
  );

  //Initiaze compare mode
  Timer_D_clearCaptureCompareInterrupt(TIMER_D0_BASE,
    TIMER_D_CAPTURECOMPARE_REGISTER_0);
```

```
Timer_D.initCompareModeParam initCompParam = {0};
initCompParam.compareRegister = TIMER_D.CAPTURECOMPARE_REGISTER_0;
initCompParam.compareInterruptEnable = TIMER_D.CAPTURECOMPARE_INTERRUPT_ENABLE;
initCompParam.compareOutputMode = TIMER_D.OUTPUTMODE_OUTBITVALUE;
initCompParam.compareValue = 50000;
Timer_D.initCompareMode(TIMER_D0_BASE, &initCompParam);

//Enter LPM0
__bis_SR_register(LPM0_bits);

//For debugger
__no_operation();
}
```



# 40 Tag Length Value

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## 40.1 Introduction

The TLV structure is a table stored in flash memory that contains device-specific information. This table is read-only and is write-protected. It contains important information for using and calibrating the device. A list of the contents of the TLV is available in the device-specific data sheet (in the Device Descriptors section), and an explanation on its functionality is available in the MSP430x5xx/MSP430x6xx Family User's Guide

## 40.2 API Functions

### Functions

- void [TLV\\_getInfo](#) (uint8\_t tag, uint8\_t instance, uint8\_t \*length, uint16\_t \*\*data.address)  
*Gets TLV Info.*
- uint16\_t [TLV\\_getDeviceType](#) ()  
*Retrieves the unique device ID from the TLV structure.*
- uint16\_t [TLV\\_getMemory](#) (uint8\_t instance)  
*Gets memory information.*
- uint16\_t [TLV\\_getPeripheral](#) (uint8\_t tag, uint8\_t instance)  
*Gets peripheral information from the TLV.*
- uint8\_t [TLV\\_getInterrupt](#) (uint8\_t tag)  
*Get interrupt information from the TLV.*

### 40.2.1 Detailed Description

The APIs that help in querying the information in the TLV structure are listed

- [TLV\\_getInfo\(\)](#) This function retrieves the value of a tag and the length of the tag.
- [TLV\\_getDeviceType\(\)](#) This function retrieves the unique device ID from the TLV structure.
- [TLV\\_getMemory\(\)](#) The returned value is zero if the end of the memory list is reached.
- [TLV\\_getPeripheral\(\)](#) The returned value is zero if the specified tag value (peripheral) is not available in the device.
- [TLV\\_getInterrupt\(\)](#) The returned value is zero if the specified interrupt vector is not defined.

## 40.2.2 Function Documentation

### TLV\_getDeviceType()

```
uint16_t TLV_getDeviceType (
    void )
```

Retrieves the unique device ID from the TLV structure.

#### Returns

The device ID is returned as type `uint16_t`.

### TLV\_getInfo()

```
void TLV_getInfo (
    uint8_t tag,
    uint8_t instance,
    uint8_t * length,
    uint16_t ** data_address )
```

#### Gets TLV Info.

The TLV structure uses a tag or base address to identify segments of the table where information is stored. Some examples of TLV tags are Peripheral Descriptor, Interrupts, Info Block and Die Record. This function retrieves the value of a tag and the length of the tag.

## Parameters

<i>tag</i>	<p>represents the tag for which the information needs to be retrieved. Valid values are:</p> <ul style="list-style-type: none"> <li>■ TLV_TAG_LD TAG</li> <li>■ TLV_TAG_PD TAG</li> <li>■ TLV_TAG_Reserved3</li> <li>■ TLV_TAG_Reserved4</li> <li>■ TLV_TAG_BLANK</li> <li>■ TLV_TAG_Reserved6</li> <li>■ TLV_TAG_Reserved7</li> <li>■ TLV_TAG_TAGEND</li> <li>■ TLV_TAG_TAGEXT</li> <li>■ TLV_TAG_TIMER_D_CAL</li> <li>■ TLV_DEVICE_ID_0</li> <li>■ TLV_DEVICE_ID_1</li> <li>■ TLV_TAG_DIERECORD</li> <li>■ TLV_TAG_ADCCAL</li> <li>■ TLV_TAG_ADC12CAL</li> <li>■ TLV_TAG_ADC10CAL</li> <li>■ TLV_TAG_REFCAL</li> <li>■ TLV_TAG_CTSD16CAL</li> </ul>
<i>instance</i>	<p>In some cases a specific tag may have more than one instance. For example there may be multiple instances of timer calibration data present under a single Timer Cal tag. This variable specifies the instance for which information is to be retrieved (0, 1, etc.). When only one instance exists; 0 is passed.</p>
<i>length</i>	<p>Acts as a return through indirect reference. The function retrieves the value of the TLV tag length. This value is pointed to by *length and can be used by the application level once the function is called. If the specified tag is not found then the pointer is null 0.</p>
<i>data_address</i>	<p>acts as a return through indirect reference. Once the function is called data_address points to the pointer that holds the value retrieved from the specified TLV tag. If the specified tag is not found then the pointer is null 0.</p>

## Returns

None

Referenced by Timer\_D\_initHighResGeneratorInFreeRunningMode(), TLV\_getInterrupt(), TLV\_getMemory(), and TLV\_getPeripheral().

## TLV\_getInterrupt()

```
uint8_t TLV_getInterrupt (
```

```
uint8_t tag )
```

Get interrupt information from the TLV.

This function is used to retrieve information on available interrupt vectors. It allows the user to check if a specific interrupt vector is defined in a given device.

#### Parameters

<i>tag</i>	represents the tag for the interrupt vector. Interrupt vector tags number from 0 to N depending on the number of available interrupts. Refer to the device datasheet for a list of available interrupts.
------------	--

#### Returns

The returned value is zero if the specified interrupt vector is not defined.

References TLV\_getInfo(), and TLV\_getMemory().

### TLV\_getMemory()

```
uint16_t TLV_getMemory (
    uint8_t instance )
```

Gets memory information.

The Peripheral Descriptor tag is split into two portions a list of the available flash memory blocks followed by a list of available peripherals. This function is used to parse through the first portion and calculate the total flash memory available in a device. The typical usage is to call the TLV\_getMemory which returns a non-zero value until the entire memory list has been parsed. When a zero is returned, it indicates that all the memory blocks have been counted and the next address holds the beginning of the device peripheral list.

#### Parameters

<i>instance</i>	In some cases a specific tag may have more than one instance. This variable specifies the instance for which information is to be retrieved (0, 1 etc). When only one instance exists; 0 is passed.
-----------------	---

#### Returns

The returned value is zero if the end of the memory list is reached.

References TLV\_getInfo().

Referenced by TLV\_getInterrupt(), and TLV\_getPeripheral().

### TLV\_getPeripheral()

```
uint16_t TLV_getPeripheral (
    uint8_t tag,
    uint8_t instance )
```

Gets peripheral information from the TLV.

The Peripheral Descriptor tag is split into two portions a list of the available flash memory blocks followed by a list of available peripherals. This function is used to parse through the second portion and can be used to check if a specific peripheral is present in a device. The function calls [TLV\\_getPeripheral\(\)](#) recursively until the end of the memory list and consequently the beginning of the peripheral list is reached. <



## Parameters

## Parameters

<i>tag</i>	<p>represents represents the tag for a specific peripheral for which the information needs to be retrieved. In the header file tlv. h specific peripheral tags are pre-defined, for example USCIA_B and TA0 are defined as TLV_PID_USCIA_AB and TLV_PID_TA2 respectively. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>TLV_PID_NO_MODULE</b> - No Module</li> <li>■ <b>TLV_PID_PORTMAPPING</b> - Port Mapping</li> <li>■ <b>TLV_PID_MSP430CPUXV2</b> - MSP430CPUXV2</li> <li>■ <b>TLV_PID_JTAG</b> - JTAG</li> <li>■ <b>TLV_PID_SBW</b> - SBW</li> <li>■ <b>TLV_PID_EEM_XS</b> - EEM X-Small</li> <li>■ <b>TLV_PID_EEM_S</b> - EEM Small</li> <li>■ <b>TLV_PID_EEM_M</b> - EEM Medium</li> <li>■ <b>TLV_PID_EEM_L</b> - EEM Large</li> <li>■ <b>TLV_PID_PMM</b> - PMM</li> <li>■ <b>TLV_PID_PMM_FR</b> - PMM FRAM</li> <li>■ <b>TLV_PID_FCTL</b> - Flash</li> <li>■ <b>TLV_PID_CRC16</b> - CRC16</li> <li>■ <b>TLV_PID_CRC16_RB</b> - CRC16 Reverse</li> <li>■ <b>TLV_PID_WDT_A</b> - WDT_A</li> <li>■ <b>TLV_PID_SFR</b> - SFR</li> <li>■ <b>TLV_PID_SYS</b> - SYS</li> <li>■ <b>TLV_PID_RAMCTL</b> - RAMCTL</li> <li>■ <b>TLV_PID_DMA_1</b> - DMA 1</li> <li>■ <b>TLV_PID_DMA_3</b> - DMA 3</li> <li>■ <b>TLV_PID_UCS</b> - UCS</li> <li>■ <b>TLV_PID_DMA_6</b> - DMA 6</li> <li>■ <b>TLV_PID_DMA_2</b> - DMA 2</li> <li>■ <b>TLV_PID_PORT1_2</b> - Port 1 + 2 / A</li> <li>■ <b>TLV_PID_PORT3_4</b> - Port 3 + 4 / B</li> <li>■ <b>TLV_PID_PORT5_6</b> - Port 5 + 6 / C</li> <li>■ <b>TLV_PID_PORT7_8</b> - Port 7 + 8 / D</li> <li>■ <b>TLV_PID_PORT9_10</b> - Port 9 + 10 / E</li> <li>■ <b>TLV_PID_PORT11_12</b> - Port 11 + 12 / F</li> <li>■ <b>TLV_PID_PORTU</b> - Port U</li> <li>■ <b>TLV_PID_PORTJ</b> - Port J</li> <li>■ <b>TLV_PID_TA2</b> - Timer A2</li> <li>■ <b>TLV_PID_TA3</b> - Timer A1</li> <li>■ <b>TLV_PID_TA5</b> - Timer A5</li> <li>■ <b>TLV_PID_TA7</b> - Timer A7</li> <li>■ <b>TLV_PID_TB3</b> - Timer B3</li> <li>■ <b>TLV_PID_TB5</b> - Timer B5</li> <li>■ <b>TLV_PID_TB7</b> - Timer B7</li> <li>■ <b>TLV_PID_RTC</b> - RTC</li> </ul>
------------	--

**Parameters**

<i>instance</i>	In some cases a specific tag may have more than one instance. For example a device may have more than a single USCI module, each of which is defined by an instance number 0, 1, 2, etc. When only one instance exists; 0 is passed.
-----------------	--

**Returns**

The returned value is zero if the specified tag value (peripheral) is not available in the device.

References TLV\_getInfo(), and TLV\_getMemory().

## 40.3 Programming Example

The following example shows some tlv operations using the APIs

```
struct s_TLV_Die_Record * pDIEREC;
unsigned char bDieRecord.bytes;

TLV_getInfo(TLV_TAG_DIERECORD,
            0,
            &bDieRecord.bytes,
            (unsigned int **)&pDIEREC
            );
```



# 41 Unified Clock System (UCS)

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## 41.1 Introduction

The UCS is based on five available clock sources (VLO, REFO, XT1, XT2, and DCO) providing signals to three system clocks (MCLK, SMCLK, ACLK). Different low power modes are achieved by turning off the MCLK, SMCLK, ACLK, and integrated LDO.

- VLO - Internal very-low-power low-frequency oscillator. 10 kHz (?0.5%/?C, ?4%/V)
- REFO - Reference oscillator. 32 kHz (?1%, ?3% over full temp range)
- XT1 (LFXT1, HFXT1) - Ultra-low-power oscillator, compatible with low-frequency 32768-Hz watch crystals and with standard XT1 (LFXT1, HFXT1) crystals, resonators, or external clock sources in the 4-MHz to 32-MHz range, including digital inputs. Most commonly used as 32-kHz watch crystal oscillator.
- XT2 - Optional high-frequency oscillator that can be used with standard crystals, resonators, or external clock sources in the 4-MHz to 32-MHz range, including digital inputs.
- DCO - Internal digitally-controlled oscillator (DCO) that can be stabilized by a frequency lock loop (FLL) that sets the DCO to a specified multiple of a reference frequency.

System Clocks and Functionality on the MSP430 MCLK Master Clock Services the CPU. Commonly sourced by DCO. Is available in Active mode only SMCLK Subsystem Master Clock Services 'fast' system peripherals. Commonly sourced by DCO. Is available in Active mode, LPM0 and LPM1 ACLK Auxiliary Clock Services 'slow' system peripherals. Commonly used for 32-kHz signal. Is available in Active mode, LPM0 to LPM3

System clocks of the MSP430x5xx generation are automatically enabled, regardless of the LPM mode of operation, if they are required for the proper operation of the peripheral module that they source. This additional flexibility of the UCS, along with improved fail-safe logic, provides a robust clocking scheme for all applications.

Fail-Safe logic The UCS fail-safe logic plays an important part in providing a robust clocking scheme for MSP430x5xx and MSP430x6xx applications. This feature hinges on the ability to detect an oscillator fault for the XT1 in both low- and high-frequency modes (XT1LFOFFG and XT1HFOFFG respectively), the high-frequency XT2 (XT2OFFG), and the DCO (DCOFFG). These flags are set and latched when the respective oscillator is enabled but not operating properly; therefore, they must be explicitly cleared in software

The oscillator fault flags on previous MSP430 generations are not latched and are asserted only as long as the failing condition exists. Therefore, an important difference between the families is that the fail-safe behavior in a 5xx-based MSP430 remains active until both the OFIFG and the respective fault flag are cleared in software.

This fail-safe behavior is implemented at the oscillator level, at the system clock level and, consequently, at the module level. Some notable highlights of this behavior are described below. For the full description of fail-safe behavior and conditions, see the MSP430x5xx/MSP430x6xx Family User's Guide (SLAU208).

- Low-frequency crystal oscillator 1 (LFXT1) The low-frequency (32768 Hz) crystal oscillator is the default reference clock to the FLL. An asserted XT1LFOFFG switches the FLL reference from the failing LFXT1 to the internal 32-kHz REFO. This can influence the DCO accuracy, because the FLL crystal ppm specification is typically tighter than the REFO accuracy over temperature and voltage of ±3%.
- System Clocks (ACLK, SMCLK, MCLK) A fault on the oscillator that is sourcing a system clock switches the source from the failing oscillator to the DCO oscillator (DCOCLKDIV). This is true for all clock sources except the LFXT1. As previously described, a fault on the LFXT1 switches the source to the REFO. Since ACLK is the active clock in LPM3 there is a notable difference in the LPM3 current consumption when the REFO is the clock source (~3 mA active) versus the LFXT1 (~300 nA active).
- Modules (WDT.A) In watchdog mode, when SMCLK or ACLK fails, the clock source defaults to the VLOCLK.

## 41.2 API Functions

### Macros

- #define **CC430\_DEVICE**
- #define **NOT\_CC430\_DEVICE**

### Functions

- void **UCS\_setExternalClockSource** (uint32\_t XT1CLK\_frequency, uint32\_t XT2CLK\_frequency)  
*Sets the external clock source.*
- void **UCS\_initClockSignal** (uint8\_t selectedClockSignal, uint16\_t clockSource, uint16\_t clockSourceDivider)  
*Initializes a clock signal.*
- void **UCS\_turnOnLFXT1** (uint16\_t xt1drive, uint8\_t xcap)  
*Initializes the XT1 crystal oscillator in low frequency mode.*
- void **UCS\_turnOnHFXT1** (uint16\_t xt1drive)  
*Initializes the XT1 crystal oscillator in high frequency mode.*
- void **UCS\_bypassXT1** (uint8\_t highOrLowFrequency)  
*Bypass the XT1 crystal oscillator.*
- bool **UCS\_turnOnLFXT1WithTimeout** (uint16\_t xt1drive, uint8\_t xcap, uint16\_t timeout)  
*Initializes the XT1 crystal oscillator in low frequency mode with timeout.*
- bool **UCS\_turnOnHFXT1WithTimeout** (uint16\_t xt1drive, uint16\_t timeout)  
*Initializes the XT1 crystal oscillator in high frequency mode with timeout.*
- bool **UCS\_bypassXT1WithTimeout** (uint8\_t highOrLowFrequency, uint16\_t timeout)  
*Bypasses the XT1 crystal oscillator with time out.*
- void **UCS\_turnOffXT1** (void)  
*Stops the XT1 oscillator using the XT1OFF bit.*
- void **UCS\_turnOnXT2** (uint16\_t xt2drive)  
*Initializes the XT2 crystal oscillator.*
- void **UCS\_bypassXT2** (void)  
*Bypasses the XT2 crystal oscillator.*
- bool **UCS\_turnOnXT2WithTimeout** (uint16\_t xt2drive, uint16\_t timeout)  
*Initializes the XT2 crystal oscillator with timeout.*

- `bool UCS.bypassXT2WithTimeout` (uint16\_t timeout)  
*Bypasses the XT2 crystal oscillator with timeout.*
- `void UCS.turnOffXT2` (void)  
*Stops the XT2 oscillator using the XT2OFF bit.*
- `void UCS.initFLLSettle` (uint16\_t fsystem, uint16\_t ratio)  
*Initializes the DCO to operate a frequency that is a multiple of the reference frequency into the FLL.*
- `void UCS.initFLL` (uint16\_t fsystem, uint16\_t ratio)  
*Initializes the DCO to operate a frequency that is a multiple of the reference frequency into the FLL.*
- `void UCS.enableClockRequest` (uint8\_t selectClock)  
*Enables conditional module requests.*
- `void UCS.disableClockRequest` (uint8\_t selectClock)  
*Disables conditional module requests.*
- `uint8_t UCS.getFaultFlagStatus` (uint8\_t mask)  
*Gets the current UCS fault flag status.*
- `void UCS.clearFaultFlag` (uint8\_t mask)  
*Clears the current UCS fault flag status for the masked bit.*
- `void UCS.turnOffSMCLK` (void)  
*Turns off SMCLK using the SMCLKOFF bit.*
- `void UCS.turnOnSMCLK` (void)  
*Turns ON SMCLK using the SMCLKOFF bit.*
- `uint32_t UCS.getACLK` (void)  
*Get the current ACLK frequency.*
- `uint32_t UCS.getSMCLK` (void)  
*Get the current SMCLK frequency.*
- `uint32_t UCS.getMCLK` (void)  
*Get the current MCLK frequency.*
- `uint16_t UCS.clearAllOscFlagsWithTimeout` (uint16\_t timeout)  
*Clears all the Oscillator Flags.*

## 41.2.1 Detailed Description

The UCS API is broken into three groups of functions: those that deal with clock configuration and control

General UCS configuration and initialization is handled by

- `UCS.initClockSignal()`,
- `UCS.initFLLSettle()`,
- `UCS.enableClockRequest()`,
- `UCS.disableClockRequest()`,
- `UCS.turnOffSMCLK()`,
- `UCS.turnOnSMCLK()`

External crystal specific configuration and initialization is handled by

- `UCS.setExternalClockSource()`,
- `UCS.turnOnLFXT1()`,
- `UCS.turnOnHFXT1()`,
- `UCS.bypassXT1()`,

- [UCS\\_turnOnLFXT1WithTimeout\(\)](#),
- [UCS\\_turnOnHFXT1WithTimeout\(\)](#),
- [UCS\\_bypassXT1WithTimeout\(\)](#),
- [UCS\\_turnOffXT1\(\)](#),
- [UCS\\_turnOnXT2\(\)](#),
- [UCS\\_turnOffXT2\(\)](#),
- [UCS\\_bypassXT2\(\)](#),
- [UCS\\_turnOnXT2WithTimeout\(\)](#),
- [UCS\\_bypassXT2WithTimeout\(\)](#)
- [UCS\\_clearAllOscFlagsWithTimeout\(\)](#)

[UCS\\_setExternalClockSource](#) must be called if an external crystal XT1 or XT2 is used and the user intends to call [UCS\\_getMCLK](#), [UCS\\_getSMCLK](#) or [UCS\\_getACLK](#) APIs. If not, it is not necessary to invoke this API.

Failure to invoke [UCS\\_initClockSignal\(\)](#) sets the clock signals to the default modes ACLK default mode - [UCS\\_XT1CLK\\_SELECT](#) SMCLK default mode - [UCS\\_DCOCLKDIV\\_SELECT](#) MCLK default mode - [UCS\\_DCOCLKDIV\\_SELECT](#)

Also fail-safe mode behavior takes effect when a selected mode fails.

The status and configuration query are done by

- [UCS\\_getFaultFlagStatus\(\)](#),
- [UCS\\_clearFaultFlag\(\)](#),
- [UCS\\_getACLK\(\)](#),
- [UCS\\_getSMCLK\(\)](#),
- [UCS\\_getMCLK\(\)](#)

## 41.2.2 Macro Definition Documentation

### CC430\_DEVICE

```
#define CC430_DEVICE
```

#### Value:

```
(defined (__CC430F5133__) || defined(__CC430F5135__) || defined(__CC430F5137__) || \
 defined(__CC430F6125__) || defined(__CC430F6126__) || defined(__CC430F6127__) || \
 defined(__CC430F6135__) || defined(__CC430F6137__) || defined(__CC430F5123__) || \
 defined(__CC430F5125__) || defined(__CC430F5143__) || defined(__CC430F5145__) || \
 defined(__CC430F5147__) || defined(__CC430F6143__) || defined(__CC430F6145__) || \
 defined(__CC430F6147__))
```

### NOT\_CC430\_DEVICE

```
#define NOT_CC430_DEVICE
```

#### Value:

```
(!defined(_CC430F5133_) && !defined(_CC430F5135_) && !defined(_CC430F5137_) && \
!defined(_CC430F6125_) && !defined(_CC430F6126_) && !defined(_CC430F6127_) && \
!defined(_CC430F6135_) && !defined(_CC430F6137_) && !defined(_CC430F5123_) && \
!defined(_CC430F5125_) && !defined(_CC430F5143_) && !defined(_CC430F5145_) && \
!defined(_CC430F5147_) && !defined(_CC430F6143_) && !defined(_CC430F6145_) && \
!defined(_CC430F6147_))
```

## 41.2.3 Function Documentation

### UCS\_bypassXT1()

```
void UCS_bypassXT1 (
    uint8_t highOrLowFrequency )
```

Bypass the XT1 crystal oscillator.

Bypasses the XT1 crystal oscillator. Loops until all oscillator fault flags are cleared, with no timeout.

#### Parameters

<i>highOrLowFrequency</i>	selects high frequency or low frequency mode for XT1. Valid values are: <ul style="list-style-type: none"> <li>■ <b>UCS_XT1_HIGH_FREQUENCY</b></li> <li>■ <b>UCS_XT1_LOW_FREQUENCY</b> [Default]</li> </ul>
---------------------------	---

Modified bits of **UCSCTL7** register, bits of **UCSCTL6** register and bits of **SFRIFG** register.

#### Returns

None

### UCS\_bypassXT1WithTimeout()

```
bool UCS_bypassXT1WithTimeout (
    uint8_t highOrLowFrequency,
    uint16_t timeout )
```

Bypasses the XT1 crystal oscillator with time out.

Bypasses the XT1 crystal oscillator with time out. Loops until all oscillator fault flags are cleared or until a timeout counter is decremented and equals to zero.

#### Parameters

<i>highOrLowFrequency</i>	selects high frequency or low frequency mode for XT1. Valid values are: <ul style="list-style-type: none"> <li>■ <b>UCS_XT1_HIGH_FREQUENCY</b></li> <li>■ <b>UCS_XT1_LOW_FREQUENCY</b> [Default]</li> </ul>
<i>timeout</i>	is the count value that gets decremented every time the loop that clears oscillator fault flags gets executed.

Modified bits of **UCSCTL7** register, bits of **UCSCTL6** register and bits of **SFRIFG** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAIL

## UCS\_bypassXT2()

```
void UCS_bypassXT2 (
    void )
```

Bypasses the XT2 crystal oscillator.

Bypasses the XT2 crystal oscillator, which supports crystal frequencies between 4 MHz and 32 MHz. Loops until all oscillator fault flags are cleared, with no timeout.

Modified bits of **UCSCTL7** register, bits of **UCSCTL6** register and bits of **SFRIFG** register.

**Returns**

None

## UCS\_bypassXT2WithTimeout()

```
bool UCS_bypassXT2WithTimeout (
    uint16_t timeout )
```

Bypasses the XT2 crystal oscillator with timeout.

Bypasses the XT2 crystal oscillator, which supports crystal frequencies between 4 MHz and 32 MHz. Loops until all oscillator fault flags are cleared or until a timeout counter is decremented and equals to zero.

**Parameters**

<i>timeout</i>	is the count value that gets decremented every time the loop that clears oscillator fault flags gets executed.
----------------	--

Modified bits of **UCSCTL7** register, bits of **UCSCTL6** register and bits of **SFRIFG** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAIL

## UCS\_clearAllOscFlagsWithTimeout()

```
uint16_t UCS_clearAllOscFlagsWithTimeout (
    uint16_t timeout )
```

Clears all the Oscillator Flags.

**Parameters**

<i>timeout</i>	is the count value that gets decremented every time the loop that clears oscillator fault flags gets executed.
----------------	--

**Returns**

Logical OR of any of the following:

- **UCS\_XT2OFFG** XT2 oscillator fault flag
- **UCS\_XT1HFOFFG** XT1 oscillator fault flag (HF mode)
- **UCS\_XT1LFOFFG** XT1 oscillator fault flag (LF mode)
- **UCS\_DCOFFG** DCO fault flag  
indicating the status of the oscillator fault flags

**UCS\_clearFaultFlag()**

```
void UCS_clearFaultFlag (
    uint8_t mask )
```

Clears the current UCS fault flag status for the masked bit.

**Parameters**

<i>mask</i>	is the masked interrupt flag status to be returned. mask parameter can be any one of the following Valid values are: <ul style="list-style-type: none"> <li>■ <b>UCS_XT2OFFG</b> - XT2 oscillator fault flag</li> <li>■ <b>UCS_XT1HFOFFG</b> - XT1 oscillator fault flag (HF mode)</li> <li>■ <b>UCS_XT1LFOFFG</b> - XT1 oscillator fault flag (LF mode)</li> <li>■ <b>UCS_DCOFFG</b> - DCO fault flag</li> </ul>
-------------	---

Modified bits of **UCSCTL7** register.

**Returns**

None

**UCS\_disableClockRequest()**

```
void UCS_disableClockRequest (
    uint8_t selectClock )
```

Disables conditional module requests.

**Parameters**

<i>selectClock</i>	selects specific request disable Valid values are: <ul style="list-style-type: none"> <li>■ <b>UCS_ACLK</b></li> <li>■ <b>UCS_SMCLK</b></li> <li>■ <b>UCS_MCLK</b></li> <li>■ <b>UCS_MODOSC</b></li> </ul>
--------------------	--

Modified bits of **UCSCTL8** register.

**Returns**

None

**UCS\_enableClockRequest()**

```
void UCS_enableClockRequest (
    uint8_t selectClock )
```

Enables conditional module requests.

**Parameters**

<i>selectClock</i>	selects specific request enables Valid values are: <ul style="list-style-type: none"> <li>■ <b>UCS_ACLK</b></li> <li>■ <b>UCS_SMCLK</b></li> <li>■ <b>UCS_MCLK</b></li> <li>■ <b>UCS_MODOSC</b></li> </ul>
--------------------	--

Modified bits of **UCSCTL8** register.

**Returns**

None

**UCS\_getACLK()**

```
uint32_t UCS_getACLK (
    void )
```

Get the current ACLK frequency.

Get the current ACLK frequency. The user of this API must ensure that UCS\_setExternalClockSource API was invoked before in case XT1 or XT2 is being used.



**Returns**

Current ACLK frequency in Hz

**UCS\_getFaultFlagStatus()**

```
uint8_t UCS_getFaultFlagStatus (
    uint8_t mask )
```

Gets the current UCS fault flag status.

**Parameters**

<i>mask</i>	<p>is the masked interrupt flag status to be returned. Mask parameter can be either any of the following selection. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>UCS_XT2OFFG</b> - XT2 oscillator fault flag</li> <li>■ <b>UCS_XT1HF OFFG</b> - XT1 oscillator fault flag (HF mode)</li> <li>■ <b>UCS_XT1LFOFFG</b> - XT1 oscillator fault flag (LF mode)</li> <li>■ <b>UCS_DCOFFG</b> - DCO fault flag</li> </ul>
-------------	---

**UCS\_getMCLK()**

```
uint32_t UCS_getMCLK (
    void )
```

Get the current MCLK frequency.

Get the current MCLK frequency. The user of this API must ensure that UCS\_setExternalClockSource API was invoked before in case XT1 or XT2 is being used.

**Returns**

Current MCLK frequency in Hz

**UCS\_getSMCLK()**

```
uint32_t UCS_getSMCLK (
    void )
```

Get the current SMCLK frequency.

Get the current SMCLK frequency. The user of this API must ensure that UCS\_setExternalClockSource API was invoked before in case XT1 or XT2 is being used.

**Returns**

Current SMCLK frequency in Hz

## UCS\_initClockSignal()

```
void UCS_initClockSignal (
    uint8_t selectedClockSignal,
    uint16_t clockSource,
    uint16_t clockSourceDivider )
```

Initializes a clock signal.

This function initializes each of the clock signals. The user must ensure that this function is called for each clock signal. If not, the default state is assumed for the particular clock signal. Refer MSP430Ware documentation for UCS module or Device Family User's Guide for details of default clock signal states.

## Parameters

<i>selectedClockSignal</i>	selected clock signal Valid values are: <ul style="list-style-type: none"> <li>■ UCS_ACLK</li> <li>■ UCS_MCLK</li> <li>■ UCS_SMCLK</li> <li>■ UCS_FLLREF</li> </ul>
<i>clockSource</i>	is clock source for the selectedClockSignal Valid values are: <ul style="list-style-type: none"> <li>■ UCS_XT1CLK_SELECT</li> <li>■ UCS_VLOCLK_SELECT</li> <li>■ UCS_REFOCLK_SELECT</li> <li>■ UCS_DCOCLK_SELECT</li> <li>■ UCS_DCOCLKDIV_SELECT</li> <li>■ UCS_XT2CLK_SELECT</li> </ul>
<i>clockSourceDivider</i>	selected the clock divider to calculate clocksignal from clock source. Valid values are: <ul style="list-style-type: none"> <li>■ UCS_CLOCK_DIVIDER_1 [Default]</li> <li>■ UCS_CLOCK_DIVIDER_2</li> <li>■ UCS_CLOCK_DIVIDER_4</li> <li>■ UCS_CLOCK_DIVIDER_8</li> <li>■ UCS_CLOCK_DIVIDER_12 - [Valid only for UCS.FLLREF]</li> <li>■ UCS_CLOCK_DIVIDER_16</li> <li>■ UCS_CLOCK_DIVIDER_32 - [Not valid for UCS.FLLREF]</li> </ul>

Modified bits of **UCSCTL5** register, bits of **UCSCTL4** register and bits of **UCSCTL3** register.

**Returns**

None

**UCS\_initFLL()**

```
void UCS_initFLL (
    uint16_t fsystem,
    uint16_t ratio )
```

Initializes the DCO to operate a frequency that is a multiple of the reference frequency into the FLL.

Initializes the DCO to operate a frequency that is a multiple of the reference frequency into the FLL. Loops until all oscillator fault flags are cleared, with no timeout. If the frequency is greater than 16 MHz, the function sets the MCLK and SMCLK source to the undivided DCO frequency. Otherwise, the function sets the MCLK and SMCLK source to the DCOCLKDIV frequency. The function [PMM\\_setVCore\(\)](#) is required to call first if the target frequency is beyond current Vcore supported frequency range.

**Parameters**

<i>fsystem</i>	is the target frequency for MCLK in kHz
<i>ratio</i>	is the ratio x/y, where x = fsystem and y = FLL reference frequency.

Modified bits of **UCSCTL0** register, bits of **UCSCTL4** register, bits of **UCSCTL7** register, bits of **UCSCTL1** register, bits of **SFRIFG1** register and bits of **UCSCTL2** register.

**Returns**

None

Referenced by [UCS\\_initFLLSettle\(\)](#).

**UCS\_initFLLSettle()**

```
void UCS_initFLLSettle (
    uint16_t fsystem,
    uint16_t ratio )
```

Initializes the DCO to operate a frequency that is a multiple of the reference frequency into the FLL.

Initializes the DCO to operate a frequency that is a multiple of the reference frequency into the FLL. Loops until all oscillator fault flags are cleared, with a timeout. If the frequency is greater than 16 MHz, the function sets the MCLK and SMCLK source to the undivided DCO frequency. Otherwise, the function sets the MCLK and SMCLK source to the DCOCLKDIV frequency. This function executes a software delay that is proportional in length to the ratio of the target FLL frequency and the FLL reference. The function [PMM\\_setVCore\(\)](#) is required to call first if the target frequency is beyond current Vcore supported frequency range.

**Parameters**

<i>fsystem</i>	is the target frequency for MCLK in kHz
<i>ratio</i>	is the ratio x/y, where x = fsystem and y = FLL reference frequency.

Modified bits of **UCSCTL0** register, bits of **UCSCTL4** register, bits of **UCSCTL7** register, bits of **UCSCTL1** register, bits of **SFRIFG1** register and bits of **UCSCTL2** register.

**Returns**

None

References UCS\_initFLL().

**UCS\_setExternalClockSource()**

```
void UCS_setExternalClockSource (
    uint32_t XT1CLK_frequency,
    uint32_t XT2CLK_frequency )
```

Sets the external clock source.

This function sets the external clock sources XT1 and XT2 crystal oscillator frequency values. This function must be called if an external crystal XT1 or XT2 is used and the user intends to call UCS\_getMCLK, UCS\_getSMCLK or UCS\_getACLK APIs. If not, it is not necessary to invoke this API.

**Parameters**

<i>XT1CLK_frequency</i>	is the XT1 crystal frequencies in Hz
<i>XT2CLK_frequency</i>	is the XT2 crystal frequencies in Hz

**Returns**

None

**UCS\_turnOffSMCLK()**

```
void UCS_turnOffSMCLK (
    void )
```

Turns off SMCLK using the SMCLKOFF bit.

Modified bits of **UCSCTL6** register.

**Returns**

None

**UCS\_turnOffXT1()**

```
void UCS_turnOffXT1 (
    void )
```

Stops the XT1 oscillator using the XT1OFF bit.

**Returns**

None

**UCS\_turnOffXT2()**

```
void UCS_turnOffXT2 (
    void )
```

Stops the XT2 oscillator using the XT2OFF bit.

Modified bits of **UCSCTL6** register.

**Returns**

None

**UCS\_turnOnHFXT1()**

```
void UCS_turnOnHFXT1 (
    uint16_t xt1drive )
```

Initializes the XT1 crystal oscillator in high frequency mode.

Initializes the XT1 crystal oscillator in high frequency mode. Loops until all oscillator fault flags are cleared, with no timeout. See the device- specific data sheet for appropriate drive settings.

**Parameters**

<i>xt1drive</i>	<p>is the target drive strength for the XT1 crystal oscillator. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>UCS_XT1_DRIVE_0</b></li> <li>■ <b>UCS_XT1_DRIVE_1</b></li> <li>■ <b>UCS_XT1_DRIVE_2</b></li> <li>■ <b>UCS_XT1_DRIVE_3</b> [Default]</li> </ul>
-----------------	--

Modified bits of **UCSCTL7** register, bits of **UCSCTL6** register and bits of **SFRIFG** register.

**Returns**

None

**UCS\_turnOnHFXT1WithTimeout()**

```
bool UCS_turnOnHFXT1WithTimeout (
    uint16_t xt1drive,
    uint16_t timeout )
```

Initializes the XT1 crystal oscillator in high frequency mode with timeout.

Initializes the XT1 crystal oscillator in high frequency mode with timeout. Loops until all oscillator fault flags are cleared or until a timeout counter is decremented and equals to zero. See the device-specific data sheet for appropriate drive settings.

**Parameters**

<i>xt1drive</i>	is the target drive strength for the XT1 crystal oscillator. Valid values are: <ul style="list-style-type: none"> <li>■ <b>UCS_XT1_DRIVE_0</b></li> <li>■ <b>UCS_XT1_DRIVE_1</b></li> <li>■ <b>UCS_XT1_DRIVE_2</b></li> <li>■ <b>UCS_XT1_DRIVE_3</b> [Default]</li> </ul>
<i>timeout</i>	is the count value that gets decremented every time the loop that clears oscillator fault flags gets executed.

Modified bits of **UCSCTL7** register, bits of **UCSCTL6** register and bits of **SFRIFG** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAIL

**UCS\_turnOnLFXT1()**

```
void UCS_turnOnLFXT1 (
    uint16_t xt1drive,
    uint8_t xcap )
```

Initializes the XT1 crystal oscillator in low frequency mode.

Initializes the XT1 crystal oscillator in low frequency mode. Loops until all oscillator fault flags are cleared, with no timeout. See the device-specific data sheet for appropriate drive settings.

## Parameters

<i>xt1drive</i>	<p>is the target drive strength for the XT1 crystal oscillator. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>UCS_XT1_DRIVE_0</b></li> <li>■ <b>UCS_XT1_DRIVE_1</b></li> <li>■ <b>UCS_XT1_DRIVE_2</b></li> <li>■ <b>UCS_XT1_DRIVE_3</b> [Default]</li> </ul> <p>Modified bits are <b>XT1DRIVE</b> of <b>UCSCTL6</b> register.</p>
<i>xcap</i>	<p>is the selected capacitor value. This parameter selects the capacitors applied to the LF crystal (XT1) or resonator in the LF mode. The effective capacitance (seen by the crystal) is <math>C_{eff}</math>. (<math>C_{XIN}</math></p> <ul style="list-style-type: none"> <li>■ 2 pF)/2. It is assumed that <math>C_{XIN} = C_{XOUT}</math> and that a parasitic capacitance of 2 pF is added by the package and the printed circuit board. For details about the typical internal and the effective capacitors, refer to the device-specific data sheet. Valid values are:</li> </ul> <ul style="list-style-type: none"> <li>■ <b>UCS_XCAP_0</b></li> <li>■ <b>UCS_XCAP_1</b></li> <li>■ <b>UCS_XCAP_2</b></li> <li>■ <b>UCS_XCAP_3</b> [Default]</li> </ul>

Modified bits are **XCAP** of **UCSCTL6** register.

## Returns

None

## UCS\_turnOnLFXT1WithTimeout()

```
bool UCS_turnOnLFXT1WithTimeout (
    uint16_t xt1drive,
    uint8_t xcap,
    uint16_t timeout )
```

Initializes the XT1 crystal oscillator in low frequency mode with timeout.

Initializes the XT1 crystal oscillator in low frequency mode with timeout. Loops until all oscillator fault flags are cleared or until a timeout counter is decremented and equals to zero. See the device-specific datasheet for appropriate drive settings.

## Parameters

<i>xt1drive</i>	<p>is the target drive strength for the XT1 crystal oscillator. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>UCS_XT1_DRIVE_0</b></li> <li>■ <b>UCS_XT1_DRIVE_1</b></li> <li>■ <b>UCS_XT1_DRIVE_2</b></li> <li>■ <b>UCS_XT1_DRIVE_3</b> [Default]</li> </ul>
-----------------	--

## Parameters

<i>xcap</i>	<p>is the selected capacitor value. This parameter selects the capacitors applied to the LF crystal (XT1) or resonator in the LF mode. The effective capacitance (seen by the crystal) is <math>C_{eff}</math>. (CXIN</p> <ul style="list-style-type: none"> <li>■ 2 pF)/2. It is assumed that <math>C_{XIN} = C_{XOUT}</math> and that a parasitic capacitance of 2 pF is added by the package and the printed circuit board. For details about the typical internal and the effective capacitors, refer to the device-specific data sheet. Valid values are:</li> <li>■ <b>UCS_XCAP_0</b></li> <li>■ <b>UCS_XCAP_1</b></li> <li>■ <b>UCS_XCAP_2</b></li> <li>■ <b>UCS_XCAP_3</b> [Default]</li> </ul>
<i>timeout</i>	is the count value that gets decremented every time the loop that clears oscillator fault flags gets executed.

Modified bits of **UCSCTL7** register, bits of **UCSCTL6** register and bits of **SFRIFG** register.

## Returns

STATUS\_SUCCESS or STATUS\_FAIL

## UCS\_turnOnSMCLK()

```
void UCS_turnOnSMCLK (
    void )
```

Turns ON SMCLK using the SMCLKOFF bit.

Modified bits of **UCSCTL6** register.

## Returns

None

## UCS\_turnOnXT2()

```
void UCS_turnOnXT2 (
    uint16_t xt2drive )
```

Initializes the XT2 crystal oscillator.

Initializes the XT2 crystal oscillator, which supports crystal frequencies between 4 MHz and 32 MHz, depending on the selected drive strength. Loops until all oscillator fault flags are cleared, with no timeout. See the device-specific data sheet for appropriate drive settings.



**Parameters**

<i>xt2drive</i>	is the target drive strength for the XT2 crystal oscillator. Valid values are: <ul style="list-style-type: none"> <li>■ <b>UCS_XT2_DRIVE_4MHZ_8MHZ</b></li> <li>■ <b>UCS_XT2_DRIVE_8MHZ_16MHZ</b></li> <li>■ <b>UCS_XT2_DRIVE_16MHZ_24MHZ</b></li> <li>■ <b>UCS_XT2_DRIVE_24MHZ_32MHZ</b> [Default]</li> </ul>
-----------------	--

Modified bits of **UCSCTL7** register, bits of **UCSCTL6** register and bits of **SFRIFG** register.

**Returns**

None

**UCS\_turnOnXT2WithTimeout()**

```
bool UCS_turnOnXT2WithTimeout (
    uint16_t xt2drive,
    uint16_t timeout )
```

Initializes the XT2 crystal oscillator with timeout.

Initializes the XT2 crystal oscillator, which supports crystal frequencies between 4 MHz and 32 MHz, depending on the selected drive strength. Loops until all oscillator fault flags are cleared or until a timeout counter is decremented and equals to zero. See the device-specific data sheet for appropriate drive settings.

**Parameters**

<i>xt2drive</i>	is the target drive strength for the XT2 crystal oscillator. Valid values are: <ul style="list-style-type: none"> <li>■ <b>UCS_XT2_DRIVE_4MHZ_8MHZ</b></li> <li>■ <b>UCS_XT2_DRIVE_8MHZ_16MHZ</b></li> <li>■ <b>UCS_XT2_DRIVE_16MHZ_24MHZ</b></li> <li>■ <b>UCS_XT2_DRIVE_24MHZ_32MHZ</b> [Default]</li> </ul>
<i>timeout</i>	is the count value that gets decremented every time the loop that clears oscillator fault flags gets executed.

Modified bits of **UCSCTL7** register, bits of **UCSCTL6** register and bits of **SFRIFG** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAIL

## 41.3 Programming Example

The following example shows some UCS operations using the APIs

```
// Set DCO FLL reference = REFO
UCS_initClockSignal(UCS_BASE,
                    UCS_FLLREF,
                    UCS_REFCLK_SELECT,
                    UCS_CLOCK_DIVIDER_1
                    );

// Set ACLK = REFO
UCS_initClockSignal(UCS_BASE,
                    UCS_ACLK,
                    UCS_REFCLK_SELECT,
                    UCS_CLOCK_DIVIDER_1
                    );

// Set Ratio and Desired MCLK Frequency and initialize DCO
UCS_initFLLSettle( UCS_BASE,
                   UCS_MCLK_DESIRED_FREQUENCY_IN_KHZ,
                   UCS_MCLK_FLLREF_RATIO
                   );

//Verify if the Clock settings are as expected
clockValue = UCS_getSMCLK (UCS_BASE);

while(1);
```

## 42 USCI Universal Asynchronous Receiver/Transmitter (USCI\_A\_UART)

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### 42.1 Introduction

The MSP430Ware library for USCI\_A\_UART mode features include:

- Odd, even, or non-parity
- Independent transmit and receive shift registers
- Separate transmit and receive buffer registers
- LSB-first or MSB-first data transmit and receive
- Built-in idle-line and address-bit communication protocols for multiprocessor systems
- Receiver start-edge detection for auto wake up from LPMx modes
- Status flags for error detection and suppression
- Status flags for address detection
- Independent interrupt capability for receive and transmit

The modes of operations supported by the USCI\_A\_UART and the library include

- USCI\_A\_UART mode
- Idle-line multiprocessor mode
- Address-bit multiprocessor mode
- USCI\_A\_UART mode with automatic baud-rate detection

In USCI\_A\_UART mode, the USCI transmits and receives characters at a bit rate asynchronous to another device. Timing for each character is based on the selected baud rate of the USCI. The transmit and receive functions use the same baud-rate frequency.

### 42.2 API Functions

#### Functions

- bool [USCI\\_A\\_UART\\_init](#) (uint16\_t baseAddress, [USCI\\_A\\_UART\\_initParam](#) \*param)  
*Advanced initialization routine for the UART block. The values to be written into the clockPrescaler, firstModReg, secondModReg and overSampling parameters should be pre-computed and passed into the initialization function.*
- void [USCI\\_A\\_UART\\_transmitData](#) (uint16\_t baseAddress, uint8\_t transmitData)  
*Transmits a byte from the UART Module.*
- uint8\_t [USCI\\_A\\_UART\\_receiveData](#) (uint16\_t baseAddress)

- Receives a byte that has been sent to the UART Module.
- void [USCI\\_A\\_UART\\_enableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)
  - Enables individual UART interrupt sources.
- void [USCI\\_A\\_UART\\_disableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)
  - Disables individual UART interrupt sources.
- uint8\_t [USCI\\_A\\_UART\\_getInterruptStatus](#) (uint16\_t baseAddress, uint8\_t mask)
  - Gets the current UART interrupt status.
- void [USCI\\_A\\_UART\\_clearInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)
  - Clears UART interrupt sources.
- void [USCI\\_A\\_UART\\_enable](#) (uint16\_t baseAddress)
  - Enables the UART block.
- void [USCI\\_A\\_UART\\_disable](#) (uint16\_t baseAddress)
  - Disables the UART block.
- uint8\_t [USCI\\_A\\_UART\\_queryStatusFlags](#) (uint16\_t baseAddress, uint8\_t mask)
  - Gets the current UART status flags.
- void [USCI\\_A\\_UART\\_setDormant](#) (uint16\_t baseAddress)
  - Sets the UART module in dormant mode.
- void [USCI\\_A\\_UART\\_resetDormant](#) (uint16\_t baseAddress)
  - Re-enables UART module from dormant mode.
- void [USCI\\_A\\_UART\\_transmitAddress](#) (uint16\_t baseAddress, uint8\_t transmitAddress)
  - Transmits the next byte to be transmitted marked as address depending on selected multiprocessor mode.
- void [USCI\\_A\\_UART\\_transmitBreak](#) (uint16\_t baseAddress)
  - Transmit break.
- uint32\_t [USCI\\_A\\_UART\\_getReceiveBufferAddressForDMA](#) (uint16\_t baseAddress)
  - Returns the address of the RX Buffer of the UART for the DMA module.
- uint32\_t [USCI\\_A\\_UART\\_getTransmitBufferAddressForDMA](#) (uint16\_t baseAddress)
  - Returns the address of the TX Buffer of the UART for the DMA module.

## 42.2.1 Detailed Description

The USCI\_A\_UART API provides the set of functions required to implement an interrupt driven USCI\_A\_UART driver. The USCI\_A\_UART initialization with the various modes and features is done by the [USCI\\_A\\_UART\\_init\(\)](#). At the end of this function USCI\_A\_UART is initialized and stays disabled. [USCI\\_A\\_UART\\_enable\(\)](#) enables the USCI\_A\_UART and the module is now ready for transmit and receive. It is recommended to initialize the USCI\_A\_UART via [USCI\\_A\\_UART\\_init\(\)](#), enable the required interrupts and then enable USCI\_A\_UART via [USCI\\_A\\_UART\\_enable\(\)](#).

The USCI\_A\_UART API is broken into three groups of functions: those that deal with configuration and control of the USCI\_A\_UART modules, those used to send and receive data, and those that deal with interrupt handling and those dealing with DMA.

Configuration and control of the USCI\_A\_UART are handled by the

- [USCI\\_A\\_UART\\_init\(\)](#)
- [USCI\\_A\\_UART\\_enable\(\)](#)
- [USCI\\_A\\_UART\\_disable\(\)](#)
- [USCI\\_A\\_UART\\_setDormant\(\)](#)
- [USCI\\_A\\_UART\\_resetDormant\(\)](#)

Sending and receiving data via the USCI\_A\_UART is handled by the

- [USCI\\_A\\_UART\\_transmitData\(\)](#)
- [USCI\\_A\\_UART\\_receiveData\(\)](#)
- [USCI\\_A\\_UART\\_transmitAddress\(\)](#)
- [USCI\\_A\\_UART\\_transmitBreak\(\)](#)

Managing the USCI\_A\_UART interrupts and status are handled by the

- [USCI\\_A\\_UART\\_enableInterrupt\(\)](#)
- [USCI\\_A\\_UART\\_disableInterrupt\(\)](#)
- [USCI\\_A\\_UART\\_getInterruptStatus\(\)](#)
- [USCI\\_A\\_UART\\_clearInterrupt\(\)](#)
- [USCI\\_A\\_UART\\_queryStatusFlags\(\)](#)

DMA related

- [USCI\\_A\\_UART\\_getReceiveBufferAddressForDMA\(\)](#)
- [USCI\\_A\\_UART\\_getTransmitBufferAddressForDMA\(\)](#)

## 42.2.2 Function Documentation

### USCI\_A\_UART\_clearInterrupt()

```
void USCI_A_UART_clearInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Clears UART interrupt sources.

The UART interrupt source is cleared, so that it no longer asserts. The highest interrupt flag is automatically cleared when an interrupt vector generator is used.

#### Parameters

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
<i>mask</i>	is a bit mask of the interrupt sources to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>USCI_A_UART_RECEIVE_INTERRUPT_FLAG</b> - Receive interrupt flag</li> <li>■ <b>USCI_A_UART_TRANSMIT_INTERRUPT_FLAG</b> - Transmit interrupt flag</li> </ul>

Modified bits of **UCAxIFG** register.

**Returns**

None

**USCI\_A\_UART\_disable()**

```
void USCI_A_UART_disable (
    uint16_t baseAddress )
```

Disables the UART block.

This will disable operation of the UART block.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
--------------------	--

Modified bits are **UCSWRST** of **UCAxCTL1** register.**Returns**

None

**USCI\_A\_UART\_disableInterrupt()**

```
void USCI_A_UART_disableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Disables individual UART interrupt sources.

Disables the indicated UART interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
<i>mask</i>	is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>USCI_A_UART_RECEIVE_INTERRUPT</b> - Receive interrupt</li> <li>■ <b>USCI_A_UART_TRANSMIT_INTERRUPT</b> - Transmit interrupt</li> <li>■ <b>USCI_A_UART_RECEIVE_ERRONEOUSCHAR_INTERRUPT</b> - Receive erroneous-character interrupt enable</li> <li>■ <b>USCI_A_UART_BREAKCHAR_INTERRUPT</b> - Receive break character interrupt enable</li> </ul>

Modified bits of **UCAxCTL1** register and bits of **UCAxIE** register.

**Returns**

None

**USCI\_A\_UART\_enable()**

```
void USCI_A_UART_enable (
    uint16_t baseAddress )
```

Enables the UART block.

This will enable operation of the UART block.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
--------------------	--

Modified bits are **UCSWRST** of **UCAxCTL1** register.**Returns**

None

**USCI\_A\_UART\_enableInterrupt()**

```
void USCI_A_UART_enableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Enables individual UART interrupt sources.

Enables the indicated UART interrupt sources. The interrupt flag is first and then the corresponding interrupt is enabled. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
<i>mask</i>	is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>USCI_A_UART_RECEIVE_INTERRUPT</b> - Receive interrupt</li> <li>■ <b>USCI_A_UART_TRANSMIT_INTERRUPT</b> - Transmit interrupt</li> <li>■ <b>USCI_A_UART_RECEIVE_ERRONEOUSCHAR_INTERRUPT</b> - Receive erroneous-character interrupt enable</li> <li>■ <b>USCI_A_UART_BREAKCHAR_INTERRUPT</b> - Receive break character interrupt enable</li> </ul>

Modified bits of **UCAxCTL1** register and bits of **UCAxIE** register.

**Returns**

None

**USCI\_A\_UART\_getInterruptStatus()**

```
uint8_t USCI_A_UART_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t mask )
```

Gets the current UART interrupt status.

This returns the interrupt status for the UART module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>USCI_A_UART_RECEIVE_INTERRUPT_FLAG</b> - Receive interrupt flag</li> <li>■ <b>USCI_A_UART_TRANSMIT_INTERRUPT_FLAG</b> - Transmit interrupt flag</li> </ul>

Modified bits of **UCAxIFG** register.

**Returns**

Logical OR of any of the following:

- **USCI\_A\_UART\_RECEIVE\_INTERRUPT\_FLAG** Receive interrupt flag
  - **USCI\_A\_UART\_TRANSMIT\_INTERRUPT\_FLAG** Transmit interrupt flag
- indicating the status of the masked flags

**USCI\_A\_UART\_getReceiveBufferAddressForDMA()**

```
uint32_t USCI_A_UART_getReceiveBufferAddressForDMA (
    uint16_t baseAddress )
```

Returns the address of the RX Buffer of the UART for the DMA module.

Returns the address of the UART RX Buffer. This can be used in conjunction with the DMA to store the received data directly to memory.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
--------------------	--



**Returns**

Address of RX Buffer

**USCI\_A\_UART\_getTransmitBufferAddressForDMA()**

```
uint32_t USCI_A_UART_getTransmitBufferAddressForDMA (
    uint16_t baseAddress )
```

Returns the address of the TX Buffer of the UART for the DMA module.

Returns the address of the UART TX Buffer. This can be used in conjunction with the DMA to obtain transmitted data directly from memory.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
--------------------	--

**Returns**

Address of TX Buffer

**USCI\_A\_UART\_init()**

```
bool USCI_A_UART_init (
    uint16_t baseAddress,
    USCI_A_UART_initParam * param )
```

Advanced initialization routine for the UART block. The values to be written into the clockPrescalar, firstModReg, secondModReg and overSampling parameters should be pre-computed and passed into the initialization function.

Upon successful initialization of the UART block, this function will have initialized the module, but the UART block still remains disabled and must be enabled with [USCI\\_A\\_UART\\_enable\(\)](#). To calculate values for clockPrescalar, firstModReg, secondModReg and overSampling please use the link below.

[http://software-dl.ti.com/msp430/msp430\\_public\\_sw/mcu/msp430/MSP430BaudRateConverter/index.html](http://software-dl.ti.com/msp430/msp430_public_sw/mcu/msp430/MSP430BaudRateConverter/index.html)

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
<i>param</i>	is the pointer to struct for initialization.

Modified bits are **UCPEN**, **UCPAR**, **UCMSB**, **UC7BIT**, **UCSPB**, **UCMODEx** and **UCSYNC** of **UCAxCTL0** register; bits **UCSSELx** and **UCSWRST** of **UCAxCTL1** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAIL of the initialization process

References USCI\_A\_UART\_initParam::clockPrescalar, USCI\_A\_UART\_initParam::firstModReg, USCI\_A\_UART\_initParam::msborLsbFirst, USCI\_A\_UART\_initParam::numberOfStopBits, USCI\_A\_UART\_initParam::overSampling, USCI\_A\_UART\_initParam::parity, USCI\_A\_UART\_initParam::secondModReg, USCI\_A\_UART\_initParam::selectClockSource, and USCI\_A\_UART\_initParam::uartMode.

**USCI\_A\_UART\_queryStatusFlags()**

```
uint8_t USCI_A_UART_queryStatusFlags (
    uint16_t baseAddress,
    uint8_t mask )
```

Gets the current UART status flags.

This returns the status for the UART module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ USCI_A_UART_LISTEN_ENABLE</li> <li>■ USCI_A_UART_FRAMING_ERROR</li> <li>■ USCI_A_UART_OVERRUN_ERROR</li> <li>■ USCI_A_UART_PARITY_ERROR</li> <li>■ USCI_A_UART_BREAK_DETECT</li> <li>■ USCI_A_UART_RECEIVE_ERROR</li> <li>■ USCI_A_UART_ADDRESS_RECEIVED</li> <li>■ USCI_A_UART_IDLELINE</li> <li>■ USCI_A_UART_BUSY</li> </ul>

Modified bits of **UCAxSTAT** register.

**Returns**

Logical OR of any of the following:

- USCI\_A\_UART\_LISTEN\_ENABLE
- USCI\_A\_UART\_FRAMING\_ERROR
- USCI\_A\_UART\_OVERRUN\_ERROR
- USCI\_A\_UART\_PARITY\_ERROR
- USCI\_A\_UART\_BREAK\_DETECT
- USCI\_A\_UART\_RECEIVE\_ERROR
- USCI\_A\_UART\_ADDRESS\_RECEIVED
- USCI\_A\_UART\_IDLELINE

**■ USCI\_A\_UART\_BUSY**

indicating the status of the masked interrupt flags

**USCI\_A\_UART\_receiveData()**

```
uint8_t USCI_A_UART_receiveData (
    uint16_t baseAddress )
```

Receives a byte that has been sent to the UART Module.

This function reads a byte of data from the UART receive data Register.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
--------------------	--

Modified bits of **UCAxRXBUF** register.

**Returns**

Returns the byte received from by the UART module, cast as an uint8.t.

**USCI\_A\_UART\_resetDormant()**

```
void USCI_A_UART_resetDormant (
    uint16_t baseAddress )
```

Re-enables UART module from dormant mode.

Not dormant. All received characters set UCRXIFG.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
--------------------	--

Modified bits are **UCDORM** of **UCAxCTL1** register.

**Returns**

None

**USCI\_A\_UART\_setDormant()**

```
void USCI_A_UART_setDormant (
    uint16_t baseAddress )
```

Sets the UART module in dormant mode.

Puts USCI in sleep mode. Only characters that are preceded by an idle-line or with address bit set UCRXIFG. In UART mode with automatic baud-rate detection, only the combination of a break and sync field sets UCRXIFG.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
--------------------	--

Modified bits of **UCAxCTL1** register.

**Returns**

None

**USCI\_A\_UART\_transmitAddress()**

```
void USCI_A_UART_transmitAddress (
    uint16_t baseAddress,
    uint8_t transmitAddress )
```

Transmits the next byte to be transmitted marked as address depending on selected multiprocessor mode.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
<i>transmitAddress</i>	is the next byte to be transmitted

Modified bits of **UCAxTXBUF** register and bits of **UCAxCTL1** register.

**Returns**

None

**USCI\_A\_UART\_transmitBreak()**

```
void USCI_A_UART_transmitBreak (
    uint16_t baseAddress )
```

Transmit break.

Transmits a break with the next write to the transmit buffer. In UART mode with automatic baud-rate detection, USCI\_A\_UART\_AUTOMATICBAUDRATE\_SYNC(0x55) must be written into UCAxTXBUF to generate the required break/sync fields. Otherwise, DEFAULT\_SYNC(0x00) must be written into the transmit buffer. Also ensures module is ready for transmitting the next data.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
--------------------	--

Modified bits of **UCAxTXBUF** register and bits of **UCAxCTL1** register.

**Returns**

None

**USCI\_A\_UART\_transmitData()**

```
void USCI_A_UART_transmitData (
    uint16_t baseAddress,
    uint8_t transmitData )
```

Transmits a byte from the UART Module.

This function will place the supplied data into UART transmit data register to start transmission

**Parameters**

<i>baseAddress</i>	is the base address of the USCI_A_UART module.
<i>transmitData</i>	data to be transmitted from the UART module

Modified bits of **UCAxTXBUF** register.

**Returns**

None

## 42.3 Programming Example

The following example shows how to use the USCI\_A\_UART API to initialize the USCI\_A\_UART, transmit characters, and receive characters.

```
if ( STATUS_FAIL == USCI_A_UART_init (USCI_A0_BASE,
    USCI_A_UART_CLOCKSOURCE_SMCLK,
    UCS_getSMCLK (UCS_BASE),
    BAUD_RATE,
    USCI_A_UART_NO_PARITY,
    USCI_A_UART_LSB_FIRST,
    USCI_A_UART_ONE_STOP_BIT,
    USCI_A_UART_MODE,
    USCI_A_UART_OVERSAMPLING_BAUDRATE_GENERATION ) )
{
    return;
}

//Enable USCI_A_UART module for operation
USCI_A_UART_enable (USCI_A0_BASE);

//Enable Receive Interrupt
USCI_A_UART_enableInterrupt (USCI_A0_BASE,
    UCRXIE);

//Transmit data
USCI_A_UART_transmitData (USCI_A0_BASE,
    transmitData++
    );

// Enter LPM3, interrupts enabled
__bis_SR_register (LPM3_bits + GIE);
__no_operation();
}
```

```

//*****
//
// This is the USCI_A0 interrupt vector service routine.
//
//*****
#pragma vector=USCI_A0_VECTOR
__interrupt void USCI_A0_ISR(void)
{
    switch(__even_in_range(UCA0IV,4))
    {
        // Vector 2 - RXIFG
        case 2:
            // Echo back RXed character, confirm TX buffer is ready first

            // USCI_A0 TX buffer ready?
            while (!USCI_A_UART_interruptStatus(USCI_A0_BASE,
                UCTXIFG)
                );

            //Receive echoed data
            receivedData = USCI_A_UART_receiveData(USCI_A0_BASE);

            //Transmit next data
            USCI_A_UART_transmitData(USCI_A0_BASE,
                transmitData++
                );

            break;
        default: break;
    }
}

```

## 43 USCI Synchronous Peripheral Interface (USCI\_A\_SPI)

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### 43.1 Introduction

The Serial Peripheral Interface Bus or USCI\_A\_SPI bus is a synchronous serial data link standard named by Motorola that operates in full duplex mode. Devices communicate in master/slave mode where the master device initiates the data frame.

This library provides the API for handling a 3-wire USCI\_A\_SPI communication

The USCI\_A\_SPI module can be configured as either a master or a slave device.

The USCI\_A\_SPI module also includes a programmable bit rate clock divider and prescaler to generate the output serial clock derived from the SSI module's input clock.

### 43.2 API Functions

#### Functions

- bool [USCI\\_A\\_SPI\\_initMaster](#) (uint16\_t baseAddress, [USCI\\_A\\_SPI\\_initMasterParam](#) \*param)  
*Initializes the SPI Master block.*
- void [USCI\\_A\\_SPI\\_changeMasterClock](#) (uint16\_t baseAddress, [USCI\\_A\\_SPI\\_changeMasterClockParam](#) \*param)  
*Initializes the SPI Master clock. At the end of this function call, SPI module is left enabled.*
- bool [USCI\\_A\\_SPI\\_initSlave](#) (uint16\_t baseAddress, uint8\_t msbFirst, uint8\_t clockPhase, uint8\_t clockPolarity)  
*Initializes the SPI Slave block.*
- void [USCI\\_A\\_SPI\\_changeClockPhasePolarity](#) (uint16\_t baseAddress, uint8\_t clockPhase, uint8\_t clockPolarity)  
*Changes the SPI clock phase and polarity. At the end of this function call, SPI module is left enabled.*
- void [USCI\\_A\\_SPI\\_transmitData](#) (uint16\_t baseAddress, uint8\_t transmitData)  
*Transmits a byte from the SPI Module.*
- uint8\_t [USCI\\_A\\_SPI\\_receiveData](#) (uint16\_t baseAddress)  
*Receives a byte that has been sent to the SPI Module.*
- void [USCI\\_A\\_SPI\\_enableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Enables individual SPI interrupt sources.*
- void [USCI\\_A\\_SPI\\_disableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Disables individual SPI interrupt sources.*
- uint8\_t [USCI\\_A\\_SPI\\_getInterruptStatus](#) (uint16\_t baseAddress, uint8\_t mask)  
*Gets the current SPI interrupt status.*
- void [USCI\\_A\\_SPI\\_clearInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Clears the selected SPI interrupt status flag.*

- void `USCI_A_SPI_enable` (uint16\_t baseAddress)  
*Enables the SPI block.*
- void `USCI_A_SPI_disable` (uint16\_t baseAddress)  
*Disables the SPI block.*
- uint32\_t `USCI_A_SPI_getReceiveBufferAddressForDMA` (uint16\_t baseAddress)  
*Returns the address of the RX Buffer of the SPI for the DMA module.*
- uint32\_t `USCI_A_SPI_getTransmitBufferAddressForDMA` (uint16\_t baseAddress)  
*Returns the address of the TX Buffer of the SPI for the DMA module.*
- uint8\_t `USCI_A_SPI_isBusy` (uint16\_t baseAddress)  
*Indicates whether or not the SPI bus is busy.*

### 43.2.1 Detailed Description

To use the module as a master, the user must call `USCI_A_SPI_initMaster()` to configure the USCI\_A\_SPI Master. This is followed by enabling the USCI\_A\_SPI module using `USCI_A_SPI_enable()`. The interrupts are then enabled (if needed). It is recommended to enable the USCI\_A\_SPI module before enabling the interrupts. A data transmit is then initiated using `USCI_A_SPI_transmitData()` and then when the receive flag is set, the received data is read using `USCI_A_SPI_receiveData()` and this indicates that an RX/TX operation is complete.

To use the module as a slave, initialization is done using `USCI_A_SPI_initSlave()` and this is followed by enabling the module using `USCI_A_SPI_enable()`. Following this, the interrupts may be enabled as needed. When the receive flag is set, data is first transmitted using `USCI_A_SPI_transmitData()` and this is followed by a data reception by `USCI_A_SPI_receiveData()`

The USCI\_A\_SPI API is broken into 3 groups of functions: those that deal with status and initialization, those that handle data, and those that manage interrupts.

The status and initialization of the USCI\_A\_SPI module are managed by

- `USCI_A_SPI_initMaster()`
- `USCI_A_SPI_initSlave()`
- `USCI_A_SPI_disable()`
- `USCI_A_SPI_enable()`
- `USCI_A_SPI_masterChangeClock()`
- `USCI_A_SPI_isBusy()`

Data handling is done by

- `USCI_A_SPI_transmitData()`
- `USCI_A_SPI_receiveData()`

Interrupts from the USCI\_A\_SPI module are managed using

- `USCI_A_SPI_disableInterrupt()`
- `USCI_A_SPI_enableInterrupt()`
- `USCI_A_SPI_getInterruptStatus()`
- `USCI_A_SPI_clearInterrupt()`

DMA related

- `USCI_A_SPI_getReceiveBufferAddressForDMA()`
- `USCI_A_SPI_getTransmitBufferAddressForDMA()`



## 43.2.2 Function Documentation

### USCI\_A\_SPI.changeClockPhasePolarity()

```
void USCI_A_SPI_changeClockPhasePolarity (
    uint16_t baseAddress,
    uint8_t clockPhase,
    uint8_t clockPolarity )
```

Changes the SPI clock phase and polarity. At the end of this function call, SPI module is left enabled.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>clockPhase</i>	is clock phase select. Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCI_A_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT_EXT</b> [Default]</li> <li>■ <b>USCI_A_SPI_PHASE_DATA_CAPTURED_ONFIRST_CHANGED_ON_NEXT_EXT</b></li> </ul>
<i>clockPolarity</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCI_A_SPI_CLOCKPOLARITY_INACTIVITY_HIGH</b></li> <li>■ <b>USCI_A_SPI_CLOCKPOLARITY_INACTIVITY_LOW</b> [Default]</li> </ul>

Modified bits are **UCCKPL** and **UCCKPH** of **UCAxCTL0** register.

#### Returns

None

### USCI\_A\_SPI.changeMasterClock()

```
void USCI_A_SPI_changeMasterClock (
    uint16_t baseAddress,
    USCI_A_SPI_changeMasterClockParam * param )
```

Initializes the SPI Master clock. At the end of this function call, SPI module is left enabled.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>param</i>	is the pointer to struct for master clock setting.

Modified bits of **UCAxBRW** register.

**Returns**

None

References USCI\_A\_SPI.changeMasterClockParam::clockSourceFrequency, and USCI\_A\_SPI.changeMasterClockParam::desiredSpiClock.

**USCI\_A\_SPI\_clearInterrupt()**

```
void USCI_A_SPI_clearInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Clears the selected SPI interrupt status flag.

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
<i>mask</i>	is the masked interrupt flag to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>USCI_A_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>USCI_A_SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIFG** register.

**Returns**

None

**USCI\_A\_SPI\_disable()**

```
void USCI_A_SPI_disable (
    uint16_t baseAddress )
```

Disables the SPI block.

This will disable operation of the SPI block.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI SPI module.
--------------------	---

Modified bits are **UCSWRST** of **UCAxCTL1** register.

**Returns**

None

**USCI\_A\_SPI\_disableInterrupt()**

```
void USCI_A_SPI_disableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Disables individual SPI interrupt sources.

Disables the indicated SPI interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
<i>mask</i>	is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>USCI_A_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>USCI_A_SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIE** register.

**Returns**

None

**USCI\_A\_SPI\_enable()**

```
void USCI_A_SPI_enable (
    uint16_t baseAddress )
```

Enables the SPI block.

This will enable operation of the SPI block.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI SPI module.
--------------------	---

Modified bits are **UCSWRST** of **UCAxCTL1** register.

**Returns**

None

**USCI\_A\_SPI\_enableInterrupt()**

```
void USCI_A_SPI_enableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Enables individual SPI interrupt sources.

Enables the indicated SPI interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
<i>mask</i>	is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>USCI_A_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>USCI_A_SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIE** register.

**Returns**

None

**USCI\_A\_SPI\_getInterruptStatus()**

```
uint8_t USCI_A_SPI_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t mask )
```

Gets the current SPI interrupt status.

This returns the interrupt status for the SPI module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>USCI_A_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>USCI_A_SPI_RECEIVE_INTERRUPT</b></li> </ul>

**Returns**

The current interrupt status as the mask of the set flags Return Logical OR of any of the following:

- **USCI\_A\_SPI\_TRANSMIT\_INTERRUPT**
  - **USCI\_A\_SPI\_RECEIVE\_INTERRUPT**
- indicating the status of the masked interrupts

**USCI\_A\_SPI\_getReceiveBufferAddressForDMA()**

```
uint32_t USCI_A_SPI_getReceiveBufferAddressForDMA (
    uint16_t baseAddress )
```

Returns the address of the RX Buffer of the SPI for the DMA module.

Returns the address of the SPI RX Buffer. This can be used in conjunction with the DMA to store the received data directly to memory.

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
--------------------	--

**Returns**

the address of the RX Buffer

**USCI\_A\_SPI\_getTransmitBufferAddressForDMA()**

```
uint32_t USCI_A_SPI_getTransmitBufferAddressForDMA (
    uint16_t baseAddress )
```

Returns the address of the TX Buffer of the SPI for the DMA module.

Returns the address of the SPI TX Buffer. This can be used in conjunction with the DMA to obtain transmitted data directly from memory.

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
--------------------	--

**Returns**

the address of the TX Buffer

**USCI\_A\_SPI\_initMaster()**

```
bool USCI_A_SPI_initMaster (
    uint16_t baseAddress,
    USCI_A_SPI_initMasterParam * param )
```

Initializes the SPI Master block.

Upon successful initialization of the SPI master block, this function will have set the bus speed for the master, but the SPI Master block still remains disabled and must be enabled with [USCLA\\_SPI\\_enable\(\)](#)

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>param</i>	is the pointer to struct for master initialization.

Modified bits are **UCCKPH**, **UCCKPL**, **UC7BIT** and **UCMSB** of **UCAxCTL0** register; bits **UCSSELx** and **UCSWRST** of **UCAxCTL1** register.

#### Returns

STATUS\_SUCCESS

References `USCLA_SPI_initMasterParam::clockPhase`, `USCLA_SPI_initMasterParam::clockPolarity`, `USCLA_SPI_initMasterParam::clockSourceFrequency`, `USCLA_SPI_initMasterParam::desiredSpiClock`, `USCLA_SPI_initMasterParam::msbFirst`, and `USCLA_SPI_initMasterParam::selectClockSource`.

### USCLA\_SPI\_initSlave()

```
bool USCLA_SPI_initSlave (
    uint16_t baseAddress,
    uint8_t msbFirst,
    uint8_t clockPhase,
    uint8_t clockPolarity )
```

Initializes the SPI Slave block.

Upon successful initialization of the SPI slave block, this function will have initialized the slave block, but the SPI Slave block still remains disabled and must be enabled with [USCLA\\_SPI\\_enable\(\)](#)

#### Parameters

<i>baseAddress</i>	is the base address of the SPI Slave module.
<i>msbFirst</i>	controls the direction of the receive and transmit shift register. Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCLA_SPI_MSB_FIRST</b></li> <li>■ <b>USCLA_SPI_LSB_FIRST</b> [Default]</li> </ul>
<i>clockPhase</i>	is clock phase select. Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCLA_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT_EXT</b> [Default]</li> <li>■ <b>USCLA_SPI_PHASE_DATA_CAPTURED_ONFIRST_CHANGED_ON_NEXT_EXT</b></li> </ul>

**Parameters**

<i>clockPolarity</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCI_A_SPI_CLOCKPOLARITY_INACTIVITY_HIGH</b></li> <li>■ <b>USCI_A_SPI_CLOCKPOLARITY_INACTIVITY_LOW</b> [Default]</li> </ul>
----------------------	---

Modified bits are **UCMSB**, **UCMST**, **UC7BIT**, **UCCKPL**, **UCCKPH** and **UCMODE** of **UCAxCTL0** register; bits **UCSWRST** of **UCAxCTL1** register.

**Returns**

STATUS\_SUCCESS

**USCI\_A\_SPI\_isBusy()**

```
uint8_t USCI_A_SPI_isBusy (
    uint16_t baseAddress )
```

Indicates whether or not the SPI bus is busy.

This function returns an indication of whether or not the SPI bus is busy. This function checks the status of the bus via UCBBUSY bit

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
--------------------	--

**Returns**

USCI\_A\_SPI\_BUSY if the SPI module transmitting or receiving is busy; otherwise, returns USCI\_A\_SPI\_NOT\_BUSY. Return one of the following:

- **USCI\_A\_SPI\_BUSY**
- **USCI\_A\_SPI\_NOT\_BUSY**  
indicating if the USCI\_A\_SPI is busy

**USCI\_A\_SPI\_receiveData()**

```
uint8_t USCI_A_SPI_receiveData (
    uint16_t baseAddress )
```

Receives a byte that has been sent to the SPI Module.

This function reads a byte of data from the SPI receive data Register.

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
--------------------	--

**Returns**

Returns the byte received from by the SPI module, cast as an uint8\_t.

**USCIA\_SPI\_transmitData()**

```
void USCIA_SPI_transmitData (
    uint16_t baseAddress,
    uint8_t transmitData )
```

Transmits a byte from the SPI Module.

This function will place the supplied data into SPI transmit data register to start transmission

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
<i>transmitData</i>	data to be transmitted from the SPI module

**Returns**

None

## 43.3 Programming Example

The following example shows how to use the USCIA\_SPI API to configure the USCIA\_SPI module as a master device, and how to do a simple send of data.

```
//Initialize Master
USCIA_SPI_initMasterParam param = {0};
param.selectClockSource = USCIA_SPI_CLOCKSOURCE_SMCLK;
param.clockSourceFrequency = UCS_getSMCLK();
param.desiredSpiClock = SPICLK;
param.msbFirst = USCIA_SPI_MSB_FIRST;
param.clockPhase = USCIA_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT;
param.clockPolarity = USCIA_SPI_CLOCKPOLARITY_INACTIVITY_HIGH;
returnValue = USCIA_SPI_initMaster(USCIA0_BASE, &param);

if (STATUS_FAIL == returnValue){
    return;
}

//Enable USCIA_SPI module
USCIA_SPI_enable(USCIA0_BASE);

//Enable Receive interrupt
USCIA_SPI_enableInterrupt(USCIA0_BASE, UCRXIE);

//Configure port pins to reset slave

// Wait for slave to initialize
_delay_cycles(100);

// Initialize data values
transmitData = 0x00;

// USCIA0 TX buffer ready?
while (!USCIA_SPI_interruptStatus(USCIA0_BASE, UCTXIFG));

//Transmit Data to slave
USCIA_SPI_transmitData(USCIA0_BASE, transmitData);
```



```
    // CPU off, enable interrupts
    _bis_SR_register(LPM0_bits + GIE);
}

//*****
//
// This is the USCI_B0 interrupt vector service routine.
//
//*****
#pragma vector=USCIA0_VECTOR
__interrupt void USCIA0_ISR(void)
{
    switch(__even_in_range(UCA0IV,4))
    {
        // Vector 2 - RXIFG
        case 2:
            // USCIA0 TX buffer ready?
            while (!USCIA_SPI_interruptStatus(USCIA0_BASE, UCTXIFG));

            receiveData = USCIA_SPI_receiveData(USCIA0_BASE);

            // Increment data
            transmitData++;

            // Send next value
            USCIA_SPI_transmitData(USCIA0_BASE, transmitData);

            //Delay between transmissions for slave to process information
            _delay_cycles(40);

            break;
        default: break;
    }
}
```

# 44 USCI Synchronous Peripheral Interface (USCI\_B\_SPI)

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## 44.1 Introduction

The Serial Peripheral Interface Bus or USCI\_B\_SPI bus is a synchronous serial data link standard named by Motorola that operates in full duplex mode. Devices communicate in master/slave mode where the master device initiates the data frame.

This library provides the API for handling a 3-wire USCI\_B\_SPI communication

The USCI\_B\_SPI module can be configured as either a master or a slave device.

The USCI\_B\_SPI module also includes a programmable bit rate clock divider and prescaler to generate the output serial clock derived from the SSI module's input clock.

## 44.2 API Functions

### Functions

- bool [USCI\\_B\\_SPI\\_initMaster](#) (uint16\_t baseAddress, [USCI\\_B\\_SPI\\_initMasterParam](#) \*param)  
*Initializes the SPI Master block.*
- void [USCI\\_B\\_SPI\\_changeMasterClock](#) (uint16\_t baseAddress, [USCI\\_B\\_SPI\\_changeMasterClockParam](#) \*param)  
*Initializes the SPI Master clock. At the end of this function call, SPI module is left enabled.*
- bool [USCI\\_B\\_SPI\\_initSlave](#) (uint16\_t baseAddress, uint8\_t msbFirst, uint8\_t clockPhase, uint8\_t clockPolarity)  
*Initializes the SPI Slave block.*
- void [USCI\\_B\\_SPI\\_changeClockPhasePolarity](#) (uint16\_t baseAddress, uint8\_t clockPhase, uint8\_t clockPolarity)  
*Changes the SPI clock phase and polarity. At the end of this function call, SPI module is left enabled.*
- void [USCI\\_B\\_SPI\\_transmitData](#) (uint16\_t baseAddress, uint8\_t transmitData)  
*Transmits a byte from the SPI Module.*
- uint8\_t [USCI\\_B\\_SPI\\_receiveData](#) (uint16\_t baseAddress)  
*Receives a byte that has been sent to the SPI Module.*
- void [USCI\\_B\\_SPI\\_enableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Enables individual SPI interrupt sources.*
- void [USCI\\_B\\_SPI\\_disableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Disables individual SPI interrupt sources.*
- uint8\_t [USCI\\_B\\_SPI\\_getInterruptStatus](#) (uint16\_t baseAddress, uint8\_t mask)  
*Gets the current SPI interrupt status.*
- void [USCI\\_B\\_SPI\\_clearInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Clears the selected SPI interrupt status flag.*

- void `USCI_B_SPI_enable` (uint16\_t baseAddress)  
*Enables the SPI block.*
- void `USCI_B_SPI_disable` (uint16\_t baseAddress)  
*Disables the SPI block.*
- uint32\_t `USCI_B_SPI_getReceiveBufferAddressForDMA` (uint16\_t baseAddress)  
*Returns the address of the RX Buffer of the SPI for the DMA module.*
- uint32\_t `USCI_B_SPI_getTransmitBufferAddressForDMA` (uint16\_t baseAddress)  
*Returns the address of the TX Buffer of the SPI for the DMA module.*
- uint8\_t `USCI_B_SPI_isBusy` (uint16\_t baseAddress)  
*Indicates whether or not the SPI bus is busy.*

## 44.2.1 Detailed Description

To use the module as a master, the user must call `USCI_B_SPI_initMaster()` to configure the USCI\_B\_SPI Master. This is followed by enabling the USCI\_B\_SPI module using `USCI_B_SPI_enable()`. The interrupts are then enabled (if needed). It is recommended to enable the USCI\_B\_SPI module before enabling the interrupts. A data transmit is then initiated using `USCI_B_SPI_transmitData()` and then when the receive flag is set, the received data is read using `USCI_B_SPI_receiveData()` and this indicates that an RX/TX operation is complete.

To use the module as a slave, initialization is done using `USCI_B_SPI_initSlave()` and this is followed by enabling the module using `USCI_B_SPI_enable()`. Following this, the interrupts may be enabled as needed. When the receive flag is set, data is first transmitted using `USCI_B_SPI_transmitData()` and this is followed by a data reception by `USCI_B_SPI_receiveData()`

The USCI\_B\_SPI API is broken into 3 groups of functions: those that deal with status and initialization, those that handle data, and those that manage interrupts.

The status and initialization of the USCI\_B\_SPI module are managed by

- `USCI_B_SPI_initMaster()`
- `USCI_B_SPI_initSlave()`
- `USCI_B_SPI_disable()`
- `USCI_B_SPI_enable()`
- `USCI_B_SPI_masterChangeClock()`
- `USCI_B_SPI_isBusy()`

Data handling is done by

- `USCI_B_SPI_transmitData()`
- `USCI_B_SPI_receiveData()`

Interrupts from the USCI\_B\_SPI module are managed using

- `USCI_B_SPI_disableInterrupt()`
- `USCI_B_SPI_enableInterrupt()`
- `USCI_B_SPI_getInterruptStatus()`
- `USCI_B_SPI_clearInterrupt()`

DMA related

- `USCI_B_SPI_getReceiveBufferAddressForDMA()`
- `USCI_B_SPI_getTransmitBufferAddressForDMA()`

## 44.2.2 Function Documentation

### USCI.B.SPI.changeClockPhasePolarity()

```
void USCI.B.SPI.changeClockPhasePolarity (
    uint16_t baseAddress,
    uint8_t clockPhase,
    uint8_t clockPolarity )
```

Changes the SPI clock phase and polarity. At the end of this function call, SPI module is left enabled.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>clockPhase</i>	is clock phase select. Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCI.B.SPI.PHASE.DATA.CHANGED.ON.FIRST.CAPTURED.ON.NEXT</b> [Default]</li> <li>■ <b>USCI.B.SPI.PHASE.DATA.CAPTURED.ON.FIRST.CHANGED.ON.NEXT</b></li> </ul>
<i>clockPolarity</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCI.B.SPI.CLOCKPOLARITY.INACTIVITY.HIGH</b></li> <li>■ <b>USCI.B.SPI.CLOCKPOLARITY.INACTIVITY.LOW</b> [Default]</li> </ul>

Modified bits are **UCCKPL** and **UCCKPH** of **UCAxCTL0** register.

#### Returns

None

### USCI.B.SPI.changeMasterClock()

```
void USCI.B.SPI.changeMasterClock (
    uint16_t baseAddress,
    USCI.B.SPI.changeMasterClockParam * param )
```

Initializes the SPI Master clock. At the end of this function call, SPI module is left enabled.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>param</i>	is the pointer to struct for master clock setting.

Modified bits of **UCAxBRW** register.

**Returns**

None

References USCI\_B\_SPI.changeMasterClockParam::clockSourceFrequency, and USCI\_B\_SPI.changeMasterClockParam::desiredSpiClock.

**USCI\_B\_SPI\_clearInterrupt()**

```
void USCI_B_SPI_clearInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Clears the selected SPI interrupt status flag.

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
<i>mask</i>	is the masked interrupt flag to be cleared. Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCI_B_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>USCI_B_SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCBxIFG** register.

**Returns**

None

**USCI\_B\_SPI\_disable()**

```
void USCI_B_SPI_disable (
    uint16_t baseAddress )
```

Disables the SPI block.

This will disable operation of the SPI block.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI SPI module.
--------------------	---

Modified bits are **UCSWRST** of **UCBxCTL1** register.

**Returns**

None

**USCI\_B\_SPI\_disableInterrupt()**

```
void USCI_B_SPI_disableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Disables individual SPI interrupt sources.

Disables the indicated SPI interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
<i>mask</i>	is the bit mask of the interrupt sources to be disabled. Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCI_B_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>USCI_B_SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCBxIE** register.

**Returns**

None

**USCI\_B\_SPI\_enable()**

```
void USCI_B_SPI_enable (
    uint16_t baseAddress )
```

Enables the SPI block.

This will enable operation of the SPI block.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI SPI module.
--------------------	---

Modified bits are **UCSWRST** of **UCBxCTL1** register.

**Returns**

None

**USCI\_B\_SPI\_enableInterrupt()**

```
void USCI_B_SPI_enableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Enables individual SPI interrupt sources.

Enables the indicated SPI interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. **Does not clear interrupt flags.**

**Parameters**

<b><i>baseAddress</i></b>	<b>is the base address of the SPI module.</b>
<b><i>mask</i></b>	<b>is the bit mask of the interrupt sources to be enabled. Valid values are:</b> <ul style="list-style-type: none"> <li>■ <b>USCI_B_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>USCI_B_SPI_RECEIVE_INTERRUPT</b></li> </ul>

**Modified bits of UCBxIE register.**

**Returns**

None

**USCI\_B\_SPI\_getInterruptStatus()**

```
uint8_t USCI_B_SPI_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t mask )
```

Gets the current SPI interrupt status.

This returns the interrupt status for the SPI module based on which flag is passed.

**Parameters**

<b><i>baseAddress</i></b>	<b>is the base address of the SPI module.</b>
<b><i>mask</i></b>	<b>is the masked interrupt flag status to be returned. Valid values are:</b> <ul style="list-style-type: none"> <li>■ <b>USCI_B_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>USCI_B_SPI_RECEIVE_INTERRUPT</b></li> </ul>

**Returns**

The current interrupt status as the mask of the set flags Return Logical OR of any of the following:

- **USCI\_B\_SPI\_TRANSMIT\_INTERRUPT**
  - **USCI\_B\_SPI\_RECEIVE\_INTERRUPT**
- indicating the status of the masked interrupts

**USCI\_B\_SPI\_getReceiveBufferAddressForDMA()**

```
uint32_t USCI_B_SPI_getReceiveBufferAddressForDMA (
    uint16_t baseAddress )
```

Returns the address of the RX Buffer of the SPI for the DMA module.

Returns the address of the SPI RX Buffer. This can be used in conjunction with the DMA to store the received data directly to memory.

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
--------------------	--

**Returns**

The address of the SPI RX buffer

**USCI\_B\_SPI\_getTransmitBufferAddressForDMA()**

```
uint32_t USCI_B_SPI_getTransmitBufferAddressForDMA (
    uint16_t baseAddress )
```

Returns the address of the TX Buffer of the SPI for the DMA module.

Returns the address of the SPI TX Buffer. This can be used in conjunction with the DMA to obtain transmitted data directly from memory.

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
--------------------	--

**Returns**

The address of the SPI TX buffer

**USCI\_B\_SPI\_initMaster()**

```
bool USCI_B_SPI_initMaster (
    uint16_t baseAddress,
    USCI_B_SPI_initMasterParam * param )
```



Initializes the SPI Master block.

Upon successful initialization of the SPI master block, this function will have set the bus speed for the master, but the SPI Master block still remains disabled and must be enabled with [USCI.B.SPI.enable\(\)](#)

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>param</i>	is the pointer to struct for master initialization.

Modified bits are **UCSSELx** and **UCSWRST** of **UCBxCTL1** register; bits **UCCKPH**, **UCCKPL**, **UC7BIT** and **UCMSB** of **UCBxCTL0** register.

#### Returns

STATUS\_SUCCESS

References `USCI.B.SPI_initMasterParam::clockPhase`, `USCI.B.SPI_initMasterParam::clockPolarity`, `USCI.B.SPI_initMasterParam::clockSourceFrequency`, `USCI.B.SPI_initMasterParam::desiredSpiClock`, `USCI.B.SPI_initMasterParam::msbFirst`, and `USCI.B.SPI_initMasterParam::selectClockSource`.

### USCI.B.SPI.initSlave()

```
bool USCI_B_SPI_initSlave (
    uint16_t baseAddress,
    uint8_t msbFirst,
    uint8_t clockPhase,
    uint8_t clockPolarity )
```

Initializes the SPI Slave block.

Upon successful initialization of the SPI slave block, this function will have initialized the slave block, but the SPI Slave block still remains disabled and must be enabled with [USCI.B.SPI.enable\(\)](#)

#### Parameters

<i>baseAddress</i>	is the base address of the SPI Slave module.
<i>msbFirst</i>	controls the direction of the receive and transmit shift register. Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCI_B_SPI_MSB_FIRST</b></li> <li>■ <b>USCI_B_SPI_LSB_FIRST</b> [Default]</li> </ul>
<i>clockPhase</i>	is clock phase select. Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCI_B_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT_EXT</b> [Default]</li> <li>■ <b>USCI_B_SPI_PHASE_DATA_CAPTURED_ONFIRST_CHANGED_ON_NEXT_EXT</b></li> </ul>

**Parameters**

<i>clockPolarity</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCI_B_SPI_CLOCKPOLARITY_INACTIVITY_HIGH</b></li> <li>■ <b>USCI_B_SPI_CLOCKPOLARITY_INACTIVITY_LOW</b> [Default]</li> </ul>
----------------------	---

Modified bits are **UCSWRST** of **UCBxCTL1** register; bits **UCMSB**, **UCMST**, **UC7BIT**, **UCCKPL**, **UCCKPH** and **UCMODE** of **UCBxCTL0** register.

**Returns**

STATUS\_SUCCESS

**USCI.B\_SPI.isBusy()**

```
uint8_t USCI_B_SPI_isBusy (
    uint16_t baseAddress )
```

Indicates whether or not the SPI bus is busy.

This function returns an indication of whether or not the SPI bus is busy. This function checks the status of the bus via UCBBUSY bit

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
--------------------	--

**Returns**

USCI\_B\_SPI\_BUSY if the SPI module transmitting or receiving is busy; otherwise, returns USCI\_B\_SPI\_NOT\_BUSY. Return one of the following:

- **USCI\_B\_SPI\_BUSY**
- **USCI\_B\_SPI\_NOT\_BUSY**  
indicating if the USCI.B\_SPI is busy

**USCI.B\_SPI.receiveData()**

```
uint8_t USCI_B_SPI_receiveData (
    uint16_t baseAddress )
```

Receives a byte that has been sent to the SPI Module.

This function reads a byte of data from the SPI receive data Register.

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
--------------------	--

**Returns**

Returns the byte received from by the SPI module, cast as an uint8\_t.

**USCI\_B\_SPI\_transmitData()**

```
void USCI_B_SPI_transmitData (
    uint16_t baseAddress,
    uint8_t transmitData )
```

Transmits a byte from the SPI Module.

This function will place the supplied data into SPI transmit data register to start transmission

**Parameters**

<i>baseAddress</i>	is the base address of the SPI module.
<i>transmitData</i>	data to be transmitted from the SPI module

**Returns**

None

## 44.3 Programming Example

The following example shows how to use the USCI\_B\_SPI API to configure the USCI\_B\_SPI module as a master device, and how to do a simple send of data.

```
//Initialize Master
USCI_B_SPI_initMasterParam param = {0};
param.selectClockSource = USCI_B_SPI_CLOCKSOURCE_SMCLK;
param.clockSourceFrequency = UCS_getSMCLK();
param.desiredSpiClock = SPICLK;
param.msbFirst = USCI_B_SPI_MSB_FIRST;
param.clockPhase = USCI_B_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT;
param.clockPolarity = USCI_B_SPI_CLOCKPOLARITY_INACTIVITY_HIGH;
returnValue = USCI_B_SPI_initMaster(USCI_B0_BASE, &param);

if (STATUS_FAIL == returnValue){
    return;
}

//Enable USCI_B_SPI module
USCI_B_SPI_enable(USCI_A0_BASE);

//Enable Receive interrupt
USCI_B_SPI_enableInterrupt(USCI_A0_BASE, UCRXIE);

//Configure port pins to reset slave

// Wait for slave to initialize
_delay_cycles(100);

// Initialize data values
transmitData = 0x00;

// USCI_A0 TX buffer ready?
while (!USCI_B_SPI_interruptStatus(USCI_A0_BASE, UCTXIFG));

//Transmit Data to slave
USCI_B_SPI_transmitData(USCI_A0_BASE, transmitData);
```

```
    // CPU off, enable interrupts
    _bis_SR_register(LPM0_bits + GIE);
}

//*****
//
// This is the USCI_B0 interrupt vector service routine.
//
//*****
#pragma vector=USCI_B0_VECTOR
__interrupt void USCI_B0_ISR(void)
{
    switch(__even_in_range(UCA0IV,4))
    {
        // Vector 2 - RXIFG
        case 2:
            // USCI_A0 TX buffer ready?
            while (!USCI_B_SPI_interruptStatus(USCI_A0_BASE, UCTXIFG));

            receiveData = USCI_B_SPI_receiveData(USCI_A0_BASE);

            // Increment data
            transmitData++;

            // Send next value
            USCI_B_SPI_transmitData(USCI_A0_BASE, transmitData);

            //Delay between transmissions for slave to process information
            _delay_cycles(40);

            break;
        default: break;
    }
}
```

## 45 USCI Inter-Integrated Circuit (USCI\_B\_I2C)

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### 45.1 Introduction

The Inter-Integrated Circuit (USCI\_B\_I2C) API provides a set of functions for using the MSP430Ware USCI\_B\_I2C modules. Functions are provided to initialize the USCI\_B\_I2C modules, to send and receive data, obtain status, and to manage interrupts for the USCI\_B\_I2C modules.

The USCI\_B\_I2C module provide the ability to communicate to other IC devices over an USCI\_B\_I2C bus. The USCI\_B\_I2C bus is specified to support devices that can both transmit and receive (write and read) data. Also, devices on the USCI\_B\_I2C bus can be designated as either a master or a slave. The MSP430Ware USCI\_B\_I2C modules support both sending and receiving data as either a master or a slave, and also support the simultaneous operation as both a master and a slave. Finally, the MSP430Ware USCI\_B\_I2C modules can operate at two speeds: Standard (100 kb/s) and Fast (400 kb/s).

USCI\_B\_I2C module can generate interrupts. The USCI\_B\_I2C module configured as a master will generate interrupts when a transmit or receive operation is completed (or aborted due to an error). The USCI\_B\_I2C module configured as a slave will generate interrupts when data has been sent or requested by a master.

### 45.2 Master Operations

To drive the master module, the APIs need to be invoked in the following order

- [USCI\\_B\\_I2C.initMaster\(\)](#)
- [USCI\\_B\\_I2C.setSlaveAddress\(\)](#)
- [USCI\\_B\\_I2C.setMode\(\)](#)
- [USCI\\_B\\_I2C.enable\(\)](#)
- [USCI\\_B\\_I2C.enableInterrupt\(\)](#) ( if interrupts are being used ) This may be followed by the APIs for transmit or receive as required

The user must first initialize the USCI\_B\_I2C module and configure it as a master with a call to [USCI\\_B\\_I2C.initMaster\(\)](#). That function will set the clock and data rates. This is followed by a call to set the slave address with which the master intends to communicate with using [USCI\\_B\\_I2C.setSlaveAddress](#). Then the mode of operation (transmit or receive) is chosen using [USCI\\_B\\_I2C.setMode](#). The USCI\_B\_I2C module may now be enabled using [USCI\\_B\\_I2C.enable](#). It is recommended to enable the USCI\_B\_I2C module before enabling the interrupts. Any transmission or reception of data may be initiated at this point after interrupts are enabled (if any).

The transaction can then be initiated on the bus by calling the transmit or receive related APIs as listed below. APIs that include a time-out can be used to avoid being stuck in an infinite loop if the device is stuck waiting for an IFG flag to be set.

Master Single Byte Transmission

- `USCI_B_I2C_masterSendSingleByte()`

Master Multiple Byte Transmission

- `USCI_B_I2C_masterSendMultiByteStart()`
- `USCI_B_I2C_masterSendMultiByteNext()`
- `USCI_B_I2C_masterSendMultiByteFinish()`
- `USCI_B_I2C_masterSendMultiByteStop()`

Master Single Byte Reception

- `USCI_B_I2C_masterReceiveSingleStart()`
- `USCI_B_I2C_masterReceiveSingle()`

Master Multiple Byte Reception

- `USCI_B_I2C_masterReceiveMultiByteStart()`
- `USCI_B_I2C_masterReceiveMultiByteNext()`
- `USCI_B_I2C_masterReceiveMultiByteFinish()`
- `USCI_B_I2C_masterReceiveMultiByteStop()`

Master Single Byte Transmission with Time-out

- `USCI_B_I2C_masterSendSingleByteWithTimeout()`

Master Multiple Byte Transmission with Time-out

- `USCI_B_I2C_masterSendMultiByteStartWithTimeout()`
- `USCI_B_I2C_masterSendMultiByteNextWithTimeout()`
- `USCI_B_I2C_masterReceiveMultiByteFinishWithTimeout()`
- `USCI_B_I2C_masterSendMultiByteStopWithTimeout()`

Master Single Byte Reception with Time-out `USCI_B_I2C_masterReceiveSingleStartWithTimeout()`

For the interrupt-driven transaction, the user must register an interrupt handler for the USCI\_B\_I2C devices and enable the USCI\_B\_I2C interrupt.

## 45.3 Slave Operations

To drive the slave module, the APIs need to be invoked in the following order

- `USCI_B_I2C_initSlave()`
- `USCI_B_I2C_setMode()`
- `USCI_B_I2C_enable()`
- `USCI_B_I2C_enableInterrupt()` ( if interrupts are being used ) This may be followed by the APIs for transmit or receive as required

The user must first call the `USCI_B_I2C.initSlave` to initialize the slave module in `USCI_B_I2C` mode and set the slave address. This is followed by a call to set the mode of operation (transmit or receive). The `USCI_B_I2C` module may now be enabled using `USCI_B_I2C.enable()`. It is recommended to enable the `USCI_B_I2C` module before enabling the interrupts. Any transmission or reception of data may be initiated at this point after interrupts are enabled (if any).

The transaction can then be initiated on the bus by calling the transmit or receive related APIs as listed below.

Slave Transmission API

- `USCI_B_I2C_slavePutData()`

Slave Reception API

- `USCI_B_I2C_slaveGetData()`

For the interrupt-driven transaction, the user must register an interrupt handler for the `USCI_B_I2C` devices and enable the `USCI_B_I2C` interrupt.

## 45.4 API Functions

### Functions

- void `USCI_B_I2C.initMaster` (uint16\_t baseAddress, `USCI_B_I2C_initMasterParam` \*param)  
*Initializes the I2C Master block.*
- void `USCI_B_I2C.initSlave` (uint16\_t baseAddress, uint8\_t slaveAddress)  
*Initializes the I2C Slave block.*
- void `USCI_B_I2C.enable` (uint16\_t baseAddress)  
*Enables the I2C block.*
- void `USCI_B_I2C.disable` (uint16\_t baseAddress)  
*Disables the I2C block.*
- void `USCI_B_I2C.setSlaveAddress` (uint16\_t baseAddress, uint8\_t slaveAddress)  
*Sets the address that the I2C Master will place on the bus.*
- void `USCI_B_I2C.setMode` (uint16\_t baseAddress, uint8\_t mode)  
*Sets the mode of the I2C device.*
- void `USCI_B_I2C_slavePutData` (uint16\_t baseAddress, uint8\_t transmitData)  
*Transmits a byte from the I2C Module.*
- uint8\_t `USCI_B_I2C_slaveGetData` (uint16\_t baseAddress)  
*Receives a byte that has been sent to the I2C Module.*
- uint8\_t `USCI_B_I2C.isBusBusy` (uint16\_t baseAddress)  
*Indicates whether or not the I2C bus is busy.*
- uint8\_t `USCI_B_I2C.isBusy` (uint16\_t baseAddress)  
*DEPRECATED - Function may be removed in future release. Indicates whether or not the I2C module is busy.*
- uint8\_t `USCI_B_I2C.masterIsStopSent` (uint16\_t baseAddress)  
*Indicates whether STOP got sent.*
- uint8\_t `USCI_B_I2C.masterIsStartSent` (uint16\_t baseAddress)  
*Indicates whether START got sent.*
- void `USCI_B_I2C.masterSendStart` (uint16\_t baseAddress)  
*This function is used by the Master module to initiate START.*
- void `USCI_B_I2C.enableInterrupt` (uint16\_t baseAddress, uint8\_t mask)

- Enables individual I2C interrupt sources.*

  - void `USCI_B_I2C_disableInterrupt` (uint16\_t baseAddress, uint8\_t mask)
- Disables individual I2C interrupt sources.*

  - void `USCI_B_I2C_clearInterrupt` (uint16\_t baseAddress, uint8\_t mask)
- Clears I2C interrupt sources.*

  - uint8\_t `USCI_B_I2C_getInterruptStatus` (uint16\_t baseAddress, uint8\_t mask)
- Gets the current I2C interrupt status.*

  - void `USCI_B_I2C_masterSendSingleByte` (uint16\_t baseAddress, uint8\_t txData)
- Does single byte transmission from Master to Slave.*

  - bool `USCI_B_I2C_masterSendSingleByteWithTimeout` (uint16\_t baseAddress, uint8\_t txData, uint32\_t timeout)
- Does single byte transmission from Master to Slave with timeout.*

  - void `USCI_B_I2C_masterSendMultiByteStart` (uint16\_t baseAddress, uint8\_t txData)
- Starts multi-byte transmission from Master to Slave.*

  - bool `USCI_B_I2C_masterSendMultiByteStartWithTimeout` (uint16\_t baseAddress, uint8\_t txData, uint32\_t timeout)
- Starts multi-byte transmission from Master to Slave with timeout.*

  - void `USCI_B_I2C_masterSendMultiByteNext` (uint16\_t baseAddress, uint8\_t txData)
- Continues multi-byte transmission from Master to Slave.*

  - bool `USCI_B_I2C_masterSendMultiByteNextWithTimeout` (uint16\_t baseAddress, uint8\_t txData, uint32\_t timeout)
- Continues multi-byte transmission from Master to Slave with timeout.*

  - void `USCI_B_I2C_masterSendMultiByteFinish` (uint16\_t baseAddress, uint8\_t txData)
- Finishes multi-byte transmission from Master to Slave.*

  - bool `USCI_B_I2C_masterSendMultiByteFinishWithTimeout` (uint16\_t baseAddress, uint8\_t txData, uint32\_t timeout)
- Finishes multi-byte transmission from Master to Slave with timeout.*

  - void `USCI_B_I2C_masterSendMultiByteStop` (uint16\_t baseAddress)
- Sends STOP byte at the end of a multi-byte transmission from Master to Slave.*

  - bool `USCI_B_I2C_masterSendMultiByteStopWithTimeout` (uint16\_t baseAddress, uint32\_t timeout)
- Sends STOP byte at the end of a multi-byte transmission from Master to Slave with timeout.*

  - void `USCI_B_I2C_masterReceiveMultiByteStart` (uint16\_t baseAddress)
- Starts multi-byte reception at the Master end.*

  - uint8\_t `USCI_B_I2C_masterReceiveMultiByteNext` (uint16\_t baseAddress)
- Starts multi-byte reception at the Master end one byte at a time.*

  - uint8\_t `USCI_B_I2C_masterReceiveMultiByteFinish` (uint16\_t baseAddress)
- Finishes multi-byte reception at the Master end.*

  - bool `USCI_B_I2C_masterReceiveMultiByteFinishWithTimeout` (uint16\_t baseAddress, uint8\_t \*rxData, uint32\_t timeout)
- Finishes multi-byte reception at the Master end with timeout.*

  - void `USCI_B_I2C_masterReceiveMultiByteStop` (uint16\_t baseAddress)
- Sends the STOP at the end of a multi-byte reception at the Master end.*

  - void `USCI_B_I2C_masterReceiveSingleStart` (uint16\_t baseAddress)
- Initiates a single byte Reception at the Master End.*

  - bool `USCI_B_I2C_masterReceiveSingleStartWithTimeout` (uint16\_t baseAddress, uint32\_t timeout)
- Initiates a single byte Reception at the Master End with timeout.*

  - uint8\_t `USCI_B_I2C_masterReceiveSingle` (uint16\_t baseAddress)
- Receives a byte that has been sent to the I2C Master Module.*

  - uint32\_t `USCI_B_I2C_getReceiveBufferAddressForDMA` (uint16\_t baseAddress)
- Returns the address of the RX Buffer of the I2C for the DMA module.*

  - uint32\_t `USCI_B_I2C_getTransmitBufferAddressForDMA` (uint16\_t baseAddress)
- Returns the address of the TX Buffer of the I2C for the DMA module.*



## 45.4.1 Detailed Description

The USCI\_B\_I2C API is broken into three groups of functions: those that deal with interrupts, those that handle status and initialization, and those that deal with sending and receiving data.

The USCI\_B\_I2C master and slave interrupts and status are handled by

- `USCI_B_I2C_enableInterrupt()`
- `USCI_B_I2C_disableInterrupt()`
- `USCI_B_I2C_clearInterrupt()`
- `USCI_B_I2C_getInterruptStatus()`
- `USCI_B_I2C_masterIsStopSent()`
- `USCI_B_I2C_masterIsStartSent()`

Status and initialization functions for the USCI\_B\_I2C modules are

- `USCI_B_I2C_initMaster()`
- `USCI_B_I2C_enable()`
- `USCI_B_I2C_disable()`
- `USCI_B_I2C_isBusBusy()`
- `USCI_B_I2C_isBusy()`
- `USCI_B_I2C_initSlave()`
- `USCI_B_I2C_interruptStatus()`
- `USCI_B_I2C_setSlaveAddress()`
- `USCI_B_I2C_setMode()`

Sending and receiving data from the USCI\_B\_I2C slave module is handled by

- `USCI_B_I2C_slavePutData()`
- `USCI_B_I2C_slaveGetData()`

Sending and receiving data from the USCI\_B\_I2C slave module is handled by

- `USCI_B_I2C_masterSendSingleByte()`
- `USCI_B_I2C_masterSendMultiByteStart()`
- `USCI_B_I2C_masterSendMultiByteNext()`
- `USCI_B_I2C_masterSendMultiByteFinish()`
- `USCI_B_I2C_masterSendMultiByteStop()`
- `USCI_B_I2C_masterReceiveMultiByteStart()`
- `USCI_B_I2C_masterReceiveMultiByteNext()`
- `USCI_B_I2C_masterReceiveMultiByteFinish()`
- `USCI_B_I2C_masterReceiveMultiByteStop()`
- `USCI_B_I2C_masterReceiveSingleStart()`
- `USCI_B_I2C_masterReceiveSingle()`
- `USCI_B_I2C_getReceiveBufferAddressForDMA()`
- `USCI_B_I2C_getTransmitBufferAddressForDMA()`

DMA related

- [USCI\\_B\\_I2C\\_getReceiveBufferAddressForDMA\(\)](#)
- [USCI\\_B\\_I2C\\_getTransmitBufferAddressForDMA\(\)](#)

## 45.4.2 Function Documentation

### USCI\_B\_I2C\_clearInterrupt()

```
void USCI_B_I2C_clearInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Clears I2C interrupt sources.

The I2C interrupt source is cleared, so that it no longer asserts. The highest interrupt flag is automatically cleared when an interrupt vector generator is used.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Slave module.
<i>mask</i>	is a bit mask of the interrupt sources to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>USCI_B_I2C_STOP_INTERRUPT</b> - STOP condition interrupt</li> <li>■ <b>USCI_B_I2C_START_INTERRUPT</b> - START condition interrupt</li> <li>■ <b>USCI_B_I2C_RECEIVE_INTERRUPT</b> - Receive interrupt</li> <li>■ <b>USCI_B_I2C_TRANSMIT_INTERRUPT</b> - Transmit interrupt</li> <li>■ <b>USCI_B_I2C_NAK_INTERRUPT</b> - Not-acknowledge interrupt</li> <li>■ <b>USCI_B_I2C_ARBITRATIONLOST_INTERRUPT</b> - Arbitration lost interrupt</li> </ul>

Modified bits of **UCBxIFG** register.

#### Returns

None

### USCI\_B\_I2C\_disable()

```
void USCI_B_I2C_disable (
    uint16_t baseAddress )
```

Disables the I2C block.

This will disable operation of the I2C block.

#### Parameters

<i>baseAddress</i>	is the base address of the USCI I2C module.
--------------------	---

Modified bits are **UCSWRST** of **UCBxCTL1** register.

**Returns**

None

### USCI\_B\_I2C\_disableInterrupt()

```
void USCI_B_I2C_disableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Disables individual I2C interrupt sources.

Disables the indicated I2C interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
<i>mask</i>	is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>USCI_B_I2C_STOP_INTERRUPT</b> - STOP condition interrupt</li> <li>■ <b>USCI_B_I2C_START_INTERRUPT</b> - START condition interrupt</li> <li>■ <b>USCI_B_I2C_RECEIVE_INTERRUPT</b> - Receive interrupt</li> <li>■ <b>USCI_B_I2C_TRANSMIT_INTERRUPT</b> - Transmit interrupt</li> <li>■ <b>USCI_B_I2C_NAK_INTERRUPT</b> - Not-acknowledge interrupt</li> <li>■ <b>USCI_B_I2C_ARBITRATIONLOST_INTERRUPT</b> - Arbitration lost interrupt</li> </ul>

Modified bits of **UCBxIE** register.

**Returns**

None

### USCI\_B\_I2C\_enable()

```
void USCI_B_I2C_enable (
    uint16_t baseAddress )
```

Enables the I2C block.

This will enable operation of the I2C block.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI I2C module.
--------------------	---

Modified bits are **UCSWRST** of **UCBxCTL1** register.

**Returns**

None

### USCI\_B\_I2C\_enableInterrupt()

```
void USCI_B_I2C_enableInterrupt (
    uint16_t baseAddress,
    uint8_t mask )
```

Enables individual I2C interrupt sources.

Enables the indicated I2C interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
<i>mask</i>	is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>USCI_B_I2C_STOP_INTERRUPT</b> - STOP condition interrupt</li> <li>■ <b>USCI_B_I2C_START_INTERRUPT</b> - START condition interrupt</li> <li>■ <b>USCI_B_I2C_RECEIVE_INTERRUPT</b> - Receive interrupt</li> <li>■ <b>USCI_B_I2C_TRANSMIT_INTERRUPT</b> - Transmit interrupt</li> <li>■ <b>USCI_B_I2C_NAK_INTERRUPT</b> - Not-acknowledge interrupt</li> <li>■ <b>USCI_B_I2C_ARBITRATIONLOST_INTERRUPT</b> - Arbitration lost interrupt</li> </ul>

Modified bits of **UCBxIE** register.

**Returns**

None

### USCI\_B\_I2C\_getInterruptStatus()

```
uint8_t USCI_B_I2C_getInterruptStatus (
    uint16_t baseAddress,
    uint8_t mask )
```

Gets the current I2C interrupt status.

This returns the interrupt status for the I2C module based on which flag is passed. mask parameter can be logic OR of any of the following selection.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

**Parameters**

<i>mask</i>	<p>is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>USCI_B_I2C_STOP_INTERRUPT</b> - STOP condition interrupt</li> <li>■ <b>USCI_B_I2C_START_INTERRUPT</b> - START condition interrupt</li> <li>■ <b>USCI_B_I2C_RECEIVE_INTERRUPT</b> - Receive interrupt</li> <li>■ <b>USCI_B_I2C_TRANSMIT_INTERRUPT</b> - Transmit interrupt</li> <li>■ <b>USCI_B_I2C_NAK_INTERRUPT</b> - Not-acknowledge interrupt</li> <li>■ <b>USCI_B_I2C_ARBITRATIONLOST_INTERRUPT</b> - Arbitration lost interrupt</li> </ul>
-------------	--

**Returns**

the masked status of the interrupt flag Return Logical OR of any of the following:

- **USCI\_B\_I2C\_STOP\_INTERRUPT** STOP condition interrupt
- **USCI\_B\_I2C\_START\_INTERRUPT** START condition interrupt
- **USCI\_B\_I2C\_RECEIVE\_INTERRUPT** Receive interrupt
- **USCI\_B\_I2C\_TRANSMIT\_INTERRUPT** Transmit interrupt
- **USCI\_B\_I2C\_NAK\_INTERRUPT** Not-acknowledge interrupt
- **USCI\_B\_I2C\_ARBITRATIONLOST\_INTERRUPT** Arbitration lost interrupt indicating the status of the masked interrupts

**USCI\_B\_I2C\_getReceiveBufferAddressForDMA()**

```
uint32_t USCI_B_I2C_getReceiveBufferAddressForDMA (
    uint16_t baseAddress )
```

Returns the address of the RX Buffer of the I2C for the DMA module.

Returns the address of the I2C RX Buffer. This can be used in conjunction with the DMA to store the received data directly to memory.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

**Returns**

the address of the RX Buffer

**USCI\_B\_I2C\_getTransmitBufferAddressForDMA()**

```
uint32_t USCI_B_I2C_getTransmitBufferAddressForDMA (
    uint16_t baseAddress )
```

Returns the address of the TX Buffer of the I2C for the DMA module.

Returns the address of the I2C TX Buffer. This can be used in conjunction with the DMA to obtain transmitted data directly from memory.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

#### Returns

the address of the TX Buffer

### USCI\_B\_I2C\_initMaster()

```
void USCI_B_I2C_initMaster (
    uint16_t baseAddress,
    USCI_B_I2C_initMasterParam * param )
```

Initializes the I2C Master block.

This function initializes operation of the I2C Master block. Upon successful initialization of the I2C block, this function will have set the bus speed for the master; however I2C module is still disabled till `USCI_B_I2C_enable` is invoked. If the parameter *dataRate* is `USCI_B_I2C_SET_DATA_RATE_400KBPS`, then the master block will be set up to transfer data at 400 kbps; otherwise, it will be set up to transfer data at 100 kbps.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>param</i>	is the pointer to struct for master initialization.

Modified bits are **UCBxBR0** of **UCBxBR1** register; bits **UCSSELx** and **UCSWRST** of **UCBxCTL1** register; bits **UCMST**, **UCMODE\_3** and **UCSYNC** of **UCBxCTL0** register.

#### Returns

None

References `USCI_B_I2C_initMasterParam::dataRate`, `USCI_B_I2C_initMasterParam::i2cClk`, and `USCI_B_I2C_initMasterParam::selectClockSource`.

### USCI\_B\_I2C\_initSlave()

```
void USCI_B_I2C_initSlave (
    uint16_t baseAddress,
    uint8_t slaveAddress )
```

Initializes the I2C Slave block.

This function initializes operation of the I2C as a Slave mode. Upon successful initialization of the I2C blocks, this function will have set the slave address but the I2C module is still disabled till `USCI_B_I2C_enable` is invoked.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Slave module.
<i>slaveAddress</i>	7-bit slave address

Modified bits of **UCBxI2COA** register; bits **UCSWRST** of **UCBxCTL1** register; bits **UCMODE\_3** and **UCSYNC** of **UCBxCTL0** register.

**Returns**

None

**USCI\_B\_I2C.isBusBusy()**

```
uint8_t USCI_B_I2C.isBusBusy (
    uint16_t baseAddress )
```

Indicates whether or not the I2C bus is busy.

This function returns an indication of whether or not the I2C bus is busy. This function checks the status of the bus via UCBBUSY bit in UCBxSTAT register.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

**Returns**

Returns USCI\_B\_I2C\_BUS\_BUSY if the I2C Master is busy; otherwise, returns USCI\_B\_I2C\_BUS\_NOT\_BUSY. Return one of the following:

- **USCI\_B\_I2C\_BUS\_BUSY**
- **USCI\_B\_I2C\_BUS\_NOT\_BUSY**  
indicating if the USCI\_B\_I2C is busy

**USCI\_B\_I2C.isBusy()**

```
uint8_t USCI_B_I2C.isBusy (
    uint16_t baseAddress )
```

DEPRECATED - Function may be removed in future release. Indicates whether or not the I2C module is busy.

This function returns an indication of whether or not the I2C module is busy transmitting or receiving data. This function checks if the Transmit or receive flag is set.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

**Returns**

Returns USCI\_B\_I2C\_BUS\_BUSY if the I2C module is busy; otherwise, returns USCI\_B\_I2C\_BUS\_NOT\_BUSY. Return one of the following:

- **USCI\_B\_I2C\_BUS\_BUSY**
- **USCI\_B\_I2C\_BUS\_NOT\_BUSY**  
indicating if the USCI\_B\_I2C is busy

**USCI\_B\_I2C\_masterIsStartSent()**

```
uint8_t USCI_B_I2C_masterIsStartSent (
    uint16_t baseAddress )
```

Indicates whether START got sent.

This function returns an indication of whether or not START got sent This function checks the status of the bus via UCTXSTT bit in UCBxCTL1 register.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

**Returns**

Returns USCI\_B\_I2C\_START\_SEND\_COMPLETE if the I2C Master finished sending START; otherwise, returns USCI\_B\_I2C\_SENDING\_START. Return one of the following:

- **USCI\_B\_I2C\_SENDING\_START**
- **USCI\_B\_I2C\_START\_SEND\_COMPLETE**

**USCI\_B\_I2C\_masterIsStopSent()**

```
uint8_t USCI_B_I2C_masterIsStopSent (
    uint16_t baseAddress )
```

Indicates whether STOP got sent.

This function returns an indication of whether or not STOP got sent This function checks the status of the bus via UCTXSTP bit in UCBxCTL1 register.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

**Returns**

Returns USCI\_B\_I2C\_STOP\_SEND\_COMPLETE if the I2C Master finished sending STOP; otherwise, returns USCI\_B\_I2C\_SENDING\_STOP. Return one of the following:

- **USCI\_B\_I2C\_SENDING\_STOP**
- **USCI\_B\_I2C\_STOP\_SEND\_COMPLETE**



**USCI\_B\_I2C\_masterReceiveMultiByteFinish()**

```
uint8_t USCI_B_I2C_masterReceiveMultiByteFinish (
    uint16_t baseAddress )
```

Finishes multi-byte reception at the Master end.

This function is used by the Master module to initiate completion of a multi-byte reception. This function does the following: - Receives the current byte and initiates the STOP from Master to Slave

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTP** of **UCBxCTL1** register.

**Returns**

Received byte at Master end.

**USCI\_B\_I2C\_masterReceiveMultiByteFinishWithTimeout()**

```
bool USCI_B_I2C_masterReceiveMultiByteFinishWithTimeout (
    uint16_t baseAddress,
    uint8_t * rxData,
    uint32_t timeout )
```

Finishes multi-byte reception at the Master end with timeout.

This function is used by the Master module to initiate completion of a multi-byte reception. This function does the following: - Receives the current byte and initiates the STOP from Master to Slave

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>rxData</i>	is a pointer to the location to store the received byte at master end
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits are **UCTXSTP** of **UCBxCTL1** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

**USCI\_B\_I2C\_masterReceiveMultiByteNext()**

```
uint8_t USCI_B_I2C_masterReceiveMultiByteNext (
    uint16_t baseAddress )
```

Starts multi-byte reception at the Master end one byte at a time.

This function is used by the Master module to receive each byte of a multi- byte reception. This function reads currently received byte

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

#### Returns

Received byte at Master end.

### USCI\_B\_I2C\_masterReceiveMultiByteStart()

```
void USCI_B_I2C_masterReceiveMultiByteStart (
    uint16_t baseAddress )
```

Starts multi-byte reception at the Master end.

This function is used by the Master module initiate reception of a single byte. This function does the following: - Sends START

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTT** of **UCBxCTL1** register.

#### Returns

None

### USCI\_B\_I2C\_masterReceiveMultiByteStop()

```
void USCI_B_I2C_masterReceiveMultiByteStop (
    uint16_t baseAddress )
```

Sends the STOP at the end of a multi-byte reception at the Master end.

This function is used by the Master module to initiate STOP

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTP** of **UCBxCTL1** register.

#### Returns

None

## USCI\_B\_I2C\_masterReceiveSingle()

```
uint8_t USCI_B_I2C_masterReceiveSingle (
    uint16_t baseAddress )
```

Receives a byte that has been sent to the I2C Master Module.

This function reads a byte of data from the I2C receive data Register.

### Parameters

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

### Returns

Returns the byte received from by the I2C module, cast as an uint8\_t.

## USCI\_B\_I2C\_masterReceiveSingleStart()

```
void USCI_B_I2C_masterReceiveSingleStart (
    uint16_t baseAddress )
```

Initiates a single byte Reception at the Master End.

This function sends a START and STOP immediately to indicate Single byte reception

### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **GIE** of **SR** register; bits **UCTXSTT** and **UCTXSTP** of **UCBxCTL1** register.

### Returns

None

## USCI\_B\_I2C\_masterReceiveSingleStartWithTimeout()

```
bool USCI_B_I2C_masterReceiveSingleStartWithTimeout (
    uint16_t baseAddress,
    uint32_t timeout )
```

Initiates a single byte Reception at the Master End with timeout.

This function sends a START and STOP immediately to indicate Single byte reception

### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits are **GIE** of **SR** register; bits **UCTXSTT** and **UCTXSTP** of **UCBxCTL1** register.

#### Returns

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

### USCI\_B\_I2C\_masterSendMultiByteFinish()

```
void USCI_B_I2C_masterSendMultiByteFinish (
    uint16_t baseAddress,
    uint8_t txData )
```

Finishes multi-byte transmission from Master to Slave.

This function is used by the Master module to send the last byte and STOP. This function does the following: - Transmits the last data byte of a multi-byte transmission to the Slave; - Sends STOP

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the last data byte to be transmitted in a multi-byte transmission

Modified bits of **UCBxTXBUF** register and bits of **UCBxCTL1** register.

#### Returns

None

### USCI\_B\_I2C\_masterSendMultiByteFinishWithTimeout()

```
bool USCI_B_I2C_masterSendMultiByteFinishWithTimeout (
    uint16_t baseAddress,
    uint8_t txData,
    uint32_t timeout )
```

Finishes multi-byte transmission from Master to Slave with timeout.

This function is used by the Master module to send the last byte and STOP. This function does the following: - Transmits the last data byte of a multi-byte transmission to the Slave; - Sends STOP

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the last data byte to be transmitted in a multi-byte transmission
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits of **UCBxTXBUF** register and bits of **UCBxCTL1** register.

#### Returns

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

**USCI\_B\_I2C\_masterSendMultiByteNext()**

```
void USCI_B_I2C_masterSendMultiByteNext (
    uint16_t baseAddress,
    uint8_t txData )
```

Continues multi-byte transmission from Master to Slave.

This function is used by the Master module continue each byte of a multi- byte transmission. This function does the following: -Transmits each data byte of a multi-byte transmission to the Slave

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the next data byte to be transmitted

Modified bits of **UCBxTXBUF** register.

**Returns**

None

**USCI\_B\_I2C\_masterSendMultiByteNextWithTimeout()**

```
bool USCI_B_I2C_masterSendMultiByteNextWithTimeout (
    uint16_t baseAddress,
    uint8_t txData,
    uint32_t timeout )
```

Continues multi-byte transmission from Master to Slave with timeout.

This function is used by the Master module continue each byte of a multi- byte transmission. This function does the following: -Transmits each data byte of a multi-byte transmission to the Slave

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the next data byte to be transmitted
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits of **UCBxTXBUF** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

**USCI\_B\_I2C\_masterSendMultiByteStart()**

```
void USCI_B_I2C_masterSendMultiByteStart (
    uint16_t baseAddress,
    uint8_t txData )
```

Starts multi-byte transmission from Master to Slave.

This function is used by the Master module to send a single byte. This function does the following:  
 - Sends START; - Transmits the first data byte of a multi-byte transmission to the Slave

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the first data byte to be transmitted

Modified bits of **UCBxTXBUF** register, bits of **UCBxIFG** register, bits of **UCBxCTL1** register and bits of **UCBxIE** register.

#### Returns

None

### USCI\_B\_I2C\_masterSendMultiByteStartWithTimeout()

```
bool USCI_B_I2C_masterSendMultiByteStartWithTimeout (
    uint16_t baseAddress,
    uint8_t txData,
    uint32_t timeout )
```

Starts multi-byte transmission from Master to Slave with timeout.

This function is used by the Master module to send a single byte. This function does the following:  
 - Sends START; - Transmits the first data byte of a multi-byte transmission to the Slave

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the first data byte to be transmitted
<i>timeout</i>	is the amount of time to wait until giving up

#### Returns

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

### USCI\_B\_I2C\_masterSendMultiByteStop()

```
void USCI_B_I2C_masterSendMultiByteStop (
    uint16_t baseAddress )
```

Send STOP byte at the end of a multi-byte transmission from Master to Slave.

This function is used by the Master module send STOP at the end of a multi- byte transmission.  
 This function does the following: - Sends a STOP after current transmission is complete

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTP** of **UCBxCTL1** register.

**Returns**

None

**USCI\_B\_I2C\_masterSendMultiByteStopWithTimeout()**

```
bool USCI_B_I2C_masterSendMultiByteStopWithTimeout (
    uint16_t baseAddress,
    uint32_t timeout )
```

Send STOP byte at the end of a multi-byte transmission from Master to Slave with timeout.

This function is used by the Master module send STOP at the end of a multi- byte transmission. This function does the following: - Sends a STOP after current transmission is complete

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits are **UCTXSTP** of **UCBxCTL1** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

**USCI\_B\_I2C\_masterSendSingleByte()**

```
void USCI_B_I2C_masterSendSingleByte (
    uint16_t baseAddress,
    uint8_t txData )
```

Does single byte transmission from Master to Slave.

This function is used by the Master module to send a single byte. This function does the following: - Sends START; - Transmits the byte to the Slave; - Sends STOP

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the data byte to be transmitted

Modified bits of **UCBxTXBUF** register, bits of **UCBxIFG** register, bits of **UCBxCTL1** register and bits of **UCBxIE** register.

**Returns**

None

**USCI\_B\_I2C\_masterSendSingleByteWithTimeout()**

```
bool USCI_B_I2C_masterSendSingleByteWithTimeout (
    uint16_t baseAddress,
    uint8_t txData,
    uint32_t timeout )
```

Does single byte transmission from Master to Slave with timeout.

This function is used by the Master module to send a single byte. This function does the following:  
 - Sends START; - Transmits the byte to the Slave; - Sends STOP

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the data byte to be transmitted
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits of **UCBxTXBUF** register, bits of **UCBxIFG** register, bits of **UCBxCTL1** register and bits of **UCBxIE** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

**USCI\_B\_I2C\_masterSendStart()**

```
void USCI_B_I2C_masterSendStart (
    uint16_t baseAddress )
```

This function is used by the Master module to initiate START.

This function is used by the Master module to initiate STOP

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

**Returns**

None

**USCI\_B\_I2C\_setMode()**

```
void USCI_B_I2C_setMode (
    uint16_t baseAddress,
```



```
uint8_t mode )
```

Sets the mode of the I2C device.

When the receive parameter is set to USCI\_B\_I2C\_TRANSMIT\_MODE, the address will indicate that the I2C module is in receive mode; otherwise, the I2C module is in send mode.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>mode</i>	indicates whether module is in transmit/receive mode Valid values are: <ul style="list-style-type: none"> <li>■ <b>USCI_B_I2C_TRANSMIT_MODE</b></li> <li>■ <b>USCI_B_I2C_RECEIVE_MODE</b> [Default]</li> </ul>

#### Returns

None

### USCI\_B\_I2C\_setSlaveAddress()

```
void USCI_B_I2C_setSlaveAddress (
    uint16_t baseAddress,
    uint8_t slaveAddress )
```

Sets the address that the I2C Master will place on the bus.

This function will set the address that the I2C Master will place on the bus when initiating a transaction.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>slaveAddress</i>	7-bit slave address

Modified bits of **UCBxI2CSA** register; bits **UCSWRST** of **UCBxCTL1** register.

#### Returns

None

### USCI\_B\_I2C\_slaveGetData()

```
uint8_t USCI_B_I2C_slaveGetData (
    uint16_t baseAddress )
```

Receives a byte that has been sent to the I2C Module.

This function reads a byte of data from the I2C receive data Register.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

**Returns**

Returns the byte received from by the I2C module, cast as an uint8\_t.

**USCI\_B\_I2C\_slavePutData()**

```
void USCI_B_I2C_slavePutData (
    uint16_t baseAddress,
    uint8_t transmitData )
```

Transmits a byte from the I2C Module.

This function will place the supplied data into I2C transmit data register to start transmission

Modified bit is UCBxTXBUF register

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
<i>transmitData</i>	data to be transmitted from the I2C module

Modified bits of **UCBxTXBUF** register.

**Returns**

None

## 45.5 Programming Example

The following example shows how to use the USCI\_B\_I2C API to send data as a master.

```
// Initialize Master
USCI_B_I2C_initMasterParam param = {0};
param.selectClockSource = USCI_B_I2C_CLOCKSOURCE_SMCLK;
param.i2cClk = UCS_getSMCLK();
param.dataRate = USCI_B_I2C_SET_DATA_RATE_400KBPS;
USCI_B_I2C_initMaster(USCI_B0_BASE, &param);

// Specify slave address
USCI_B_I2C_setSlaveAddress(USCI_B0_BASE, SLAVE_ADDRESS);

// Set in transmit mode
USCI_B_I2C_setMode(USCI_B0_BASE, USCI_B_I2C_TRANSMIT_MODE);

// Enable USCI_B_I2C Module to start operations
USCI_B_I2C_enable(USCI_B0_BASE);

while (1)
{
    // Send single byte data.
    USCI_B_I2C_masterSendSingleByte(USCI_B0_BASE, transmitData);
}
```

```
// Delay until transmission completes
while(USCI_B_I2C.busBusy(USCI_B0_BASE));

// Increment transmit data counter
transmitData++;
}
```

## 46 WatchDog Timer (WDT\_A)

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### 46.1 Introduction

The Watchdog Timer (WDT\_A) API provides a set of functions for using the MSP430Ware WDT\_A modules. Functions are provided to initialize the Watchdog in either timer interval mode, or watchdog mode, with selectable clock sources and dividers to define the timer interval.

The WDT\_A module can generate only 1 kind of interrupt in timer interval mode. If in watchdog mode, then the WDT\_A module will assert a reset once the timer has finished.

### 46.2 API Functions

#### Functions

- void [WDT\\_A\\_hold](#) (uint16\_t baseAddress)  
*Holds the Watchdog Timer.*
- void [WDT\\_A\\_start](#) (uint16\_t baseAddress)  
*Starts the Watchdog Timer.*
- void [WDT\\_A\\_resetTimer](#) (uint16\_t baseAddress)  
*Resets the timer counter of the Watchdog Timer.*
- void [WDT\\_A\\_initWatchdogTimer](#) (uint16\_t baseAddress, uint8\_t clockSelect, uint8\_t clockDivider)  
*Sets the clock source for the Watchdog Timer in watchdog mode.*
- void [WDT\\_A\\_initIntervalTimer](#) (uint16\_t baseAddress, uint8\_t clockSelect, uint8\_t clockDivider)  
*Sets the clock source for the Watchdog Timer in timer interval mode.*

#### 46.2.1 Detailed Description

The WDT\_A API is one group that controls the WDT\_A module.

- [WDT\\_A\\_hold\(\)](#)
- [WDT\\_A\\_start\(\)](#)
- [WDT\\_A\\_clearCounter\(\)](#)
- [WDT\\_A\\_initWatchdogTimer\(\)](#)
- [WDT\\_A\\_initIntervalTimer\(\)](#)

## 46.2.2 Function Documentation

### WDT\_A\_hold()

```
void WDT_A_hold (
    uint16_t baseAddress )
```

Holds the Watchdog Timer.

This function stops the watchdog timer from running, that way no interrupt or PUC is asserted.

#### Parameters

<i>baseAddress</i>	is the base address of the WDT_A module.
--------------------	--

#### Returns

None

### WDT\_A\_initIntervalTimer()

```
void WDT_A_initIntervalTimer (
    uint16_t baseAddress,
    uint8_t clockSelect,
    uint8_t clockDivider )
```

Sets the clock source for the Watchdog Timer in timer interval mode.

This function sets the watchdog timer as timer interval mode, which will assert an interrupt without causing a PUC.

#### Parameters

<i>baseAddress</i>	is the base address of the WDT_A module.
<i>clockSelect</i>	is the clock source that the watchdog timer will use. Valid values are: <ul style="list-style-type: none"> <li>■ <b>WDT_A_CLOCKSOURCE_SMCLK</b> [Default]</li> <li>■ <b>WDT_A_CLOCKSOURCE_ACLK</b></li> <li>■ <b>WDT_A_CLOCKSOURCE_VLOCLK</b></li> <li>■ <b>WDT_A_CLOCKSOURCE_XCLK</b></li> </ul> Modified bits are <b>WDTSSSEL</b> of <b>WDTCTL</b> register.

**Parameters**

<i>clockDivider</i>	<p>is the divider of the clock source, in turn setting the watchdog timer interval. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>WDT_A_CLOCKDIVIDER_2G</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_128M</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_8192K</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_512K</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_32K</b> [Default]</li> <li>■ <b>WDT_A_CLOCKDIVIDER_8192</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_512</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_64</b></li> </ul> <p>Modified bits are <b>WDTIS</b> and <b>WDTHOLD</b> of <b>WDTCTL</b> register.</p>
---------------------	---

**Returns**

None

**WDT\_A\_initWatchdogTimer()**

```
void WDT_A_initWatchdogTimer (
    uint16_t baseAddress,
    uint8_t clockSelect,
    uint8_t clockDivider )
```

Sets the clock source for the Watchdog Timer in watchdog mode.

This function sets the watchdog timer in watchdog mode, which will cause a PUC when the timer overflows. When in the mode, a PUC can be avoided with a call to [WDT\\_A\\_resetTimer\(\)](#) before the timer runs out.

**Parameters**

<i>baseAddress</i>	is the base address of the WDT_A module.
<i>clockSelect</i>	<p>is the clock source that the watchdog timer will use. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>WDT_A_CLOCKSOURCE_SMCLK</b> [Default]</li> <li>■ <b>WDT_A_CLOCKSOURCE_ACLK</b></li> <li>■ <b>WDT_A_CLOCKSOURCE_VLOCLK</b></li> <li>■ <b>WDT_A_CLOCKSOURCE_XCLK</b></li> </ul> <p>Modified bits are <b>WDTSEL</b> of <b>WDTCTL</b> register.</p>

**Parameters**

<i>clockDivider</i>	<p>is the divider of the clock source, in turn setting the watchdog timer interval. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>WDT_A_CLOCKDIVIDER_2G</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_128M</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_8192K</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_512K</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_32K</b> [Default]</li> <li>■ <b>WDT_A_CLOCKDIVIDER_8192</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_512</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_64</b></li> </ul> <p>Modified bits are <b>WDTIS</b> and <b>WDTHOLD</b> of <b>WDTCTL</b> register.</p>
---------------------	---

**Returns**

None

**WDT\_A\_resetTimer()**

```
void WDT_A_resetTimer (
    uint16_t baseAddress )
```

Resets the timer counter of the Watchdog Timer.

This function resets the watchdog timer to 0x0000h.

**Parameters**

<i>baseAddress</i>	is the base address of the WDT_A module.
--------------------	--

**Returns**

None

**WDT\_A\_start()**

```
void WDT_A_start (
    uint16_t baseAddress )
```

Starts the Watchdog Timer.

This function starts the watchdog timer functionality to start counting again.

**Parameters**

<i>baseAddress</i>	is the base address of the WDT_A module.
--------------------	--

**Returns**

None

## 46.3 Programming Example

The following example shows how to initialize and use the WDT\_A API to interrupt about every 32 ms, toggling the LED in the ISR.

```
//Initialize WDT_A module in timer interval mode,  
//with SMCLK as source at an interval of 32 ms.  
WDT_A_initIntervalTimer(WDT_A.BASE,  
    WDT_A.CLOCKSOURCE_SMCLK,  
    WDT_A.CLOCKDIVIDER_32K);  
  
//Enable Watchdog Interrupt  
SFR.enableInterrupt(SFR.WATCHDOG_INTERVAL_TIMER_INTERRUPT);  
  
//Set P1.0 to output direction  
GPIO_setAsOutputPin(  
    GPIO_PORT_P1,  
    GPIO_PIN0  
);  
  
//Enter LPM0, enable interrupts  
__bis_SR_register(LPM0_bits + GIE);  
//For debugger  
__no_operation();
```



# 47 Data Structure Documentation

## 47.1 Data Structures

Here are the data structures with brief descriptions:

<a href="#">ADC12_A_configureMemoryParam</a>	
Used in the <a href="#">ADC12_A_configureMemory()</a> function as the param parameter	609
<a href="#">Calendar</a>	
Used in the <a href="#">RTC_A_initCalendar()</a> function as the CalendarTime parameter	614
<a href="#">Comp_B_configureReferenceVoltageParam</a>	
Used in the <a href="#">Comp_B_configureReferenceVoltage()</a> function as the param parameter	562
<a href="#">Comp_B_initParam</a>	
Used in the <a href="#">Comp_B_init()</a> function as the param parameter	617
<a href="#">DAC12_A_initParam</a>	
Used in the <a href="#">DAC12_A_init()</a> function as the param parameter	589
<a href="#">DMA_initParam</a>	
Used in the <a href="#">DMA_init()</a> function as the param parameter	606
<a href="#">EUSCI_A_SPI_changeMasterClockParam</a>	
Used in the <a href="#">EUSCI_A_SPI_changeMasterClock()</a> function as the param parameter	582
<a href="#">EUSCI_A_SPI_initMasterParam</a>	
Used in the <a href="#">EUSCI_A_SPI_initMaster()</a> function as the param parameter	587
<a href="#">EUSCI_A_SPI_initSlaveParam</a>	
Used in the <a href="#">EUSCI_A_SPI_initSlave()</a> function as the param parameter	621
<a href="#">EUSCI_A_UART_initParam</a>	
Used in the <a href="#">EUSCI_A_UART_init()</a> function as the param parameter	576
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## 47.2 Timer\_D\_initCompareModeParam Struct Reference

Used in the [Timer\\_D\\_initCompareMode\(\)](#) function as the param parameter.

```
#include <timer_d.h>
```

### Data Fields

- `uint16_t compareRegister`
- `uint16_t compareInterruptEnable`
- `uint16_t compareOutputMode`
- `uint16_t compareValue`

*Is the count to be compared with in compare mode.*

### 47.2.1 Detailed Description

Used in the [Timer\\_D\\_initCompareMode\(\)](#) function as the param parameter.

### 47.2.2 Field Documentation

#### compareInterruptEnable

```
uint16_t Timer_D_initCompareModeParam::compareInterruptEnable
```

Is to enable or disable timer captureCompare interrupt.

Valid values are:

- **TIMER\_D\_CAPTURECOMPARE\_INTERRUPT\_ENABLE**
- **TIMER\_D\_CAPTURECOMPARE\_INTERRUPT\_DISABLE** [Default]

Referenced by [Timer\\_D\\_initCompareMode\(\)](#).

#### compareOutputMode

```
uint16_t Timer_D_initCompareModeParam::compareOutputMode
```

Specifies the output mode.

Valid values are:

- **TIMER\_D\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_D\_OUTPUTMODE\_SET**
- **TIMER\_D\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_D\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_D\_OUTPUTMODE\_TOGGLE**
- **TIMER\_D\_OUTPUTMODE\_RESET**
- **TIMER\_D\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_D\_OUTPUTMODE\_RESET\_SET**

Referenced by `Timer_D_initCompareMode()`.

## compareRegister

```
uint16_t Timer_D_initCompareModeParam::compareRegister
```

Selects the Capture register being used.  
Valid values are:

- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_0**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_1**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_2**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_3**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_4**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_5**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_6**

Referenced by `Timer_D_initCompareMode()`.

The documentation for this struct was generated from the following file:

- `timer_d.h`

## 47.3 Timer\_B\_initContinuousModeParam Struct Reference

Used in the `Timer_B_initContinuousMode()` function as the param parameter.

```
#include <timer_b.h>
```

### Data Fields

- `uint16_t` [clockSource](#)
- `uint16_t` [clockSourceDivider](#)
- `uint16_t` [timerInterruptEnable\\_TBIE](#)
- `uint16_t` [timerClear](#)
- `bool` [startTimer](#)

*Whether to start the timer immediately.*

### 47.3.1 Detailed Description

Used in the `Timer_B.initContinuousMode()` function as the param parameter.

### 47.3.2 Field Documentation

#### clockSource

```
uint16_t Timer_B_initContinuousModeParam::clockSource
```

Selects the clock source

Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_B\_CLOCKSOURCE\_ACLK**
- **TIMER\_B\_CLOCKSOURCE\_SMCLK**
- **TIMER\_B\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by `Timer_B.initContinuousMode()`.

#### clockSourceDivider

```
uint16_t Timer_B_initContinuousModeParam::clockSourceDivider
```

Is the divider for Clock source.

Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_48**

- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_B_initContinuousMode()`.

## timerClear

```
uint16_t Timer_B_initContinuousModeParam::timerClear
```

Decides if Timer\_B clock divider, count direction, count need to be reset.  
Valid values are:

- **TIMER\_B\_DO\_CLEAR**
- **TIMER\_B\_SKIP\_CLEAR** [Default]

Referenced by `Timer_B_initContinuousMode()`.

## timerInterruptEnable\_TBIE

```
uint16_t Timer_B_initContinuousModeParam::timerInterruptEnable_TBIE
```

Is to enable or disable Timer\_B interrupt  
Valid values are:

- **TIMER\_B\_TBIE\_INTERRUPT\_ENABLE**
- **TIMER\_B\_TBIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_B_initContinuousMode()`.

The documentation for this struct was generated from the following file:

- `timer_b.h`

## 47.4 Timer\_D\_outputPWMParm Struct Reference

Used in the `Timer_D_outputPWM()` function as the param parameter.

```
#include <timer_d.h>
```

### Data Fields

- `uint16_t clockSource`
- `uint16_t clockSourceDivider`
- `uint16_t clockingMode`
- `uint16_t timerPeriod`  
*Is the specified timer period.*
- `uint16_t compareRegister`
- `uint16_t compareOutputMode`
- `uint16_t dutyCycle`  
*Specifies the dutycycle for the generated waveform.*

## 47.4.1 Detailed Description

Used in the [Timer\\_D\\_outputPWM\(\)](#) function as the param parameter.

## 47.4.2 Field Documentation

### clockingMode

```
uint16_t Timer_D_outputPWMParam::clockingMode
```

Is the selected clock mode register values.

Valid values are:

- **TIMER\_D\_CLOCKINGMODE\_EXTERNAL\_CLOCK** [Default]
- **TIMER\_D\_CLOCKINGMODE\_HIRES\_LOCAL\_CLOCK**
- **TIMER\_D\_CLOCKINGMODE\_AUXILIARY\_CLK**

Referenced by [Timer\\_D\\_outputPWM\(\)](#).

### clockSource

```
uint16_t Timer_D_outputPWMParam::clockSource
```

Selects Clock source.

Valid values are:

- **TIMER\_D\_CLOCKSOURCE\_EXTERNAL\_TDCLK** [Default]
- **TIMER\_D\_CLOCKSOURCE\_ACLK**
- **TIMER\_D\_CLOCKSOURCE\_SMCLK**
- **TIMER\_D\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TDCLK**

Referenced by [Timer\\_D\\_outputPWM\(\)](#).

### clockSourceDivider

```
uint16_t Timer_D_outputPWMParam::clockSourceDivider
```

Is the divider for clock source.

Valid values are:

- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_7**

- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_D_outputPWM()`.

## compareOutputMode

```
uint16_t Timer_D_outputPWMParam::compareOutputMode
```

Specifies the output mode.

Valid values are:

- **TIMER\_D\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_D\_OUTPUTMODE\_SET**
- **TIMER\_D\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_D\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_D\_OUTPUTMODE\_TOGGLE**
- **TIMER\_D\_OUTPUTMODE\_RESET**
- **TIMER\_D\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_D\_OUTPUTMODE\_RESET\_SET**

Referenced by `Timer_D_outputPWM()`.

## compareRegister

```
uint16_t Timer_D_outputPWMParam::compareRegister
```

Selects the compare register being used.

Valid values are:

- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_0**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_1**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_2**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_3**



- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_4**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_5**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_6**

Referenced by `Timer_D_outputPWM()`.

The documentation for this struct was generated from the following file:

- `timer_d.h`

## 47.5 SD24\_B\_initParam Struct Reference

Used in the `SD24_B_init()` function as the `param` parameter.

```
#include <sd24_b.h>
```

### Data Fields

- `uint16_t` [clockSourceSelect](#)
- `uint16_t` [clockPreDivider](#)
- `uint16_t` [clockDivider](#)
- `uint16_t` [referenceSelect](#)

### 47.5.1 Detailed Description

Used in the `SD24_B_init()` function as the `param` parameter.

### 47.5.2 Field Documentation

#### clockDivider

```
uint16_t SD24_B_initParam::clockDivider
```

Selects the amount that the clock will be divided.

Valid values are:

- **SD24\_B\_CLOCKDIVIDER\_1** [Default]
- **SD24\_B\_CLOCKDIVIDER\_2**
- **SD24\_B\_CLOCKDIVIDER\_3**
- **SD24\_B\_CLOCKDIVIDER\_4**
- **SD24\_B\_CLOCKDIVIDER\_5**
- **SD24\_B\_CLOCKDIVIDER\_6**
- **SD24\_B\_CLOCKDIVIDER\_7**
- **SD24\_B\_CLOCKDIVIDER\_8**
- **SD24\_B\_CLOCKDIVIDER\_9**

- SD24\_B\_CLOCKDIVIDER\_10
- SD24\_B\_CLOCKDIVIDER\_11
- SD24\_B\_CLOCKDIVIDER\_12
- SD24\_B\_CLOCKDIVIDER\_13
- SD24\_B\_CLOCKDIVIDER\_14
- SD24\_B\_CLOCKDIVIDER\_15
- SD24\_B\_CLOCKDIVIDER\_16
- SD24\_B\_CLOCKDIVIDER\_17
- SD24\_B\_CLOCKDIVIDER\_18
- SD24\_B\_CLOCKDIVIDER\_19
- SD24\_B\_CLOCKDIVIDER\_20
- SD24\_B\_CLOCKDIVIDER\_21
- SD24\_B\_CLOCKDIVIDER\_22
- SD24\_B\_CLOCKDIVIDER\_23
- SD24\_B\_CLOCKDIVIDER\_24
- SD24\_B\_CLOCKDIVIDER\_25
- SD24\_B\_CLOCKDIVIDER\_26
- SD24\_B\_CLOCKDIVIDER\_27
- SD24\_B\_CLOCKDIVIDER\_28
- SD24\_B\_CLOCKDIVIDER\_29
- SD24\_B\_CLOCKDIVIDER\_30
- SD24\_B\_CLOCKDIVIDER\_31
- SD24\_B\_CLOCKDIVIDER\_32

Referenced by SD24\_B\_init().

## clockPreDivider

```
uint16_t SD24_B_initParam::clockPreDivider
```

Selects the amount that the clock will be predivided  
Valid values are:

- SD24\_B\_PRECLOCKDIVIDER\_1 [Default]
- SD24\_B\_PRECLOCKDIVIDER\_2
- SD24\_B\_PRECLOCKDIVIDER\_4
- SD24\_B\_PRECLOCKDIVIDER\_8
- SD24\_B\_PRECLOCKDIVIDER\_16
- SD24\_B\_PRECLOCKDIVIDER\_32
- SD24\_B\_PRECLOCKDIVIDER\_64
- SD24\_B\_PRECLOCKDIVIDER\_128

Referenced by SD24\_B\_init().

## clockSourceSelect

```
uint16_t SD24_B_initParam::clockSourceSelect
```

Selects the clock that will be used as the SD24\_B core

Valid values are:

- **SD24\_B\_CLOCKSOURCE\_MCLK** [Default]
- **SD24\_B\_CLOCKSOURCE\_SMCLK**
- **SD24\_B\_CLOCKSOURCE\_ACLK**
- **SD24\_B\_CLOCKSOURCE\_SD24CLK**

Referenced by SD24\_B\_init().

## referenceSelect

```
uint16_t SD24_B_initParam::referenceSelect
```

Selects the reference source for the SD24\_B core

Valid values are:

- **SD24\_B\_REF\_EXTERNAL** [Default]
- **SD24\_B\_REF\_INTERNAL**

Referenced by SD24\_B\_init().

The documentation for this struct was generated from the following file:

- sd24\_b.h

## 47.6 USCI\_B\_SPI\_changeMasterClockParam Struct Reference

Used in the [USCI\\_B\\_SPI\\_changeMasterClock\(\)](#) function as the param parameter.

```
#include <usci_b_spi.h>
```

### Data Fields

- `uint32_t` [clockSourceFrequency](#)  
*Is the frequency of the selected clock source.*
- `uint32_t` [desiredSpiClock](#)  
*Is the desired clock rate for SPI communication.*

### 47.6.1 Detailed Description

Used in the [USCI\\_B\\_SPI\\_changeMasterClock\(\)](#) function as the param parameter.

The documentation for this struct was generated from the following file:

- `usci_b_spi.h`

## 47.7 Timer\_A\_initUpModeParam Struct Reference

Used in the [Timer\\_A\\_initUpMode\(\)](#) function as the param parameter.

```
#include <timer_a.h>
```

### Data Fields

- `uint16_t clockSource`
- `uint16_t clockSourceDivider`
- `uint16_t timerPeriod`
- `uint16_t timerInterruptEnable_TAIE`
- `uint16_t captureCompareInterruptEnable_CCR0_CCIE`
- `uint16_t timerClear`
- `bool startTimer`

*Whether to start the timer immediately.*

### 47.7.1 Detailed Description

Used in the [Timer\\_A\\_initUpMode\(\)](#) function as the param parameter.

### 47.7.2 Field Documentation

#### captureCompareInterruptEnable\_CCR0\_CCIE

```
uint16_t Timer_A_initUpModeParam::captureCompareInterruptEnable_CCR0_CCIE
```

Is to enable or disable Timer\_A CCR0 captureComapre interrupt.

Valid values are:

- **TIMER\_A\_CCIE\_CCR0\_INTERRUPT\_ENABLE**
- **TIMER\_A\_CCIE\_CCR0\_INTERRUPT\_DISABLE** [Default]

Referenced by [Timer\\_A\\_initUpMode\(\)](#).

#### clockSource

```
uint16_t Timer_A_initUpModeParam::clockSource
```

Selects Clock source.

Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]

- **TIMER\_A\_CLOCKSOURCE\_ACLK**
- **TIMER\_A\_CLOCKSOURCE\_SMCLK**
- **TIMER\_A\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by `Timer_A_initUpMode()`.

## clockSourceDivider

```
uint16_t Timer_A_initUpModeParam::clockSourceDivider
```

Is the desired divider for the clock source

Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_A_initUpMode()`.

## timerClear

```
uint16_t Timer_A_initUpModeParam::timerClear
```

Decides if `Timer_A` clock divider, count direction, count need to be reset.

Valid values are:

- **TIMER\_A\_DO\_CLEAR**
- **TIMER\_A\_SKIP\_CLEAR** [Default]

Referenced by `Timer_A_initUpMode()`.

## timerInterruptEnable\_TAIE

```
uint16_t Timer_A_initUpModeParam::timerInterruptEnable_TAIE
```

Is to enable or disable Timer\_A interrupt

Valid values are:

- **TIMER\_A\_TAIE\_INTERRUPT\_ENABLE**
- **TIMER\_A\_TAIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_A_initUpMode()`.

## timerPeriod

```
uint16_t Timer_A_initUpModeParam::timerPeriod
```

Is the specified Timer\_A period. This is the value that gets written into the CCR0. Limited to 16 bits[uint16\_t]

Referenced by `Timer_A_initUpMode()`.

The documentation for this struct was generated from the following file:

- `timer_a.h`

## 47.8 USCI\_B\_I2C\_initMasterParam Struct Reference

Used in the [USCI\\_B\\_I2C\\_initMaster\(\)](#) function as the param parameter.

```
#include <usci_b_i2c.h>
```

### Data Fields

- `uint8_t` [selectClockSource](#)
- `uint32_t` [i2cClk](#)  
*Is the rate of the clock supplied to the I2C module.*
- `uint32_t` [dataRate](#)

### 47.8.1 Detailed Description

Used in the [USCI\\_B\\_I2C\\_initMaster\(\)](#) function as the param parameter.

### 47.8.2 Field Documentation

#### dataRate

```
uint32_t USCI_B_I2C_initMasterParam::dataRate
```

Set up for selecting data transfer rate.  
Valid values are:

- **USCI\_B\_I2C\_SET\_DATA\_RATE\_400KBPS**
- **USCI\_B\_I2C\_SET\_DATA\_RATE\_100KBPS**

Referenced by USCI\_B\_I2C\_initMaster().

## selectClockSource

```
uint8_t USCI_B_I2C_initMasterParam::selectClockSource
```

Is the clocksource.  
Valid values are:

- **USCI\_B\_I2C\_CLOCKSOURCE\_ACLK**
- **USCI\_B\_I2C\_CLOCKSOURCE\_SMCLK**

Referenced by USCI\_B\_I2C\_initMaster().

The documentation for this struct was generated from the following file:

- usci\_b\_i2c.h

## 47.9 EUSCI\_B\_SPI\_initSlaveParam Struct Reference

Used in the [EUSCI\\_B\\_SPI\\_initSlave\(\)](#) function as the param parameter.

```
#include <eusci_b_spi.h>
```

### Data Fields

- uint16\_t [msbFirst](#)
- uint16\_t [clockPhase](#)
- uint16\_t [clockPolarity](#)
- uint16\_t [spiMode](#)

### 47.9.1 Detailed Description

Used in the [EUSCI\\_B\\_SPI\\_initSlave\(\)](#) function as the param parameter.

### 47.9.2 Field Documentation

#### clockPhase

```
uint16_t EUSCI_B_SPI_initSlaveParam::clockPhase
```

Is clock phase select.  
Valid values are:

- **EUSCI\_B\_SPI\_PHASE\_DATA\_CHANGED\_ONFIRST\_CAPTURED\_ON\_NEXT** [Default]
- **EUSCI\_B\_SPI\_PHASE\_DATA\_CAPTURED\_ONFIRST\_CHANGED\_ON\_NEXT**

Referenced by EUSCI\_B\_SPI\_initSlave().

## clockPolarity

```
uint16_t EUSCI_B_SPI_initSlaveParam::clockPolarity
```

Is clock polarity select  
Valid values are:

- **EUSCI\_B\_SPI\_CLOCKPOLARITY\_INACTIVITY\_HIGH**
- **EUSCI\_B\_SPI\_CLOCKPOLARITY\_INACTIVITY\_LOW** [Default]

Referenced by EUSCI\_B\_SPI\_initSlave().

## msbFirst

```
uint16_t EUSCI_B_SPI_initSlaveParam::msbFirst
```

Controls the direction of the receive and transmit shift register.  
Valid values are:

- **EUSCI\_B\_SPI\_MSB\_FIRST**
- **EUSCI\_B\_SPI\_LSB\_FIRST** [Default]

Referenced by EUSCI\_B\_SPI\_initSlave().

## spiMode

```
uint16_t EUSCI_B_SPI_initSlaveParam::spiMode
```

Is SPI mode select  
Valid values are:

- **EUSCI\_B\_SPI\_3PIN**
- **EUSCI\_B\_SPI\_4PIN\_UCxSTE\_ACTIVE\_HIGH**
- **EUSCI\_B\_SPI\_4PIN\_UCxSTE\_ACTIVE\_LOW**

Referenced by EUSCI\_B\_SPI\_initSlave().

The documentation for this struct was generated from the following file:

- eusci\_b\_spi.h



## 47.10 Timer\_A\_initCompareModeParam Struct Reference

Used in the [Timer\\_A\\_initCompareMode\(\)](#) function as the param parameter.

```
#include <timer_a.h>
```

### Data Fields

- uint16\_t [compareRegister](#)
- uint16\_t [compareInterruptEnable](#)
- uint16\_t [compareOutputMode](#)
- uint16\_t [compareValue](#)

*Is the count to be compared with in compare mode.*

### 47.10.1 Detailed Description

Used in the [Timer\\_A\\_initCompareMode\(\)](#) function as the param parameter.

### 47.10.2 Field Documentation

#### compareInterruptEnable

```
uint16_t Timer_A_initCompareModeParam::compareInterruptEnable
```

Is to enable or disable timer captureCompare interrupt.

Valid values are:

- **TIMER\_A\_CAPTURECOMPARE\_INTERRUPT\_DISABLE** [Default]
- **TIMER\_A\_CAPTURECOMPARE\_INTERRUPT\_ENABLE**

Referenced by [Timer\\_A\\_initCompareMode\(\)](#).

#### compareOutputMode

```
uint16_t Timer_A_initCompareModeParam::compareOutputMode
```

Specifies the output mode.

Valid values are:

- **TIMER\_A\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_A\_OUTPUTMODE\_SET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_A\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE**
- **TIMER\_A\_OUTPUTMODE\_RESET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE\_SET**

- **TIMER\_A\_OUTPUTMODE\_RESET\_SET**

Referenced by `Timer_A_initCompareMode()`.

## compareRegister

```
uint16_t Timer_A_initCompareModeParam::compareRegister
```

Selects the Capture register being used. Refer to datasheet to ensure the device has the capture compare register being used.

Valid values are:

- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_0**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_1**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_2**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_3**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_4**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_5**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_6**

Referenced by `Timer_A_initCompareMode()`.

The documentation for this struct was generated from the following file:

- `timer_a.h`

## 47.11 EUSCI\_B\_SPI\_changeMasterClockParam Struct Reference

Used in the `EUSCI_B_SPI_changeMasterClock()` function as the param parameter.

```
#include <eusci_b_spi.h>
```

### Data Fields

- `uint32_t` [clockSourceFrequency](#)  
*Is the frequency of the selected clock source.*
- `uint32_t` [desiredSpiClock](#)  
*Is the desired clock rate for SPI communication.*

#### 47.11.1 Detailed Description

Used in the `EUSCI_B_SPI_changeMasterClock()` function as the param parameter.

The documentation for this struct was generated from the following file:

- `eusci_b_spi.h`

## 47.12 Timer\_B\_initUpDownModeParam Struct Reference

Used in the [Timer\\_B\\_initUpDownMode\(\)](#) function as the param parameter.

```
#include <timer_b.h>
```

### Data Fields

- [uint16\\_t clockSource](#)
- [uint16\\_t clockSourceDivider](#)
- [uint16\\_t timerPeriod](#)  
*Is the specified Timer\_B period.*
- [uint16\\_t timerInterruptEnable\\_TBIE](#)
- [uint16\\_t captureCompareInterruptEnable\\_CCR0\\_CCIE](#)
- [uint16\\_t timerClear](#)
- [bool startTimer](#)  
*Whether to start the timer immediately.*

### 47.12.1 Detailed Description

Used in the [Timer\\_B\\_initUpDownMode\(\)](#) function as the param parameter.

### 47.12.2 Field Documentation

#### captureCompareInterruptEnable\_CCR0\_CCIE

```
uint16_t Timer_B_initUpDownModeParam::captureCompareInterruptEnable_CCR0_CCIE
```

Is to enable or disable Timer\_B CCR0 capture compare interrupt.

Valid values are:

- **TIMER\_B\_CCIE\_CCR0\_INTERRUPT\_ENABLE**
- **TIMER\_B\_CCIE\_CCR0\_INTERRUPT\_DISABLE** [Default]

Referenced by [Timer\\_B\\_initUpDownMode\(\)](#).

#### clockSource

```
uint16_t Timer_B_initUpDownModeParam::clockSource
```

Selects the clock source

Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_B\_CLOCKSOURCE\_ACLK**
- **TIMER\_B\_CLOCKSOURCE\_SMCLK**
- **TIMER\_B\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by [Timer\\_B\\_initUpDownMode\(\)](#).

## clockSourceDivider

```
uint16_t Timer_B_initUpDownModeParam::clockSourceDivider
```

Is the divider for Clock source.

Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_B_initUpDownMode()`.

## timerClear

```
uint16_t Timer_B_initUpDownModeParam::timerClear
```

Decides if Timer\_B clock divider, count direction, count need to be reset.

Valid values are:

- **TIMER\_B\_DO\_CLEAR**
- **TIMER\_B\_SKIP\_CLEAR** [Default]

Referenced by `Timer_B_initUpDownMode()`.

## timerInterruptEnable\_TBIE

```
uint16_t Timer_B_initUpDownModeParam::timerInterruptEnable_TBIE
```

Is to enable or disable Timer\_B interrupt

Valid values are:

- **TIMER\_B\_TBIE\_INTERRUPT\_ENABLE**
- **TIMER\_B\_TBIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_B_initUpDownMode()`.

The documentation for this struct was generated from the following file:

- `timer_b.h`

## 47.13 Timer\_D\_initUpModeParam Struct Reference

Used in the `Timer_D_initUpMode()` function as the param parameter.

```
#include <timer_d.h>
```

### Data Fields

- `uint16_t` [clockSource](#)
- `uint16_t` [clockSourceDivider](#)
- `uint16_t` [clockingMode](#)
- `uint16_t` [timerPeriod](#)
- `uint16_t` [timerInterruptEnable\\_TDIE](#)
- `uint16_t` [captureCompareInterruptEnable\\_CCR0\\_CCIE](#)
- `uint16_t` [timerClear](#)

### 47.13.1 Detailed Description

Used in the `Timer_D_initUpMode()` function as the param parameter.

### 47.13.2 Field Documentation

#### captureCompareInterruptEnable\_CCR0\_CCIE

```
uint16_t Timer_D_initUpModeParam::captureCompareInterruptEnable_CCR0_CCIE
```

Is to enable or disable timer CCR0 captureComapre interrupt.

Valid values are:

- **TIMER\_D\_CCIE\_CCR0\_INTERRUPT\_ENABLE**
- **TIMER\_D\_CCIE\_CCR0\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_D_initUpMode()`.

## clockingMode

```
uint16_t Timer_D_initUpModeParam::clockingMode
```

Is the selected clock mode register values.

Valid values are:

- **TIMER\_D\_CLOCKINGMODE\_EXTERNAL\_CLOCK** [Default]
- **TIMER\_D\_CLOCKINGMODE\_HIRES\_LOCAL\_CLOCK**
- **TIMER\_D\_CLOCKINGMODE\_AUXILIARY\_CLK**

Referenced by `Timer_D_initUpMode()`.

## clockSource

```
uint16_t Timer_D_initUpModeParam::clockSource
```

Selects Clock source.

Valid values are:

- **TIMER\_D\_CLOCKSOURCE\_EXTERNAL\_TDCLK** [Default]
- **TIMER\_D\_CLOCKSOURCE\_ACLK**
- **TIMER\_D\_CLOCKSOURCE\_SMCLK**
- **TIMER\_D\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TDCLK**

Referenced by `Timer_D_initUpMode()`.

## clockSourceDivider

```
uint16_t Timer_D_initUpModeParam::clockSourceDivider
```

Is the divider for clock source.

Valid values are:

- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_20**

- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_D_initUpMode()`.

## timerClear

```
uint16_t Timer_D_initUpModeParam::timerClear
```

Decides if timer clock divider, count direction, count need to be reset.  
Valid values are:

- **TIMER\_D\_DO\_CLEAR**
- **TIMER\_D\_SKIP\_CLEAR** [Default]

Referenced by `Timer_D_initUpMode()`.

## timerInterruptEnable\_TDIE

```
uint16_t Timer_D_initUpModeParam::timerInterruptEnable_TDIE
```

Is to enable or disable timer interrupt  
Valid values are:

- **TIMER\_D\_TDIE\_INTERRUPT\_ENABLE**
- **TIMER\_D\_TDIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_D_initUpMode()`.

## timerPeriod

```
uint16_t Timer_D_initUpModeParam::timerPeriod
```

Is the specified timer period. This is the value that gets written into the CCR0. Limited to 16 bits  
[uint16\_t]

Referenced by `Timer_D_initUpMode()`.

The documentation for this struct was generated from the following file:

- `timer_d.h`

## 47.14 Timer\_A\_initContinuousModeParam Struct Reference

Used in the [Timer\\_A\\_initContinuousMode\(\)](#) function as the param parameter.

```
#include <timer_a.h>
```

### Data Fields

- uint16\_t [clockSource](#)
- uint16\_t [clockSourceDivider](#)
- uint16\_t [timerInterruptEnable\\_TAIE](#)
- uint16\_t [timerClear](#)
- bool [startTimer](#)

*Whether to start the timer immediately.*

### 47.14.1 Detailed Description

Used in the [Timer\\_A\\_initContinuousMode\(\)](#) function as the param parameter.

### 47.14.2 Field Documentation

#### clockSource

```
uint16_t Timer_A_initContinuousModeParam::clockSource
```

Selects Clock source.

Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_A\_CLOCKSOURCE\_ACLK**
- **TIMER\_A\_CLOCKSOURCE\_SMCLK**
- **TIMER\_A\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by [Timer\\_A\\_initContinuousMode\(\)](#).

#### clockSourceDivider

```
uint16_t Timer_A_initContinuousModeParam::clockSourceDivider
```

Is the desired divider for the clock source

Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_4**



- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_A_initContinuousMode()`.

## timerClear

```
uint16_t Timer_A_initContinuousModeParam::timerClear
```

Decides if `Timer_A` clock divider, count direction, count need to be reset.  
Valid values are:

- **TIMER\_A\_DO\_CLEAR**
- **TIMER\_A\_SKIP\_CLEAR** [Default]

Referenced by `Timer_A_initContinuousMode()`.

## timerInterruptEnable\_TAIE

```
uint16_t Timer_A_initContinuousModeParam::timerInterruptEnable_TAIE
```

Is to enable or disable `Timer_A` interrupt  
Valid values are:

- **TIMER\_A\_TAIE\_INTERRUPT\_ENABLE**
- **TIMER\_A\_TAIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_A_initContinuousMode()`.

The documentation for this struct was generated from the following file:

- `timer_a.h`

## 47.15 EUSCI\_B\_I2C\_initSlaveParam Struct Reference

Used in the [EUSCI\\_B\\_I2C\\_initSlave\(\)](#) function as the param parameter.

```
#include <eusci_b_i2c.h>
```

### Data Fields

- `uint8_t` [slaveAddress](#)  
*7-bit slave address*
- `uint8_t` [slaveAddressOffset](#)
- `uint32_t` [slaveOwnAddressEnable](#)

### 47.15.1 Detailed Description

Used in the [EUSCI\\_B\\_I2C\\_initSlave\(\)](#) function as the param parameter.

### 47.15.2 Field Documentation

#### slaveAddressOffset

```
uint8_t EUSCI_B_I2C_initSlaveParam::slaveAddressOffset
```

Own address Offset referred to- 'x' value of UCBxI2COAx.  
Valid values are:

- **EUSCI\_B\_I2C\_OWN\_ADDRESS\_OFFSET0**
- **EUSCI\_B\_I2C\_OWN\_ADDRESS\_OFFSET1**
- **EUSCI\_B\_I2C\_OWN\_ADDRESS\_OFFSET2**
- **EUSCI\_B\_I2C\_OWN\_ADDRESS\_OFFSET3**

Referenced by [EUSCI\\_B\\_I2C\\_initSlave\(\)](#).

#### slaveOwnAddressEnable

```
uint32_t EUSCI_B_I2C_initSlaveParam::slaveOwnAddressEnable
```

Selects if the specified address is enabled or disabled.  
Valid values are:

- **EUSCI\_B\_I2C\_OWN\_ADDRESS\_DISABLE**
- **EUSCI\_B\_I2C\_OWN\_ADDRESS\_ENABLE**

Referenced by [EUSCI\\_B\\_I2C\\_initSlave\(\)](#).

The documentation for this struct was generated from the following file:

- `eusci_b_i2c.h`

## 47.16 Comp\_B\_configureReferenceVoltageParam Struct Reference

Used in the [Comp\\_B\\_configureReferenceVoltage\(\)](#) function as the param parameter.

```
#include <comp_b.h>
```

### Data Fields

- [uint16\\_t supplyVoltageReferenceBase](#)
- [uint16\\_t lowerLimitSupplyVoltageFractionOf32](#)
- [uint16\\_t upperLimitSupplyVoltageFractionOf32](#)
- [uint16\\_t referenceAccuracy](#)

#### 47.16.1 Detailed Description

Used in the [Comp\\_B\\_configureReferenceVoltage\(\)](#) function as the param parameter.

#### 47.16.2 Field Documentation

##### lowerLimitSupplyVoltageFractionOf32

```
uint16_t Comp_B_configureReferenceVoltageParam::lowerLimitSupplyVoltageFractionOf32
```

Is the numerator of the equation to generate the reference voltage for the lower limit reference voltage.

Referenced by [Comp\\_B\\_configureReferenceVoltage\(\)](#).

##### referenceAccuracy

```
uint16_t Comp_B_configureReferenceVoltageParam::referenceAccuracy
```

is the reference accuracy setting of the Comp\_B. Clocked is for low power/low accuracy. Valid values are:

- **COMP\_B\_ACCURACY\_STATIC**
- **COMP\_B\_ACCURACY\_CLOCKED**

Referenced by [Comp\\_B\\_configureReferenceVoltage\(\)](#).

##### supplyVoltageReferenceBase

```
uint16_t Comp_B_configureReferenceVoltageParam::supplyVoltageReferenceBase
```

Decides the source and max amount of Voltage that can be used as a reference. Valid values are:

- **COMP\_B\_VREFBASE\_VCC**
- **COMP\_B\_VREFBASE1\_5V**
- **COMP\_B\_VREFBASE2\_0V**
- **COMP\_B\_VREFBASE2\_5V**

Referenced by `Comp_B_configureReferenceVoltage()`.

### upperLimitSupplyVoltageFractionOf32

```
uint16_t Comp_B_configureReferenceVoltageParam::upperLimitSupplyVoltageFractionOf32
```

Is the numerator of the equation to generate the reference voltage for the upper limit reference voltage.

Referenced by `Comp_B_configureReferenceVoltage()`.

The documentation for this struct was generated from the following file:

- `comp_b.h`

## 47.17 Timer\_A\_initCaptureModeParam Struct Reference

Used in the `Timer_A_initCaptureMode()` function as the param parameter.

```
#include <timer_a.h>
```

### Data Fields

- `uint16_t captureRegister`
- `uint16_t captureMode`
- `uint16_t captureInputSelect`
- `uint16_t synchronizeCaptureSource`
- `uint16_t captureInterruptEnable`
- `uint16_t captureOutputMode`

#### 47.17.1 Detailed Description

Used in the `Timer_A_initCaptureMode()` function as the param parameter.

#### 47.17.2 Field Documentation

##### captureInputSelect

```
uint16_t Timer_A_initCaptureModeParam::captureInputSelect
```

Decides the Input Select  
Valid values are:

- **TIMER\_A\_CAPTURE\_INPUTSELECT\_CC1xA**
- **TIMER\_A\_CAPTURE\_INPUTSELECT\_CC1xB**
- **TIMER\_A\_CAPTURE\_INPUTSELECT\_GND**
- **TIMER\_A\_CAPTURE\_INPUTSELECT\_Vcc**

Referenced by `Timer_A_initCaptureMode()`.

## captureInterruptEnable

```
uint16_t Timer_A_initCaptureModeParam::captureInterruptEnable
```

Is to enable or disable timer captureCompare interrupt.

Valid values are:

- **TIMER\_A\_CAPTURECOMPARE\_INTERRUPT\_DISABLE** [Default]
- **TIMER\_A\_CAPTURECOMPARE\_INTERRUPT\_ENABLE**

Referenced by `Timer_A_initCaptureMode()`.

## captureMode

```
uint16_t Timer_A_initCaptureModeParam::captureMode
```

Is the capture mode selected.

Valid values are:

- **TIMER\_A\_CAPTUREMODE\_NO\_CAPTURE** [Default]
- **TIMER\_A\_CAPTUREMODE\_RISING\_EDGE**
- **TIMER\_A\_CAPTUREMODE\_FALLING\_EDGE**
- **TIMER\_A\_CAPTUREMODE\_RISING\_AND\_FALLING\_EDGE**

Referenced by `Timer_A_initCaptureMode()`.

## captureOutputMode

```
uint16_t Timer_A_initCaptureModeParam::captureOutputMode
```

Specifies the output mode.

Valid values are:

- **TIMER\_A\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_A\_OUTPUTMODE\_SET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_A\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE**
- **TIMER\_A\_OUTPUTMODE\_RESET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_A\_OUTPUTMODE\_RESET\_SET**

Referenced by `Timer_A_initCaptureMode()`.

## captureRegister

```
uint16_t Timer_A_initCaptureModeParam::captureRegister
```

Selects the Capture register being used. Refer to datasheet to ensure the device has the capture compare register being used.

Valid values are:

- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_0**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_1**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_2**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_3**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_4**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_5**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_6**

Referenced by `Timer_A_initCaptureMode()`.

## synchronizeCaptureSource

```
uint16_t Timer_A_initCaptureModeParam::synchronizeCaptureSource
```

Decides if capture source should be synchronized with timer clock

Valid values are:

- **TIMER\_A\_CAPTURE\_ASYNCHRONOUS** [Default]
- **TIMER\_A\_CAPTURE\_SYNCHRONOUS**

Referenced by `Timer_A_initCaptureMode()`.

The documentation for this struct was generated from the following file:

- `timer_a.h`

## 47.18 USCI\_A\_UART\_initParam Struct Reference

Used in the `USCI_A_UART_init()` function as the param parameter.

```
#include <usci_a_uart.h>
```

### Data Fields

- `uint8_t` [selectClockSource](#)
- `uint16_t` [clockPrescalar](#)  
*Is the value to be written into UCBRx bits.*
- `uint8_t` [firstModReg](#)
- `uint8_t` [secondModReg](#)
- `uint8_t` [parity](#)
- `uint8_t` [msborLsbFirst](#)

- uint8\_t [numberOfStopBits](#)
- uint8\_t [uartMode](#)
- uint8\_t [overSampling](#)

## 47.18.1 Detailed Description

Used in the [USCI\\_A\\_UART\\_init\(\)](#) function as the param parameter.

## 47.18.2 Field Documentation

### firstModReg

```
uint8_t USCI_A_UART_initParam::firstModReg
```

Is First modulation stage register setting. This value is a pre- calculated value which can be obtained from the Device Users Guide. This value is written into UCBRFx bits of UCAxMCTLW.

Referenced by [USCI\\_A\\_UART\\_init\(\)](#).

### msborLsbFirst

```
uint8_t USCI_A_UART_initParam::msborLsbFirst
```

Controls direction of receive and transmit shift register.  
Valid values are:

- **USCI\_A\_UART\_MSB\_FIRST**
- **USCI\_A\_UART\_LSB\_FIRST** [Default]

Referenced by [USCI\\_A\\_UART\\_init\(\)](#).

### numberOfStopBits

```
uint8_t USCI_A_UART_initParam::numberOfStopBits
```

Indicates one/two STOP bits  
Valid values are:

- **USCI\_A\_UART\_ONE\_STOP\_BIT** [Default]
- **USCI\_A\_UART\_TWO\_STOP\_BITS**

Referenced by [USCI\\_A\\_UART\\_init\(\)](#).

### overSampling

```
uint8_t USCI_A_UART_initParam::overSampling
```

Indicates low frequency or oversampling baud generation  
Valid values are:

- **USCI\_A\_UART\_OVERSAMPLING\_BAUDRATE\_GENERATION**
- **USCI\_A\_UART\_LOW\_FREQUENCY\_BAUDRATE\_GENERATION**

Referenced by USCI\_A\_UART\_init().

## parity

```
uint8_t USCI_A_UART_initParam::parity
```

Is the desired parity.

Valid values are:

- **USCI\_A\_UART\_NO\_PARITY** [Default]
- **USCI\_A\_UART\_ODD\_PARITY**
- **USCI\_A\_UART\_EVEN\_PARITY**

Referenced by USCI\_A\_UART\_init().

## secondModReg

```
uint8_t USCI_A_UART_initParam::secondModReg
```

Is Second modulation stage register setting. This value is a pre- calculated value which can be obtained from the Device Users Guide. This value is written into UCBR<sub>Sx</sub> bits of UCAXMCTLW.

Referenced by USCI\_A\_UART\_init().

## selectClockSource

```
uint8_t USCI_A_UART_initParam::selectClockSource
```

Selects Clock source.

Valid values are:

- **USCI\_A\_UART\_CLOCKSOURCE\_SMCLK**
- **USCI\_A\_UART\_CLOCKSOURCE\_ACLK**

Referenced by USCI\_A\_UART\_init().

## uartMode

```
uint8_t USCI_A_UART_initParam::uartMode
```

Selects the mode of operation

Valid values are:

- **USCI\_A\_UART\_MODE** [Default]
- **USCI\_A\_UART\_IDLE\_LINE\_MULTI\_PROCESSOR\_MODE**
- **USCI\_A\_UART\_ADDRESS\_BIT\_MULTI\_PROCESSOR\_MODE**



- **USCI\_A\_UART\_AUTOMATIC\_BAUDRATE\_DETECTION\_MODE**

Referenced by `USCI_A_UART_init()`.

The documentation for this struct was generated from the following file:

- `usci_a_uart.h`

## 47.19 RTC\_C\_configureCalendarAlarmParam Struct Reference

Used in the `RTC_C_configureCalendarAlarm()` function as the `param` parameter.

```
#include <rtc_c.h>
```

### Data Fields

- `uint8_t minutesAlarm`
- `uint8_t hoursAlarm`
- `uint8_t dayOfWeekAlarm`
- `uint8_t dayOfMonthAlarm`

### 47.19.1 Detailed Description

Used in the `RTC_C_configureCalendarAlarm()` function as the `param` parameter.

### 47.19.2 Field Documentation

#### dayOfMonthAlarm

```
uint8_t RTC_C_configureCalendarAlarmParam::dayOfMonthAlarm
```

Is the alarm condition for the day of the month.

Valid values are:

- **RTC\_C\_ALARMCONDITION\_OFF** [Default]

Referenced by `RTC_C_configureCalendarAlarm()`.

#### dayOfWeekAlarm

```
uint8_t RTC_C_configureCalendarAlarmParam::dayOfWeekAlarm
```

Is the alarm condition for the day of week.

Valid values are:

- **RTC\_C\_ALARMCONDITION\_OFF** [Default]

Referenced by `RTC_C_configureCalendarAlarm()`.

## hoursAlarm

```
uint8_t RTC_C_configureCalendarAlarmParam::hoursAlarm
```

Is the alarm condition for the hours.

Valid values are:

- **RTC\_C\_ALARMCONDITION\_OFF** [Default]

Referenced by `RTC_C_configureCalendarAlarm()`.

## minutesAlarm

```
uint8_t RTC_C_configureCalendarAlarmParam::minutesAlarm
```

Is the alarm condition for the minutes.

Valid values are:

- **RTC\_C\_ALARMCONDITION\_OFF** [Default]

Referenced by `RTC_C_configureCalendarAlarm()`.

The documentation for this struct was generated from the following file:

- `rtc.c.h`

## 47.20 USCI\_A\_SPI\_initMasterParam Struct Reference

Used in the [USCI\\_A\\_SPI\\_initMaster\(\)](#) function as the param parameter.

```
#include <usci_a_spi.h>
```

### Data Fields

- `uint8_t` [selectClockSource](#)
- `uint32_t` [clockSourceFrequency](#)  
*Is the frequency of the selected clock source.*
- `uint32_t` [desiredSpiClock](#)  
*Is the desired clock rate for SPI communication.*
- `uint8_t` [msbFirst](#)
- `uint8_t` [clockPhase](#)
- `uint8_t` [clockPolarity](#)

### 47.20.1 Detailed Description

Used in the [USCI\\_A\\_SPI\\_initMaster\(\)](#) function as the param parameter.

## 47.20.2 Field Documentation

### clockPhase

```
uint8_t USCI_A_SPI_initMasterParam::clockPhase
```

Is clock phase select.

Valid values are:

- **USCI\_A\_SPI\_PHASE\_DATA\_CHANGED\_ONFIRST\_CAPTURED\_ON\_NEXT** [Default]
- **USCI\_A\_SPI\_PHASE\_DATA\_CAPTURED\_ONFIRST\_CHANGED\_ON\_NEXT**

Referenced by USCI\_A\_SPI\_initMaster().

### clockPolarity

```
uint8_t USCI_A_SPI_initMasterParam::clockPolarity
```

Valid values are:

- **USCI\_A\_SPI\_CLOCKPOLARITY\_INACTIVITY\_HIGH**
- **USCI\_A\_SPI\_CLOCKPOLARITY\_INACTIVITY\_LOW** [Default]

Referenced by USCI\_A\_SPI\_initMaster().

### msbFirst

```
uint8_t USCI_A_SPI_initMasterParam::msbFirst
```

Controls the direction of the receive and transmit shift register.

Valid values are:

- **USCI\_A\_SPI\_MSB\_FIRST**
- **USCI\_A\_SPI\_LSB\_FIRST** [Default]

Referenced by USCI\_A\_SPI\_initMaster().

### selectClockSource

```
uint8_t USCI_A_SPI_initMasterParam::selectClockSource
```

Selects Clock source.

Valid values are:

- **USCI\_A\_SPI\_CLOCKSOURCE\_ACLK**
- **USCI\_A\_SPI\_CLOCKSOURCE\_SMCLK**

Referenced by USCI\_A\_SPI\_initMaster().

The documentation for this struct was generated from the following file:

- `usci_a_spi.h`

## 47.21 USCI\_B\_SPI\_initMasterParam Struct Reference

Used in the `USCI_B_SPI_initMaster()` function as the param parameter.

```
#include <usci_b_spi.h>
```

### Data Fields

- `uint8_t` [selectClockSource](#)
- `uint32_t` [clockSourceFrequency](#)  
*Is the frequency of the selected clock source.*
- `uint32_t` [desiredSpiClock](#)  
*Is the desired clock rate for SPI communication.*
- `uint8_t` [msbFirst](#)
- `uint8_t` [clockPhase](#)
- `uint8_t` [clockPolarity](#)

### 47.21.1 Detailed Description

Used in the `USCI_B_SPI_initMaster()` function as the param parameter.

### 47.21.2 Field Documentation

#### clockPhase

```
uint8_t USCI_B_SPI_initMasterParam::clockPhase
```

Is clock phase select.  
Valid values are:

- `USCI_B_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT` [Default]
- `USCI_B_SPI_PHASE_DATA_CAPTURED_ONFIRST_CHANGED_ON_NEXT`

Referenced by `USCI_B_SPI_initMaster()`.

#### clockPolarity

```
uint8_t USCI_B_SPI_initMasterParam::clockPolarity
```

Valid values are:

- `USCI_B_SPI_CLOCKPOLARITY_INACTIVITY_HIGH`
- `USCI_B_SPI_CLOCKPOLARITY_INACTIVITY_LOW` [Default]

Referenced by `USCI_B_SPI_initMaster()`.

## msbFirst

```
uint8_t USCI_B_SPI_initMasterParam::msbFirst
```

Controls the direction of the receive and transmit shift register.

Valid values are:

- **USCI\_B\_SPI\_MSB\_FIRST**
- **USCI\_B\_SPI\_LSB\_FIRST** [Default]

Referenced by `USCI_B_SPI_initMaster()`.

## selectClockSource

```
uint8_t USCI_B_SPI_initMasterParam::selectClockSource
```

Selects Clock source.

Valid values are:

- **USCI\_B\_SPI\_CLOCKSOURCE\_ACLK**
- **USCI\_B\_SPI\_CLOCKSOURCE\_SMCLK**

Referenced by `USCI_B_SPI_initMaster()`.

The documentation for this struct was generated from the following file:

- `usci_b_spi.h`

## 47.22 TEC\_initExternalFaultInputParam Struct Reference

Used in the [TEC\\_initExternalFaultInput\(\)](#) function as the param parameter.

```
#include <tec.h>
```

### Data Fields

- `uint8_t` [selectedExternalFault](#)
- `uint16_t` [signalType](#)
- `uint8_t` [signalHold](#)
- `uint8_t` [polarityBit](#)

### 47.22.1 Detailed Description

Used in the [TEC\\_initExternalFaultInput\(\)](#) function as the param parameter.

## 47.22.2 Field Documentation

### polarityBit

```
uint8_t TEC_initExternalFaultInputParam::polarityBit
```

Is the selected signal type

Valid values are:

- **TEC\_EXTERNAL\_FAULT\_POLARITY\_FALLING\_EDGE\_OR\_LOW\_LEVEL** [Default]
- **TEC\_EXTERNAL\_FAULT\_POLARITY\_RISING\_EDGE\_OR\_HIGH\_LEVEL**

Referenced by TEC\_initExternalFaultInput().

### selectedExternalFault

```
uint8_t TEC_initExternalFaultInputParam::selectedExternalFault
```

Is the selected external fault

Valid values are:

- **TEC\_EXTERNAL\_FAULT\_0**
- **TEC\_EXTERNAL\_FAULT\_1**
- **TEC\_EXTERNAL\_FAULT\_2**
- **TEC\_EXTERNAL\_FAULT\_3**
- **TEC\_EXTERNAL\_FAULT\_4**
- **TEC\_EXTERNAL\_FAULT\_5**
- **TEC\_EXTERNAL\_FAULT\_6**

Referenced by TEC\_initExternalFaultInput().

### signalHold

```
uint8_t TEC_initExternalFaultInputParam::signalHold
```

Is the selected signal hold

Valid values are:

- **TEC\_EXTERNAL\_FAULT\_SIGNAL\_NOT\_HELD** [Default]
- **TEC\_EXTERNAL\_FAULT\_SIGNAL\_HELD**

Referenced by TEC\_initExternalFaultInput().

### signalType

```
uint16_t TEC_initExternalFaultInputParam::signalType
```

Is the selected signal type

Valid values are:

- `TEC_EXTERNAL_FAULT_SIGNALTYPE_EDGE_SENSITIVE` [Default]
- `TEC_EXTERNAL_FAULT_SIGNALTYPE_LEVEL_SENSITIVE`

Referenced by `TEC_initExternalFaultInput()`.

The documentation for this struct was generated from the following file:

- `tec.h`

## 47.23 USCI\_A\_SPI\_changeMasterClockParam Struct Reference

Used in the `USCI_A_SPI_changeMasterClock()` function as the `param` parameter.

```
#include <usci_a_spi.h>
```

### Data Fields

- `uint32_t clockSourceFrequency`  
*Is the frequency of the selected clock source.*
- `uint32_t desiredSpiClock`  
*Is the desired clock rate for SPI communication.*

### 47.23.1 Detailed Description

Used in the `USCI_A_SPI_changeMasterClock()` function as the `param` parameter.

The documentation for this struct was generated from the following file:

- `usci_a_spi.h`

## 47.24 SD24\_B\_initConverterParam Struct Reference

Used in the `SD24_B_initConverter()` function as the `param` parameter.

```
#include <sd24_b.h>
```

### Data Fields

- `uint8_t converter`
- `uint8_t alignment`
- `uint8_t startSelect`
- `uint8_t conversionMode`

## 47.24.1 Detailed Description

Used in the [SD24\\_B\\_initConverter\(\)](#) function as the param parameter.

## 47.24.2 Field Documentation

### alignment

```
uint8_t SD24_B_initConverterParam::alignment
```

Selects how the data will be aligned in result  
Valid values are:

- **SD24\_B\_ALIGN\_RIGHT** [Default]
- **SD24\_B\_ALIGN\_LEFT**

Referenced by [SD24\\_B\\_initConverter\(\)](#).

### conversionMode

```
uint8_t SD24_B_initConverterParam::conversionMode
```

Determines whether the converter will do continuous samples or a single sample  
Valid values are:

- **SD24\_B\_CONTINUOUS\_MODE** [Default]
- **SD24\_B\_SINGLE\_MODE**

Referenced by [SD24\\_B\\_initConverter\(\)](#).

### converter

```
uint8_t SD24_B_initConverterParam::converter
```

Selects the converter that will be configured. Check datasheet for available converters on device.  
Valid values are:

- **SD24\_B\_CONVERTER\_0**
- **SD24\_B\_CONVERTER\_1**
- **SD24\_B\_CONVERTER\_2**
- **SD24\_B\_CONVERTER\_3**
- **SD24\_B\_CONVERTER\_4**
- **SD24\_B\_CONVERTER\_5**
- **SD24\_B\_CONVERTER\_6**
- **SD24\_B\_CONVERTER\_7**

Referenced by [SD24\\_B\\_initConverter\(\)](#).



## startSelect

```
uint8_t SD24_B_initConverterParam::startSelect
```

Selects what will trigger the start of the converter

Valid values are:

- **SD24\_B\_CONVERSION\_SELECT\_SD24SC** [Default]
- **SD24\_B\_CONVERSION\_SELECT\_EXT1**
- **SD24\_B\_CONVERSION\_SELECT\_EXT2**
- **SD24\_B\_CONVERSION\_SELECT\_EXT3**
- **SD24\_B\_CONVERSION\_SELECT\_GROUP0**
- **SD24\_B\_CONVERSION\_SELECT\_GROUP1**
- **SD24\_B\_CONVERSION\_SELECT\_GROUP2**
- **SD24\_B\_CONVERSION\_SELECT\_GROUP3**

Referenced by `SD24_B_initConverter()`.

The documentation for this struct was generated from the following file:

- `sd24_b.h`

## 47.25 EUSCI\_A\_UART\_initParam Struct Reference

Used in the `EUSCI_A_UART_init()` function as the param parameter.

```
#include <eusci_a_uart.h>
```

### Data Fields

- `uint8_t` [selectClockSource](#)
- `uint16_t` [clockPrescalar](#)  
*Is the value to be written into UCBRx bits.*
- `uint8_t` [firstModReg](#)
- `uint8_t` [secondModReg](#)
- `uint8_t` [parity](#)
- `uint16_t` [msborLsbFirst](#)
- `uint16_t` [numberOfStopBits](#)
- `uint16_t` [uartMode](#)
- `uint8_t` [overSampling](#)

### 47.25.1 Detailed Description

Used in the `EUSCI_A_UART_init()` function as the param parameter.

## 47.25.2 Field Documentation

### firstModReg

```
uint8_t EUSCI_A_UART_initParam::firstModReg
```

Is First modulation stage register setting. This value is a pre- calculated value which can be obtained from the Device Users Guide. This value is written into UCBRFx bits of UCAXMCTLW.

Referenced by EUSCI\_A\_UART\_init().

### msborLsbFirst

```
uint16_t EUSCI_A_UART_initParam::msborLsbFirst
```

Controls direction of receive and transmit shift register.  
Valid values are:

- **EUSCI\_A\_UART\_MSB\_FIRST**
- **EUSCI\_A\_UART\_LSB\_FIRST** [Default]

Referenced by EUSCI\_A\_UART\_init().

### numberOfStopBits

```
uint16_t EUSCI_A_UART_initParam::numberOfStopBits
```

Indicates one/two STOP bits  
Valid values are:

- **EUSCI\_A\_UART\_ONE\_STOP\_BIT** [Default]
- **EUSCI\_A\_UART\_TWO\_STOP\_BITS**

Referenced by EUSCI\_A\_UART\_init().

### overSampling

```
uint8_t EUSCI_A_UART_initParam::overSampling
```

Indicates low frequency or oversampling baud generation  
Valid values are:

- **EUSCI\_A\_UART\_OVERSAMPLING\_BAUDRATE\_GENERATION**
- **EUSCI\_A\_UART\_LOW\_FREQUENCY\_BAUDRATE\_GENERATION**

Referenced by EUSCI\_A\_UART\_init().

### parity

```
uint8_t EUSCI_A_UART_initParam::parity
```

Is the desired parity.  
Valid values are:

- **EUSCI\_A\_UART\_NO\_PARITY** [Default]
- **EUSCI\_A\_UART\_ODD\_PARITY**
- **EUSCI\_A\_UART\_EVEN\_PARITY**

Referenced by EUSCI\_A\_UART\_init().

## secondModReg

```
uint8_t EUSCI_A_UART_initParam::secondModReg
```

Is Second modulation stage register setting. This value is a pre- calculated value which can be obtained from the Device Users Guide. This value is written into UCBR<sub>Sx</sub> bits of UCA<sub>x</sub>MCTLW.

Referenced by EUSCI\_A\_UART\_init().

## selectClockSource

```
uint8_t EUSCI_A_UART_initParam::selectClockSource
```

Selects Clock source. Refer to device specific datasheet for available options.  
Valid values are:

- **EUSCI\_A\_UART\_CLOCKSOURCE\_SMCLK**
- **EUSCI\_A\_UART\_CLOCKSOURCE\_ACLK**

Referenced by EUSCI\_A\_UART\_init().

## uartMode

```
uint16_t EUSCI_A_UART_initParam::uartMode
```

Selects the mode of operation  
Valid values are:

- **EUSCI\_A\_UART\_MODE** [Default]
- **EUSCI\_A\_UART\_IDLE\_LINE\_MULTI\_PROCESSOR\_MODE**
- **EUSCI\_A\_UART\_ADDRESS\_BIT\_MULTI\_PROCESSOR\_MODE**
- **EUSCI\_A\_UART\_AUTOMATIC\_BAUDRATE\_DETECTION\_MODE**

Referenced by EUSCI\_A\_UART\_init().

The documentation for this struct was generated from the following file:

- eusci\_a\_uart.h

## 47.26 Timer\_B\_outputPWMParm Struct Reference

Used in the [Timer\\_B\\_outputPWM\(\)](#) function as the param parameter.

```
#include <timer_b.h>
```

### Data Fields

- `uint16_t` [clockSource](#)
- `uint16_t` [clockSourceDivider](#)
- `uint16_t` [timerPeriod](#)  
*Selects the desired Timer.B period.*
- `uint16_t` [compareRegister](#)
- `uint16_t` [compareOutputMode](#)
- `uint16_t` [dutyCycle](#)  
*Specifies the dutycycle for the generated waveform.*

### 47.26.1 Detailed Description

Used in the [Timer\\_B\\_outputPWM\(\)](#) function as the param parameter.

### 47.26.2 Field Documentation

#### clockSource

```
uint16_t Timer_B_outputPWMParm::clockSource
```

Selects the clock source

Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_B\_CLOCKSOURCE\_ACLK**
- **TIMER\_B\_CLOCKSOURCE\_SMCLK**
- **TIMER\_B\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by [Timer\\_B\\_outputPWM\(\)](#).

#### clockSourceDivider

```
uint16_t Timer_B_outputPWMParm::clockSourceDivider
```

Is the divider for Clock source.

Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_3**

- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_B_outputPWM()`.

## compareOutputMode

```
uint16_t Timer_B_outputPWMPParam::compareOutputMode
```

Specifies the output mode.

Valid values are:

- **TIMER\_B\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_B\_OUTPUTMODE\_SET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_B\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE**
- **TIMER\_B\_OUTPUTMODE\_RESET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_B\_OUTPUTMODE\_RESET\_SET**

Referenced by `Timer_B_outputPWM()`.

## compareRegister

```
uint16_t Timer_B_outputPWMPParam::compareRegister
```

Selects the compare register being used. Refer to datasheet to ensure the device has the compare register being used.

Valid values are:

- `TIMER_B_CAPTURECOMPARE_REGISTER_0`
- `TIMER_B_CAPTURECOMPARE_REGISTER_1`
- `TIMER_B_CAPTURECOMPARE_REGISTER_2`
- `TIMER_B_CAPTURECOMPARE_REGISTER_3`
- `TIMER_B_CAPTURECOMPARE_REGISTER_4`
- `TIMER_B_CAPTURECOMPARE_REGISTER_5`
- `TIMER_B_CAPTURECOMPARE_REGISTER_6`

Referenced by `Timer_B_outputPWM()`.

The documentation for this struct was generated from the following file:

- `timer_b.h`

## 47.27 EUSCI\_B\_I2C\_initMasterParam Struct Reference

Used in the `EUSCI_B_I2C_initMaster()` function as the `param` parameter.

```
#include <eusci_b_i2c.h>
```

### Data Fields

- `uint8_t selectClockSource`
- `uint32_t i2cClk`
- `uint32_t dataRate`
- `uint8_t byteCounterThreshold`  
*Sets threshold for automatic STOP or UCSTPIFG.*
- `uint8_t autoSTOPGeneration`

### 47.27.1 Detailed Description

Used in the `EUSCI_B_I2C_initMaster()` function as the `param` parameter.

### 47.27.2 Field Documentation

#### autoSTOPGeneration

```
uint8_t EUSCI_B_I2C_initMasterParam::autoSTOPGeneration
```

Sets up the STOP condition generation.

Valid values are:

- `EUSCI_B_I2C_NO_AUTO_STOP`
- `EUSCI_B_I2C_SET_BYTECOUNT_THRESHOLD_FLAG`
- `EUSCI_B_I2C_SEND_STOP_AUTOMATICALLY_ON_BYTECOUNT_THRESHOLD`

Referenced by `EUSCI_B_I2C_initMaster()`.

## dataRate

```
uint32_t EUSCI_B_I2C_initMasterParam::dataRate
```

Setup for selecting data transfer rate.

Valid values are:

- **EUSCI\_B\_I2C\_SET\_DATA\_RATE\_400KBPS**
- **EUSCI\_B\_I2C\_SET\_DATA\_RATE\_100KBPS**

Referenced by `EUSCI_B_I2C_initMaster()`.

## i2cClk

```
uint32_t EUSCI_B_I2C_initMasterParam::i2cClk
```

Is the rate of the clock supplied to the I2C module (the frequency in Hz of the clock source specified in `selectClockSource`).

Referenced by `EUSCI_B_I2C_initMaster()`.

## selectClockSource

```
uint8_t EUSCI_B_I2C_initMasterParam::selectClockSource
```

Selects the clocksource. Refer to device specific datasheet for available options.

Valid values are:

- **EUSCI\_B\_I2C\_CLOCKSOURCE\_ACLK**
- **EUSCI\_B\_I2C\_CLOCKSOURCE\_SMCLK**

Referenced by `EUSCI_B_I2C_initMaster()`.

The documentation for this struct was generated from the following file:

- `eusci_b_i2c.h`

## 47.28 EUSCI\_A\_SPI\_changeMasterClockParam Struct Reference

Used in the `EUSCI_A_SPI_changeMasterClock()` function as the param parameter.

```
#include <eusci_a_spi.h>
```

### Data Fields

- `uint32_t` [clockSourceFrequency](#)  
*Is the frequency of the selected clock source.*
- `uint32_t` [desiredSpiClock](#)  
*Is the desired clock rate for SPI communication.*

### 47.28.1 Detailed Description

Used in the [EUSCIA\\_SPI\\_changeMasterClock\(\)](#) function as the param parameter.

The documentation for this struct was generated from the following file:

- [eusci\\_a\\_spi.h](#)

## 47.29 Timer\_B\_initUpModeParam Struct Reference

Used in the [Timer\\_B\\_initUpMode\(\)](#) function as the param parameter.

```
#include <timer_b.h>
```

### Data Fields

- [uint16\\_t clockSource](#)
- [uint16\\_t clockSourceDivider](#)
- [uint16\\_t timerPeriod](#)
- [uint16\\_t timerInterruptEnable\\_TBIE](#)
- [uint16\\_t captureCompareInterruptEnable\\_CCR0\\_CCIE](#)
- [uint16\\_t timerClear](#)
- [bool startTimer](#)

*Whether to start the timer immediately.*

### 47.29.1 Detailed Description

Used in the [Timer\\_B\\_initUpMode\(\)](#) function as the param parameter.

### 47.29.2 Field Documentation

#### captureCompareInterruptEnable\_CCR0\_CCIE

```
uint16_t Timer_B_initUpModeParam::captureCompareInterruptEnable_CCR0_CCIE
```

Is to enable or disable Timer\_B CCR0 capture compare interrupt.

Valid values are:

- **TIMER\_B\_CCIE\_CCR0\_INTERRUPT\_ENABLE**
- **TIMER\_B\_CCIE\_CCR0\_INTERRUPT\_DISABLE** [Default]

Referenced by [Timer\\_B\\_initUpMode\(\)](#).

#### clockSource

```
uint16_t Timer_B_initUpModeParam::clockSource
```



Selects the clock source  
Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_B\_CLOCKSOURCE\_ACLK**
- **TIMER\_B\_CLOCKSOURCE\_SMCLK**
- **TIMER\_B\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by `Timer_B_initUpMode()`.

## clockSourceDivider

```
uint16_t Timer_B_initUpModeParam::clockSourceDivider
```

Is the divider for Clock source.  
Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_B_initUpMode()`.

## timerClear

```
uint16_t Timer_B_initUpModeParam::timerClear
```

Decides if Timer\_B clock divider, count direction, count need to be reset.

Valid values are:

- **TIMER\_B\_DO\_CLEAR**
- **TIMER\_B\_SKIP\_CLEAR** [Default]

Referenced by `Timer_B_initUpMode()`.

## timerInterruptEnable\_TBIE

```
uint16_t Timer_B_initUpModeParam::timerInterruptEnable_TBIE
```

Is to enable or disable Timer\_B interrupt

Valid values are:

- **TIMER\_B\_TBIE\_INTERRUPT\_ENABLE**
- **TIMER\_B\_TBIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_B_initUpMode()`.

## timerPeriod

```
uint16_t Timer_B_initUpModeParam::timerPeriod
```

Is the specified Timer\_B period. This is the value that gets written into the CCR0. Limited to 16 bits[uint16\_t]

Referenced by `Timer_B_initUpMode()`.

The documentation for this struct was generated from the following file:

- `timer_b.h`

## 47.30 Timer\_B\_initCompareModeParam Struct Reference

Used in the `Timer_B_initCompareMode()` function as the param parameter.

```
#include <timer_b.h>
```

### Data Fields

- `uint16_t compareRegister`
- `uint16_t compareInterruptEnable`
- `uint16_t compareOutputMode`
- `uint16_t compareValue`

*Is the count to be compared with in compare mode.*

## 47.30.1 Detailed Description

Used in the [Timer\\_B.initCompareMode\(\)](#) function as the param parameter.

## 47.30.2 Field Documentation

### compareInterruptEnable

```
uint16_t Timer_B_initCompareModeParam::compareInterruptEnable
```

Is to enable or disable Timer\_B capture compare interrupt.

Valid values are:

- **TIMER\_B\_CAPTURECOMPARE\_INTERRUPT\_DISABLE** [Default]
- **TIMER\_B\_CAPTURECOMPARE\_INTERRUPT\_ENABLE**

Referenced by [Timer\\_B.initCompareMode\(\)](#).

### compareOutputMode

```
uint16_t Timer_B_initCompareModeParam::compareOutputMode
```

Specifies the output mode.

Valid values are:

- **TIMER\_B\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_B\_OUTPUTMODE\_SET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_B\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE**
- **TIMER\_B\_OUTPUTMODE\_RESET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_B\_OUTPUTMODE\_RESET\_SET**

Referenced by [Timer\\_B.initCompareMode\(\)](#).

### compareRegister

```
uint16_t Timer_B_initCompareModeParam::compareRegister
```

Selects the compare register being used. Refer to datasheet to ensure the device has the compare register being used.

Valid values are:

- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_0**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_1**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_2**

- [TIMER\\_B\\_CAPTURECOMPARE\\_REGISTER\\_3](#)
- [TIMER\\_B\\_CAPTURECOMPARE\\_REGISTER\\_4](#)
- [TIMER\\_B\\_CAPTURECOMPARE\\_REGISTER\\_5](#)
- [TIMER\\_B\\_CAPTURECOMPARE\\_REGISTER\\_6](#)

Referenced by `Timer_B_initCompareMode()`.

The documentation for this struct was generated from the following file:

- `timer_b.h`

## 47.31 EUSCI\_A\_SPI\_initMasterParam Struct Reference

Used in the [EUSCI\\_A\\_SPI\\_initMaster\(\)](#) function as the `param` parameter.

```
#include <eusci_a_spi.h>
```

### Data Fields

- `uint8_t` [selectClockSource](#)
- `uint32_t` [clockSourceFrequency](#)  
*Is the frequency of the selected clock source.*
- `uint32_t` [desiredSpiClock](#)  
*Is the desired clock rate for SPI communication.*
- `uint16_t` [msbFirst](#)
- `uint16_t` [clockPhase](#)
- `uint16_t` [clockPolarity](#)
- `uint16_t` [spiMode](#)

### 47.31.1 Detailed Description

Used in the [EUSCI\\_A\\_SPI\\_initMaster\(\)](#) function as the `param` parameter.

### 47.31.2 Field Documentation

#### clockPhase

```
uint16_t EUSCI_A_SPI_initMasterParam::clockPhase
```

Is clock phase select.

Valid values are:

- `EUSCI_A_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT` [Default]
- `EUSCI_A_SPI_PHASE_DATA_CAPTURED_ONFIRST_CHANGED_ON_NEXT`

Referenced by `EUSCI_A_SPI_initMaster()`.

## clockPolarity

```
uint16_t EUSCI_A_SPI_initMasterParam::clockPolarity
```

Is clock polarity select

Valid values are:

- **EUSCI\_A\_SPI\_CLOCKPOLARITY\_INACTIVITY\_HIGH**
- **EUSCI\_A\_SPI\_CLOCKPOLARITY\_INACTIVITY\_LOW** [Default]

Referenced by EUSCI\_A\_SPI\_initMaster().

## msbFirst

```
uint16_t EUSCI_A_SPI_initMasterParam::msbFirst
```

Controls the direction of the receive and transmit shift register.

Valid values are:

- **EUSCI\_A\_SPI\_MSB\_FIRST**
- **EUSCI\_A\_SPI\_LSB\_FIRST** [Default]

Referenced by EUSCI\_A\_SPI\_initMaster().

## selectClockSource

```
uint8_t EUSCI_A_SPI_initMasterParam::selectClockSource
```

Selects Clock source. Refer to device specific datasheet for available options.

Valid values are:

- **EUSCI\_A\_SPI\_CLOCKSOURCE\_ACLK**
- **EUSCI\_A\_SPI\_CLOCKSOURCE\_SMCLK**

Referenced by EUSCI\_A\_SPI\_initMaster().

## spiMode

```
uint16_t EUSCI_A_SPI_initMasterParam::spiMode
```

Is SPI mode select

Valid values are:

- **EUSCI\_A\_SPI\_3PIN**
- **EUSCI\_A\_SPI\_4PIN\_UCxSTE\_ACTIVE\_HIGH**
- **EUSCI\_A\_SPI\_4PIN\_UCxSTE\_ACTIVE\_LOW**

Referenced by EUSCI\_A\_SPI\_initMaster().

The documentation for this struct was generated from the following file:

- eusci\_a\_spi.h

## 47.32 DAC12\_A\_initParam Struct Reference

Used in the [DAC12\\_A\\_init\(\)](#) function as the param parameter.

```
#include <dac12_a.h>
```

### Data Fields

- [uint8\\_t submoduleSelect](#)
- [uint16\\_t outputSelect](#)
- [uint16\\_t positiveReferenceVoltage](#)
- [uint16\\_t outputVoltageMultiplier](#)
- [uint8\\_t amplifierSetting](#)
- [uint16\\_t conversionTriggerSelect](#)

### 47.32.1 Detailed Description

Used in the [DAC12\\_A\\_init\(\)](#) function as the param parameter.

### 47.32.2 Field Documentation

#### amplifierSetting

```
uint8_t DAC12_A_initParam::amplifierSetting
```

Is the setting of the settling speed and current of the Vref+ and the Vout buffer.

Valid values are:

- **DAC12\_A\_AMP\_OFF\_PINOUTHIGHZ** [Default] - Initialize the DAC12\_A Module with settings, but do not turn it on.
- **DAC12\_A\_AMP\_OFF\_PINOUTLOW** - Initialize the DAC12\_A Module with settings, and allow it to take control of the selected output pin to pull it low (Note: this takes control away port mapping module).
- **DAC12\_A\_AMP\_LOWIN\_LOWOUT** - Select a slow settling speed and current for Vref+ input buffer and for Vout output buffer.
- **DAC12\_A\_AMP\_LOWIN\_MEDOUT** - Select a slow settling speed and current for Vref+ input buffer and a medium settling speed and current for Vout output buffer.
- **DAC12\_A\_AMP\_LOWIN\_HIGHOUT** - Select a slow settling speed and current for Vref+ input buffer and a high settling speed and current for Vout output buffer.
- **DAC12\_A\_AMP\_MEDIN\_MEDOUT** - Select a medium settling speed and current for Vref+ input buffer and for Vout output buffer.
- **DAC12\_A\_AMP\_MEDIN\_HIGHOUT** - Select a medium settling speed and current for Vref+ input buffer and a high settling speed and current for Vout output buffer.
- **DAC12\_A\_AMP\_HIGHIN\_HIGHOUT** - Select a high settling speed and current for Vref+ input buffer and for Vout output buffer.

Referenced by [DAC12\\_A\\_init\(\)](#).

## conversionTriggerSelect

```
uint16_t DAC12_A_initParam::conversionTriggerSelect
```

Selects the trigger that will start a conversion.

Valid values are:

- **DAC12\_A\_TRIGGER\_ENCBYPASS** [Default] - Automatically converts data as soon as it is written into the data buffer. (Note: Do not use this selection if grouping DAC's).
- **DAC12\_A\_TRIGGER\_ENC** - Requires a call to `enableConversions()` to allow a conversion, but starts a conversion as soon as data is written to the data buffer (Note: with DAC12\_A module's grouped, data has to be set in BOTH DAC12\_A data buffers to start a conversion).
- **DAC12\_A\_TRIGGER\_TA** - Requires a call to `enableConversions()` to allow a conversion, and a rising edge of Timer\_A's Out1 (TA1) to start a conversion.
- **DAC12\_A\_TRIGGER\_TB** - Requires a call to `enableConversions()` to allow a conversion, and a rising edge of Timer\_B's Out2 (TB2) to start a conversion.

Referenced by `DAC12_A_init()`.

## outputSelect

```
uint16_t DAC12_A_initParam::outputSelect
```

Selects the output pin that the selected DAC12\_A module will output to.

Valid values are:

- **DAC12\_A\_OUTPUT\_1** [Default]
- **DAC12\_A\_OUTPUT\_2**

Referenced by `DAC12_A_init()`.

## outputVoltageMultiplier

```
uint16_t DAC12_A_initParam::outputVoltageMultiplier
```

Is the multiplier of the Vout voltage.

Valid values are:

- **DAC12\_A\_VREFx1** [Default]
- **DAC12\_A\_VREFx2**
- **DAC12\_A\_VREFx3**

Referenced by `DAC12_A_init()`.

## positiveReferenceVoltage

```
uint16_t DAC12_A_initParam::positiveReferenceVoltage
```

Is the upper limit voltage that the data can be converted in to.

Valid values are:

- **DAC12\_A\_VREF\_INT** [Default]
- **DAC12\_A\_VREF\_AVCC**
- **DAC12\_A\_VREF\_EXT** - For devices with CTSD16, use Ref module `Ref_enableReferenceVoltageOutput/Ref_disableReferenceVoltageOutput` to select `VeREF`(external reference signal) or `VREFBG`(internally generated reference signal)

Referenced by `DAC12_A_init()`.

## submoduleSelect

```
uint8_t DAC12_A_initParam::submoduleSelect
```

Decides which DAC12\_A sub-module to configure.  
Valid values are:

- **DAC12\_A\_SUBMODULE\_0**
- **DAC12\_A\_SUBMODULE\_1**

Referenced by `DAC12_A_init()`.

The documentation for this struct was generated from the following file:

- `dac12_a.h`

## 47.33 Timer\_D\_initCaptureModeParam Struct Reference

Used in the [Timer\\_D\\_initCaptureMode\(\)](#) function as the param parameter.

```
#include <timer_d.h>
```

### Data Fields

- `uint16_t` [captureRegister](#)
- `uint16_t` [captureMode](#)
- `uint16_t` [captureInputSelect](#)
- `uint16_t` [synchronizeCaptureSource](#)
- `uint16_t` [captureInterruptEnable](#)
- `uint16_t` [captureOutputMode](#)
- `uint8_t` [channelCaptureMode](#)

### 47.33.1 Detailed Description

Used in the [Timer\\_D\\_initCaptureMode\(\)](#) function as the param parameter.



## 47.33.2 Field Documentation

### captureInputSelect

```
uint16_t Timer_D_initCaptureModeParam::captureInputSelect
```

Decides the Input Select

Valid values are:

- **TIMER\_D\_CAPTURE\_INPUTSELECT\_CC1xA** [Default]
- **TIMER\_D\_CAPTURE\_INPUTSELECT\_CC1xB**
- **TIMER\_D\_CAPTURE\_INPUTSELECT\_GND**
- **TIMER\_D\_CAPTURE\_INPUTSELECT\_Vcc**

Referenced by `Timer_D_initCaptureMode()`.

### captureInterruptEnable

```
uint16_t Timer_D_initCaptureModeParam::captureInterruptEnable
```

Is to enable or disabel capture interrupt

Valid values are:

- **TIMER\_D\_CAPTURE\_INTERRUPT\_ENABLE**
- **TIMER\_D\_CAPTURE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_D_initCaptureMode()`.

### captureMode

```
uint16_t Timer_D_initCaptureModeParam::captureMode
```

Is the capture mode selected.

Valid values are:

- **TIMER\_D\_CAPTUREMODE\_NO\_CAPTURE** [Default]
- **TIMER\_D\_CAPTUREMODE\_RISING\_EDGE**
- **TIMER\_D\_CAPTUREMODE\_FALLING\_EDGE**
- **TIMER\_D\_CAPTUREMODE\_RISING\_AND\_FALLING\_EDGE**

Referenced by `Timer_D_initCaptureMode()`.

### captureOutputMode

```
uint16_t Timer_D_initCaptureModeParam::captureOutputMode
```

Specifies the output mode.

Valid values are:

- **TIMER\_D\_OUTPUTMODE\_OUTBITVALUE** [Default]

- **TIMER\_D\_OUTPUTMODE\_SET**
- **TIMER\_D\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_D\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_D\_OUTPUTMODE\_TOGGLE**
- **TIMER\_D\_OUTPUTMODE\_RESET**
- **TIMER\_D\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_D\_OUTPUTMODE\_RESET\_SET**

Referenced by `Timer_D_initCaptureMode()`.

## captureRegister

```
uint16_t Timer_D_initCaptureModeParam::captureRegister
```

Selects the Capture register being used. Refer to datasheet to ensure the device has the capture compare register being used

Valid values are:

- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_0**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_1**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_2**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_3**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_4**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_5**
- **TIMER\_D\_CAPTURECOMPARE\_REGISTER\_6**

Referenced by `Timer_D_initCaptureMode()`.

## channelCaptureMode

```
uint8_t Timer_D_initCaptureModeParam::channelCaptureMode
```

Specifies single/dual capture mode.

Valid values are:

- **TIMER\_D\_SINGLE\_CAPTURE\_MODE** - value],
- **TIMER\_D\_DUAL\_CAPTURE\_MODE**

Referenced by `Timer_D_initCaptureMode()`.

## synchronizeCaptureSource

```
uint16_t Timer_D_initCaptureModeParam::synchronizeCaptureSource
```

Decides if capture source should be synchronized with timer clock

Valid values are:

- **TIMER\_D\_CAPTURE\_ASYNCHRONOUS** [Default]

- **TIMER\_D\_CAPTURE\_SYNCHRONOUS**

Referenced by `Timer_D_initCaptureMode()`.

The documentation for this struct was generated from the following file:

- `timer_d.h`

## 47.34 Timer\_B\_initCaptureModeParam Struct Reference

Used in the `Timer_B_initCaptureMode()` function as the `param` parameter.

```
#include <timer_b.h>
```

### Data Fields

- `uint16_t captureRegister`
- `uint16_t captureMode`
- `uint16_t captureInputSelect`
- `uint16_t synchronizeCaptureSource`
- `uint16_t captureInterruptEnable`
- `uint16_t captureOutputMode`

### 47.34.1 Detailed Description

Used in the `Timer_B_initCaptureMode()` function as the `param` parameter.

### 47.34.2 Field Documentation

#### `captureInputSelect`

```
uint16_t Timer_B_initCaptureModeParam::captureInputSelect
```

Decides the Input Select

Valid values are:

- **TIMER\_B\_CAPTURE\_INPUTSELECT\_CC1xA** [Default]
- **TIMER\_B\_CAPTURE\_INPUTSELECT\_CC1xB**
- **TIMER\_B\_CAPTURE\_INPUTSELECT\_GND**
- **TIMER\_B\_CAPTURE\_INPUTSELECT\_Vcc**

Referenced by `Timer_B_initCaptureMode()`.

## captureInterruptEnable

```
uint16_t Timer_B_initCaptureModeParam::captureInterruptEnable
```

Is to enable or disable Timer\_B capture compare interrupt.

Valid values are:

- **TIMER\_B\_CAPTURECOMPARE\_INTERRUPT\_DISABLE** [Default]
- **TIMER\_B\_CAPTURECOMPARE\_INTERRUPT\_ENABLE**

Referenced by `Timer_B_initCaptureMode()`.

## captureMode

```
uint16_t Timer_B_initCaptureModeParam::captureMode
```

Is the capture mode selected.

Valid values are:

- **TIMER\_B\_CAPTUREMODE\_NO\_CAPTURE** [Default]
- **TIMER\_B\_CAPTUREMODE\_RISING\_EDGE**
- **TIMER\_B\_CAPTUREMODE\_FALLING\_EDGE**
- **TIMER\_B\_CAPTUREMODE\_RISING\_AND\_FALLING\_EDGE**

Referenced by `Timer_B_initCaptureMode()`.

## captureOutputMode

```
uint16_t Timer_B_initCaptureModeParam::captureOutputMode
```

Specifies the output mode.

Valid values are:

- **TIMER\_B\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_B\_OUTPUTMODE\_SET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_B\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE**
- **TIMER\_B\_OUTPUTMODE\_RESET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_B\_OUTPUTMODE\_RESET\_SET**

Referenced by `Timer_B_initCaptureMode()`.

## captureRegister

```
uint16_t Timer_B_initCaptureModeParam::captureRegister
```

Selects the capture register being used. Refer to datasheet to ensure the device has the capture register being used.

Valid values are:

- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_0**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_1**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_2**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_3**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_4**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_5**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_6**

Referenced by `Timer_B_initCaptureMode()`.

## synchronizeCaptureSource

```
uint16_t Timer_B_initCaptureModeParam::synchronizeCaptureSource
```

Decides if capture source should be synchronized with Timer\_B clock

Valid values are:

- **TIMER\_B\_CAPTURE\_ASYNCHRONOUS** [Default]
- **TIMER\_B\_CAPTURE\_SYNCHRONOUS**

Referenced by `Timer_B_initCaptureMode()`.

The documentation for this struct was generated from the following file:

- `timer_b.h`

## 47.35 EUSCI\_B\_SPI\_initMasterParam Struct Reference

Used in the [EUSCI\\_B\\_SPI\\_initMaster\(\)](#) function as the param parameter.

```
#include <eusci_b_spi.h>
```

### Data Fields

- `uint8_t` [selectClockSource](#)
- `uint32_t` [clockSourceFrequency](#)  
*Is the frequency of the selected clock source.*
- `uint32_t` [desiredSpiClock](#)  
*Is the desired clock rate for SPI communication.*
- `uint16_t` [msbFirst](#)
- `uint16_t` [clockPhase](#)
- `uint16_t` [clockPolarity](#)
- `uint16_t` [spiMode](#)

## 47.35.1 Detailed Description

Used in the `EUSCI_B_SPI_initMaster()` function as the `param` parameter.

## 47.35.2 Field Documentation

### clockPhase

```
uint16_t EUSCI_B_SPI_initMasterParam::clockPhase
```

Is clock phase select.

Valid values are:

- **EUSCI\_B\_SPI\_PHASE\_DATA\_CHANGED\_ONFIRST\_CAPTURED\_ON\_NEXT** [Default]
- **EUSCI\_B\_SPI\_PHASE\_DATA\_CAPTURED\_ONFIRST\_CHANGED\_ON\_NEXT**

Referenced by `EUSCI_B_SPI_initMaster()`.

### clockPolarity

```
uint16_t EUSCI_B_SPI_initMasterParam::clockPolarity
```

Is clock polarity select

Valid values are:

- **EUSCI\_B\_SPI\_CLOCKPOLARITY\_INACTIVITY\_HIGH**
- **EUSCI\_B\_SPI\_CLOCKPOLARITY\_INACTIVITY\_LOW** [Default]

Referenced by `EUSCI_B_SPI_initMaster()`.

### msbFirst

```
uint16_t EUSCI_B_SPI_initMasterParam::msbFirst
```

Controls the direction of the receive and transmit shift register.

Valid values are:

- **EUSCI\_B\_SPI\_MSB\_FIRST**
- **EUSCI\_B\_SPI\_LSB\_FIRST** [Default]

Referenced by `EUSCI_B_SPI_initMaster()`.

### selectClockSource

```
uint8_t EUSCI_B_SPI_initMasterParam::selectClockSource
```

Selects Clock source. Refer to device specific datasheet for available options.

Valid values are:

- **EUSCI\_B\_SPI\_CLOCKSOURCE\_ACLK**
- **EUSCI\_B\_SPI\_CLOCKSOURCE\_SMCLK**

Referenced by `EUSCI_B_SPI_initMaster()`.

## spiMode

```
uint16_t EUSCI_B_SPI_initMasterParam::spiMode
```

Is SPI mode select

Valid values are:

- **EUSCI\_B\_SPI\_3PIN**
- **EUSCI\_B\_SPI\_4PIN\_UCxSTE\_ACTIVE\_HIGH**
- **EUSCI\_B\_SPI\_4PIN\_UCxSTE\_ACTIVE\_LOW**

Referenced by `EUSCI_B_SPI_initMaster()`.

The documentation for this struct was generated from the following file:

- `eusci_b_spi.h`

## 47.36 SD24\_B\_initConverterAdvancedParam Struct Reference

Used in the [SD24\\_B\\_initConverterAdvanced\(\)](#) function as the param parameter.

```
#include <sd24_b.h>
```

### Data Fields

- `uint8_t converter`
- `uint8_t alignment`
- `uint8_t startSelect`
- `uint8_t conversionMode`
- `uint8_t dataFormat`
- `uint8_t sampleDelay`
- `uint16_t oversampleRatio`
- `uint8_t gain`

### 47.36.1 Detailed Description

Used in the [SD24\\_B\\_initConverterAdvanced\(\)](#) function as the param parameter.

## 47.36.2 Field Documentation

### alignment

```
uint8_t SD24_B_initConverterAdvancedParam::alignment
```

Selects how the data will be aligned in result

Valid values are:

- **SD24\_B\_ALIGN\_RIGHT** [Default]
- **SD24\_B\_ALIGN\_LEFT**

Referenced by `SD24_B_initConverterAdvanced()`.

### conversionMode

```
uint8_t SD24_B_initConverterAdvancedParam::conversionMode
```

Determines whether the converter will do continuous samples or a single sample

Valid values are:

- **SD24\_B\_CONTINUOUS\_MODE** [Default]
- **SD24\_B\_SINGLE\_MODE**

Referenced by `SD24_B_initConverterAdvanced()`.

### converter

```
uint8_t SD24_B_initConverterAdvancedParam::converter
```

Selects the converter that will be configured. Check datasheet for available converters on device.

Valid values are:

- **SD24\_B\_CONVERTER\_0**
- **SD24\_B\_CONVERTER\_1**
- **SD24\_B\_CONVERTER\_2**
- **SD24\_B\_CONVERTER\_3**
- **SD24\_B\_CONVERTER\_4**
- **SD24\_B\_CONVERTER\_5**
- **SD24\_B\_CONVERTER\_6**
- **SD24\_B\_CONVERTER\_7**

Referenced by `SD24_B_initConverterAdvanced()`.

### dataFormat

```
uint8_t SD24_B_initConverterAdvancedParam::dataFormat
```

Selects how the data format of the results

Valid values are:



- **SD24\_B\_DATA\_FORMAT\_BINARY** [Default]
- **SD24\_B\_DATA\_FORMAT\_2COMPLEMENT**

Referenced by `SD24_B_initConverterAdvanced()`.

## gain

```
uint8_t SD24_B_initConverterAdvancedParam::gain
```

Selects the gain for the converter

Valid values are:

- **SD24\_B\_GAIN\_1** [Default]
- **SD24\_B\_GAIN\_2**
- **SD24\_B\_GAIN\_4**
- **SD24\_B\_GAIN\_8**
- **SD24\_B\_GAIN\_16**
- **SD24\_B\_GAIN\_32**
- **SD24\_B\_GAIN\_64**
- **SD24\_B\_GAIN\_128**

Referenced by `SD24_B_initConverterAdvanced()`.

## oversampleRatio

```
uint16_t SD24_B_initConverterAdvancedParam::oversampleRatio
```

Selects oversampling ratio for the converter

Valid values are:

- **SD24\_B\_OVERSAMPLE\_32**
- **SD24\_B\_OVERSAMPLE\_64**
- **SD24\_B\_OVERSAMPLE\_128**
- **SD24\_B\_OVERSAMPLE\_256**
- **SD24\_B\_OVERSAMPLE\_512**
- **SD24\_B\_OVERSAMPLE\_1024**

Referenced by `SD24_B_initConverterAdvanced()`.

## sampleDelay

```
uint8_t SD24_B_initConverterAdvancedParam::sampleDelay
```

Selects the delay for the interrupt

Valid values are:

- **SD24\_B\_FOURTH\_SAMPLE\_INTERRUPT** [Default]

- **SD24\_B\_THIRD\_SAMPLE\_INTERRUPT**
- **SD24\_B\_SECOND\_SAMPLE\_INTERRUPT**
- **SD24\_B\_FIRST\_SAMPLE\_INTERRUPT**

Referenced by `SD24_B_initConverterAdvanced()`.

## startSelect

```
uint8_t SD24_B_initConverterAdvancedParam::startSelect
```

Selects what will trigger the start of the converter

Valid values are:

- **SD24\_B\_CONVERSION\_SELECT\_SD24SC** [Default]
- **SD24\_B\_CONVERSION\_SELECT\_EXT1**
- **SD24\_B\_CONVERSION\_SELECT\_EXT2**
- **SD24\_B\_CONVERSION\_SELECT\_EXT3**
- **SD24\_B\_CONVERSION\_SELECT\_GROUP0**
- **SD24\_B\_CONVERSION\_SELECT\_GROUP1**
- **SD24\_B\_CONVERSION\_SELECT\_GROUP2**
- **SD24\_B\_CONVERSION\_SELECT\_GROUP3**

Referenced by `SD24_B_initConverterAdvanced()`.

The documentation for this struct was generated from the following file:

- `sd24_b.h`

## 47.37 Timer\_D\_combineTDCCRToOutputPWMParm Struct Reference

Used in the [Timer\\_D\\_combineTDCCRToOutputPWM\(\)](#) function as the param parameter.

```
#include <timer_d.h>
```

### Data Fields

- `uint16_t clockSource`
- `uint16_t clockSourceDivider`
- `uint16_t clockingMode`
- `uint16_t timerPeriod`  
*Is the specified timer period.*
- `uint16_t combineCCRRegistersCombination`
- `uint16_t compareOutputMode`
- `uint16_t dutyCycle1`  
*Specifies the dutycycle for the generated waveform.*
- `uint16_t dutyCycle2`  
*Specifies the dutycycle for the generated waveform.*

## 47.37.1 Detailed Description

Used in the `Timer_D.combineTDCCRToOutputPWM()` function as the `param` parameter.

## 47.37.2 Field Documentation

### clockingMode

```
uint16_t Timer_D.combineTDCCRToOutputPWMParam::clockingMode
```

Is the selected clock mode register values.

Valid values are:

- **TIMER\_D\_CLOCKINGMODE\_EXTERNAL\_CLOCK** [Default]
- **TIMER\_D\_CLOCKINGMODE\_HIRES\_LOCAL\_CLOCK**
- **TIMER\_D\_CLOCKINGMODE\_AUXILIARY\_CLK**

Referenced by `Timer_D.combineTDCCRToOutputPWM()`.

### clockSource

```
uint16_t Timer_D.combineTDCCRToOutputPWMParam::clockSource
```

Selects Clock source.

Valid values are:

- **TIMER\_D\_CLOCKSOURCE\_EXTERNAL\_TDCLK** [Default]
- **TIMER\_D\_CLOCKSOURCE\_ACLK**
- **TIMER\_D\_CLOCKSOURCE\_SMCLK**
- **TIMER\_D\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TDCLK**

Referenced by `Timer_D.combineTDCCRToOutputPWM()`.

### clockSourceDivider

```
uint16_t Timer_D.combineTDCCRToOutputPWMParam::clockSourceDivider
```

Is the divider for clock source.

Valid values are:

- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_7**

- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_D_combineTDCCRToOutputPWM()`.

### combineCCRRegistersCombination

```
uint16_t Timer_D_combineTDCCRToOutputPWMParam::combineCCRRegistersCombination
```

Selects desired CCR registers to combine

Valid values are:

- **TIMER\_D\_COMBINE\_CCR1\_CCR2**
- **TIMER\_D\_COMBINE\_CCR3\_CCR4** - (available on Timer\_D5, Timer\_D7)
- **TIMER\_D\_COMBINE\_CCR5\_CCR6** - (available only on Timer\_D7)

Referenced by `Timer_D_combineTDCCRToOutputPWM()`.

### compareOutputMode

```
uint16_t Timer_D_combineTDCCRToOutputPWMParam::compareOutputMode
```

Specifies the output mode.

Valid values are:

- **TIMER\_D\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_D\_OUTPUTMODE\_SET**
- **TIMER\_D\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_D\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_D\_OUTPUTMODE\_TOGGLE**
- **TIMER\_D\_OUTPUTMODE\_RESET**
- **TIMER\_D\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_D\_OUTPUTMODE\_RESET\_SET**

Referenced by `Timer_D_combineTDCCRToOutputPWM()`.

The documentation for this struct was generated from the following file:

- `timer_d.h`

## 47.38 Timer\_D\_initContinuousModeParam Struct Reference

Used in the [Timer\\_D\\_initContinuousMode\(\)](#) function as the param parameter.

```
#include <timer_d.h>
```

### Data Fields

- uint16\_t [clockSource](#)
- uint16\_t [clockSourceDivider](#)
- uint16\_t [clockingMode](#)
- uint16\_t [timerInterruptEnable\\_TDIE](#)
- uint16\_t [timerClear](#)

### 47.38.1 Detailed Description

Used in the [Timer\\_D\\_initContinuousMode\(\)](#) function as the param parameter.

### 47.38.2 Field Documentation

#### clockingMode

```
uint16_t Timer_D_initContinuousModeParam::clockingMode
```

Is the selected clock mode register values.

Valid values are:

- **TIMER\_D\_CLOCKINGMODE\_EXTERNAL\_CLOCK** [Default]
- **TIMER\_D\_CLOCKINGMODE\_HIRES\_LOCAL\_CLOCK**
- **TIMER\_D\_CLOCKINGMODE\_AUXILIARY\_CLK**

Referenced by [Timer\\_D\\_initContinuousMode\(\)](#).

#### clockSource

```
uint16_t Timer_D_initContinuousModeParam::clockSource
```

Selects Clock source.

Valid values are:

- **TIMER\_D\_CLOCKSOURCE\_EXTERNAL\_TDCLK** [Default]
- **TIMER\_D\_CLOCKSOURCE\_ACLK**
- **TIMER\_D\_CLOCKSOURCE\_SMCLK**
- **TIMER\_D\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TDCLK**

Referenced by [Timer\\_D\\_initContinuousMode\(\)](#).

## clockSourceDivider

`uint16_t Timer_D_initContinuousModeParam::clockSourceDivider`

Is the divider for clock source.

Valid values are:

- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_D_initContinuousMode()`.

## timerClear

`uint16_t Timer_D_initContinuousModeParam::timerClear`

Decides if timer clock divider, count direction, count need to be reset.

Valid values are:

- **TIMER\_D\_DO\_CLEAR**
- **TIMER\_D\_SKIP\_CLEAR** [Default]

Referenced by `Timer_D_initContinuousMode()`.

## timerInterruptEnable\_TDIE

```
uint16_t Timer_D_initContinuousModeParam::timerInterruptEnable_TDIE
```

Is to enable or disable timer interrupt

Valid values are:

- **TIMER\_D\_TDIE\_INTERRUPT\_ENABLE**
- **TIMER\_D\_TDIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_D_initContinuousMode()`.

The documentation for this struct was generated from the following file:

- `timer_d.h`

## 47.39 DMA\_initParam Struct Reference

Used in the [DMA\\_init\(\)](#) function as the param parameter.

```
#include <dma.h>
```

### Data Fields

- `uint8_t` [channelSelect](#)
- `uint16_t` [transferModeSelect](#)
- `uint16_t` [transferSize](#)
- `uint8_t` [triggerSourceSelect](#)
- `uint8_t` [transferUnitSelect](#)
- `uint8_t` [triggerTypeSelect](#)

### 47.39.1 Detailed Description

Used in the [DMA\\_init\(\)](#) function as the param parameter.

### 47.39.2 Field Documentation

#### channelSelect

```
uint8_t DMA_initParam::channelSelect
```

Is the specified channel to initialize.

Valid values are:

- **DMA\_CHANNEL\_0**
- **DMA\_CHANNEL\_1**
- **DMA\_CHANNEL\_2**

- **DMA\_CHANNEL\_3**
- **DMA\_CHANNEL\_4**
- **DMA\_CHANNEL\_5**
- **DMA\_CHANNEL\_6**
- **DMA\_CHANNEL\_7**

Referenced by DMA\_init().

## transferModeSelect

```
uint16_t DMA_initParam::transferModeSelect
```

Is the transfer mode of the selected channel.

Valid values are:

- **DMA\_TRANSFER\_SINGLE** [Default] - Single transfer, transfers disabled after transferAmount of transfers.
- **DMA\_TRANSFER\_BLOCK** - Multiple transfers of transferAmount, transfers disabled once finished.
- **DMA\_TRANSFER\_BURSTBLOCK** - Multiple transfers of transferAmount interleaved with CPU activity, transfers disabled once finished.
- **DMA\_TRANSFER\_REPEATED\_SINGLE** - Repeated single transfer by trigger.
- **DMA\_TRANSFER\_REPEATED\_BLOCK** - Multiple transfers of transferAmount by trigger.
- **DMA\_TRANSFER\_REPEATED\_BURSTBLOCK** - Multiple transfers of transferAmount by trigger interleaved with CPU activity.

Referenced by DMA\_init().

## transferSize

```
uint16_t DMA_initParam::transferSize
```

Is the amount of transfers to complete in a block transfer mode, as well as how many transfers to complete before the interrupt flag is set. Valid value is between 1-65535, if 0, no transfers will occur.

Referenced by DMA\_init().

## transferUnitSelect

```
uint8_t DMA_initParam::transferUnitSelect
```

Is the specified size of transfers.

Valid values are:

- **DMA\_SIZE\_SRCWORD\_DSTWORD** [Default]
- **DMA\_SIZE\_SRCBYTE\_DSTWORD**
- **DMA\_SIZE\_SRCWORD\_DSTBYTE**



- **DMA\_SIZE\_SRCBYTE\_DSTBYTE**

Referenced by DMA\_init().

## triggerSourceSelect

```
uint8_t DMA_initParam::triggerSourceSelect
```

Is the source that will trigger the start of each transfer, note that the sources are device specific. Valid values are:

- **DMA\_TRIGGERSOURCE\_0** [Default]
- **DMA\_TRIGGERSOURCE\_1**
- **DMA\_TRIGGERSOURCE\_2**
- **DMA\_TRIGGERSOURCE\_3**
- **DMA\_TRIGGERSOURCE\_4**
- **DMA\_TRIGGERSOURCE\_5**
- **DMA\_TRIGGERSOURCE\_6**
- **DMA\_TRIGGERSOURCE\_7**
- **DMA\_TRIGGERSOURCE\_8**
- **DMA\_TRIGGERSOURCE\_9**
- **DMA\_TRIGGERSOURCE\_10**
- **DMA\_TRIGGERSOURCE\_11**
- **DMA\_TRIGGERSOURCE\_12**
- **DMA\_TRIGGERSOURCE\_13**
- **DMA\_TRIGGERSOURCE\_14**
- **DMA\_TRIGGERSOURCE\_15**
- **DMA\_TRIGGERSOURCE\_16**
- **DMA\_TRIGGERSOURCE\_17**
- **DMA\_TRIGGERSOURCE\_18**
- **DMA\_TRIGGERSOURCE\_19**
- **DMA\_TRIGGERSOURCE\_20**
- **DMA\_TRIGGERSOURCE\_21**
- **DMA\_TRIGGERSOURCE\_22**
- **DMA\_TRIGGERSOURCE\_23**
- **DMA\_TRIGGERSOURCE\_24**
- **DMA\_TRIGGERSOURCE\_25**
- **DMA\_TRIGGERSOURCE\_26**
- **DMA\_TRIGGERSOURCE\_27**
- **DMA\_TRIGGERSOURCE\_28**
- **DMA\_TRIGGERSOURCE\_29**
- **DMA\_TRIGGERSOURCE\_30**
- **DMA\_TRIGGERSOURCE\_31**

Referenced by DMA\_init().

## triggerTypeSelect

```
uint8_t DMA_initParam::triggerTypeSelect
```

Is the type of trigger that the trigger signal needs to be to start a transfer.

Valid values are:

- **DMA\_TRIGGER\_RISINGEDGE** [Default]
- **DMA\_TRIGGER\_HIGH** - A trigger would be a high signal from the trigger source, to be held high through the length of the transfer(s).

Referenced by DMA\_init().

The documentation for this struct was generated from the following file:

- dma.h

## 47.40 ADC12\_A\_configureMemoryParam Struct Reference

Used in the [ADC12\\_A\\_configureMemory\(\)](#) function as the param parameter.

```
#include <adc12_a.h>
```

### Data Fields

- uint8\_t [memoryBufferControlIndex](#)
- uint8\_t [inputSourceSelect](#)
- uint8\_t [positiveRefVoltageSourceSelect](#)
- uint8\_t [negativeRefVoltageSourceSelect](#)
- uint8\_t [endOfSequence](#)

### 47.40.1 Detailed Description

Used in the [ADC12\\_A\\_configureMemory\(\)](#) function as the param parameter.

### 47.40.2 Field Documentation

#### endOfSequence

```
uint8_t ADC12_A_configureMemoryParam::endOfSequence
```

Indicates that the specified memory buffer will be the end of the sequence if a sequenced conversion mode is selected

Valid values are:

- **ADC12\_A\_NOTENDOFSEQUENCE** [Default] - The specified memory buffer will NOT be the end of the sequence OR a sequenced conversion mode is not selected.

- **ADC12\_A.ENDOFSEQUENCE** - The specified memory buffer will be the end of the sequence.

Referenced by `ADC12_A.configureMemory()`.

## inputSourceSelect

```
uint8_t ADC12_A.configureMemoryParam::inputSourceSelect
```

Is the input that will store the converted data into the specified memory buffer.

Valid values are:

- **ADC12\_A.INPUT\_A0** [Default]
- **ADC12\_A.INPUT\_A1**
- **ADC12\_A.INPUT\_A2**
- **ADC12\_A.INPUT\_A3**
- **ADC12\_A.INPUT\_A4**
- **ADC12\_A.INPUT\_A5**
- **ADC12\_A.INPUT\_A6**
- **ADC12\_A.INPUT\_A7**
- **ADC12\_A.INPUT\_A8**
- **ADC12\_A.INPUT\_A9**
- **ADC12\_A.INPUT\_TEMPSENSOR**
- **ADC12\_A.INPUT\_BATTERYMONITOR**
- **ADC12\_A.INPUT\_A12**
- **ADC12\_A.INPUT\_A13**
- **ADC12\_A.INPUT\_A14**
- **ADC12\_A.INPUT\_A15**

Referenced by `ADC12_A.configureMemory()`.

## memoryBufferControlIndex

```
uint8_t ADC12_A.configureMemoryParam::memoryBufferControlIndex
```

Is the selected memory buffer to set the configuration for.

Valid values are:

- **ADC12\_A.MEMORY\_0** [Default]
- **ADC12\_A.MEMORY\_1**
- **ADC12\_A.MEMORY\_2**
- **ADC12\_A.MEMORY\_3**
- **ADC12\_A.MEMORY\_4**
- **ADC12\_A.MEMORY\_5**
- **ADC12\_A.MEMORY\_6**

- **ADC12\_A\_MEMORY\_7**
- **ADC12\_A\_MEMORY\_8**
- **ADC12\_A\_MEMORY\_9**
- **ADC12\_A\_MEMORY\_10**
- **ADC12\_A\_MEMORY\_11**
- **ADC12\_A\_MEMORY\_12**
- **ADC12\_A\_MEMORY\_13**
- **ADC12\_A\_MEMORY\_14**
- **ADC12\_A\_MEMORY\_15**

Referenced by `ADC12_A_configureMemory()`.

### negativeRefVoltageSourceSelect

```
uint8_t ADC12_A_configureMemoryParam::negativeRefVoltageSourceSelect
```

Is the reference voltage source to set as the lower limit for the conversion stored in the specified memory.

Valid values are:

- **ADC12\_A\_VREFNEG\_AVSS** [Default]
- **ADC12\_A\_VREFNEG\_EXT**

Referenced by `ADC12_A_configureMemory()`.

### positiveRefVoltageSourceSelect

```
uint8_t ADC12_A_configureMemoryParam::positiveRefVoltageSourceSelect
```

Is the reference voltage source to set as the upper limit for the conversion stored in the specified memory.

Valid values are:

- **ADC12\_A\_VREFPOS\_AVCC** [Default]
- **ADC12\_A\_VREFPOS\_EXT**
- **ADC12\_A\_VREFPOS\_INT**

Referenced by `ADC12_A_configureMemory()`.

The documentation for this struct was generated from the following file:

- `adc12_a.h`

## 47.41 Timer\_D\_initHighResGeneratorInRegulatedMode↔ Param Struct Reference

Used in the [Timer\\_D\\_initHighResGeneratorInRegulatedMode\(\)](#) function as the param parameter.

```
#include <timer.d.h>
```

### Data Fields

- uint16\_t [clockSource](#)
- uint16\_t [clockSourceDivider](#)
- uint16\_t [clockingMode](#)
- uint8\_t [highResClockMultiplyFactor](#)
- uint8\_t [highResClockDivider](#)

#### 47.41.1 Detailed Description

Used in the [Timer\\_D\\_initHighResGeneratorInRegulatedMode\(\)](#) function as the param parameter.

#### 47.41.2 Field Documentation

##### clockingMode

```
uint16_t Timer_D_initHighResGeneratorInRegulatedModeParam::clockingMode
```

Is the selected clock mode register values.

Valid values are:

- **TIMER\_D\_CLOCKINGMODE\_EXTERNAL\_CLOCK** [Default]
- **TIMER\_D\_CLOCKINGMODE\_HIRES\_LOCAL\_CLOCK**
- **TIMER\_D\_CLOCKINGMODE\_AUXILIARY\_CLK**

Referenced by [Timer\\_D\\_initHighResGeneratorInRegulatedMode\(\)](#).

##### clockSource

```
uint16_t Timer_D_initHighResGeneratorInRegulatedModeParam::clockSource
```

Selects Clock source.

Valid values are:

- **TIMER\_D\_CLOCKSOURCE\_EXTERNAL\_TDCLK** [Default]
- **TIMER\_D\_CLOCKSOURCE\_ACLK**
- **TIMER\_D\_CLOCKSOURCE\_SMCLK**

**■ TIMER\_D\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TDCLK**

Referenced by `Timer_D_initHighResGeneratorInRegulatedMode()`.

**clockSourceDivider**

```
uint16_t Timer_D_initHighResGeneratorInRegulatedModeParam::clockSourceDivider
```

Is the divider for clock source.

Valid values are:

- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_D_initHighResGeneratorInRegulatedMode()`.

**highResClockDivider**

```
uint8_t Timer_D_initHighResGeneratorInRegulatedModeParam::highResClockDivider
```

Selects the high resolution divider.

Valid values are:

- **TIMER\_D\_HIGHRES\_CLK\_DIVIDER\_1**
- **TIMER\_D\_HIGHRES\_CLK\_DIVIDER\_2**
- **TIMER\_D\_HIGHRES\_CLK\_DIVIDER\_4**
- **TIMER\_D\_HIGHRES\_CLK\_DIVIDER\_8**

Referenced by `Timer_D_initHighResGeneratorInRegulatedMode()`.

## highResClockMultiplyFactor

```
uint8_t Timer_D_initHighResGeneratorInRegulatedModeParam::highResClockMultiplyFactor
```

Selects the high resolution multiply factor.

Valid values are:

- **TIMER\_D\_HIGHRES\_CLK\_MULTIPLY\_FACTOR\_8x**
- **TIMER\_D\_HIGHRES\_CLK\_MULTIPLY\_FACTOR\_16x**

Referenced by `Timer_D_initHighResGeneratorInRegulatedMode()`.

The documentation for this struct was generated from the following file:

- `timer_d.h`

## 47.42 Calendar Struct Reference

Used in the `RTC_A_initCalendar()` function as the `CalendarTime` parameter.

```
#include <rtc_a.h>
```

### Data Fields

- `uint8_t` [Seconds](#)  
*Seconds of minute between 0-59.*
- `uint8_t` [Minutes](#)  
*Minutes of hour between 0-59.*
- `uint8_t` [Hours](#)  
*Hour of day between 0-23.*
- `uint8_t` [DayOfWeek](#)  
*Day of week between 0-6.*
- `uint8_t` [DayOfMonth](#)  
*Day of month between 1-31.*
- `uint8_t` [Month](#)  
*Month between 0-11.*
- `uint16_t` [Year](#)  
*Year between 0-4095.*

### 47.42.1 Detailed Description

Used in the `RTC_A_initCalendar()` function as the `CalendarTime` parameter.

Used in the `RTC_C_initCalendar()` function as the `CalendarTime` parameter.

Used in the `RTC_B_initCalendar()` function as the `CalendarTime` parameter.

## 47.42.2 Field Documentation

### Month

```
uint8_t Calendar::Month
```

Month between 0-11.

Month between 1-12.

Referenced by `RTC_A_getCalendarTime()`, `RTC_A_initCalendar()`, `RTC_B_getCalendarTime()`, `RTC_B_initCalendar()`, `RTC_C_getCalendarTime()`, and `RTC_C_initCalendar()`.

The documentation for this struct was generated from the following files:

- `rtc_a.h`
- `rtc_b.h`
- `rtc_c.h`

## 47.43 Timer\_A\_initUpDownModeParam Struct Reference

Used in the `Timer_A_initUpDownMode()` function as the param parameter.

```
#include <timer_a.h>
```

### Data Fields

- `uint16_t clockSource`
- `uint16_t clockSourceDivider`
- `uint16_t timerPeriod`  
*Is the specified Timer\_A period.*
- `uint16_t timerInterruptEnable_TAIE`
- `uint16_t captureCompareInterruptEnable_CCR0_CCIE`
- `uint16_t timerClear`
- `bool startTimer`

*Whether to start the timer immediately.*

### 47.43.1 Detailed Description

Used in the `Timer_A_initUpDownMode()` function as the param parameter.

### 47.43.2 Field Documentation

#### captureCompareInterruptEnable\_CCR0\_CCIE

```
uint16_t Timer_A_initUpDownModeParam::captureCompareInterruptEnable_CCR0_CCIE
```

Is to enable or disable Timer\_A CCR0 captureComapre interrupt.

Valid values are:



- **TIMER\_A\_CCIE\_CCR0\_INTERRUPT\_ENABLE**
- **TIMER\_A\_CCIE\_CCR0\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_A_initUpDownMode()`.

## clockSource

```
uint16_t Timer_A_initUpDownModeParam::clockSource
```

Selects Clock source.

Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_A\_CLOCKSOURCE\_ACLK**
- **TIMER\_A\_CLOCKSOURCE\_SMCLK**
- **TIMER\_A\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by `Timer_A_initUpDownMode()`.

## clockSourceDivider

```
uint16_t Timer_A_initUpDownModeParam::clockSourceDivider
```

Is the desired divider for the clock source

Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_A_initUpDownMode()`.

## timerClear

```
uint16_t Timer_A_initUpDownModeParam::timerClear
```

Decides if Timer\_A clock divider, count direction, count need to be reset.

Valid values are:

- **TIMER\_A\_DO\_CLEAR**
- **TIMER\_A\_SKIP\_CLEAR** [Default]

Referenced by `Timer_A_initUpDownMode()`.

## timerInterruptEnable\_TAIE

```
uint16_t Timer_A_initUpDownModeParam::timerInterruptEnable_TAIE
```

Is to enable or disable Timer\_A interrupt

Valid values are:

- **TIMER\_A\_TAIE\_INTERRUPT\_ENABLE**
- **TIMER\_A\_TAIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_A_initUpDownMode()`.

The documentation for this struct was generated from the following file:

- `timer_a.h`

## 47.44 Comp\_B\_initParam Struct Reference

Used in the `Comp_B_init()` function as the param parameter.

```
#include <comp_b.h>
```

### Data Fields

- `uint8_t` [positiveTerminalInput](#)
- `uint8_t` [negativeTerminalInput](#)
- `uint16_t` [powerModeSelect](#)
- `uint8_t` [outputFilterEnableAndDelayLevel](#)
- `uint16_t` [invertedOutputPolarity](#)

### 47.44.1 Detailed Description

Used in the `Comp_B_init()` function as the param parameter.

## 47.44.2 Field Documentation

### invertedOutputPolarity

```
uint16_t Comp_B_initParam::invertedOutputPolarity
```

Controls if the output will be inverted or not  
Valid values are:

- **COMP\_B\_NORMALOUTPUTPOLARITY** [Default]
- **COMP\_B\_INVERTEDOUTPUTPOLARITY**

Referenced by `Comp_B_init()`.

### negativeTerminalInput

```
uint8_t Comp_B_initParam::negativeTerminalInput
```

Selects the input to the negative terminal.  
Valid values are:

- **COMP\_B\_INPUT0** [Default]
- **COMP\_B\_INPUT1**
- **COMP\_B\_INPUT2**
- **COMP\_B\_INPUT3**
- **COMP\_B\_INPUT4**
- **COMP\_B\_INPUT5**
- **COMP\_B\_INPUT6**
- **COMP\_B\_INPUT7**
- **COMP\_B\_INPUT8**
- **COMP\_B\_INPUT9**
- **COMP\_B\_INPUT10**
- **COMP\_B\_INPUT11**
- **COMP\_B\_INPUT12**
- **COMP\_B\_INPUT13**
- **COMP\_B\_INPUT14**
- **COMP\_B\_INPUT15**
- **COMP\_B\_VREF**

Referenced by `Comp_B_init()`.

### outputFilterEnableAndDelayLevel

```
uint8_t Comp_B_initParam::outputFilterEnableAndDelayLevel
```

Controls the output filter delay state, which is either off or enabled with a specified delay level. This parameter is device specific and delay levels should be found in the device's datasheet.  
Valid values are:

- **COMP\_B\_FILTEROUTPUT\_OFF** [Default]
- **COMP\_B\_FILTEROUTPUT\_DLYLVL1**
- **COMP\_B\_FILTEROUTPUT\_DLYLVL2**
- **COMP\_B\_FILTEROUTPUT\_DLYLVL3**
- **COMP\_B\_FILTEROUTPUT\_DLYLVL4**

Referenced by `Comp_B_init()`.

## positiveTerminalInput

```
uint8_t Comp_B_initParam::positiveTerminalInput
```

Selects the input to the positive terminal.  
Valid values are:

- **COMP\_B\_INPUT0** [Default]
- **COMP\_B\_INPUT1**
- **COMP\_B\_INPUT2**
- **COMP\_B\_INPUT3**
- **COMP\_B\_INPUT4**
- **COMP\_B\_INPUT5**
- **COMP\_B\_INPUT6**
- **COMP\_B\_INPUT7**
- **COMP\_B\_INPUT8**
- **COMP\_B\_INPUT9**
- **COMP\_B\_INPUT10**
- **COMP\_B\_INPUT11**
- **COMP\_B\_INPUT12**
- **COMP\_B\_INPUT13**
- **COMP\_B\_INPUT14**
- **COMP\_B\_INPUT15**
- **COMP\_B\_VREF**

Referenced by `Comp_B_init()`.

## powerModeSelect

```
uint16_t Comp_B_initParam::powerModeSelect
```

Selects the power mode at which the `Comp_B` module will operate at.  
Valid values are:

- **COMP\_B\_POWERMODE\_HIGHSPEED** [Default]
- **COMP\_B\_POWERMODE\_NORMALMODE**
- **COMP\_B\_POWERMODE\_ULTRALOWPOWER**

Referenced by `Comp_B_init()`.

The documentation for this struct was generated from the following file:

- `comp_b.h`

## 47.45 RTC\_A\_configureCalendarAlarmParam Struct Reference

Used in the [RTC\\_A\\_configureCalendarAlarm\(\)](#) function as the param parameter.

```
#include <rtc_a.h>
```

### Data Fields

- `uint8_t` [minutesAlarm](#)
- `uint8_t` [hoursAlarm](#)
- `uint8_t` [dayOfWeekAlarm](#)
- `uint8_t` [dayOfMonthAlarm](#)

### 47.45.1 Detailed Description

Used in the [RTC\\_A\\_configureCalendarAlarm\(\)](#) function as the param parameter.

### 47.45.2 Field Documentation

#### dayOfMonthAlarm

```
uint8_t RTC_A_configureCalendarAlarmParam::dayOfMonthAlarm
```

Is the alarm condition for the day of the month.

Valid values are:

- **RTC\_A\_ALARMCONDITION\_OFF** [Default]

Referenced by [RTC\\_A\\_configureCalendarAlarm\(\)](#).

#### dayOfWeekAlarm

```
uint8_t RTC_A_configureCalendarAlarmParam::dayOfWeekAlarm
```

Is the alarm condition for the day of week.

Valid values are:

- **RTC\_A\_ALARMCONDITION\_OFF** [Default]

Referenced by [RTC\\_A\\_configureCalendarAlarm\(\)](#).

## hoursAlarm

```
uint8_t RTC_A_configureCalendarAlarmParam::hoursAlarm
```

Is the alarm condition for the hours.

Valid values are:

- **RTC\_A\_ALARMCONDITION\_OFF** [Default]

Referenced by `RTC_A_configureCalendarAlarm()`.

## minutesAlarm

```
uint8_t RTC_A_configureCalendarAlarmParam::minutesAlarm
```

Is the alarm condition for the minutes.

Valid values are:

- **RTC\_A\_ALARMCONDITION\_OFF** [Default]

Referenced by `RTC_A_configureCalendarAlarm()`.

The documentation for this struct was generated from the following file:

- `rtc.a.h`

## 47.46 EUSCI\_A\_SPI\_initSlaveParam Struct Reference

Used in the [EUSCI\\_A\\_SPI\\_initSlave\(\)](#) function as the param parameter.

```
#include <eusci_a_spi.h>
```

### Data Fields

- `uint16_t` [msbFirst](#)
- `uint16_t` [clockPhase](#)
- `uint16_t` [clockPolarity](#)
- `uint16_t` [spiMode](#)

### 47.46.1 Detailed Description

Used in the [EUSCI\\_A\\_SPI\\_initSlave\(\)](#) function as the param parameter.

### 47.46.2 Field Documentation

#### clockPhase

```
uint16_t EUSCI_A_SPI_initSlaveParam::clockPhase
```

Is clock phase select.  
Valid values are:

- **EUSCI\_A\_SPI\_PHASE\_DATA\_CHANGED\_ONFIRST\_CAPTURED\_ON\_NEXT** [Default]
- **EUSCI\_A\_SPI\_PHASE\_DATA\_CAPTURED\_ONFIRST\_CHANGED\_ON\_NEXT**

Referenced by EUSCI\_A\_SPI\_initSlave().

## clockPolarity

```
uint16_t EUSCI_A_SPI_initSlaveParam::clockPolarity
```

Is clock polarity select  
Valid values are:

- **EUSCI\_A\_SPI\_CLOCKPOLARITY\_INACTIVITY\_HIGH**
- **EUSCI\_A\_SPI\_CLOCKPOLARITY\_INACTIVITY\_LOW** [Default]

Referenced by EUSCI\_A\_SPI\_initSlave().

## msbFirst

```
uint16_t EUSCI_A_SPI_initSlaveParam::msbFirst
```

Controls the direction of the receive and transmit shift register.  
Valid values are:

- **EUSCI\_A\_SPI\_MSB\_FIRST**
- **EUSCI\_A\_SPI\_LSB\_FIRST** [Default]

Referenced by EUSCI\_A\_SPI\_initSlave().

## spiMode

```
uint16_t EUSCI_A_SPI_initSlaveParam::spiMode
```

Is SPI mode select  
Valid values are:

- **EUSCI\_A\_SPI\_3PIN**
- **EUSCI\_A\_SPI\_4PIN\_UCxSTE\_ACTIVE\_HIGH**
- **EUSCI\_A\_SPI\_4PIN\_UCxSTE\_ACTIVE\_LOW**

Referenced by EUSCI\_A\_SPI\_initSlave().

The documentation for this struct was generated from the following file:

- eusci\_a\_spi.h

## 47.47 Timer\_D\_initUpDownModeParam Struct Reference

Used in the [Timer\\_D\\_initUpDownMode\(\)](#) function as the param parameter.

```
#include <timer_d.h>
```

### Data Fields

- uint16\_t [clockSource](#)
- uint16\_t [clockSourceDivider](#)
- uint16\_t [clockingMode](#)
- uint16\_t [timerPeriod](#)
  - Is the specified timer period.*
- uint16\_t [timerInterruptEnable\\_TDIE](#)
- uint16\_t [captureCompareInterruptEnable\\_CCR0\\_CCIE](#)
- uint16\_t [timerClear](#)

### 47.47.1 Detailed Description

Used in the [Timer\\_D\\_initUpDownMode\(\)](#) function as the param parameter.

### 47.47.2 Field Documentation

#### captureCompareInterruptEnable\_CCR0\_CCIE

```
uint16_t Timer_D_initUpDownModeParam::captureCompareInterruptEnable_CCR0_CCIE
```

Is to enable or disable timer CCR0 captureComapre interrupt.

Valid values are:

- **TIMER\_D\_CCIE\_CCR0\_INTERRUPT\_ENABLE**
- **TIMER\_D\_CCIE\_CCR0\_INTERRUPT\_DISABLE** [Default]

Referenced by [Timer\\_D\\_initUpDownMode\(\)](#).

#### clockingMode

```
uint16_t Timer_D_initUpDownModeParam::clockingMode
```

Is the selected clock mode register values.

Valid values are:

- **TIMER\_D\_CLOCKINGMODE\_EXTERNAL\_CLOCK** [Default]
- **TIMER\_D\_CLOCKINGMODE\_HIRES\_LOCAL\_CLOCK**
- **TIMER\_D\_CLOCKINGMODE\_AUXILIARY\_CLK**

Referenced by [Timer\\_D\\_initUpDownMode\(\)](#).



## clockSource

```
uint16_t Timer_D_initUpDownModeParam::clockSource
```

Selects Clock source.

Valid values are:

- **TIMER\_D\_CLOCKSOURCE\_EXTERNAL\_TDCLK** [Default]
- **TIMER\_D\_CLOCKSOURCE\_ACLK**
- **TIMER\_D\_CLOCKSOURCE\_SMCLK**
- **TIMER\_D\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TDCLK**

Referenced by `Timer_D_initUpDownMode()`.

## clockSourceDivider

```
uint16_t Timer_D_initUpDownModeParam::clockSourceDivider
```

Is the divider for clock source.

Valid values are:

- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_D\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_D_initUpDownMode()`.

## timerClear

```
uint16_t Timer_D_initUpDownModeParam::timerClear
```

Decides if timer clock divider, count direction, count need to be reset.

Valid values are:

- **TIMER\_D\_DO\_CLEAR**
- **TIMER\_D\_SKIP\_CLEAR** [Default]

Referenced by `Timer_D_initUpDownMode()`.

## timerInterruptEnable\_TDIE

```
uint16_t Timer_D_initUpDownModeParam::timerInterruptEnable_TDIE
```

Is to enable or disable timer interrupt

Valid values are:

- **TIMER\_D\_TDIE\_INTERRUPT\_ENABLE**
- **TIMER\_D\_TDIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_D_initUpDownMode()`.

The documentation for this struct was generated from the following file:

- `timer_d.h`

## 47.48 PMAP\_initPortsParam Struct Reference

Used in the `PMAP_initPorts()` function as the param parameter.

```
#include <pmap.h>
```

### Data Fields

- `const uint8_t * portMapping`  
*Is the pointer to init Data.*
- `uint8_t * PxMAPy`  
*Is the pointer start of first PMAP to initialize.*
- `uint8_t numberOfPorts`  
*Is the number of Ports to initialize.*
- `uint8_t portMapReconfigure`

### 47.48.1 Detailed Description

Used in the `PMAP_initPorts()` function as the param parameter.

## 47.48.2 Field Documentation

### portMapReconfigure

```
uint8_t PMAP_initPortsParam::portMapReconfigure
```

Is used to enable/disable reconfiguration

Valid values are:

- **PMAP\_ENABLE\_RECONFIGURATION**
- **PMAP\_DISABLE\_RECONFIGURATION** [Default]

Referenced by PMAP\_initPorts().

The documentation for this struct was generated from the following file:

- pmap.h

## 47.49 RTC\_B\_configureCalendarAlarmParam Struct Reference

Used in the [RTC\\_B\\_configureCalendarAlarm\(\)](#) function as the param parameter.

```
#include <rtc_b.h>
```

### Data Fields

- uint8\_t [minutesAlarm](#)
- uint8\_t [hoursAlarm](#)
- uint8\_t [dayOfWeekAlarm](#)
- uint8\_t [dayOfMonthAlarm](#)

### 47.49.1 Detailed Description

Used in the [RTC\\_B\\_configureCalendarAlarm\(\)](#) function as the param parameter.

### 47.49.2 Field Documentation

#### dayOfMonthAlarm

```
uint8_t RTC_B_configureCalendarAlarmParam::dayOfMonthAlarm
```

Is the alarm condition for the day of the month.

Valid values are:

- **RTC\_B\_ALARMCONDITION\_OFF** [Default]

Referenced by RTC\_B\_configureCalendarAlarm().

## dayOfWeekAlarm

```
uint8_t RTC_B_configureCalendarAlarmParam::dayOfWeekAlarm
```

Is the alarm condition for the day of week.

Valid values are:

- **RTC\_B\_ALARMCONDITION\_OFF** [Default]

Referenced by `RTC_B_configureCalendarAlarm()`.

## hoursAlarm

```
uint8_t RTC_B_configureCalendarAlarmParam::hoursAlarm
```

Is the alarm condition for the hours.

Valid values are:

- **RTC\_B\_ALARMCONDITION\_OFF** [Default]

Referenced by `RTC_B_configureCalendarAlarm()`.

## minutesAlarm

```
uint8_t RTC_B_configureCalendarAlarmParam::minutesAlarm
```

Is the alarm condition for the minutes.

Valid values are:

- **RTC\_B\_ALARMCONDITION\_OFF** [Default]

Referenced by `RTC_B_configureCalendarAlarm()`.

The documentation for this struct was generated from the following file:

- `rtc_b.h`

## 47.50 Timer\_A\_outputPWMParm Struct Reference

Used in the `Timer_A_outputPWM()` function as the param parameter.

```
#include <timer_a.h>
```

### Data Fields

- `uint16_t clockSource`
- `uint16_t clockSourceDivider`
- `uint16_t timerPeriod`  
*Selects the desired timer period.*
- `uint16_t compareRegister`

- uint16\_t [compareOutputMode](#)
- uint16\_t [dutyCycle](#)

*Specifies the dutycycle for the generated waveform.*

## 47.50.1 Detailed Description

Used in the [Timer\\_A\\_outputPWM\(\)](#) function as the param parameter.

## 47.50.2 Field Documentation

### clockSource

```
uint16_t Timer_A_outputPWMParam::clockSource
```

Selects Clock source.

Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_A\_CLOCKSOURCE\_ACLK**
- **TIMER\_A\_CLOCKSOURCE\_SMCLK**
- **TIMER\_A\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by [Timer\\_A\\_outputPWM\(\)](#).

### clockSourceDivider

```
uint16_t Timer_A_outputPWMParam::clockSourceDivider
```

Is the desired divider for the clock source

Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_20**

- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_A_outputPWM()`.

## compareOutputMode

```
uint16_t Timer_A_outputPWMParam::compareOutputMode
```

Specifies the output mode.

Valid values are:

- **TIMER\_A\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_A\_OUTPUTMODE\_SET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_A\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE**
- **TIMER\_A\_OUTPUTMODE\_RESET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_A\_OUTPUTMODE\_RESET\_SET**

Referenced by `Timer_A_outputPWM()`.

## compareRegister

```
uint16_t Timer_A_outputPWMParam::compareRegister
```

Selects the compare register being used. Refer to datasheet to ensure the device has the capture compare register being used.

Valid values are:

- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_0**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_1**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_2**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_3**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_4**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_5**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_6**

Referenced by `Timer_A_outputPWM()`.

The documentation for this struct was generated from the following file:

- `timer_a.h`

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