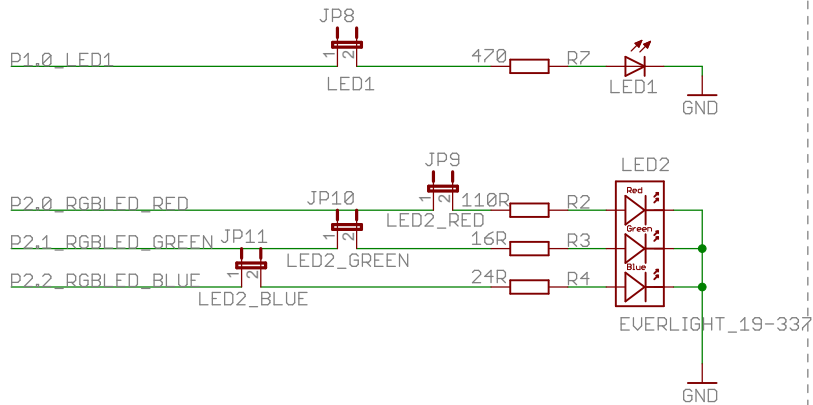
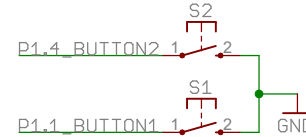


Buttons and LEDs

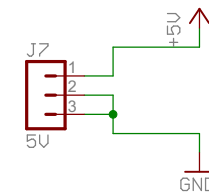
User LEDs



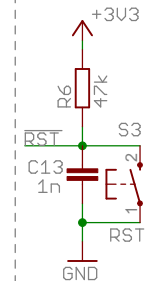
User Buttons



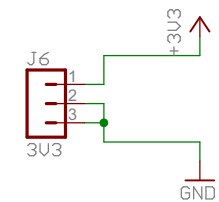
5V Header



RESET



3V3 Header



Mounting Holes: 125 mil for 4-40/M2.5/M3 screws



Silkscreen

3V3

COMPANY: Texas Instruments

TITLE: MSP-EXP432P401R

DRAWN: Mike Stein

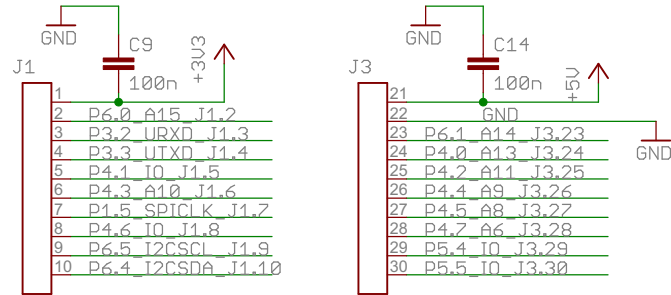
CHECKED:

APPROVED:

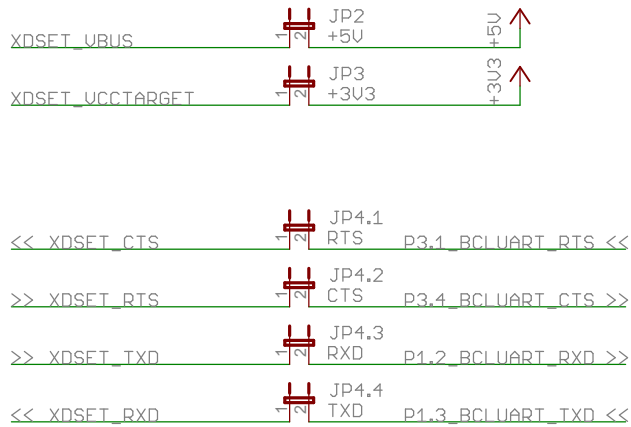
12/12/2014 2:55:58 PM Rev: Rev1.0

Sheet: 2/7

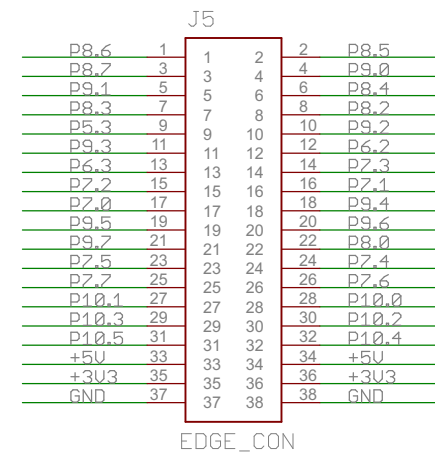
BoosterPack Headers



XDS-ET <-----> LaunchPad

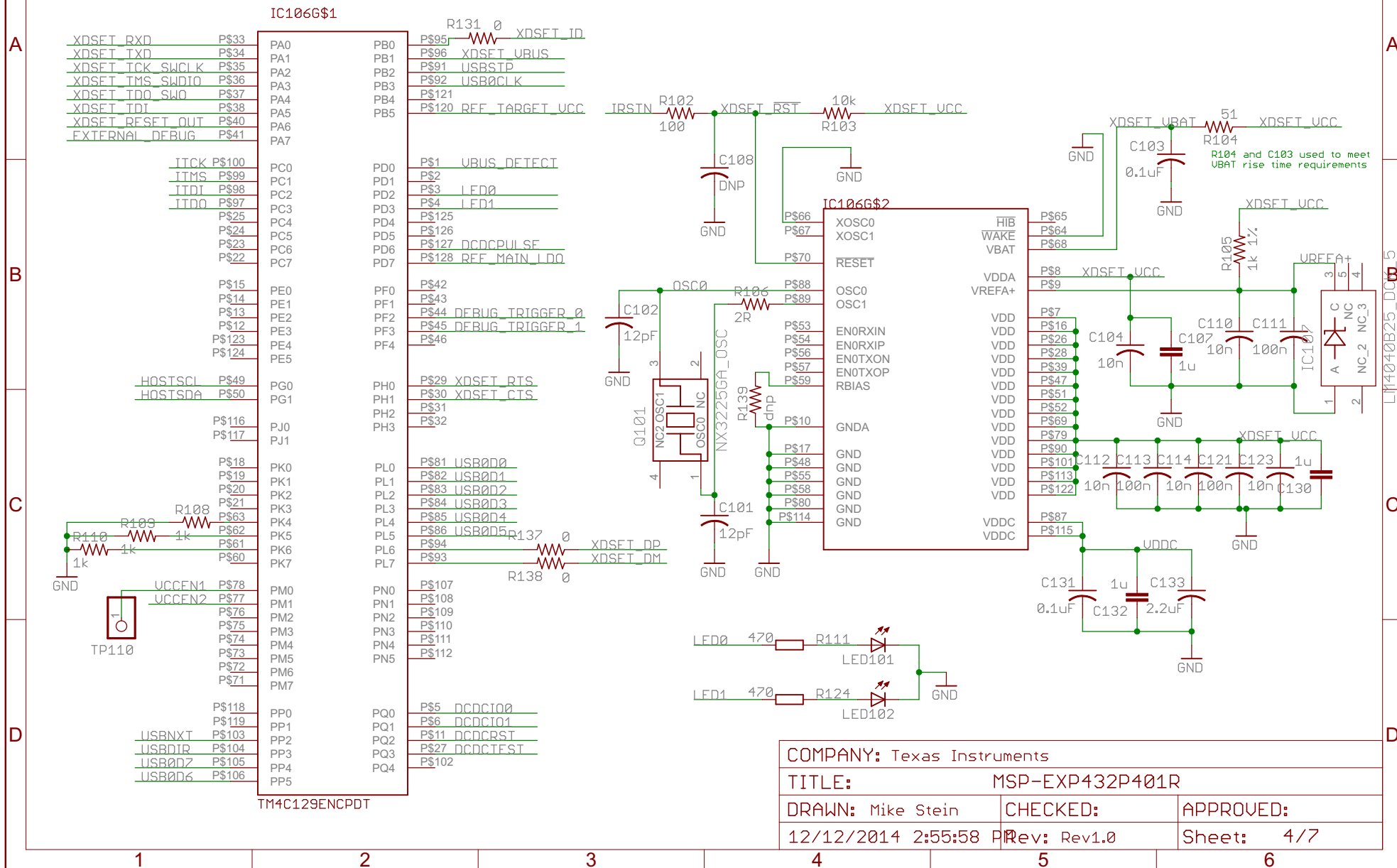


Edge Connector for Unused Pin Access



COMPANY: Texas Instruments		
TITLE: MSP-EXP432P401R		
DRAWN: Mike Stein	CHECKED:	APPROVED:
12/12/2014 2:55:58 PM	Rev: Rev1.0	Sheet: 3/7

XDS-ET Emulation and Debug



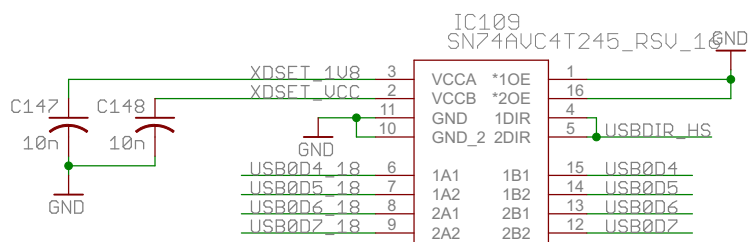
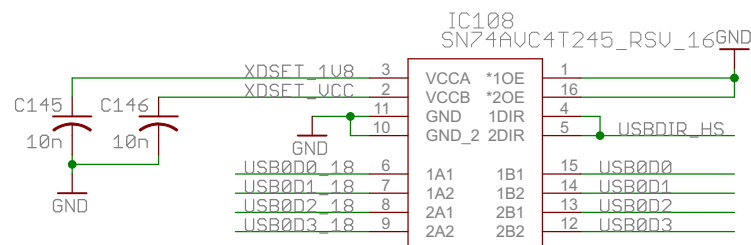
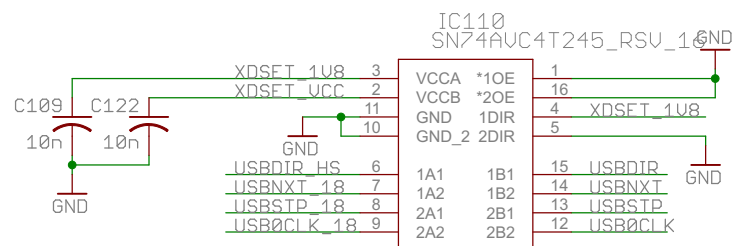
Software-controlled DCDC converter

Energy measurement method protected under U.S. Patent Application 13/329,073 and subsequent patent applications

COMPANY: Texas Instruments		
TITLE: MSP-EXP432P401R		
DRAWN: Mike Stein	CHECKED:	APPROVED:
12/12/2014 2:55:58 PM	Rev: Rev1.0	Sheet: 5/7

COMPANY: Texas Instruments		
TITLE: MSP-EXP432P401R		
DRAWN: Mike Stein	CHECKED:	APPROVED:
12/12/2014 2:55:58 PM	Rev: Rev1.0	Sheet: 5/7

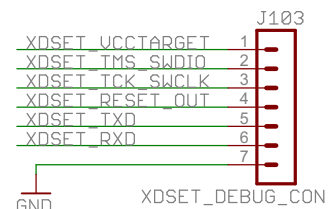
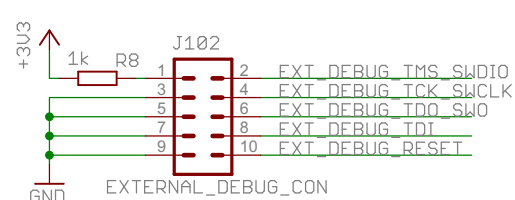
High Speed USB PHY 1.8V <--> 3.3V Interface



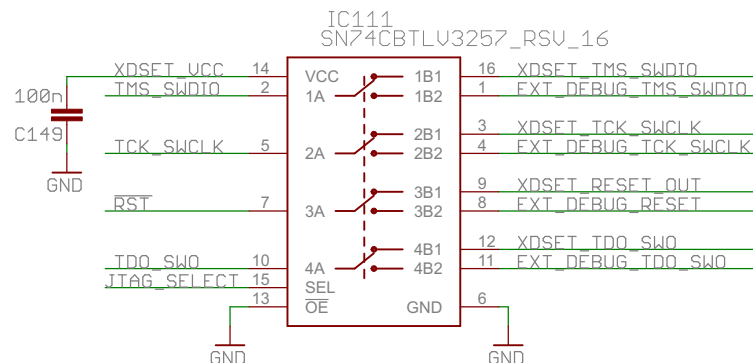
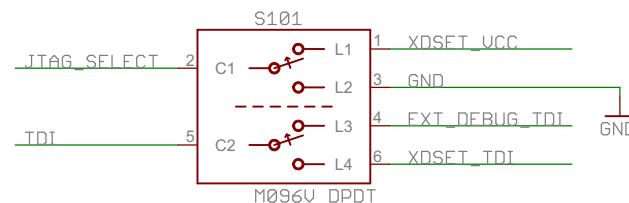
JTAG Switch and External Debug Connectors

External Debugger Connector
for MSP432P401R device
Bypass XDS-ET, use S101

External XDS-ET access
Debug external targets
Set S101 appropriately



Physical JTAG Select switch
JTAG_SELECT low = XDS-ET
JTAG_SELECT high = External Debugger



COMPANY: Texas Instruments

TITLE: MSP-EXP432P401R

DRAWN: Mike Stein

CHECKED:

APPROVED:

12/12/2014 2:55:58 PM Rev: Rev1.0

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