

Legend

Requirement Met	Pass/Fail/NRTP/Total: Fail=0
Requirement Partially Met	Pass/Fail/NRTP/Total: P >= F
Requirement not Met	Pass/Fail/NRTP/Total: P < F
Tests not Run	Pass/Fail/NRTP/Total: P = F = 0
Tests not Defined or Mapped	NT
Tests Skipped	
Requirement Not Applicable to Platform NA	

Requirement Metrics for: j7200-evm

Component	Total	Met or Partially Met	Met	Partially Met	Not Met	Not Validated	Test Gap
All Components	181 (100%)	137 (76%)	114 (63%)	23 (13%)	18 (10%)	24 (13%)	2 (1%)
Connectivity	66 (100%)	34 (52%)	31 (47%)	3 (5%)	8 (12%)	23 (35%)	1 (2%)
Baseport	114 (100%)	103 (90%)	83 (73%)	20 (18%)	10 (9%)	1 (1%)	0 (0%)
System Integration 1	1 (100%)	0 (0%)	0 (0%)	0 (0%)	0 (0%)	0 (0%)	1 (100%)

Report generated at 05:57, Thu 10 Aug 2023

Component	Subcomponent	Priority	Requirement ID	Requirement Description	Defects	j7200-evm 09.00.00-007 (5)
Baseport	ADC	P3-Medium	<a href="#">LCPD-16216</a>	ADC driver shall support one-shot mode of reading digitized value from sysfs entry		<a href="#">1/0/1/2</a>
Baseport	ADC	P3-Medium	<a href="#">LCPD-16215</a>	ADC driver shall support continous mode of operation by reading /dev/iio:deviceN file		<a href="#">1/0/0/1</a>
Baseport	AES Crypto	P3-Medium	<a href="#">LCPD-16329</a>	HW AES crypto acceleration shall be supported		<a href="#">1/0/0/1</a>
Baseport	ATF	P3-Medium	<a href="#">LCPD-17102</a>	ATF: Support starting secure runtime BL32 in TrustZone-EL1		<a href="#">3/0/0/3</a>
Baseport	ATF	P3-Medium	<a href="#">LCPD-17100</a>	ATF: Support PSCI call for system reset		<a href="#">2/1/0/3</a>
Baseport	ATF	P3-Medium	<a href="#">LCPD-17101</a>	ATF: Support starting BL33 non-secure boot-stage in EL2		<a href="#">1/0/0/1</a>
Baseport	ATF	P3-Medium	<a href="#">LCPD-17099</a>	ATF: Support initial GIC programming and interrupt group partitioning		<a href="#">1/0/0/1</a>
Baseport	ATF	P3-Medium	<a href="#">LCPD-32381</a>	ATF: Enable L2 ECC		<a href="#">1/0/0/1</a>
Baseport	ATF	P3-Medium	<a href="#">LCPD-17092</a>	ATF: Support PSCI call for system reset		<a href="#">2/1/0/3</a>
Baseport	ATF	P3-Medium	<a href="#">LCPD-17091</a>	ATF: Support initial GIC programming and interrupt group partitioning		<a href="#">1/0/0/1</a>
Baseport	Boot	P3-Medium	<a href="#">LCPD-16310</a>	System shall detect correct SoC during boot		<a href="#">1/0/0/1</a>
Baseport	Boot	P3-Medium	<a href="#">LCPD-16318</a>	Device shall have boot success rate higher than 99.9%	<a href="#">LCPD-35089</a> [Baseport Connectivity System Integration]	<a href="#">0/2/0/2</a>
Baseport	Boot	P3-Medium	<a href="#">LCPD-16319</a>	Support jailhouse device tree overlay for hypervisor		<a href="#">1/0/0/1</a>
Baseport	Crypto	P3-Medium	<a href="#">LCPD-16323</a>	Support HW Crypto for IPsec		<a href="#">1/1/0/2</a>
Baseport	Crypto DES	P3-Medium	<a href="#">LCPD-16326</a>	HW 3-DES crypto acceleration shall be supported		<a href="#">1/0/0/1</a>
Baseport	Crypto SHA	P3-Medium	<a href="#">LCPD-16324</a>	HW SHA crypto acceleration shall be supported		<a href="#">3/0/0/7</a>
Baseport	Debug	P3-Medium	<a href="#">LCPD-16314</a>	The PMU shall be configurable through kernel interfaces.		<a href="#">1/0/0/2</a>

Baseport	GPIO	P3-Medium	<a href="#">LCPD-16331</a>	GPIO driver shall enable configuring, managing, and interrupt handling of available GPIO pins.	<a href="#">LCPD-34242</a> [Baseport]	<a href="#">0/2/0/7</a>
Baseport	HYPERBUS	P3-Medium	<a href="#">LCPD-16209</a>	Hyperflash support as a kernel driver		<a href="#">1/0/0/1</a>
Baseport	HYPERBUS	P3-Medium	<a href="#">LCPD-19423</a>	Kernel: Hyperflash performance should be 80% of theoretical max		<a href="#">1/0/0/1</a>
Baseport	HYPERBUS U-Boot	P3-Medium	<a href="#">LCPD-16210</a>	Hyperflash support in U-Boot		<a href="#">1/0/0/1</a>
Baseport	HYPERBUS U-Boot	P3-Medium	<a href="#">LCPD-19420</a>	U-Boot/SPL: Hyperflash: Performance goal should be to reach 80% of raw bandwidth		<a href="#">1/0/0/1</a>
Baseport	HYPERBUS U-Boot	P3-Medium	<a href="#">LCPD-19431</a>	SPL: Hyperflash boot support		<a href="#">1/0/0/1</a>
Baseport	I2C	P3-Medium	<a href="#">LCPD-16228</a>	I2C master driver shall support the highest speed supported by slowest speed slave on the bus subject to a maximum of 3.4Mbit/s	<a href="#">LCPD-32957</a> [System Test]	<a href="#">3/0/0/6</a>
Baseport	I2C	P3-Medium	<a href="#">LCPD-16226</a>	I2C driver shall support interrupt mode for data transfers.	<a href="#">LCPD-32957</a> [System Test]	<a href="#">2/0/0/5</a>
Baseport	IPC RemoteProc	P3-Medium	<a href="#">LCPD-18219</a>	Support a user space library for exercising rpmsg-char driver on K3 SoCs		<a href="#">2/0/0/8</a>
Baseport	IPC SPL	P3-Medium	<a href="#">LCPD-19238</a>	R5 SPL: SPL to support booting of firmware image on MCU R5F		<a href="#">1/1/0/2</a>
Baseport	IPC U-Boot	P3-Medium	<a href="#">LCPD-16307</a>	U-Boot: Support early booting of Main R5FSS0 R5Fs in non-SMP mode, non-lockstep mode		<a href="#">1/1/0/2</a>
Baseport	IPC U-Boot	P3-Medium	<a href="#">LCPD-16303</a>	U-boot: to support configuring the order of remote cores being loaded		<a href="#">1/1/0/2</a>
Baseport	k3conf	P3-Medium	<a href="#">LCPD-28132</a>	Core SDK Linux shall support K3conf		<a href="#">1/0/0/1</a>
Baseport	Linux	P2-High	<a href="#">LCPD-32704</a>	DRA821: PMIC RTC control from Linux		<a href="#">2/0/0/2</a>
Baseport	MMCSDB	P3-Medium	<a href="#">LCPD-16233</a>	MMC: Read throughput within 1 standard deviation from historical data		<a href="#">7/1/0/10</a>
Baseport	MMCSDB	P3-Medium	<a href="#">LCPD-16232</a>	ext3 file system shall be supported on MMC		<a href="#">2/0/0/2</a>
Baseport	MMCSDB	P3-Medium	<a href="#">LCPD-16234</a>	MMC: Write throughput within 1 standard deviation from historical data		<a href="#">6/1/0/9</a>
Baseport	MMCSDB	P3-Medium	<a href="#">LCPD-16262</a>	eMMC: Support for HS200 cards in kernel	<a href="#">LCPD-34999</a> [System Test]	<a href="#">4/0/0/5</a>
Baseport	MMCSDB	P3-Medium	<a href="#">LCPD-16271</a>	MMC/SD/SDIO: support building and insertion of driver as a kernel module		<a href="#">0/0/6/6</a>
Baseport	MMCSDB	P3-Medium	<a href="#">LCPD-19515</a>	Kernel: eMMC: Support HS400 speed mode	<a href="#">LCPD-34999</a> [System Test]	<a href="#">4/0/0/5</a>
Baseport	MMCSDB	P3-Medium	<a href="#">LCPD-16231</a>	ext2 file system shall be supported on MMC		<a href="#">4/0/0/5</a>
Baseport	MMCSDB	P3-Medium	<a href="#">LCPD-19496</a>	ext4 file system shall be supported on MMC		<a href="#">1/0/0/1</a>
Baseport	MMCSDB	P2-High	<a href="#">LCPD-19504</a>	Kernel: eMMC: Support for Command Queue Engine (CQE)		<a href="#">1/0/0/1</a>
Baseport	MMCSDB	P3-Medium	<a href="#">LCPD-16230</a>	MMC driver shall support 8-bit data transfers to the MMC/eMMC card		<a href="#">1/0/0/1</a>
Baseport	MMCSDB	P3-Medium	<a href="#">LCPD-16214</a>	MMC: Kernel: support ADMA for DMA transfer		<a href="#">5/0/0/6</a>
Baseport	MMCSDB	P3-Medium	<a href="#">LCPD-19575</a>	Kernel: Support SDR104 in SD card	<a href="#">LCPD-34999</a> [System Test]	<a href="#">5/1/0/6</a>
Baseport	MMCSDB	P3-	<a href="#">LCPD-19574</a>	Kernel: Support DDR50 in SD	<a href="#">LCPD-34999</a>	<a href="#">5/1/0/6</a>

		Medium	card	[System Test]	
Baseport	MMCSDBaseport	P3-Medium	<a href="#">LCPD-16229</a>	MMC driver shall support 4-bit data transfers to the MMC/SD card	<a href="#">48/0/1/68</a>
Baseport	MMCSDBaseport	P3-Medium	<a href="#">LCPD-16268</a>	MMC: Stress Test eMMC HS200 mode with data write-read (500 MB) and reboot	<a href="#">1/0/0/1</a>
Baseport	MMCSDBaseport	P3-Medium	<a href="#">LCPD-19618</a>	Kernel: eMMC basic support	<a href="#">1/0/0/1</a>
Baseport	MMCSDBaseport	P3-Medium	<a href="#">LCPD-19615</a>	Kernel: SD card basic support	<a href="#">1/0/0/1</a>
Baseport	MMCSDBaseport	P3-Medium	<a href="#">LCPD-19529</a>	U-Boot: SD: Support SDR104	<a href="#">1/1/0/4</a>
Baseport	MMCSDBaseport	P3-Medium	<a href="#">LCPD-19528</a>	U-BOOT: SD: Support DDR50	<a href="#">1/1/0/4</a>
Baseport	MMCSDBaseport	P3-Medium	<a href="#">LCPD-19525</a>	U-Boot: Support for eMMC	<a href="#">2/0/0/2</a>
Baseport	MMCSDBaseport	P2-High	<a href="#">LCPD-19511</a>	SPL: eMMC: Support HS200	<a href="#">LCPD-32957</a> [System Test] <a href="#">2/0/0/2</a>
Baseport	MMCSDBaseport	P2-High	<a href="#">LCPD-16295</a>	U-BOOT: eMMC: Support HS400 speed mode	<a href="#">LCPD-32957</a> [System Test] <a href="#">2/0/0/2</a>
Baseport	MMCSDBaseport	P3-Medium	<a href="#">LCPD-19509</a>	SPL: Support eMMC alternative boot mode support	<a href="#">1/0/0/1</a>
Baseport	MMCSDBaseport	P2-High	<a href="#">LCPD-19526</a>	U-BOOT: MMC: ADMA support	<a href="#">2/0/0/2</a>
Baseport	MMCSDBaseport	P3-Medium	<a href="#">LCPD-19514</a>	U-Boot: Support for SD card	<a href="#">2/0/0/2</a>
Baseport	OSPI	P2-High	<a href="#">LCPD-16212</a>	Kernel: OSPI Performance Requirement: OSPI Raw - read At least 250MB/sec write throughput of 1MB/sec	<a href="#">LCPD-32544</a> [Baseport] <a href="#">1/0/0/2</a>
Baseport	OSPI	P3-Medium	<a href="#">LCPD-17680</a>	Kernel: Support xSPI compliant Octal flash	<a href="#">2/0/0/4</a>
Baseport	OSPI	P3-Medium	<a href="#">LCPD-16287</a>	Kernel: Support Octal SPI (OSPI) flash devices	<a href="#">2/0/0/2</a>
Baseport	OSPI U-Boot	P3-Medium	<a href="#">LCPD-16297</a>	SPL: OSPI read performance should be at least 80% of theoretical maximum	<a href="#">LCPD-35087</a> [Baseport] <a href="#">1/0/0/1</a>
Baseport	OSPI U-Boot	P3-Medium	<a href="#">LCPD-16284</a>	SPL: Support for OSPI boot (non XIP)	<a href="#">1/0/0/1</a>
Baseport	OSPI U-Boot	P3-Medium	<a href="#">LCPD-17682</a>	SPL: Support booting from xSPI compliant Octal Flash	<a href="#">1/0/0/1</a>
Baseport	OSPI U-Boot	P3-Medium	<a href="#">LCPD-16296</a>	U-Boot: OSPI read performance should be at least 80% of theoretical maximum	<a href="#">LCPD-35087</a> [Baseport] <a href="#">1/0/0/1</a>
Baseport	OSPI U-Boot	P3-Medium	<a href="#">LCPD-16288</a>	U-Boot: Support Octal SPI(OSPI) flash devices	<a href="#">LCPD-35087</a> [Baseport] <a href="#">2/0/0/3</a>
Baseport	SPI	P2-High	<a href="#">LCPD-29902</a>	Linux SDK shall support SPI: UDMA mode of operation with real time performance	<a href="#">LCPD-34925</a> [Baseport] <a href="#">0/1/1/2</a>
Baseport	SPL	P3-Medium	<a href="#">LCPD-17149</a>	SPL: Support for Leo PMIC TPS65941 in SPL	<a href="#">1/0/0/1</a>
Baseport	SPL U-Boot	P2-High	<a href="#">LCPD-28685</a>	j7VCL/J721e: Enable booting from boot1 partition of eMMC	<a href="#">1/0/0/1</a>
Baseport	System	P3-Medium	<a href="#">LCPD-16315</a>	Whetstone performance is between 1 std dev of previous release	<a href="#">1/0/0/1</a>
Baseport	System	P3-Medium	<a href="#">LCPD-16316</a>	Dhrystone performance is between 1 std dev of previous release	<a href="#">1/0/0/1</a>
Baseport	System	P3-Medium	<a href="#">LCPD-16312</a>	OS Kernel shall support preemptive scheduler configuration	<a href="#">1/0/0/2</a>
Baseport	System	P3-Medium	<a href="#">LCPD-6869</a>	Whetstone performance is between 1 std dev of previous release	<a href="#">1/0/0/1</a>
Baseport	System	P3-Medium	<a href="#">LCPD-6870</a>	Dhrystone performance is between 1 std dev of previous release	<a href="#">1/0/0/1</a>

Baseport	System_Test	P3-Medium	<a href="#">LCPD-28236</a>	MultiBench benchmark Support		<a href="#">1/0/0/1</a>
Baseport	System_Test	P3-Medium	<a href="#">LCPD-28237</a>	SpecInt2K6: benchmark Support	<a href="#">LCPD-28142</a> [System Test]	<a href="#">0/1/0/1</a>
Baseport	Timers	P3-Medium	<a href="#">LCPD-16311</a>	Support for Hardware Timer		<a href="#">1/0/0/2</a>
Baseport	U-Boot UART	P3-Medium	<a href="#">LCPD-16261</a>	SPL: Support UART boot		<a href="#">1/0/0/2</a>
Baseport	U-Boot USB USBDEVICE	P3-Medium	<a href="#">LCPD-17195</a>	UBOOT: USB DFU support to OSPI flash		<a href="#">1/0/0/1</a>
Baseport	U-Boot USB USBDEVICE	P2-High	<a href="#">LCPD-19435</a>	UBOOT: support for DFU download		<a href="#">1/0/0/1</a>
Baseport	U-Boot USB USBDEVICE	P2-High	<a href="#">LCPD-19437</a>	UBOOT: support for DFU download to eMMC		<a href="#">1/0/0/1</a>
Baseport	U-Boot USB USBDEVICE	P2-High	<a href="#">LCPD-19436</a>	UBOOT: support for DFU download to SD card		<a href="#">1/0/0/1</a>
Baseport	U-Boot USB USBDEVICE	P2-High	<a href="#">LCPD-19438</a>	SPL: DFU boot support		<a href="#">1/0/0/1</a>
Baseport	UART	P3-Medium	<a href="#">LCPD-16239</a>	UART driver shall support settings of 115200 baud rate, 8 bit data, 1 stop bit, no parity, no flow control and no DMA usage for Debug Console functionality		<a href="#">1/0/0/1</a>
Baseport	UART	P3-Medium	<a href="#">LCPD-16237</a>	UART driver shall support configuration of the baudrate.	<a href="#">LCPD-22814</a> [System Test]	<a href="#">0/1/6/30</a>
Baseport	UART	P3-Medium	<a href="#">LCPD-16238</a>	UART driver shall support baud rates from 2400 bps up to 3.6 Mbps.	<a href="#">LCPD-22814</a> [System Test]	<a href="#">0/2/6/43</a>
Baseport	UART UBOOT	P3-Medium	<a href="#">LCPD-16328</a>	Support Symmetric multiprocessing (SMP)		<a href="#">43/6/0/67</a>
Baseport	UART UBOOT	P3-Medium	<a href="#">LCPD-16327</a>	The software driver / support for RNG shall be provided.		<a href="#">2/2/0/4</a>
Baseport	UBOOT	P2-High	<a href="#">LCPD-16375</a>	Optimized ROM boot flow support		<a href="#">3/0/0/3</a>
Baseport	UBOOT	P3-Medium	<a href="#">LCPD-16313</a>	Bootloader shall support kernel image either as an uncompressed image file or as a ZImage compressed kernel file.		<a href="#">1/0/0/1</a>
Baseport	UBOOT	P3-Medium	<a href="#">LCPD-18939</a>	Uboot: R5/SPL: Support of Leo + Hera PMIC		<a href="#">1/0/0/1</a>
Baseport	UBOOT	P3-Medium	<a href="#">LCPD-16317</a>	Device shall boot to Linux prompt		<a href="#">1/0/0/1</a>
Baseport	UBOOT driver	P3-Medium	<a href="#">LCPD-16302</a>	ESM driver support in uboot	<a href="#">LCPD-34904</a> [Baseport]	<a href="#">0/1/0/1</a>
Baseport		P3-Medium	<a href="#">LCPD-16336</a>	Support mailbox		<a href="#">2/0/0/4</a>
Baseport		P2-High	<a href="#">LCPD-16321</a>	Support IPC from A72 to each of the cores of MAIN R5FSS in lockstep mode - remoteproc used to boot the remote CPU		<a href="#">1/0/0/1</a>
Baseport		P3-Medium	<a href="#">LCPD-18835</a>	Add support for DMA-Heaps allocations from CMA regions		<a href="#">1/0/0/1</a>
Baseport		P3-Medium	<a href="#">LCPD-18833</a>	Linux Benchmarking: Coremark Pro Multicore run to be included in ARM benchmark testsuite.		<a href="#">1/0/0/1</a>
Baseport		P2-High	<a href="#">LCPD-14959</a>	Add CoreMark-Pro for the benchmarking		<a href="#">1/0/0/1</a>
Baseport		P3-Medium	<a href="#">LCPD-18148</a>	Add support for DMA-Heaps allocations from Carveout regions		<a href="#">1/0/0/1</a>
Baseport		P3-Medium	<a href="#">LCPD-16337</a>	Support HW spinlocks	<a href="#">LCPD-32957</a> [System Test]	<a href="#">1/0/0/1</a>
Baseport		P3-Medium	<a href="#">LCPD-16347</a>	Support remote proc booting of Main R5FSS in non-SMP mode, non-lockstep mode		<a href="#">1/1/0/2</a>
Baseport		P3-Medium	<a href="#">LCPD-16346</a>	Support remote proc booting of Main R5FSS in lockstep mode		<a href="#">1/0/0/1</a>

Baseport	P2-High	<a href="#">LCPD-28673</a>	J7200/J7VCL: support for thermal mitigation using cpufreq		<a href="#">3/0/0/4</a>
Baseport	P2-High	<a href="#">LCPD-16340</a>	Support IPC from A72 to Main R5FSS running in lockstep mode - Uboot used to boot the remote CPU		<a href="#">1/1/0/2</a>
Baseport	P3-Medium	<a href="#">LCPD-10121</a>	Add STREAM benchmark for Single-Core		<a href="#">1/0/0/1</a>
Baseport	P2-High	<a href="#">LCPD-28611</a>	MCSPI: Enable Slave support	<a href="#">LCPD-34925</a> [Baseport]	<a href="#">0/1/1/2</a>
Baseport	P3-Medium	<a href="#">LCPD-6871</a>	Linpack performance is between 1 std dev of previous release		<a href="#">1/0/0/1</a>
Baseport	P3-Medium	<a href="#">LCPD-6873</a>	LMBench performance is between 1 std dev of previous release		<a href="#">1/0/0/1</a>
Baseport	P2-High	<a href="#">LCPD-28674</a>	DRA821: Add support for cpufreq		<a href="#">3/0/0/4</a>
Baseport	P1-Urgent	<a href="#">LCPD-26599</a>	J7: poweroff: Add support for poweroff command from Linux	<a href="#">LCPD-35027</a> [Baseport]	<a href="#">0/1/0/1</a>
Baseport	P3-Medium	<a href="#">LCPD-16322</a>	The RNG driver shall provision the retrieval of 32 bit random number from the hardware generator		<a href="#">2/2/0/4</a>
Baseport	P3-Medium	<a href="#">LCPD-16325</a>	Watchdog shall be supported	<a href="#">LCPD-34904</a> [Baseport]	<a href="#">0/9/0/10</a>
Baseport	P3-Medium	<a href="#">LCPD-16335</a>	Support IPC from A72 to each of the cores of Main R5FSS in non-SMP, non-lockstep mode - Uboot used to boot the remote CPU		<a href="#">1/1/0/2</a>
Baseport	P3-Medium	<a href="#">LCPD-6872</a>	NBench performance is between 1 std dev of previous release		<a href="#">1/0/0/1</a>
Baseport	P2-High	<a href="#">LCPD-16338</a>	Support IPC from A72 to MCU Island R5FSS in lockstep mode - R5 SPL used to boot the remote CPU		<a href="#">1/1/0/2</a>
Baseport	P2-High	<a href="#">LCPD-16339</a>	Support IPC from A72 to each of the cores of Main R5FSS in non-SMP, non-lockstep mode - remoteproc used to boot the remote CPU		<a href="#">1/1/0/2</a>
Connectivity CPSW	P2-High	<a href="#">LCPD-24552</a>	CPSW5G: Support TSN capabilities	<a href="#">LCPD-34999</a> [System Test]	<a href="#">10/0/0/10</a>
Connectivity CPSW	P2-High	<a href="#">LCPD-24548</a>	CPSW5G: Support Switch mode natively		<a href="#">0/1/0/1</a>
Connectivity CPSW	P2-High	<a href="#">LCPD-24550</a>	CPSW5G: Support Timesync	<a href="#">LCPD-32519</a> [System Test]	<a href="#">0/0/1/1</a>
Connectivity CPSW	P3-Medium	<a href="#">LCPD-24546</a>	CPSW5G: Support MAC mode natively	<a href="#">LCPD-34216</a> [System Test]	<a href="#">0/0/4/12</a>
Connectivity CPSW	P3-Medium	<a href="#">LCPD-20667</a>	Ethernet Performance Measurement for default CPSW interface	<a href="#">LCPD-34829</a> [NETWORKING]	<a href="#">8/3/0/11</a>
Connectivity CPSW CPSW2G TSN	P3-Medium	<a href="#">LCPD-22876</a>	CPSW2G TSN Endpoint: Frame Preemption CPSW HW offload		<a href="#">0/0/1/1</a>
Connectivity CPSW CPTS	P3-Medium	<a href="#">LCPD-16277</a>	PTP using CPSW CPTS for 1588 Time-stamping in Linux	<a href="#">LCPD-32519</a> [System Test]	<a href="#">0/0/1/1</a>
Connectivity CPSW ETHERNET	P3-Medium	<a href="#">LCPD-16272</a>	CPSW: Ethernet driver shall support placing the interface in promiscuous mode	<a href="#">LCPD-34829</a> [NETWORKING]	<a href="#">1/1/0/2</a>
Connectivity CPSW ETHERNET	P3-Medium	<a href="#">LCPD-16300</a>	NFS support : support root filesystem over CPSW2G ethernet		<a href="#">1/0/0/1</a>
Connectivity CPSW ETHERNET	P3-Medium	<a href="#">LCPD-16275</a>	am65xx: CPSW Checksum Offload Support	<a href="#">LCPD-34829</a> [NETWORKING]	<a href="#">1/0/0/1</a>
			CPSW: In switch mode the		

Connectivity	CPSW ETHERNET ETHERNETSWITCH	P3- Medium	<a href="#">LCPD-16218</a>	ethernet driver downstream ports (and the PHYs) shall be configured to support auto-negotiation CPSW: The ethernet switch driver shall provide a suitable interface for adding/querying/modifying entries in the ALE	<a href="#">4/0/0/4</a>
Connectivity	CPSW ETHERNET ETHERNETSWITCH	P3- Medium	<a href="#">LCPD-16220</a>	CPSW: The ethernet switch driver shall support multicast frame filtering based on entries programmed in the ALE	<a href="#">LCPD-34829</a> [ <b>NETWORKING</b> ]
Connectivity	CPSW ETHERNET ETHERNETSWITCH	P3- Medium	<a href="#">LCPD-16222</a>	CPSW: The Ethernet switch driver shall provide an interface to configure the speed and duplexity of the downstream ports (same options for both independent ports).	<a href="#">0/0/1/2</a>
Connectivity	CPSW ETHERNET ETHERNETSWITCH	P3- Medium	<a href="#">LCPD-16223</a>	CPSW: The Ethernet switch driver will support a configuration whereby the only unicast packets forwarded to the host are those that match the host's MAC address	<a href="#">5/0/0/5</a>
Connectivity	CPSW ETHERNET ETHERNETSWITCH	P3- Medium	<a href="#">LCPD-16221</a>	CPSW: Ethernet switch driver shall support ability to dump statistics of the switch	<a href="#">1/0/0/1</a>
Connectivity	CPSW ETHERNET ETHERNETSWITCH	P3- Medium	<a href="#">LCPD-16225</a>	CPSW: The ethernet switch driver shall provide for MAC address configuration through ifconfig command. The default value shall be read from EFUSES/EEPROM as supported by the board/SoC.	<a href="#">LCPD-34829</a> [ <b>NETWORKING</b> ]
Connectivity	CPSW ETHERNET ETHERNETSWITCH	P3- Medium	<a href="#">LCPD-16219</a>	CPSW: The Ethernet switch driver shall provide the ability to limit the number of multicast/broadcast frames forwarded to the host over a given time interval.	<a href="#">1/0/0/1</a>
Connectivity	CPSW ETHERNET ETHERNETSWITCH	P3- Medium	<a href="#">LCPD-16224</a>	CPSW: Ethernet driver shall gate its clock when interface is down	<a href="#">0/0/1/1</a>
Connectivity	CPSW ETHERNET runtime_pm	P3- Medium	<a href="#">LCPD-16259</a>	UBOOT: Support for Ethernet	<a href="#">1/0/0/1</a>
Connectivity	CPSW ETHERNET U-Boot	P3- Medium	<a href="#">LCPD-19440</a>	CPSW2G TSN Endpoint: support for Time Aware Shaper (TAS) CPSW HW offload	<a href="#">1/0/0/1</a>
Connectivity	CPSW TSN	P3- Medium	<a href="#">LCPD-22875</a>	USB: Dual Role: Support role switching based on ID pin status	NT
Connectivity	DUALROLE USB	P3- Medium	<a href="#">LCPD-16263</a>	U-Boot: Support xSPI compliant Octal Flash	<a href="#">1/0/0/1</a>
Connectivity	OSPI U-Boot	P3- Medium	<a href="#">LCPD-17681</a>	PCIe EP: Support for Gen3 mode of operation	<a href="#">2/0/0/2</a>
Connectivity	PCIe	P2-High	<a href="#">LCPD-16281</a>	PCIe EP: endpoint must support MSI-X interrupts	<a href="#">2/0/0/2</a>
Connectivity	PCIe	P3- Medium	<a href="#">LCPD-16293</a>	PCIe: Functional support for PCIe based WiFi cards	<a href="#">0/0/8/8</a>
Connectivity	PCIe	P3- Medium	<a href="#">LCPD-16273</a>		<a href="#">2/0/1/3</a>
Connectivity	PCIe	P2-High	<a href="#">LCPD-16289</a>	PCIe: PCIe EP mode support	<a href="#">LCPD-32564</a> [ <b>Connectivity</b> <b>NETWORKING</b> ]
Connectivity	PCIe	P3- Medium	<a href="#">LCPD-17196</a>	PCIe Benchmarking: Throughput number for SSD device over NVMe	<a href="#">0/0/11/11</a>
Connectivity	PCIe	P3- Medium	<a href="#">LCPD-16291</a>	PCIe RC: support MSI interrupts	<a href="#">2/0/0/2</a>
Connectivity	PCIe	P3- Medium	<a href="#">LCPD-16294</a>	PCIe: EP: Support CPU based transfer across the interface	<a href="#">1/0/2/5</a>
Connectivity	PCIe	P3- Medium			<a href="#">0/0/7/7</a>

Connectivity PCIe	P3-Medium	<a href="#">LCPD-16301</a>	PCIe Benchmarking: Throughput number for DMA transfer	<a href="#">0/0/2/2</a>
Connectivity PCIe	P3-Medium	<a href="#">LCPD-16211</a>	PCIe root complex driver shall support both PCIe lanes	<a href="#">3/0/0/4</a>
Connectivity PCIe	P2-High	<a href="#">LCPD-16278</a>	PCIe EP : support for multifunction capability in EP mode of operation	<a href="#">0/0/7/7</a>
Connectivity PCIe	P2-High	<a href="#">LCPD-16282</a>	PCIe EP : driver must support configurable number of lanes	<a href="#">0/0/3/3</a>
Connectivity PCIe	P2-High	<a href="#">LCPD-16280</a>	PCIe RC: Support for Gen3 mode of operation	<a href="#">2/0/0/2</a>
Connectivity PCIe	P3-Medium	<a href="#">LCPD-16290</a>	PCIe EP: endpoint must support MSI	<a href="#">0/0/3/3</a>
Connectivity PCIe	P3-Medium	<a href="#">LCPD-16235</a>	PCIe: Read throughput within 1 standard deviation from historical data	<a href="#">4/16/0/24</a>
Connectivity PCIe	P3-Medium	<a href="#">LCPD-16292</a>	PCIe RC: support MSI-X interrupts	<a href="#">0/1/2/3</a>
Connectivity PCIe	P3-Medium	<a href="#">LCPD-16236</a>	PCIe: Write throughput within 1 standard deviation from historical data	<a href="#">2/16/0/22</a>
Connectivity PCIe	P2-High	<a href="#">LCPD-16279</a>	PCIe EP : support for virtual function and SRIOV	<a href="#">0/0/11/11</a>
Connectivity PCIe	P3-Medium	<a href="#">LCPD-16276</a>	PCIe: EP: Support DMA based transfer across the interface	<a href="#">0/0/13/13</a>
Connectivity PCIe	P2-High	<a href="#">LCPD-16283</a>	PCIe RC : driver must support configurable number of lanes	<a href="#">1/0/0/1</a>
Connectivity SERDES SPL U-Boot	P3-Medium	<a href="#">LCPD-19162</a>	SERDES sharing - support for configuring SERDES in R5-SPL	<a href="#">1/0/0/1</a>
Connectivity USB USBCLIENT	P3-Medium	<a href="#">LCPD-16240</a>	USB client gadget driver for CDC ECM (Communication Device Class for Ethernet Control Model) shall be supported.	<a href="#">0/0/3/3</a>
Connectivity USB USBCLIENT	P3-Medium	<a href="#">LCPD-16242</a>	USB client driver shall support cable connect and disconnect feature.	<a href="#">0/1/2/3</a>
Connectivity USB USBCLIENT	P3-Medium	<a href="#">LCPD-16243</a>	USB client gadget driver for composite device (CDC + MSC and CDC (ACM) + CDC (ECM)) shall be supported.	<a href="#">0/2/3/5</a>
Connectivity USB USBCLIENT	P3-Medium	<a href="#">LCPD-16245</a>	USB client gadget driver for MultiFunction Composite (CDC Ethernet + CDC Serial + Storage) device shall be supported.	<a href="#">0/1/2/3</a>
Connectivity USB USBCLIENT	P3-Medium	<a href="#">LCPD-16241</a>	USB client gadget driver for CDC(Communication Device Class for Abstract Control Model) shall be supported.	<a href="#">0/0/2/3</a>
Connectivity USB USBCLIENT	P2-High	<a href="#">LCPD-16264</a>	USB-DEVICE: ZLP Support in USB driver	<a href="#">0/0/1/1</a>
Connectivity USB USBCLIENT	P3-Medium	<a href="#">LCPD-16265</a>	USB: DEVICE: Support for NCM gadget driver	<a href="#">0/0/1/1</a>
Connectivity USB USBCLIENT	P2-High	<a href="#">LCPD-16266</a>	USB: DEVICE: Support for composite gadget (Running NCM and ACM function simultaneously) USB client driver shall support	<a href="#">0/1/1/2</a>

Connectivity USB USBCLIENT	P3-Medium	<a href="#">LCPD-16246</a>	both High-speed and Full-speed mode for all supported client gadget drivers		<a href="#">0/0/8/8</a>
Connectivity USB USBCLIENT	P3-Medium	<a href="#">LCPD-16244</a>	USB client gadget driver for MultiFunction Composite (CDC/RNDIS Ethernet + CDC Serial + Storage) device shall be supported.	<a href="#">LCPD-32701</a> [Connectivity NETWORKING]	<a href="#">0/0/2/7</a>
Connectivity USB USBHOST	P3-Medium	<a href="#">LCPD-16252</a>	USB host driver shall support HUB class client devices.		<a href="#">1/0/0/1</a>
Connectivity USB USBHOST	P3-Medium	<a href="#">LCPD-16247</a>	USB host driver shall support Human interface client devices.		<a href="#">1/0/2/3</a>
Connectivity USB USBHOST	P3-Medium	<a href="#">LCPD-16250</a>	USB host driver shall support CDC ACM class client devices.		<a href="#">1/0/1/2</a>
Connectivity USB USBHOST	P3-Medium	<a href="#">LCPD-16251</a>	USB host driver shall support CDC ECM class client devices.		<a href="#">2/0/0/2</a>
Connectivity USB USBHOST	P3-Medium	<a href="#">LCPD-16253</a>	USB host driver shall support WiFi client devices.		<a href="#">1/0/0/1</a>
Connectivity USB USBHOST	P3-Medium	<a href="#">LCPD-16254</a>	USB host driver shall support enumeration of Low speed devices		<a href="#">1/0/0/1</a>
Connectivity USB USBHOST	P3-Medium	<a href="#">LCPD-16257</a>	USB host driver shall support High Speed, Full Speed, and Low-speed devices simultaneously (via Hubs)		<a href="#">1/0/0/1</a>
Connectivity USB USBHOST	P3-Medium	<a href="#">LCPD-16256</a>	USB host driver shall support enumeration of High speed devices		<a href="#">1/0/1/2</a>
Connectivity USB USBHOST	P3-Medium	<a href="#">LCPD-16248</a>	USB host driver shall support Video class client devices.		<a href="#">2/0/0/2</a>
Connectivity USB USBHOST	P3-Medium	<a href="#">LCPD-16255</a>	USB host driver shall support enumeration of Full speed devices		<a href="#">1/0/0/1</a>
Connectivity USB USBHOST	P3-Medium	<a href="#">LCPD-16249</a>	USB host driver shall support Audio class client devices.	<a href="#">LCPD-32923</a> [Connectivity]	<a href="#">15/3/0/18</a>
Connectivity USB USBHOST	P3-Medium	<a href="#">LCPD-16258</a>	USB host driver shall support selective suspend.	<a href="#">LCPD-24595</a> [Connectivity NETWORKING System Test]	<a href="#">0/0/0/2</a>
Connectivity	P3-Medium	<a href="#">LCPD-20668</a>	Ethernet Performance Measurement for virtual CPSW interface		<a href="#">0/0/0/38</a>
System Integration	Yocto	P1-Urgent	<a href="#">LCPD-22006</a>	Minimal platform: tinySDK image: minimal boot default: packages	NT