

Legend

Requirement Met	Pass/Fail/NRTP/Total: Fail=0
Requirement Partially Met	Pass/Fail/NRTP/Total: P >= F
Requirement not Met	Pass/Fail/NRTP/Total: P < F
Tests not Run	Pass/Fail/NRTP/Total: P = F = 0
Tests not Defined or Mapped	NT
Tests Skipped	
Requirement Not Applicable to Platform NA	

Requirement Metrics for: j722s\_evm-fs

Component	Total	Met or Partially Met	Met	Partially Met	Not Met	Not Validated	Test Gap
All Components	228 (100%)	179 (79%)	168 (74%)	11 (5%)	30 (13%)	9 (4%)	10 (4%)
Baseport	115 (100%)	92 (80%)	86 (75%)	6 (5%)	18 (16%)	3 (3%)	2 (2%)
Graphics	34 (100%)	29 (85%)	29 (85%)	0 (0%)	3 (9%)	0 (0%)	2 (6%)
Multimedia	31 (100%)	27 (87%)	27 (87%)	0 (0%)	0 (0%)	4 (13%)	0 (0%)
NETWORKING	1 (100%)	1 (100%)	1 (100%)	0 (0%)	0 (0%)	0 (0%)	0 (0%)
Connectivity	47 (100%)	30 (64%)	25 (53%)	5 (11%)	9 (19%)	2 (4%)	6 (13%)

Report generated at 08:24, Fri 29 Mar 2024

Component	Subcomponent	Priority	Requirement ID	Requirement Description	Defects	j722s_evm-fs 09.02.00-008 (4)
Baseport	Baseport OSPI	P3-Medium	<a href="#">LCPD-33096</a>	Linux SDK shall support OSPI: Programmable device sizes	<a href="#">4/0/0/7</a>	
Baseport	Baseport MMCSd	P3-Medium	<a href="#">LCPD-33135</a>	Core SDK Linux shall include performance documentation for MMCSd EXT4 and RAW read/Write performance	<a href="#">1/0/0/1</a>	
Baseport	Baseport MMCSd	P3-Medium	<a href="#">LCPD-33136</a>	Core SDK Linux shall include performance documentation for eMMC EXT4 Read/Write and Raw performance	<a href="#">1/0/0/2</a>	
Graphics		P3-Medium	<a href="#">LCPD-28554</a>	Graphics driver shall support RGB565 texture format	<a href="#">1/0/0/1</a>	
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-30687</a>	Linux driver for H.264 (AVC) hardware video decoder	<a href="#">2/0/0/2</a>	
Graphics		P3-Medium	<a href="#">LCPD-30690</a>	Linux SDK shall support GPU: Graphics driver shall support DMABUF import	<a href="#">1/0/0/1</a>	
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-30684</a>	Linux driver for H.265 (HEVC) hardware video decoder	<a href="#">1/0/0/1</a>	
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-30682</a>	Video codecs shall optimize the memory allocation	<a href="#">0/0/1/1</a>	
Graphics		P3-Medium	<a href="#">LCPD-30674</a>	Linux SDK shall support GPU: Graphics driver shall support upto 8Kx8K textures	<a href="#">1/0/0/1</a>	
Graphics	Graphics	P3-Medium	<a href="#">LCPD-30664</a>	Linux SDK shall support GPU: Graphics drivers shall be published in instrumentation mode configuration for debug purposes	<a href="#">1/0/0/1</a>	
Graphics		P3-Medium	<a href="#">LCPD-30662</a>	Graphics: Composition with wayland	<a href="#">1/0/0/1</a>	
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-30645</a>	Linux Video Encoder support	<a href="#">8/0/0/8</a>	
Baseport	Baseport OSPI	P3-Medium	<a href="#">LCPD-33097</a>	Linux SDK shall support OSPI: Programmable delays between transactions	<a href="#">3/0/0/3</a>	
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33056</a>	Linux SDK shall support DISPC: Two video pipe with YUV and scaler support	<a href="#">8/0/2/81</a>	
		P3-		Linux SDK shall support		

Baseport	Baseport Display	Medium	<a href="#">LCPD-33065</a>	DISPC: DSS Gamma correction	<a href="#">0/1/0/1</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33062</a>	Linux SDK shall support DISPC: DSS cropping	<a href="#">0/1/0/1</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33068</a>	Linux SDK shall support DISPC: DSS Color Phase Rotation (CPR)	<a href="#">1/0/0/1</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33061</a>	Linux SDK shall support DISPC: Display driver shall support synchronization using DMABUF fence	<a href="#">LCPD-37725</a> <b>[Baseport]</b> <a href="#">0/1/0/1</a>
Baseport	Baseport SPI	P3-Medium	<a href="#">LCPD-33052</a>	Linux SDK shall support SPI: Programmable Clock phase and polarity	<a href="#">LCPD-37699</a> <b>[Baseport]</b> <a href="#">0/2/0/2</a>
Baseport	Baseport SPI	P3-Medium	<a href="#">LCPD-33051</a>	Linux SDK shall support SPI: 4 Chip Select options	<a href="#">LCPD-37699</a> <b>[Baseport]</b> <a href="#">0/2/0/2</a>
Baseport	Baseport I2C	P3-Medium	<a href="#">LCPD-33077</a>	Linux SDK shall support I2C: full 7-bit/10-bit address field	<a href="#">1/0/0/1</a>
Baseport	Baseport	P3-Medium	<a href="#">LCPD-33041</a>	SDK shall support CMA region with support for fencing and physical address passing	<a href="#">1/0/0/1</a>
Baseport	Baseport SPI	P3-Medium	<a href="#">LCPD-33053</a>	Linux SDK shall support SPI: 18 Mbps for a 40 MHz clock	<a href="#">LCPD-37699</a> <b>[Baseport]</b> <a href="#">0/2/0/2</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33063</a>	Linux SDK shall support DISPC: Support for configuring Color Space Conversion (CSC) from user space	<a href="#">1/0/0/1</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-32977</a>	Linux driver for DSS (display) - DSI support	<a href="#">0/0/1/1</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-37654</a>	Linux SDK shall support VPU Performance of 480 Mpix/s	<a href="#">1/0/0/1</a>
Graphics	Graphics	P3-Medium	<a href="#">LCPD-29986</a>	Linux SDK shall support GPU: Support for offline analysis of GPU API traces	<a href="#">1/0/0/1</a>
Graphics		P2-High	<a href="#">LCPD-28568</a>	Graphics driver shall support DRM based Null Window System	<a href="#">1/0/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-28561</a>	Graphics driver shall support YUYV texture format	<a href="#">1/0/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-28558</a>	Graphics driver shall support ARGB8888 texture format	<a href="#">1/0/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-28557</a>	Graphics driver shall support RGBA5551 texture format	<a href="#">0/1/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-28553</a>	Graphics driver shall support BGRA8888 surface format	<a href="#">1/0/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-28552</a>	Graphics driver shall support ARGB8888 surface format	<a href="#">1/0/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-28551</a>	Graphics driver shall support RGB565 surface format	<a href="#">1/0/0/1</a>
Baseport	Baseport OSPI	P3-Medium	<a href="#">LCPD-33093</a>	Linux SDK shall support OSPI: OSPI access via CPU	<a href="#">2/0/0/2</a>
Baseport	Baseport MMCSD	P3-Medium	<a href="#">LCPD-33092</a>	Linux SDK shall support MMCSD: ext3 file system shall be supported on MMC	<a href="#">2/0/0/2</a>
Baseport	Baseport MMCSD	P3-Medium	<a href="#">LCPD-33091</a>	Linux SDK shall support MMCSD: ext2 file system shall be supported on MMC	<a href="#">3/2/0/5</a>
Baseport	Baseport MMCSD	P3-Medium	<a href="#">LCPD-33088</a>	Linux SDK shall support MMCSD: DDR50	<a href="#">1/0/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-28559</a>	Graphics driver shall support BGRA8888 texture format	<a href="#">1/0/0/1</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-30161</a>	Linux SDK shall support VDEC: Progressive video decode	<a href="#">10/0/0/10</a>
Graphics	Display	P3-Medium	<a href="#">LCPD-34460</a>	Enable chromium based browser	<a href="#">1/0/0/1</a>
Multimedia	Multimedia	P3-	<a href="#">LCPD-30157</a>	Linux SDK shall support VDEC: FLUSH for bitstream	<a href="#">2/0/0/2</a>

		Medium		and picture buffers	
Graphics	Graphics	P3-Medium	<a href="#">LCPD-29981</a>	Linux SDK shall support GPU: Weston shall support USB mouse as HID	<a href="#">1/0/0/1</a>
Graphics	Graphics	P3-Medium	<a href="#">LCPD-29982</a>	Linux SDK shall support GPU: Weston shall support USB keyboard as HID	<a href="#">1/0/0/1</a>
Graphics	Audio	P2-High	<a href="#">LCPD-21255</a>	Graphics driver shall support OpenGL ES 3.2 API	NT
Graphics	Capture DRM Display Writeback	P2-High	<a href="#">LCPD-21245</a>	Graphics driver shall support OpenGL ES 1.x API	<a href="#">1/0/0/1</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35129</a>	Wave5 VPU should support 12 channel D1@30 fps enc + 12 channel D1@30 dec	<a href="#">0/0/1/1</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35131</a>	Linux SDK shall support VENC: Query APIs to get encoded buffer sizes and pixel formats.	<a href="#">2/0/0/2</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35134</a>	Linux SDK shall support VENC: Rate control $i_c^{1/2}$ Storage profile (VBR and CBR)	<a href="#">2/0/0/2</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35114</a>	Linux SDK shall support VDEC: Strided buffers for output and input buffers	<a href="#">1/0/0/2</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35115</a>	Linux SDK shall support VENC: Rate control $i_c^{1/2}$ Video Conferencing profile, 2- 10 Mbps (VBR and CBR)	<a href="#">2/0/0/2</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35112</a>	Linux VENC GST plugin along with V4L2 shall take < 5 MHz CPU per channel	<a href="#">1/0/0/1</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35113</a>	Linux SDK shall support VDEC: Performance profiler	<a href="#">4/0/0/4</a>
Baseport	Baseport SPI	P3-Medium	<a href="#">LCPD-33158</a>	Linux Support for SPI Slave	NT
Baseport	Baseport OSPI	P3-Medium	<a href="#">LCPD-33137</a>	Core SDK Linux shall include performance documentation for OSPI UBIFS and raw performance	<a href="#">3/0/0/5</a>
Baseport	Baseport Crypto Security	P3-Medium	<a href="#">LCPD-33130</a>	Support for OPTEE Replay Protected Memory Block (RPMB)	<a href="#">2/0/0/2</a>
Baseport	Baseport	P3-Medium	<a href="#">LCPD-33148</a>	Need stress-ng test included in AM6x Linux Performance Benchmarks	<a href="#">18/0/0/25</a>
NETWORKING	PCIe	P3-Medium	<a href="#">LCPD-36634</a>	PCIe Wifi card	<a href="#">LCPD-37516</a> <b>[System Test]</b> <a href="#">2/0/0/8</a>
Graphics		P3-Medium	<a href="#">LCPD-29975</a>	Linux SDK shall support GPU: Graphics driver shall support ARGB1555 texture format	<a href="#">1/0/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-29974</a>	Linux SDK shall support GPU: Graphics driver shall support UYVY texture format	<a href="#">1/0/0/1</a>
Graphics	Graphics	P3-Medium	<a href="#">LCPD-29984</a>	Linux SDK shall support GPU: Support for offline analysis of GPU profile data	<a href="#">1/0/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-28563</a>	Graphics driver shall support YUV420 planar texture format	<a href="#">1/0/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-28556</a>	Graphics driver shall support ARGB4444 texture format	<a href="#">1/0/0/1</a>
Connectivity	PCIe	P3-Medium	<a href="#">LCPD-36729</a>	Linux SDK shall support PCIe: Root Complex	<a href="#">LCPD-37516</a> <b>[System Test]</b> <a href="#">19/3/0/25</a>
Connectivity	PCIe	P3-Medium	<a href="#">LCPD-36726</a>	Linux SDK shall support PCIe: 1L Gen3 speed	<a href="#">LCPD-37516</a> <b>[System Test]</b> <a href="#">19/2/0/25</a>
Connectivity	PCIe	P3-Medium	<a href="#">LCPD-36725</a>	Linux SDK shall support PCIe: NVMe SSD support	<a href="#">LCPD-37516</a> <b>[System Test]</b> <a href="#">19/3/0/25</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36716</a>	Linux SDK shall support USB: USB host driver shall support selective suspend.	NT

Connectivity	PCIe	P3-Medium	<a href="#">LCPD-36720</a>	Linux SDK shall support PCIe: MSI-X with MSI-X interrupts	<a href="#">LCPD-37516</a> [System Test]	<a href="#">19/2/0/25</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36703</a>	Linux SDK shall support USB: USB host driver shall support CDC ECM class client devices.		<a href="#">0/2/0/2</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36699</a>	Linux SDK shall support USB: USB host driver shall support WiFi client devices.		<a href="#">1/0/0/1</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36700</a>	Linux SDK shall support USB: USB client gadget driver for composite device (CDC + MSC and CDC (ACM) + CDC (ECM)) shall be supported.		<a href="#">1/2/0/5</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36705</a>	Linux SDK shall support USB: USB: DEVICE: Support for composite gadget (Running NCM and ACM function simultaneously)		<a href="#">0/2/0/4</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36701</a>	Linux SDK shall support USB: USB host driver shall support Video class client devices.		<a href="#">2/0/0/2</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36702</a>	Linux SDK shall support USB: USB host driver shall support Audio class client devices.		<a href="#">15/3/0/18</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36707</a>	CPSW TCP performance		<a href="#">2/0/0/2</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36695</a>	Linux SDK shall support USB: USB: DEVICE: Support for NCM gadget driver		<a href="#">0/3/0/6</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36688</a>	Linux SDK shall support CPSW: PTP using CPSW CPTS for 1588 Time-stamping in Linux		NT
Connectivity	UBOOT USB	P3-Medium	<a href="#">LCPD-36697</a>	SPL/U-Boot USB MSC boot support		<a href="#">0/1/1/2</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36691</a>	Linux SDK shall support USB: USB host driver shall support enumeration of Full speed devices		<a href="#">1/0/0/1</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36690</a>	Linux SDK shall support USB: USB host driver shall support enumeration of Low speed devices		<a href="#">1/0/0/1</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36692</a>	Linux SDK shall support USB: USB host driver shall support enumeration of High speed devices		<a href="#">1/0/1/2</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36696</a>	Linux SDK shall support CPSW: IP/UDP/TCP checksum acceleration support in HW		<a href="#">1/0/0/1</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36689</a>	Linux SDK shall support CPSW: NFS support : support root filesystem over ethernet		<a href="#">1/0/0/1</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36694</a>	Linux SDK shall support USB: ZLP (Zero length packet) in USB gadget driver and test case to verify the zero length packet.		<a href="#">0/1/0/1</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36693</a>	Linux SDK shall support USB: USB host driver shall support High Speed, Full Speed, and Low-speed devices simultaneously (via Hubs)		<a href="#">1/0/0/1</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36686</a>	Linux SDK shall support CPSW: Dynamic Configuration of MAC and IP Address	<a href="#">LCPD-37584</a> [Connectivity]	<a href="#">0/2/0/2</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36687</a>	Linux SDK shall support CPSW: CPSW - interrupt pacing support		<a href="#">1/0/0/1</a>

Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36685</a>	Linux SDK shall support CPSW: Support packet size up to 1516 bytes	<a href="#">1/0/0/1</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36684</a>	Linux SDK shall support CPSW: Support per-port statistics	NT
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36682</a>	Linux SDK shall support CPSW: 1Gbps RGMII	<a href="#">1/0/0/1</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36679</a>	Linux SDK shall support USB: USB client driver shall support cable connect and disconnect feature. <b>[System Test]</b>	<a href="#">0/0/2/3</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36681</a>	Linux SDK shall support USB: USB host driver shall support HUB class client devices.	<a href="#">1/0/0/1</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36680</a>	Linux SDK shall support USB: USB host driver shall support Human interface client devices.	<a href="#">1/0/2/3</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36677</a>	Linux SDK shall support USB: USB Client driver shall support FS (12 Mbps)	<a href="#">1/0/0/1</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36678</a>	Linux SDK shall support USB: USB Client driver shall support LS (1.5 Mbps)	<a href="#">1/0/0/1</a>
Graphics	Graphics	P3-Medium	<a href="#">LCPD-29988</a>	Linux SDK shall support GPU: Support for profiling and analyzing GPU load at run time	<a href="#">1/0/0/1</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36675</a>	Linux SDK shall support USB: Host Mode	<a href="#">1/0/0/1</a>
Connectivity	USB	P3-Medium	<a href="#">LCPD-36676</a>	Linux SDK shall support USB: USB Client driver shall support HS(480 Mbps)	<a href="#">1/0/0/1</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36673</a>	Linux SDK shall support CPSW3G: 802.1AS-2020: Time Synchronization	NT
Connectivity	USB	P3-Medium	<a href="#">LCPD-36674</a>	Linux SDK shall support USB: Device Mode <b>[System Test]</b>	<a href="#">0/3/0/3</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36672</a>	Linux SDK shall support CPSW3G: 802.1Q VLANs	<a href="#">1/0/0/1</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36664</a>	Support CPSW 3G credit based shaper (802.1qav) with Linux in Switch and Endpoint mode	NT
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36671</a>	Linux SDK shall support CPSW3G: Scatter Gather	<a href="#">1/0/0/1</a>
Connectivity	UBOOT USB	P3-Medium	<a href="#">LCPD-36669</a>	SPL/U-Boot - USB slave boot from external host support (DFU mode)	<a href="#">0/1/0/1</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36670</a>	Linux time synchronization support with CPSW-3G	NT
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36668</a>	Support CPSW-3g EST (802.1qbv/Time Aware Shaper) with Linux for switch/endpoint	<a href="#">10/0/0/11</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36667</a>	Support CPSW 3G as a switch with Linux	<a href="#">1/0/0/4</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36666</a>	CPSW3G - Support in Linux for Dual Mac	<a href="#">1/0/0/1</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36663</a>	Support CPSW-3G IET (frame preemption) feature with Linux for switch/endpoint	<a href="#">1/0/0/5</a>
Connectivity	CPSW	P3-Medium	<a href="#">LCPD-36661</a>	CPSW3G - support for basic policer/rate limiter configuration, i.e., for broadcast/multicast storm prevention	<a href="#">0/0/0/1</a>
Connectivity	CPSW UBOOT	P3-	<a href="#">LCPD-36660</a>	U-Boot support for CPSW-3G	<a href="#">1/0/0/1</a>

		Medium			
Graphics		P3-Medium	<a href="#">LCPD-29987</a>	Linux SDK shall support GPU: Graphics driver shall support exporting allocated memory as DMABUF	NT
Graphics		P3-Medium	<a href="#">LCPD-29985</a>	Linux SDK shall support GPU: Graphics driver shall support usage of DMABUF as render surface	<a href="#">1/0/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-29990</a>	Linux SDK shall support GPU: Support for GPU performance benchmarking with glmark2	<a href="#">2/0/0/2</a>
Graphics		P3-Medium	<a href="#">LCPD-29976</a>	Linux SDK shall support GPU: Graphics driver shall support NV12 texture format	<a href="#">1/0/0/1</a>
Graphics	Graphics	P3-Medium	<a href="#">LCPD-29989</a>	Linux SDK shall support GPU: Support for collecting Graphics API traces	<a href="#">0/1/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-29992</a>	Linux SDK shall support GPU: GPU shall support Active Power Management controlled by the GPU firmware	<a href="#">LCPD-37417</a> <b>[Graphics]</b> <a href="#">0/1/0/1</a>
Graphics		P3-Medium	<a href="#">LCPD-29979</a>	Linux SDK shall support Linux: Report out with explanantion benchmark numbers on Glmark2	<a href="#">2/0/0/2</a>
Baseport	Baseport Crypto Security	P3-Medium	<a href="#">LCPD-33131</a>	OP-TEE secure storage through DKEK	<a href="#">1/0/0/1</a>
Baseport	Baseport IPC	P3-Medium	<a href="#">LCPD-33181</a>	Linux IPC support for 2x C7x	<a href="#">3/0/0/4</a>
Baseport	Baseport IPC	P3-Medium	<a href="#">LCPD-33182</a>	Linux to Main R5 IPC: simple shared memory example	<a href="#">1/0/0/1</a>
Baseport		P3-Medium	<a href="#">LCPD-33184</a>	Linux multiple CSI Instance Support	<a href="#">LCPD-37044</a> <b>[System Test]</b> <a href="#">1/0/0/1</a>
Baseport	Baseport IPC	P3-Medium	<a href="#">LCPD-33180</a>	Linux IPC support for Main R5	<a href="#">LCPD-37333</a> <b>[System Test]</b> <a href="#">1/0/0/2</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-30151</a>	Linux SDK shall support VDEC: Multi threaded implementation	<a href="#">2/0/0/3</a>
Baseport	Baseport IPC	P3-Medium	<a href="#">LCPD-33183</a>	Linux to 2x C7x IPC: simple shared memory example	<a href="#">4/0/0/4</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-30150</a>	Linux SDK shall support VDEC: Deblocking filter	<a href="#">1/0/0/1</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-30152</a>	H.264 and H.265 shall be Universal Decoders	<a href="#">2/0/0/2</a>
Baseport	Baseport Crypto Security	P3-Medium	<a href="#">LCPD-33174</a>	Support for booting HS-FS devices	<a href="#">3/0/0/3</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-30159</a>	Linux SDK shall support VDEC: Publish Codec Capabilities through API	<a href="#">2/0/0/2</a>
Baseport	Baseport IPC	P3-Medium	<a href="#">LCPD-33173</a>	Linux IPC: remoteproc load and IPC communication with MCU R5	<a href="#">1/0/0/1</a>
Baseport	Baseport IPC	P3-Medium	<a href="#">LCPD-33172</a>	Linux IPC support for DM R5	<a href="#">3/0/0/3</a>
Baseport	Baseport MMCSd	P3-Medium	<a href="#">LCPD-33147</a>	Linux SDK shall support eMMC: Report out with explanation benchmark numbers on eMMC	<a href="#">1/0/0/1</a>
Baseport	Baseport Crypto Security	P3-Medium	<a href="#">LCPD-33144</a>	Core SDK Linux shall include performance documentation for Crypto Driver performance	<a href="#">LCPD-37705</a> <b>[Baseport]</b> <a href="#">0/1/0/1</a>
Baseport	Baseport OSPI	P3-Medium	<a href="#">LCPD-33145</a>	Linux SDK shall support OSPI: Report out with explanation benchmark numbers on OSPI	<a href="#">1/0/0/1</a>
				Linux SDK shall support	

Baseport	Baseport MMCS	P3-Medium	<a href="#">LCPD-33146</a>	MMCS: Report out with explanantion benchmark numbers on MMCS	<a href="#">2/1/0/3</a>
Baseport	Baseport MMCS	P3-Medium	<a href="#">LCPD-33143</a>	Core SDK Linux shall include performance documentation for eMMC EXT4 Read/Write and Raw performance	<a href="#">1/0/0/1</a>
Baseport	Baseport Crypto Security	P3-Medium	<a href="#">LCPD-33140</a>	Linux SDK shall support SA2UL: Context cache module to auto fetch security context	<a href="#">3/1/0/4</a>
Baseport	Baseport MMCS	P3-Medium	<a href="#">LCPD-33142</a>	Core SDK Linux shall include performance documentation for MMCS EXT4 and RAW read/Write performance	<a href="#">1/0/0/1</a>
Baseport	Baseport MMCS	P3-Medium	<a href="#">LCPD-33139</a>	Linux SDK shall support Linux: Report out with explanantion benchmark numbers on MMCS Boot time	<a href="#">1/0/0/1</a>
Baseport	Baseport Crypto Security	P3-Medium	<a href="#">LCPD-33133</a>	Linux SDK shall support SA2UL: Context cache module to auto fetch security context	<a href="#">3/1/0/4</a>
Graphics	Graphics	P3-Medium	<a href="#">LCPD-29983</a>	Linux SDK shall support GPU: Support for collecting GPU profile information in a file	<a href="#">1/0/0/1</a>
Graphics	Graphics	P3-Medium	<a href="#">LCPD-29991</a>	Linux SDK shall support GPU: Support for collecting GPU profile information through an API	<a href="#">1/0/0/1</a>
Baseport	Baseport OSPI	P3-Medium	<a href="#">LCPD-33112</a>	Linux Driver for OSPI NOR	<a href="#">3/0/0/4</a>
Baseport	Baseport Crypto OP-TEE Security	P3-Medium	<a href="#">LCPD-33114</a>	Support for ARM TrustZone (OP-TEE)	<a href="#">2/0/0/2</a>
Baseport	Baseport	P3-Medium	<a href="#">LCPD-33104</a>	Linux support for DMTIMER	<a href="#">1/0/0/1</a>
Baseport	Baseport IPC	P3-Medium	<a href="#">LCPD-33101</a>	Support a user space library for exercising rpmsg-char driver	<a href="#">3/0/0/4</a>
Baseport	Baseport IPC	P3-Medium	<a href="#">LCPD-33100</a>	Linux IPC should co-exist with RTOS to RTOS IPC	<a href="#">6/0/0/8</a>
Baseport	ATF Baseport	P3-Medium	<a href="#">LCPD-33072</a>	Linux SDK shall support ATF: ATF: Support PSCI call for system reset	<a href="#">2/0/0/2</a>
Baseport	Baseport I2C	P3-Medium	<a href="#">LCPD-33079</a>	Linux SDK shall support I2C: I2C driver shall be capable of handling bus timeouts.	<a href="#">1/0/0/1</a>
Baseport	ATF Baseport	P3-Medium	<a href="#">LCPD-33074</a>	Linux SDK shall support ATF: ATF: Support starting BL33 non-secure boot-stage in EL2	<a href="#">1/0/0/1</a>
Baseport	ATF Baseport	P3-Medium	<a href="#">LCPD-33073</a>	Linux SDK shall support ATF: ATF: Support PSCI calls for CPU online / offline	<a href="#">1/0/0/1</a>
Baseport	Baseport UART	P3-Medium	<a href="#">LCPD-33080</a>	Linux SDK shall support UART: Baud-rate from 300 bits/s up to 3.6864 Mbits/s if 48MHz functional clock is used	<a href="#">9/0/0/9</a>
Baseport	Baseport OSPI	P3-Medium	<a href="#">LCPD-33095</a>	Linux SDK shall support OSPI: DTR Protocol (including Octal DDR protocol with DQS for OCTAL SPI Devices)	<a href="#">2/0/0/2</a>
Baseport	Baseport UART	P3-Medium	<a href="#">LCPD-33081</a>	Linux SDK shall support UART: UART driver shall support settings of 115200 baud rate, 8 bit data, 1 stop bit, no parity, no flow control and no DMA usage for Debug Console functionality	<a href="#">1/0/0/1</a>
Baseport	Baseport I2C	P3-Medium	<a href="#">LCPD-33076</a>	Linux SDK shall support I2C: Fast (up to 400 KHz)	<a href="#">0/1/0/1</a>

[LCPD-37704](#)  
[Baseport]

Baseport	ATF Baseport	P3-Medium	<a href="#">LCPD-33075</a>	Linux SDK shall support ATF: ATF: Support starting secure runtime BL32 in TrustZone-EL1		<a href="#">1/0/0/1</a>
Baseport	Baseport I2C	P3-Medium	<a href="#">LCPD-33078</a>	Linux SDK shall support I2C: I2C driver shall support interrupt mode for data transfers.		<a href="#">3/0/0/5</a>
Baseport	Baseport SDIO	P3-Medium	<a href="#">LCPD-33084</a>	Linux SDK shall support MMCS: SDIO: SDR12		<a href="#">1/0/0/1</a>
Baseport	ATF Baseport	P3-Medium	<a href="#">LCPD-33071</a>	Linux SDK shall support ATF: ATF: Support initial GIC programming and interrupt group partitioning		<a href="#">1/0/0/1</a>
Baseport	Baseport OSPI	P3-Medium	<a href="#">LCPD-33094</a>	Linux SDK shall support OSPI: OSPI access via DMA		<a href="#">2/0/0/2</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33060</a>	Linux SDK shall support DISPC: RGB32		<a href="#">4/0/0/8</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33064</a>	Linux SDK shall support DISPC: DRM: Allow using unused primary planes as overlay planes		<a href="#">1/0/0/1</a>
Baseport	Baseport SPI	P3-Medium	<a href="#">LCPD-33059</a>	Linux SDK shall support SPI: Full and Half duplex	<a href="#">LCPD-37699</a> [Baseport]	<a href="#">0/2/0/2</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33069</a>	Linux SDK shall support DISPC: DRM: Support DMAbuf	<a href="#">LCPD-37725</a> [Baseport]	<a href="#">0/1/0/1</a>
Baseport	Baseport SPI	P3-Medium	<a href="#">LCPD-33070</a>	Linux SDK shall support SPI: DMA mode of operation	<a href="#">LCPD-37699</a> [Baseport]	<a href="#">0/2/0/2</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33066</a>	Linux SDK shall support DISPC: DSS global alpha blending		<a href="#">1/0/0/1</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33067</a>	Linux SDK shall support DISPC: DSS per pixel alpha blending (pre & non-pre-multiplied)		<a href="#">1/0/0/1</a>
Baseport	Baseport SPI	P3-Medium	<a href="#">LCPD-33054</a>	Linux SDK shall support SPI: FIFO mode of operation	<a href="#">LCPD-37699</a> [Baseport] <a href="#">LCPD-37044</a>	<a href="#">0/2/0/2</a>
Baseport	Baseport CSI	P3-Medium	<a href="#">LCPD-33055</a>	Linux SDK shall support CSI-Rx: YUV420, YUV422, RGB, Raw	<a href="#">LCPD-37333</a> [System Test]	<a href="#">1/0/0/1</a>
Baseport	Baseport SPI	P3-Medium	<a href="#">LCPD-33057</a>	Linux SDK shall support SPI: Interrupt mode	<a href="#">LCPD-37699</a> [Baseport]	<a href="#">0/2/0/2</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33058</a>	Linux SDK shall support DISPC: Two graphics pipe (a.k.a. VidLite) with YUV support (no scaler)		<a href="#">3/0/1/5</a>
Baseport	ATF Baseport	P3-Medium	<a href="#">LCPD-33043</a>	ARM trusted firmware support		<a href="#">1/0/0/1</a>
Baseport	Baseport	P3-Medium	<a href="#">LCPD-33045</a>	Performance profiling - enable HW performance counter using PMU		<a href="#">1/0/0/2</a>
Baseport	Baseport SPI	P3-Medium	<a href="#">LCPD-33047</a>	Linux SDK shall support SPI: up to 50 MHz operation	<a href="#">LCPD-37699</a> [Baseport]	<a href="#">0/2/0/2</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33048</a>	Linux shall support DSS with XRGB888 (XR24) data		<a href="#">1/0/1/2</a>
Baseport	Baseport	P3-Medium	<a href="#">LCPD-33044</a>	Linux baseport support - Quad A53		<a href="#">1/0/0/1</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-33050</a>	Linux shall support DSS with YUV NV12 input format		<a href="#">1/0/0/1</a>
Baseport	Baseport CSI	P3-Medium	<a href="#">LCPD-33036</a>	Linux support for CSI2 - color format support	<a href="#">LCPD-37044</a> [System Test]	<a href="#">1/0/0/1</a>
Baseport	SDK	P3-Medium	<a href="#">LCPD-33038</a>	libDRM support	<a href="#">LCPD-37333</a> [System Test]	<a href="#">1/0/0/1</a>



Baseport	Baseport Power_Management	P3-Medium	<a href="#">LCPD-33033</a>	Clock and reset support for peripheral and cores in Linux	<a href="#">2/0/0/2</a>
Baseport	Baseport	P3-Medium	<a href="#">LCPD-33042</a>	Support for optimized ROM boot flow in SPL which allows combined loading of bootloader and TIFS by ROM	<a href="#">3/0/0/3</a>
Baseport	Baseport Crypto Security	P3-Medium	<a href="#">LCPD-33040</a>	Crypto acceleration performance requirements	<a href="#">1/0/0/2</a>
Baseport	Baseport IPC	P3-Medium	<a href="#">LCPD-33037</a>	Linux driver for HW Spinlock	<a href="#">1/0/0/1</a>
Baseport	Baseport VTM	P3-Medium	<a href="#">LCPD-33034</a>	Support read of On-die temperature sensor in Linux	<a href="#">1/0/0/1</a>
Baseport	Baseport CSI	P3-Medium	<a href="#">LCPD-33035</a>	Linux support for CSI2 virtual channel/multiple camera support	<a href="#">LCPD-37044</a> [System Test] <a href="#">1/0/0/1</a> <a href="#">LCPD-37333</a> [System Test]
Baseport	Baseport	P3-Medium	<a href="#">LCPD-33039</a>	Core SDK Linux shall support K3conf	<a href="#">1/0/0/1</a>
Baseport	Baseport Crypto Security	P3-Medium	<a href="#">LCPD-33027</a>	Linux Driver support for SHA - HS-FS device	<a href="#">2/0/0/2</a>
Baseport	Baseport Crypto Security	P3-Medium	<a href="#">LCPD-33030</a>	Linux driver for AES - HS-FS device	<a href="#">1/0/0/2</a>
Baseport	Baseport Crypto Security	P3-Medium	<a href="#">LCPD-33031</a>	Support for Crypto accelerated IPSec - HS-FS device	<a href="#">LCPD-37702</a> [Baseport] <a href="#">0/2/0/2</a>
Baseport	Baseport IPC	P3-Medium	<a href="#">LCPD-33026</a>	Linux to DM R5 IPC: simple shared memory example	<a href="#">2/0/0/2</a>
Baseport	Baseport IPC	P3-Medium	<a href="#">LCPD-33025</a>	Linux to MCU R5 IPC: simple shared memory example	<a href="#">2/0/0/2</a>
Baseport	Baseport IPC	P3-Medium	<a href="#">LCPD-33024</a>	U-Boot support for remoteproc booting of MCU R5F	<a href="#">1/0/0/1</a>
Baseport	Baseport I2C	P3-Medium	<a href="#">LCPD-33023</a>	U-Boot: I2C master support	<a href="#">LCPD-37704</a> [Baseport] <a href="#">0/1/0/1</a>
Baseport	Baseport GPIO UBOOT	P3-Medium	<a href="#">LCPD-33022</a>	U-Boot: GPIO support	<a href="#">1/0/0/1</a>
Baseport	SDK	P3-Medium	<a href="#">LCPD-33108</a>	Linux Benchmark support	<a href="#">6/1/0/7</a>
Baseport	Baseport CSI	P3-Medium	<a href="#">LCPD-33007</a>	Linux driver support for FPDLinkv3	<a href="#">LCPD-37044</a> [System Test] <a href="#">1/0/0/1</a> <a href="#">LCPD-37333</a> [System Test]
Baseport	Baseport RTC	P3-Medium	<a href="#">LCPD-33003</a>	Linux driver for RTC	<a href="#">LCPD-37706</a> [Baseport] <a href="#">0/10/0/10</a>
Baseport	Baseport	P3-Medium	<a href="#">LCPD-33004</a>	Linux must support LPDDR4 speed of 4000 MT/s	<a href="#">1/0/0/1</a>
Baseport	Baseport CSI	P3-Medium	<a href="#">LCPD-33001</a>	Linux support for CSI	<a href="#">LCPD-37044</a> [System Test] <a href="#">1/0/0/1</a> <a href="#">LCPD-37333</a> [System Test]
Baseport	Baseport	P3-Medium	<a href="#">LCPD-33000</a>	Linux Driver for DMSS	<a href="#">1/0/0/1</a>
Baseport	Baseport	P3-Medium	<a href="#">LCPD-32995</a>	Linux Driver for mailbox	<a href="#">2/0/0/4</a>
Baseport	Baseport Crypto Security	P3-Medium	<a href="#">LCPD-32993</a>	Support OpenSSL with Linux - HS-FS devices	<a href="#">LCPD-37705</a> [Baseport] <a href="#">1/1/0/2</a>
Baseport	Baseport I2C	P3-Medium	<a href="#">LCPD-32987</a>	Linux Driver for I2C master	<a href="#">3/0/0/6</a>
Baseport	Baseport MMCSD	P3-Medium	<a href="#">LCPD-32989</a>	SPL/U-Boot eMMC flash boot mode support	<a href="#">1/0/0/1</a>
Baseport	Baseport MMCSD	P3-Medium	<a href="#">LCPD-32984</a>	Linux Driver for eMMC and SD card	<a href="#">2/0/0/2</a>
Baseport	Baseport Crypto Security	P3-Medium	<a href="#">LCPD-32982</a>	Linux Driver for secure proxy	NT
Baseport	Baseport SPI	P3-Medium	<a href="#">LCPD-32997</a>	Linux Driver for SPI - master mode	<a href="#">LCPD-37699</a> <a href="#">0/2/0/2</a>

Baseport	Baseport UART	P3-Medium	<a href="#">LCPD-32999</a>	SPL/U-Boot: UART boot mode support	<a href="#">1/0/0/1</a>
Baseport	Baseport MMCSd	P3-Medium	<a href="#">LCPD-32994</a>	SPL/U-Boot: MMCSd with SD card boot mode support	<a href="#">1/0/0/2</a>
Baseport	Baseport UART	P3-Medium	<a href="#">LCPD-32991</a>	Linux Driver for UART	<a href="#">2/0/0/2</a>
Baseport	Baseport	P3-Medium	<a href="#">LCPD-32986</a>	Support standard Linux boot flow using SPL and U-Boot	<a href="#">3/0/0/3</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-32976</a>	Linux driver for DSS (display) - DPI support	<a href="#">9/0/2/85</a>
Baseport	Baseport GPIO	P3-Medium	<a href="#">LCPD-32973</a>	Linux Driver for GPIO	<a href="#">7/0/0/7</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-32978</a>	Linux driver for DSS (display) - OLDI Dual Link	<a href="#">0/0/2/2</a>
Baseport	Baseport Display	P3-Medium	<a href="#">LCPD-32979</a>	Linux driver for DSS (display) - OLDI Single Link	<a href="#">0/0/1/1</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-30153</a>	Linux SDK shall support VDEC: I, P, and B frame decoding	<a href="#">1/0/0/2</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-30156</a>	Linux SDK shall support VDEC: Decoding Stream headers	<a href="#">2/0/0/2</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35117</a>	v4l2h264dec shall support DMA Buffers	<a href="#">3/0/0/3</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35119</a>	Linux SDK shall support VENC: Strided buffers for output and input buffers	<a href="#">1/0/0/1</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35121</a>	Linux VENC V4L2 driver shall optimize DDR memory usage per channel for internal buffers	<a href="#">0/0/1/1</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35122</a>	Linux SDK shall support VDEC: Constant and Variable Bit Rates	<a href="#">2/0/0/2</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35137</a>	Linux VDEC driver shall support no frame copy (CPU or DMA) for exchanging application to codec buffer	<a href="#">3/0/0/3</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35139</a>	Linux SDK shall support VDEC: API to query number of reference frames for bitstream	<a href="#">2/0/0/3</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35125</a>	Linux VENC V4L2 driver shall optimize DDR memory usage per channel for IO buffers	<a href="#">0/0/1/1</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35126</a>	Linux SDK shall support VENC: Configurable GOP - I, P, B support	<a href="#">2/0/0/2</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35128</a>	Linux SDK shall support VDEC: Multi-stream capable	<a href="#">2/0/0/2</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-35135</a>	Linux SDK shall support VDEC: Free resources on error recovery	<a href="#">4/0/0/4</a>
Multimedia	Multimedia	P3-Medium	<a href="#">LCPD-30158</a>	Linux SDK shall support VDEC: Send EOS notification	<a href="#">3/0/0/4</a>