Getting Started with IPC

A. Installing IPC

Requirements:

- DSP/BIOS 6.30
- XDCTools 3.20
- Unzip the IPC zip package to some location on Linux or Windows box. We recommend using the same root directory where XDCtools and BIOS are installed (e.g. C:\Program Files\Texas Instruments)

```
% unzip ipc_1_21_xx_xx.zip
```

<u>NOTE</u>: Steps 2-5 are needed if and only if it is necessary to build IPC applications outside CCSv4

2. Add the IPC package path (i.e. <ipc_install>/ipc_1_21_xx_xx/packages)into XDCPATH. XDCTools and BIOS6 must also be present in the XDCPATH. The following template may be used to configure XDCPATH:

```
C:/Program Files/Texas
Instruments/CCSv4/DebugServer/packages;C:/Program Files/Texas
Instruments/<IPC_PRODUCT>/packages;C:/Program Files/Texas
Instruments/<BIOS_PRODUCT>/packages;C:/Program Files/Texas
Instruments/<XDCTOOLS_PRODUCT>/packages;
```

3. Copy and rename the supplied default config.bld as specified below

%cp <ipc_install>/ipc_1_21_xx_xx/etc/config.bld.default
<ipc_install>/ipc_1_21_xx_xx/packages/config.bld

- 4. Set/modify the codegen tool paths if necessary (the defaults usually work).
- 5. Set the rootDir in config.bld to point to your codegen tools.
- 6. Uncomment the Build.targets as needed.

<u>NOTE</u>: The config.bld.default supplied with IPC file is very similar to the one supplied with BIOS 6

B. Building IPC examples outside CCSv4

Building Single-core Examples

The single core examples can be built in two different ways. The first is via the "xdc" command. This approach uses the config.bld described in the above section.

- 1. Change directory to the desired directory
 % cd <ipc_install>/ipc_1_21_xx_xx/packages/ti/sdo/utils/examples
- 2. Run the "xdc" command % xdc

The other way is via the CCS templates. Refer to Section C (Building IPC examples within CCSv4) for more details.

The following are the examples:

ti\sdo\utils\examples

- <u>list</u>: example of the ti.sdo.utils.List module
- <u>multiProc</u>: example of the ti.sdo.utils.MultiProc module
- <u>multiProcSingleImage</u>: example of the MultiProc module when a single needs to be loaded on multiple cores.

ti\sdo\io\examples

• <u>stream</u>: example of the ti.sdo.io.Stream module

ti\sdo\ipc\examples\singlecore

- <u>notify loopback</u>: Example of the ti.sdo.ipc.Notify module using loopback functionality (which allows it to run on a single core).
- <u>message</u>: Example of the ti.sdo.ipc.MessageQ module on a single core.

Building Multi-core Examples

The multi-core examples can be built in two different ways. The first is via the "xdc" command. This approach uses the config.bld described in the above section.

- 1. Change directory to the desired directory
 % cd <ipc_install>/ipc_1_21_xx_xx
 /packages/ti/sdo/ipc/examples/multicore/c6474
- 2. Run the "xdc" command

% xdc

<u>NOTE</u>: Another way to build examples is by using the CCS templates. Refer to Section C (Building IPC examples within CCSv4) for more information.

Each platform example directory contains a multiprocessor Notify and MessageQ example. Refer to the readme.txt files in the respective directories for more details.

Rebuilding IPC modules

The individual modules can be rebuilt also via the "xdc" command. For example, to rebuild the utils modules:

- 1. Change directory to the desired directory (e.g. utils)
 % cd <ipc_install>/ipc_1_21_xx_xx/packages/ti/sdo/utils
- 2. Run the "xdc" command % xdc

C. Building IPC examples within CCSv4

1. Untar the IPC product into C:\Program Files\Texas Instruments\

You should see C:\Program Files\Texas Instruments\ipc_1_21_xx_xx_eng\packages after it has untarred

- 2. Launch CCS and ensure that the new IPC product is in your RTSC path:
 - You may automatically be prompted to use the new product. If you are, select 'yes'
 - Click on 'Window' \rightarrow 'Preferences'
 - Navigate to $CCS \rightarrow RTSC$
 - Ensure that a BIOS 6.30 release is selected in the "Products and Repositories" list
 - Ensure that an XDCtools 3.20 release is selected in the "XDCtools version" dropdown list
 - Ensure that your IPC product that was untarred in Step 1 is selected in "Products and Repositories"
 - Add/select any other repositories you might need for your application in this configuration panel
- 3. Restart CCS if any changes were made in Step 2
- Now you should be able to create a new CCSv4 project. Click on File→New Project

💱 New CCS Proje	ect			×
CCS Project				-6
Create a new CCS Pr	roject.			
Project name: myl	Project			
Use <u>d</u> efault loc	ation			
Location: E:/Doc	uments/workspac	e/myProject		Browse
0	< <u>B</u> ack	<u>N</u> ext >	<u>F</u> inish	Cancel

- Select the class of processors that you need to build for `i.e. c6000'
 Select device-specific attributes as shown below.

💱 New CCS Project				×
Project Settings Select the project settings.				G
Output type: Executable				•
Project settings Device Variant:	<select filter=""></select>	Generic C64x+ Device		More
Device Endianness:	little		•	
Code Generation tools:	TI v7.0.2		•	<u>M</u> ore
Output Format:	legacy COFF		Ŧ	
Linker Command File:			•	Browse
Runtime Support Library:	<automatic></automatic>		•	B <u>r</u> owse
0	< <u>B</u> ack	<u>N</u> ext > <u>F</u> inisł	n _ [Cancel

7. Select an IPC example for your device from the 'Projects Templates' wizard pane.

Select one of the available project templates.		×
Multiproc Multiproc Multiproc Single Image Multiproc Single Image Multipro	*	The notify_single example shows how to use Notify on EVM6474. It is a single-image example that must be loaded on all cores. Refer to the readme txt for more details.
0		< <u>B</u> ack <u>N</u> ext > <u>F</u> inish Cancel

8. Ensure that the correct repositories are selected and then select the IPC platform corresponding to your device/core. I.e. for c6474:

💱 New CCS Project				×
RTSC Configuration Settings Select the RTSC Configuration project setting	8.			F
XDCtools version: 3.20.00.39.eng ■ Products and Repositories 00 Orde	r]		2	. <u>M</u> ore
 Inter-processor Communication Inter-proce	n			Add Edit Remove Select All Deselect All
RTSC Target: [ti.targets.C64P RTSC Platform: [ti.sdo.ipc.examples.platf RTSC Build-Profile: whole_program	orms.evm6474.c	ore0		×
0	< <u>B</u> ack	<u>N</u> ext >	<u> </u>	Cancel

9. Click 'Finish' and you will see your newly created project in the 'C/C++ Projects' pane.

10. You can build your project immediately by right clicking `myProject' and clicking `Build'



11. You are done!