



- eXpressDSP Digital Media (XDM) interface complaint
- Optimized in Linear ASM, Scheduled ASM and C implementation with Intrinsic
- Bit-exact with OPUS open source standard version 1.1
- Based on OPUS coding algorithm for speech/music signals sampled at either 48 kHz, 24 kHz, 16 kHz, 12 kHz or 8 kHz.
- Operates on variable frame sizes of 2.5ms, 5ms, 10ms, 20ms, 40ms, and 60ms.
- Supports bit rates ranging from 6 kbps to 510 kbps.
- Supports mono and stereo (2) channels at Encoder input and as well at Decoder output.
- Supports Forward Error Correction (FEC) at the Encoder.
- Supports run-time data buffers relocation and table relocation
- Supports Big Endian and Little Endian modes of operation
- Run-time control of the complexity level. Supported values from 0 to 10.
- Run-time control of DTX.
- Supports RTP payload format specified in by the reference C code (RFC 6716)
- Supports packet loss concealment as specified by the reference C code
- Validated on C6678 EVM using Code Composer Studio version 5.2 with the code generation tools version 7.3.2
- This codec can be used on any of TI's C66x based platforms such as C6678, C6678L and their derivatives

#### description

OPUS Codec was developed as an open source standard in the year 2012 and standardized by the Internet Engineering Task Force (IETF) as RFC 6716. The codec operates on variable frame lengths (2.5ms, 5ms, 10ms, 20ms, 40ms, and 60ms) of 16-bit PCM speech/audio signals sampled at 8 KHz, 12 KHz, 16 KHz, 24 KHz, or 48 KHz and generates a compressed bit stream having bit rates in the range of 6 kbps to 510 kbps respectively.

OPUS Codec has incorporated the compression techniques from Skype's SILK Codec and Xiph.org's CELT Codec. The Encoder provides an option of selecting the complexity, which varies from 0 – 10. The Codec supports both Constant Bit Rate (CBR) and Variable Bit Rate (VBR) and also a constrained VBR mode. The Codec supports mono and stereo (2) channels at encoder input and as well as at the decoder output. The encoder supports Forward Error Correction (FEC), which



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

All trademarks are the property of their respective owners.

PRODUCT PREVIEW information concerns products in the formative or design phase of development. Characteristic data and other specifications are design goals. Texas Instruments reserves the right to change or discontinue these products without notice.



Copyright © 2013, Texas Instruments Incorporated

## OPUS Encoder/Decoder (v01.00.03) on C66x



SPRS908 - APRIL 2015

---

increases the robustness against the packet losses. The Decoder supports Packet Loss Concealment (PLC).

OPUS Codec is designed for interactive speech and music transmission over Internet and also intended for storage and streaming applications. OPUS Codec can be used in a wide range applications such as Voice over IP (VoIP), Video Conferencing, In-Game chat, and remote live music performances. OPUS Codec has been integrated into WebRTC chrome as well

**PRODUCT PREVIEW**



**summary of performance**

This section describes the performance of OPUS Encoder/Decoder on C6678L EVM.

**Table 1. Configuration Table**

CONFIGURATION	ID
Encoder – Big Endian	OPUS_001
Encoder – Little Endian	OPUS_002
Decoder – Big Endian	OPUS_003
Decoder – Little Endian	OPUS_004
Full Duplex – Big Endian	OPUS_005
Full Duplex – Little Endian	OPUS_006

**Table 2. Cycles Information – Profiled on C6678L EVM with Code Generation Tools version 7.3.2**

CONFIGURATION ID	PERFORMANCE STATISTICS (IN MEGACYCLES / SEC) <sup>1,2,3</sup>									
	AVERAGE					PEAK				
	NB	MB	WB	SWB	FB	NB	MB	WB	SWB	FB
OPUS_001	8.15	10.44	12.73	9.71	15.62	9.80	12.46	14.52	10.63	22.51
OPUS_002	8.15	10.45	12.72	9.71	15.56	9.81	12.46	14.50	10.63	22.42
OPUS_003	1.38	1.75	2.33	7.59	16.97	1.70	2.79	2.82	8.99	17.91
OPUS_004	1.38	1.75	2.32	7.56	16.83	1.70	2.79	2.82	8.91	17.78
OPUS_005	9.53	12.19	15.06	17.3	32.59	11.5	15.25	17.34	19.62	40.42
OPUS_006	9.53	12.2	15.04	17.27	32.39	11.51	15.25	17.32	19.54	40.2

<sup>1</sup> Measured with program and data memory, stack, and I/O buffers in internal Memory (L2 SRAM) and L1P and L1D caches are thrashed at frame boundaries.

<sup>2</sup> Average and peak MCPS measurements can vary by +/-5%.

<sup>3</sup> Measured with frame size = 20ms, Complexity=3, VBR Enabled, FEC disabled

**Table 3. Memory Statistics – Generated with Code Generation Tools Version 7.3.2**

CONFIGURATION	MEMORY STATISTICS <sup>5</sup>				
	PROGRAM MEMORY	DATA MEMORY			TOTAL
		INTERNAL	EXTERNAL	STACK	
OPUS_001	250.88	94.41	0	3	348.29
OPUS_002	250.81	94.41	0	3	348.22





OPUS_003	122.72	69.14	0	2.25	194.11
OPUS_004	123	69.14	0	2.25	194.39
OPUS_005	308.28	120.45	0	3	431.73
OPUS_006	308.28	120.45	0	3	431.73

<sup>5</sup> All memory requirements are expressed in kilobytes (1-kilobyte = 1024 bytes).

**Table 4. Internal Data Memory Split-up**

CONFIGURATION	DATA MEMORY – EXTERNAL <sup>6</sup>		
	SHARED		INSTANCE <sup>7</sup>
	CONSTANTS	SCRATCH	
OPUS_001	18.07	43.41	32.93
OPUS_002	18.07	43.41	32.93
OPUS_003	18.07	25.03	26.04
OPUS_004	18.07	25.03	26.04
OPUS_005	18.07	43.41	58.97
OPUS_006	18.07	43.41	58.97

<sup>6</sup> All memory requirements are expressed in kilobytes (1-kilobyte = 1024 bytes).

<sup>7</sup> Does not include I/O buffers.



## notes

- I/O Buffers:
  - Encoder (Decoder) Input buffer size = 11520 (3825) bytes
  - Encoder (Decoder) Output buffer size = 3825 (11520) bytes
- Total Data Memory for N *Non-Pre-Emptive* Instances =  
Constants + Runtime Tables + Scratch + N\*(Instance + I/O buffers + Stack)
- Total Data Memory for N *Pre-Emptive* Instances =  
Constants + Runtime Tables + N\*(Instance + I/O buffers + Stack + Scratch)

## references

- [opus\\_api -1.1.pdf](#) – OPUS 1.1
- [opusfile\\_api-0.2.pdf](#) – OPUS file 0.2
- [rfc6716.pdf](#) – Definition of the OPUS Audio Codec
- [www.opus\\_codec.org](#)
- *OPUS Encoder/Decoder on C66x User's Guide*

## glossary

Constants	Elements that go into .const memory section
Scratch	Memory space that can be reused across different instances of the algorithm
Shared	Sum of Constants and Scratch
Instance	Persistent-memory that contains persistent information - allocated for each instance of the algorithm

## acronyms

EVM	Evaluation Module
DTMF	Dual Tone Multi Frequency
DTX	Discontinuous Transmission
Kbps	Kilo bits per second
RTP	Real Time Protocol
XDM	eXpress DSP Digital Media
PLC	Packet Loss Concealment
VAD	Voice Activity Detection
FEC	Forward Error Correction
IETF	Internet Engineering Task Force
RFC	Request For Comments
VBR	Variable Bit Rate

## OPUS Encoder/Decoder (v01.00.03) on C66x



SPRS908 - APRIL 2015

---

CBR	Constant Bit Rate
NB	Narrow Band
MB	Medium Band
WB	Wide Band
SWB	Super Wide Band
FB	Full Band

**PRODUCT PREVIEW**

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as “components”) are sold subject to TI’s terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI’s terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers’ products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers’ products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI’s goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or “enhanced plastic” are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have *not* been so designated is solely at the Buyer’s risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

### Products

Audio	<a href="http://www.ti.com/audio">www.ti.com/audio</a>
Amplifiers	<a href="http://amplifier.ti.com">amplifier.ti.com</a>
Data Converters	<a href="http://dataconverter.ti.com">dataconverter.ti.com</a>
DLP® Products	<a href="http://www.dlp.com">www.dlp.com</a>
DSP	<a href="http://dsp.ti.com">dsp.ti.com</a>
Clocks and Timers	<a href="http://www.ti.com/clocks">www.ti.com/clocks</a>
Interface	<a href="http://interface.ti.com">interface.ti.com</a>
Logic	<a href="http://logic.ti.com">logic.ti.com</a>
Power Mgmt	<a href="http://power.ti.com">power.ti.com</a>
Microcontrollers	<a href="http://microcontroller.ti.com">microcontroller.ti.com</a>
RFID	<a href="http://www.ti-rfid.com">www.ti-rfid.com</a>
OMAP Applications Processors	<a href="http://www.ti.com/omap">www.ti.com/omap</a>
Wireless Connectivity	<a href="http://www.ti.com/wirelessconnectivity">www.ti.com/wirelessconnectivity</a>

### Applications

Automotive & Transportation	<a href="http://www.ti.com/automotive">www.ti.com/automotive</a>
Communications & Telecom	<a href="http://www.ti.com/communications">www.ti.com/communications</a>
Computers & Peripherals	<a href="http://www.ti.com/computers">www.ti.com/computers</a>
Consumer Electronics	<a href="http://www.ti.com/consumer-apps">www.ti.com/consumer-apps</a>
Energy and Lighting	<a href="http://www.ti.com/energyapps">www.ti.com/energyapps</a>
Industrial	<a href="http://www.ti.com/industrial">www.ti.com/industrial</a>
Medical	<a href="http://www.ti.com/medical">www.ti.com/medical</a>
Security	<a href="http://www.ti.com/security">www.ti.com/security</a>
Space, Avionics & Defense	<a href="http://www.ti.com/space-avionics-defense">www.ti.com/space-avionics-defense</a>
Video & Imaging	<a href="http://www.ti.com/video">www.ti.com/video</a>

**TI E2E Community** [e2e.ti.com](http://e2e.ti.com)

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265

Copyright© 2013, Texas Instruments Incorporated