

JPEG Progressive Support Decoder (v03.01.02) on C66x

FEATURES

- eXpressDSP™ Digital Media (XDM 1.0 IIMGDEC1) interface compliant
- Validated on TMS320C6678 EVM
- Baseline sequential mode with both interleaved and non-interleaved input format supported
- Progressive mode supported
- YUV 444, YUV 422, YUV 420, YUV 411, and Gray scale color sub-sampling formats supported
- YUV 422 with sampling format ((1,2), (1, 1), (1,1)) for baseline sequential mode with interleaved input format supported, baseline sequential mode with non-interleaved input format supported and progressive
- RGB16, BGR24, and BGR32 output format supported
- Maximum of three components supported
- Maximum of three quantization tables supported
- Maximum of four huffman tables each for ac and dc DCT coefficients supported
- Arbitrary image size for both sequential and progressive JPEG images supported
- 8-bit and 16-bit quantization tables supported
- Resizing the output image by a factor of 1/2, 1/4, and 1/8 supported
- On-the-fly resizing with respect to set maxHeight and maxWidth supported
- YUV planar or YUV 422 interleaved output format supported
- Frame level decoding of images for sequential mode and scan level decoding for progressive mode supported
- All the data buffers and tables are placed in the external memory
- Source images of 12-bits per sample not supported
- JPEG File Interchange Format (JFIF) header skipped
- Restart management for bit stream with Define Restart Interval Marker (DRI) and Restart Marker (RST) enabled
- Sectional Decoding is supported
- Supports sub region decoding
- Supports up scaling the output image by a factor of 2, 4, and 8
- Supports ELF ABI format

DESCRIPTION

JPEG is an international standard for color image compression. This standard is defined in the ISO 10918-1 JPEG Draft International Standard | CCITT Recommendation T.81. Supports baseline sequential mode with both interleaved and non-interleaved input format and progressive mode.



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Performance Summary

This section describes performance of JPEG Progressive Support Decoder on TMS320C6678 EVM

Table 1. Configuration Table

CONFIGURATION	ID
Progressive Support decoder (4:2:0 input and 4:2:2 interleaved output) with no internal memory	JPEG_DEC_001

Table 2. Cycles Information – Profiled on TMS320C6678 EVM With Code Generation Tools Version 7.4.0

CONFIGURATION ID	PERFORMANCE STATISTICS (CYCLES PER PIXEL) ⁽¹⁾ ⁽²⁾		
	TEST DESCRIPTION	AVERAGE	PEAK ⁽³⁾
JPEG_DEC_001	REMI0003.jpg, 2048 x 1536 , Baseline Sequential Interleave	10.12	None
	REMI0003.jpg, 2048 x 1536 , Baseline Sequential non-Interleave	8.87	None
	remi003_prog.jpg, 2048 x 1536 , Progressive Image	8.62	None
	REMI0003.jpg, 2048 x 1536 , Baseline Sequential Interleave, Sectional Decoding, one row decode for each process call	9.67	None
	REMI0003.jpg, 2048 x 1536 , Baseline Sequential non-Interleave, Sectional Decoding, one row decode for each process call	8.65	None
	remi003_prog.jpg, 2048 x 1536 , Progressive Image, Sectional Decoding, one row decode for each process call	8.68	None
	REMI0003.jpg, 2048 x 1536 , Baseline Sequential Interleave, Sub-Region Decoding, X origin @ 512,length 1024 and Y origin @ 384,length 768	23.96	None
	REMI0003.jpg, 2048 x 1536 , Baseline Sequential non-Interleave, Sub-Region Decoding, X origin @ 512,length 1024 and Y origin @ 384,length 768	23.55	None
	remi003_prog.jpg, 2048 x 1536 , Progressive Image, Sub-Region Decoding, X origin @ 512,length 1024 and Y origin @ 384,length 768	33.10	None
	REMI0003.jpg, 2048 x 1536 , Baseline Sequential Interleave, Sectional based Scaling, scaling the output by 2	31.69	None
	REMI0003.jpg, 2048 x 1536 , Baseline Sequential non-Interleave, Sectional based Scaling, scaling the output by 2	32.69	None
	remi003_prog.jpg, 2048 x 1536 , Progressive Image, Sectional based Scaling, scaling the output by 2	25.41	None

(1) Measured with stack, and I/O buffers in external memory and the program memory in SL2. Measured with 10:1 compression ratio

(2) Average and peak MCPS measurements can vary by $\pm 5\%$

(3) Peak value is not calculated for this version of JPEG Decoder.

Note:

- Default cache configuration (L1D cache: 32 K-bytes, L2 cache: 64 K-bytes, L1P cache: 32 K-bytes).
- If TMS320C6678 runs on 1GHz, then Mega pixels/sec will be 1GHz/Cycles per pixel = 1GHz/10 = 100 Mega pixels/second.
- Example to compute Time taken (for processing in milli second) from cycles per pixel mentioned in the Cycles Information table.

Time taken = Cycles per pixel * Resolution of the image specified (i.e., height and width)/Frequency of the processor on which the processing is being done.

As per the value given in the data sheet:

For baseline sequential image REMI0003.jpg of resolution 2048 x 1536, cycles per pixel mentioned is 10.12 cycles/pixel.

If TMS320C6678 runs on 1GHz,

Time taken = $10.12 * 2048 * 1536 / (1000 * 1024 * 1024)$

= 31834767.36 / 1048576000

= 30.36 msec

Table 3. Memory Statistics - Generated With Code Generation Tools Version 7.4.0

CONFIGURATION ID	MEMORY STATISTICS ⁽¹⁾				TOTAL
	PROGRAM MEMORY	DATA MEMORY			
		INTERNAL	EXTERNAL	STACK	
JPEG_DEC_001	135	0	38	8	181 ⁽²⁾
JPEG_DEC_001 (720 x 480)	135	0	2063	8	2206
JPEG_DEC_001 (1280 x 1024)	135	0	7718	8	7861
JPEG_DEC_001 (1600 x 1200)	135	0	11288	8	11431
JPEG_DEC_001 (2048 x 1536)	135	0	18470	8	18613
JPEG_DEC_001 (2560 x 2048)	135	0	30758	8	30901

- (1) All memory requirements are expressed in kilobytes (1K-byte = 1024 bytes).
(2) Applicable for Baseline Sequential Images.

Table 4. Internal Data Memory Split-Up

CONFIGURATION ID	DATA MEMORY-INTERNAL ⁽¹⁾		
	SHARED		INSTANCE ⁽²⁾
	CONSTANTS	SCRATCH	
JPEG_DEC_001	0	0	0

- (1) All memory requirements are expressed in kilobytes.
(2) Does not include I/O buffer.

Table 5. External Data Memory Split-Up

CONFIGURATION ID	DATA MEMORY - EXTERNAL ⁽¹⁾		
	SHARED		INSTANCE ⁽²⁾
	CONSTANTS	SCRATCH	
JPEG_DEC_001	1	7	30 ⁽³⁾
JPEG_DEC_001 (720 x 480)	1	7	2055
JPEG_DEC_001 (1280 x 1024)	1	7	7710
JPEG_DEC_001 (1600 x 1200)	1	7	11280
JPEG_DEC_001 (2048 x 1536)	1	7	18462
JPEG_DEC_001 (2560 x 2048)	1	7	30750

- (1) All memory requirements are expressed in kilobytes.
(2) I/O buffers not included.
(3) Applicable for Baseline Sequential Images.

Table 6. Co-Processor(s) Memory Statistics

CONFIGURATION ID	SEQ DATA MEMORY	SEQ PROG MEMORY	IMX WORKING MEM	IMX IMG BUF	IMX CMD MEM
JPEG_DEC_001	0	0	0	0	0

Note: The decoder does not use co-processors and hence all the values are zero.

Notes

- Total data memory for N non pre-emptive instances = Constants + Scratch + N*(Instance + I/O buffers + Stack)

References

- ITU-CCITT recommendation T.81 (reproduction of ISO/IEC 10918-1)
- eXpressDSP Algorithm Interoperability Standard (TMS320 Algorithm Interface Standard)
- JPEG Progressive Support Decoder on C66x User's Guide (SPRUH88C)

Glossary

TERM	DESCRIPTION
Constants	Elements that go into .const memory section
Scratch	Memory space that can be reused across different instances of the algorithm
Shared	Sum of Constants and Scratch
Instance	Persistent-memory that contains persistent information - allocated for each instance of the algorithm

Acronyms

ACRONYM	DESCRIPTION
ABI	Application Binary Interface
CCITT	Committee Consultative International Telephone and Telegraph
DCT	Discrete Cosine Transform
DRI	Define Restart Interval Marker
DSP	Digital Signal Processing
ELF	Executable and Linkable Format
IEC	International Electrotechnical Commission
ISO	International Organization for Standardization
ITU	International Telecommunication Union
JFIF	JPEG File Interchange Format
JPEG	Joint Photographic Experts Group
RST	Restart Marker
XDM	eXpressDSP Digital Media

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PREVIEW**

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