



DSP/BIOS VPBE Device Driver

Version: 1.10.03

Release Notes

April 16, 2009

The product release notes in this document are for DM6437 DSP/BIOS VPBE Device Driver, which is part of the DM6437 BIOS package.

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1 General information

The VPBE device driver included in this release package supports h/w capabilities of DSP VPBE (VPSS Front End) peripheral device.

The driver supports only interrupt modes of operation.

The driver is based on an architecture that allows for easy customization/extension. It separates usage policies such as buffering scheme and blocked-calls from basic device management for data transfers.

The driver is a single instance and re-entrant safe for use in multi-threaded environment.

2 New In This Release

- The pragma definitions, macro definitions and compiler switches used are documented in the user guide .

3 System Requirements

Details about the tools and the BIOS version that the driver is compatible with can be found in the system Release Notes.

4 Installation and Usage

1. Install BIOS package as per instructions provided along with the package.
2. VPBE Device driver sources are available in `<root>\packages\ti\sdo\pspdriers\drivers\vpbe\src` folder.
3. Build the VPBE project file in build directory to build the debug/idebug/release/irelease library.
4. Sample application / test code is provided in `<root>\packages\ti\sdo\pspdriers\system\dm6437\bios\evmDM6437\video\sample\build\vpbe` folder.

5 Uninstallation

1. Un-install the Driver package as per instructions provided with the package.

6 Adding instance of the device driver

To have VPBE device driver included in the application, it is required to add the TCI file i.e. "dm6437_vpbe0.tci" in the TCF file of the application. Refer to section 5 in DM6437_BIOS_PSP_Release_Notes.pdf.

7 Fixed In This Release

DPSP00010263 is fixed by removing the redundant statement. File(s) affected are: `packages\ti\sdo\pspdriers\drivers\vpbe\src\ddc_vpbe.c`

8 Known Issues

1.	MR status
Release Note <ul style="list-style-type: none"> The hardware issue is suspected for MR No DPSP00005336, DPSP00005712 and DPSP00005697. The hardware issue is suspected for MR No DPSP00006496. The data lines 8 and 9 coming from MT9T001 sensor board is not coming at the interface of Dm6437 board and sandwich board. The Chroma artifacts mentioned in MR No DPSP00006537 is reduced with S-video input. MR No DPSP00006536 is suspected to be interlacing effects. MR No DPSP00006002 is a known observation. The interlacing effect will be seen when front end (capture) is configured in interlaced mode and back end (display) is configured in progressive mode. 	
Workaround	

2.	MR No DPSP00006408 and DPSP00007083
Release Note <ul style="list-style-type: none"> Gel issue is suspected. The loopback application works properly by removing the gel file on consecutive re-load. 	
Workaround: The issue is not reproducible using gel file provided along with the package.	

Note: Channel handle should not be shared across multiple tasks. Sharing of handle across multiple tasks might cause corruption in the GIO layer.

9 Revision history

Date	Author	Comments	Version
November 23, 2006	Maulik Desai	BFT release 0.2.0	1.0
December 1, 2006	Maulik Desai	Modified for the release 0.3.0	1.1
February 3, 2007	Maulik Desai	Modified for the release 0.5.0	1.2
June 22, 2007	Anuj Aggarwal	Modified for GA patch release 1.00.01	1.3
June29, 2007	Amit Chatterjee	Modified Release Version	1.4
June30, 2007	Maulik Desai	Modified for GA patch release 1.00.03.00	1.5
November 29, 2007	Sivaraj R	PSP merge package changes - directory structure changes, FVID_allocBuffer and FVID_freeBuffer functions are implemented as GIO control commands	1.6
February 29, 2008	Sivaraj R	Bug fixed - LLC_setOsdFBStartAddress function called twice to update frame buffer pointer	1.7
May 28, 2008	M Sriram	Updated for release 1.10.01	1.8