DSP/BIOS I2C Device Driver

Version: 1.10.03

Release Notes

April 16, 2009

The product release notes in this document are for DM648/C6452/DM6437/C6424 DSP/BIOS I2C Device Driver.

TEXAS INSTRUMENTS

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1 General information

The I2C device driver included in this release package supports h/w capabilities of DSP I2C peripheral device.

The driver supports Synchronous I/O and implements Polled, Interrupt enabled modes of operation.

The driver is based on an architecture that allows for easy customization/extension. It separates blocked-calls from basic device management for data transfers.

The driver is multi-instantiable and re-entrant safe for use in multi-threaded environment.

1.1 Sample Application

The sample DSP/ BIOS application (sample folder) is a representative test program. In application driver configuration parameters initialization is done via registering the init function with the BIOS I2C driver. The sample application has a task which does starting up the driver in polled mode (which is the default). It then performs typical IO, which is to light On-Off LED on board.

2 New In This Release

None.

3 System Requirements

Refer system level release notes for tools and BIOS versions.

4 Installation and Usage

1. Install BIOS package as per instructions provided along with the package.
2. I2C Device driver sources are available in <root>/drivers/i2c folder.
3. Build the i2c project file in build directory to build the debug/idebug/irelease/release library.
4. Sample application details are provided in i2c user guide document.

5 Uninstallation

1. Un-install the Driver package as per instructions provided with the package.

6 Fixed In This Release

7 Known Issues

1.	**Loopback is not supported in interrupt mode**
Release Note	
• Loopback is not supported in interrupt mode.This is the limitation.	

Workaround
• **None**

2	**Time out of I2C driver might hang the bus when operating in read/write mode.**
Release Note	
• When i2c driver times out during read/write operation, bus hangs, as it is not allowed to reset the bus during the transfer. This is the limitation	
Workaround	
• Give a proper time out value while performing read operation.	

8 Revision history

Date	Author	Comments	Version
November 23, 2006	Maulik Desai	BFT release 0.2.0	1.0
December 01, 2006	Maulik Desai	Modified for the release 0.3.0	1.1
December 13, 2006	Nagarjuna Kristam	Derived from DM64LC and modified for DM648/C6452 0.3 release	1.2
December 19, 2006	Nagarjuna Kristam	Updated CCS and BIOS version	1.3
January 9, 2007	Nagarjuna Kristam	Updated known issues	1.4
January 9, 2007	Nagarjuna Kristam	Renamed DM64G to DM648	1.5
February 2, 2007	Vichu	Corrected minor typo errors	1.6
March 12, 2007	Nagarjuna Kristam	Updated for 0.4 release	1.7
June 6, 2007	Nagarjuna Kristam	Updated for 0.6 release	1.8
June 15, 2007	Nagarjuna Kristam	Updated revision number and version numbers for tools used	1.9
August 25, 2007	Nagarjuna Kristam	Updated fro 1.10.00.04 release	1.10
September 20, 2007	Nagarjuna Kristam	Updated fro 1.10.00.05 release	1.11
October 22, 2007	Nagarjuna Kristam	Updated XDC and BIOS versions	1.12

November 14, 2007	Nagarjuna Kristam	Updated for DM6437/C6424 and DM648/C6424 merge	1.13
January 17, 2008	Nagarjuna Kristam	Updated for 1_10_00_09 release	1.14
February 29, 2008	M Sriram	Updated for release 1.10.00.10	1.15
May 28, 2008	Chandan Nath	Updated for release 1.10.01	1.16