




# **MSP430 DriverLib for MSP430FR57xx Devices**

## **User's Guide**

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# 1 Introduction

The Texas Instruments® MSP430® Peripheral Driver Library is a set of drivers for accessing the peripherals found on the MSP430 FR5xx/FR6xx family of microcontrollers. While they are not drivers in the pure operating system sense (that is, they do not have a common interface and do not connect into a global device driver infrastructure), they do provide a mechanism that makes it easy to use the device's peripherals.

The capabilities and organization of the drivers are governed by the following design goals:

- They are written entirely in C except where absolutely not possible.
- They demonstrate how to use the peripheral in its common mode of operation.
- They are easy to understand.
- They are reasonably efficient in terms of memory and processor usage.
- They are as self-contained as possible.
- Where possible, computations that can be performed at compile time are done there instead of at run time.
- They can be built with more than one tool chain.

Some consequences of these design goals are:

- The drivers are not necessarily as efficient as they could be (from a code size and/or execution speed point of view). While the most efficient piece of code for operating a peripheral would be written in assembly and custom tailored to the specific requirements of the application, further size optimizations of the drivers would make them more difficult to understand.
- The drivers do not support the full capabilities of the hardware. Some of the peripherals provide complex capabilities which cannot be utilized by the drivers in this library, though the existing code can be used as a reference upon which to add support for the additional capabilities.
- The APIs have a means of removing all error checking code. Because the error checking is usually only useful during initial program development, it can be removed to improve code size and speed.

For many applications, the drivers can be used as is. But in some cases, the drivers will have to be enhanced or rewritten in order to meet the functionality, memory, or processing requirements of the application. If so, the existing driver can be used as a reference on how to operate the peripheral.

Each MSP430ware driverlib API takes in the base address of the corresponding peripheral as the first parameter. This base address is obtained from the msp430 device specific header files (or from the device datasheet). The example code for the various peripherals show how base address is used. When using CCS, the eclipse shortcut "Ctrl + Space" helps. Type `_MSP430` and "Ctrl + Space", and the list of base addresses from the included device specific header files is listed.

The following tool chains are supported:

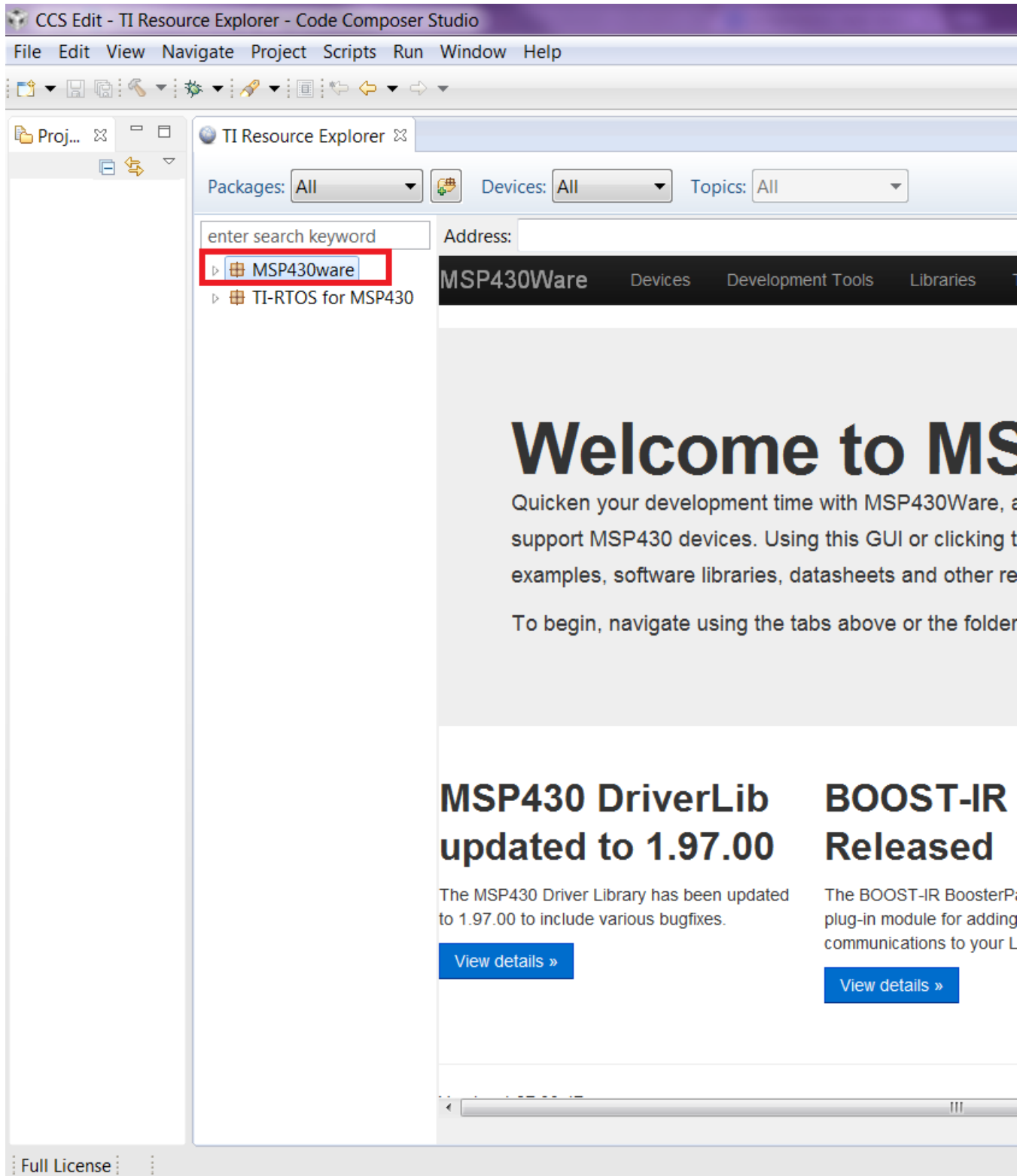
- IAR Embedded Workbench®
- Texas Instruments Code Composer Studio™

Using assert statements to debug

Assert statements are disabled by default. To enable the assert statement edit the `hw_regaccess.h` file in the `inc` folder. Comment out the statement `#define NDEBUG` -> `//#define NDEBUG` Asserts in CCS work only if the project is optimized for size.

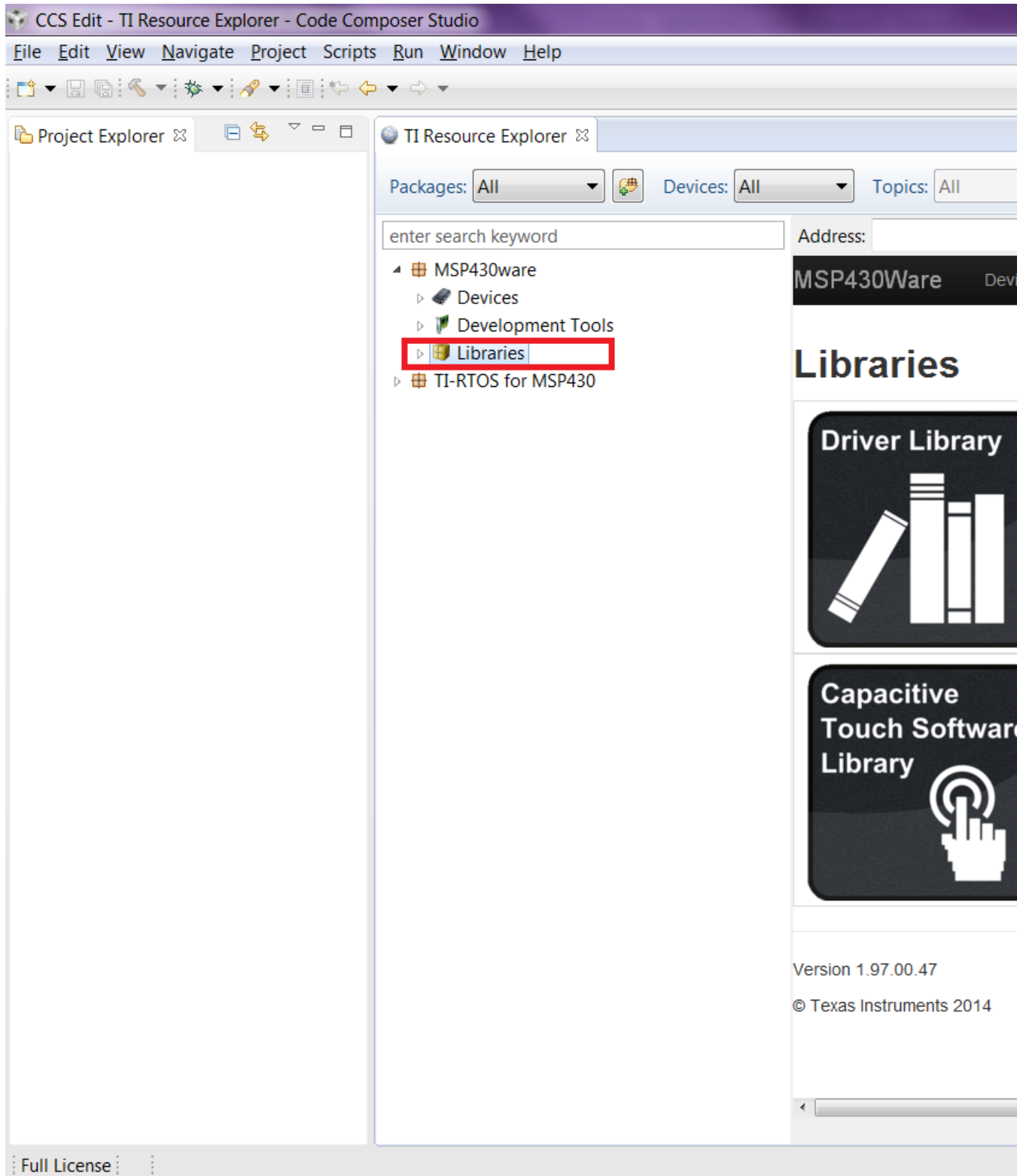


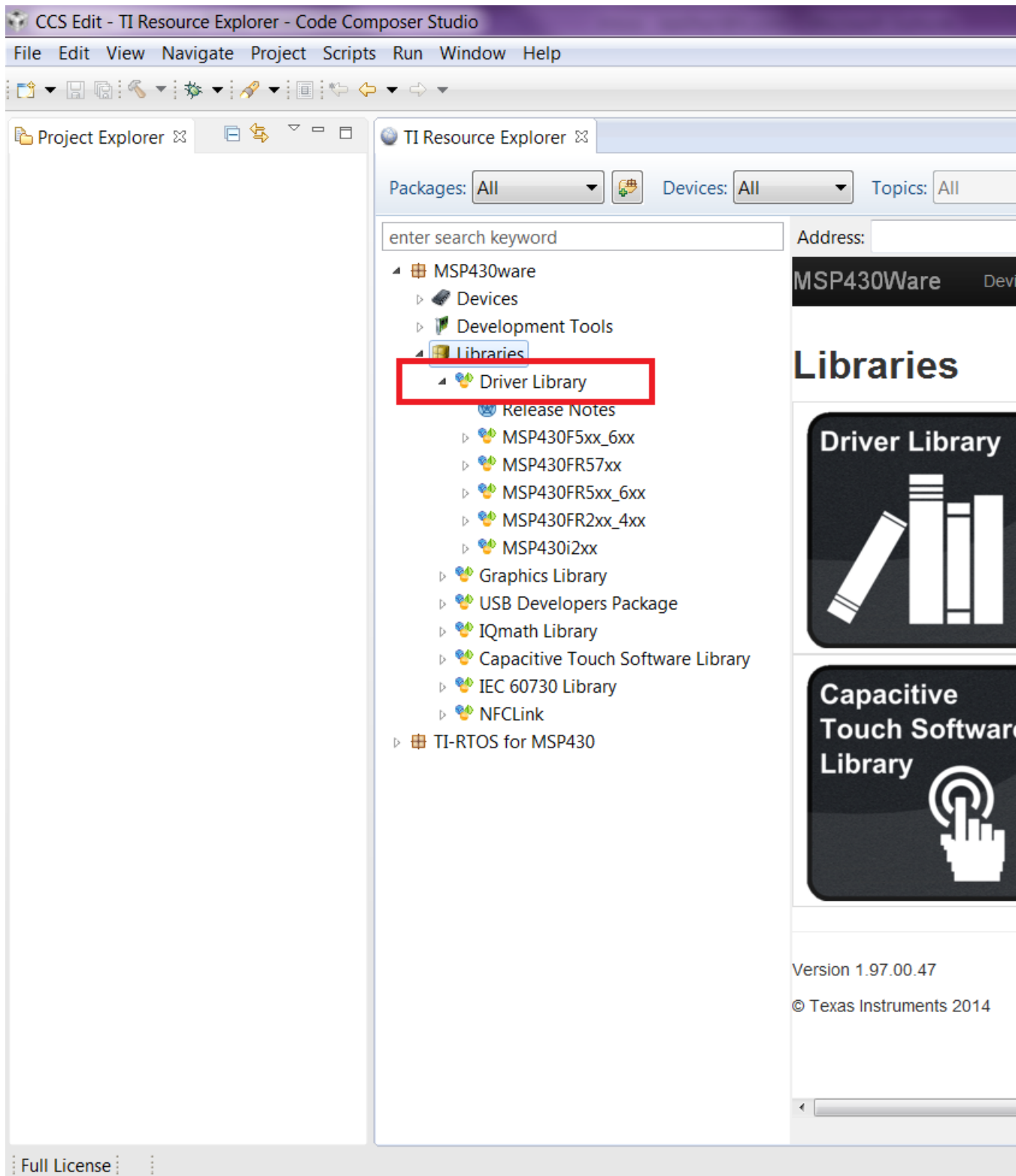




Clicking MSP430ware takes you to the introductory page. The version of the latest MSP430ware installed is available in this page. In this screenshot the version is 1.30.00.15 The various

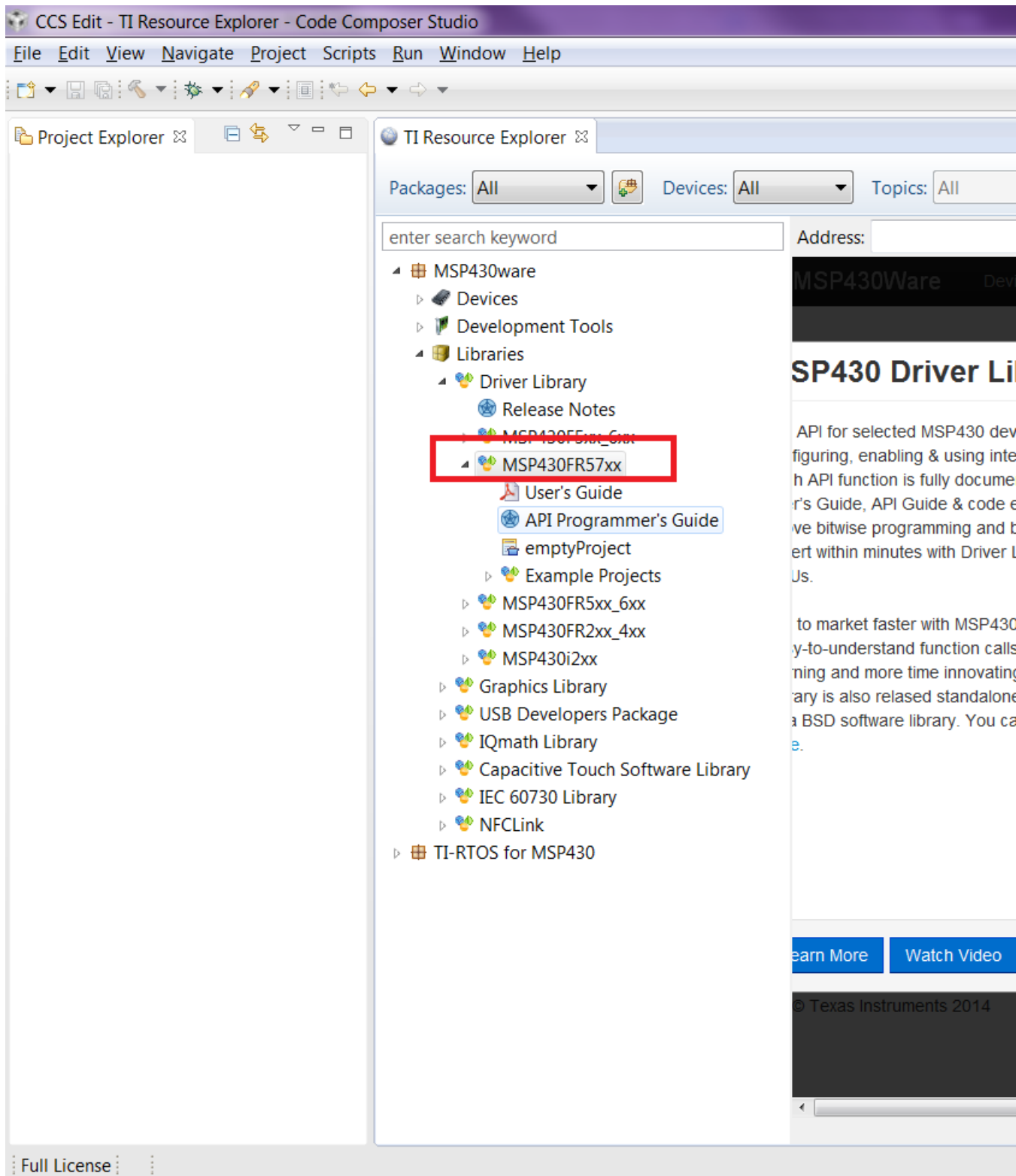
software, collateral, code examples, datasheets and user guides can be navigated by clicking the different topics under MSP430ware. To proceed to driverlib, click on Libraries->Driverlib as shown in the next two screenshots.



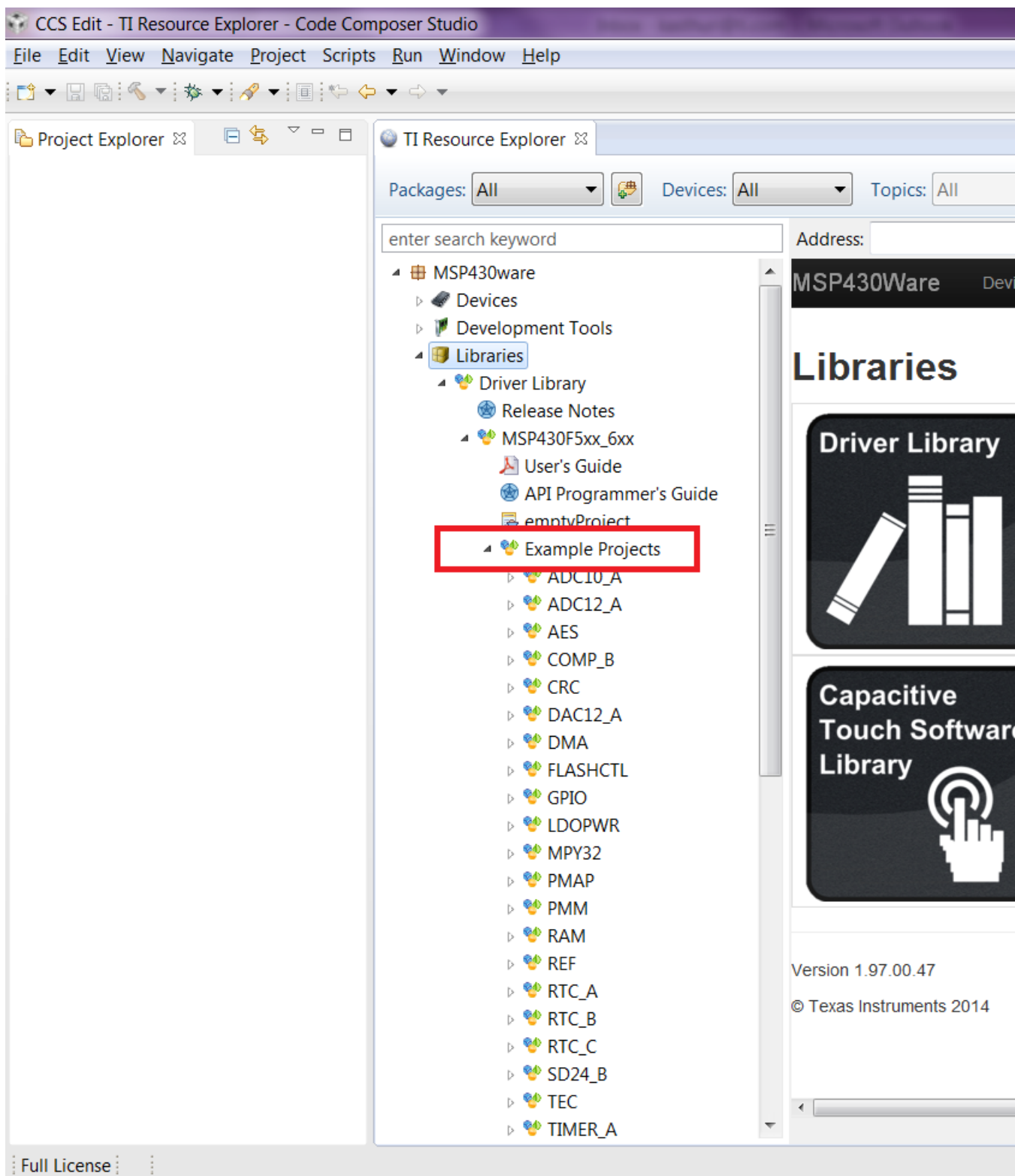


Driverlib is designed per Family. If a common device family user's guide exists for a group of devices, these devices belong to the same 'family'. Currently driverlib is available for the following

family of devices. MSP430F5xx\_6xx MSP430FR57xx MSP430FR2xx\_4xx MSP430FR5xx\_6xx  
MSP430i2xx

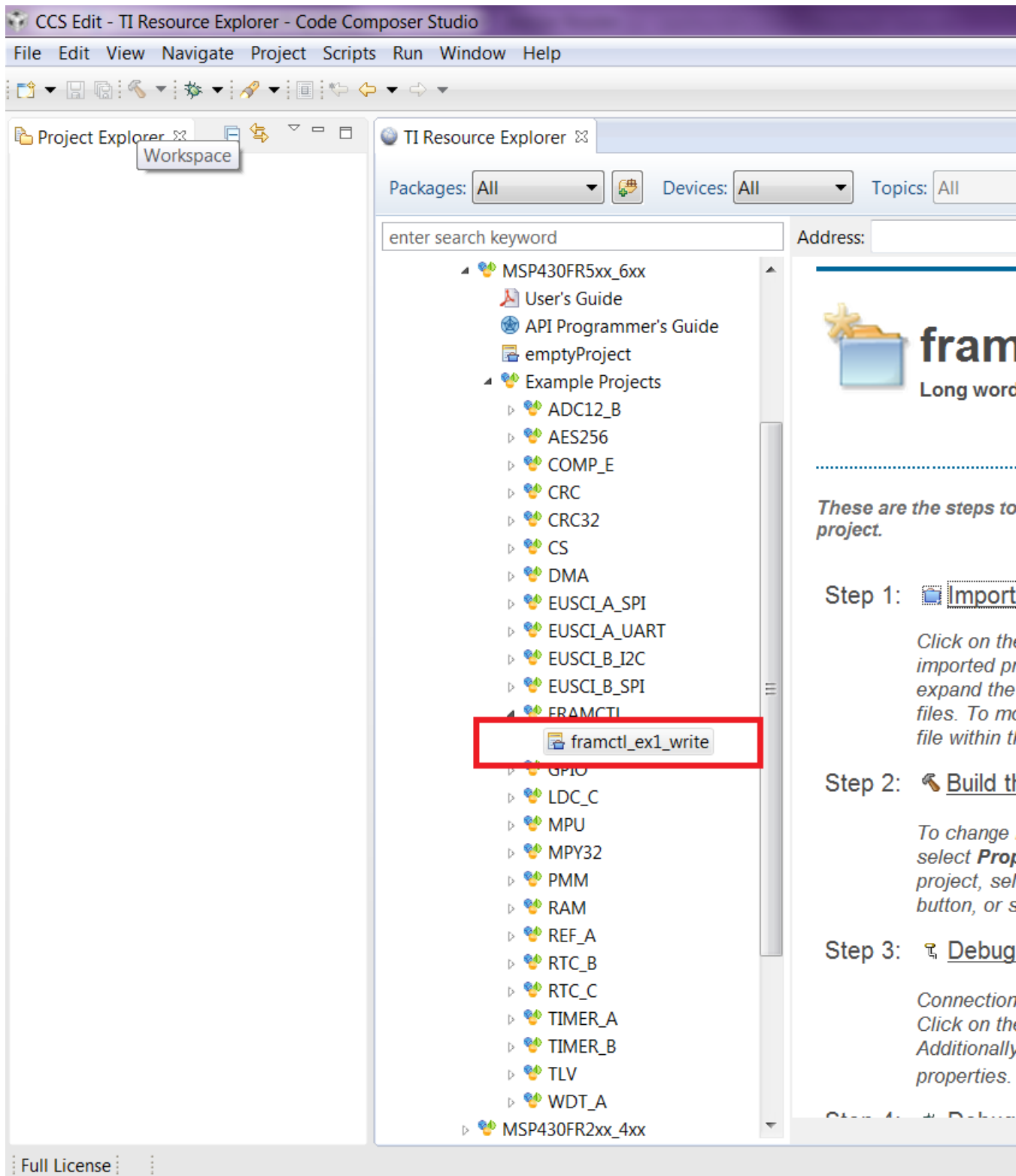


Click on the MSP430FR5xx\_6xx to navigate to the driverlib based example code for that family.



The various peripherals are listed in alphabetical order. The names of peripherals are as in device family user's guide. Clicking on a peripheral name lists the driverlib example code for that peripheral. The screenshot below shows an example when the user clicks on GPIO peripheral.





Now click on the specific example you are interested in. On the right side there are options to Import/Build/Download and Debug. Import the project by clicking on the "Import the example

project into CCS”

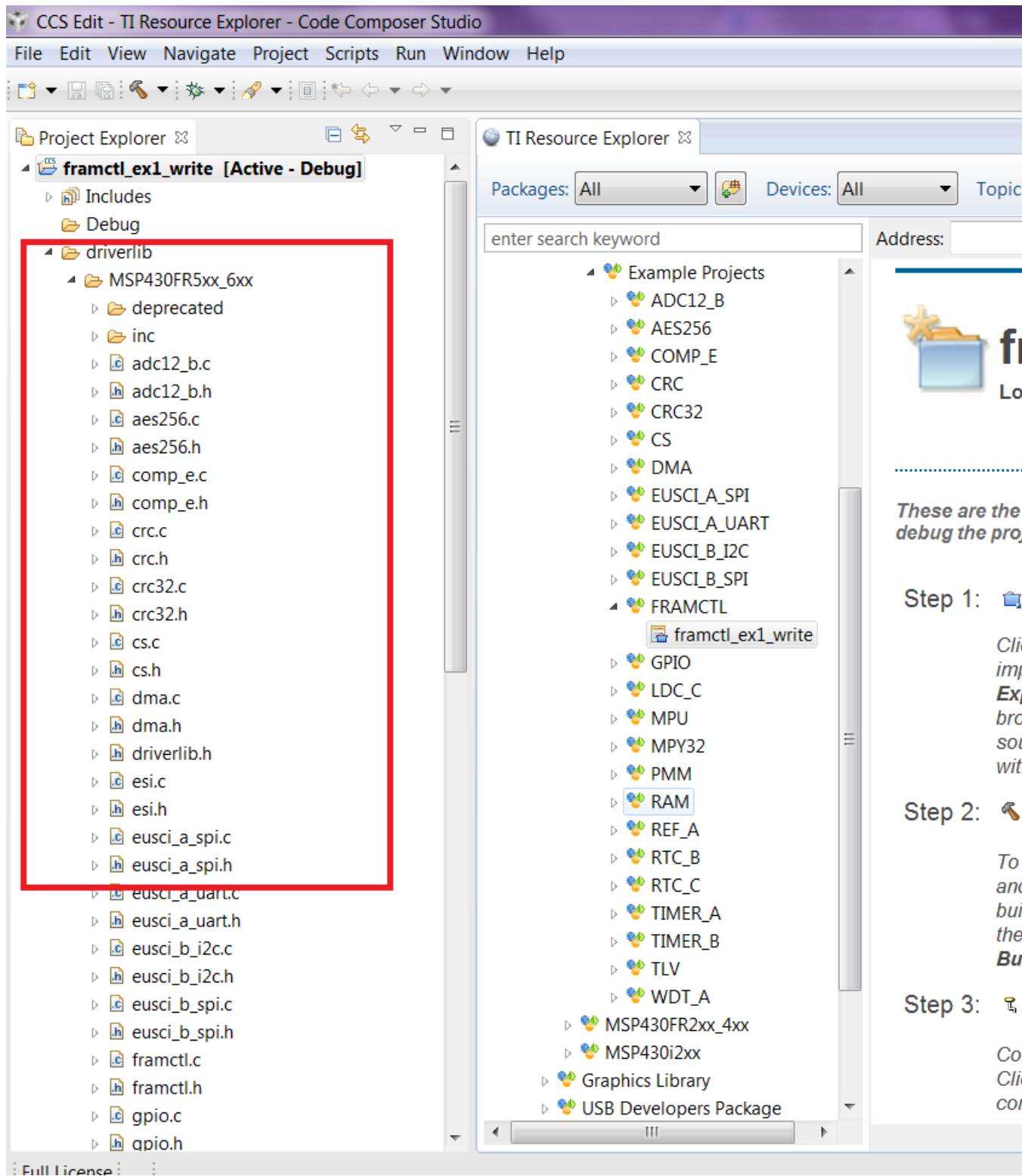
The screenshot shows the CCS Resource Explorer interface. On the left, a tree view displays the project structure under 'MSP430FR5xx\_6xx'. The 'FRAMCTL' folder is expanded, and 'framctl\_ex1\_write' is selected. On the right, the project page for 'framctl\_ex1\_write' is displayed, featuring a star icon and the title 'Long word writes to FRAM'. Below the title, there is a section with the text 'These are the steps to import the project, build the project, and project.' followed by three numbered steps:

**Step 1: [Import the example project into CCS](#)**  
 Click on the link above to import the project. The imported project is available in the **Project Explorer**. Expand the project node to browse the imported source files. To modify source code, double clicks on the source file within the project to open the source file editor.

**Step 2: [Build the imported project](#)**  
 To change build options, right click on the project and select **Properties** from the context menu. To build the project, select the link above, or select the **Build** toolbar button, or select the **Project | Build Project** menu item.

**Step 3: [Debugger Configuration](#)**  
 Connection: **TI MSP430 USB1**  
 Click on the link above to change the device connection. Additionally, this option is also available in the project properties.

The imported project can be viewed on the left in the Project Explorer. All required driverlib source and header files are included inside the driverlib folder. All driverlib source and header files are linked to the example projects. So if the user modifies any of these source or header files, the original copy of the installed MSP430ware driverlib source and header files get modified.

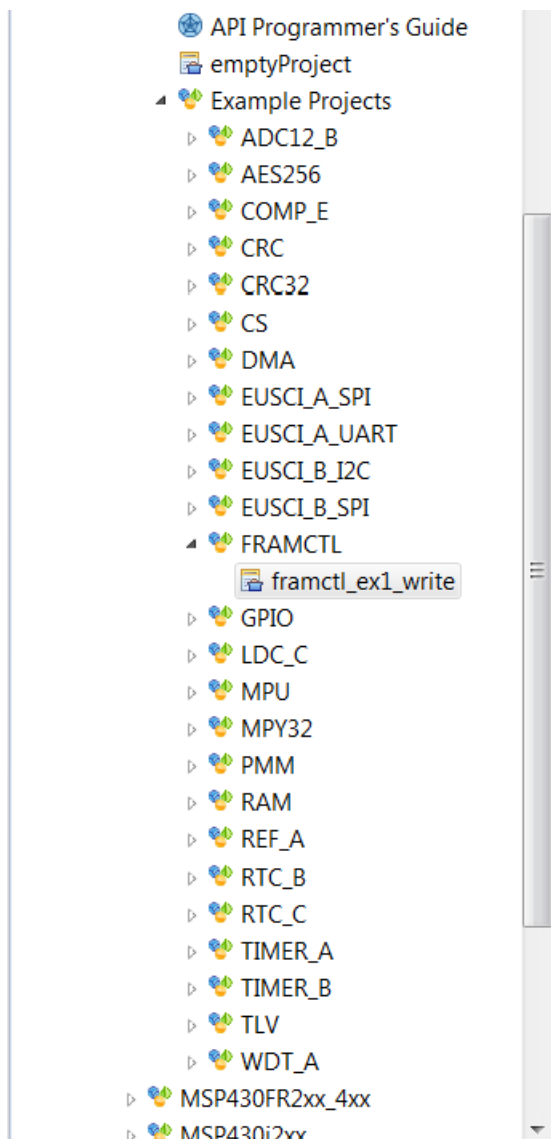


Now click on Build the imported project on the right to build the example project.

The screenshot displays the CCS Resource Explorer interface. On the left, a tree view shows the project structure under 'Example Projects'. The 'FRAMCTL' folder is expanded, and the 'framctl\_ex1\_write' project is selected. On the right, the project overview page for 'framctl\_ex1\_write' is shown, featuring a folder icon and the title 'Long word writes to FRAM'. Below the title, there is a section titled 'These are the steps to import the project, build the project, and debug the project.' followed by three numbered steps:

- Step 1:** [Import the example project into CCS](#)  
*Click on the link above to import the project. The imported project is available in the **Project Explorer**. Expand the project node to browse the imported source files. To modify source code, double clicks on the source file within the project to open the source file editor.*
- Step 2:** [Build the imported project](#)  
*To change build options, right click on the project and select **Properties** from the context menu. To build the project, select the link above, or select the **Build** tool button, or select the **Project | Build Project** menu item.*
- Step 3:** [Debugger Configuration](#)  
*Connection: **TI MSP430 USB1**  
Click on the link above to change the device connection. Additionally, this option is also available in the project properties.*

Now click on Build the imported project on the right to build the example project.



The screenshot shows the CCS Resource Explorer interface. On the left, a tree view displays the project structure. The 'Example Projects' folder is expanded, and the 'framctl\_ex1\_write' project is selected. The tree includes various peripheral drivers such as ADC12\_B, AES256, COMP\_E, CRC, CRC32, CS, DMA, EUSCI\_A\_SPI, EUSCI\_A\_UART, EUSCI\_B\_I2C, EUSCI\_B\_SPI, GPIO, LDC\_C, MPU, MPY32, PMM, RAM, REF\_A, RTC\_B, RTC\_C, TIMER\_A, TIMER\_B, TLV, and WDT\_A. At the bottom, two device folders are visible: 'MSP430FR2xx\_4xx' and 'MSP430i2xx'.

**Step 1: [Import the example project into CCS](#)**

Click on the link above to import the project. The imported project is available in the **Project Explorer**. Expand the project node to browse the imported source files. To modify source code, double clicks on the source file within the project to open the source file editor.

**Step 2: [Build the imported project](#)**

To change build options, right click on the project and select **Properties** from the context menu. To build the project, select the link above, or select the **Build** toolbar button, or select the **Project | Build Project** menu item.

**Step 3: [Debugger Configuration](#)**

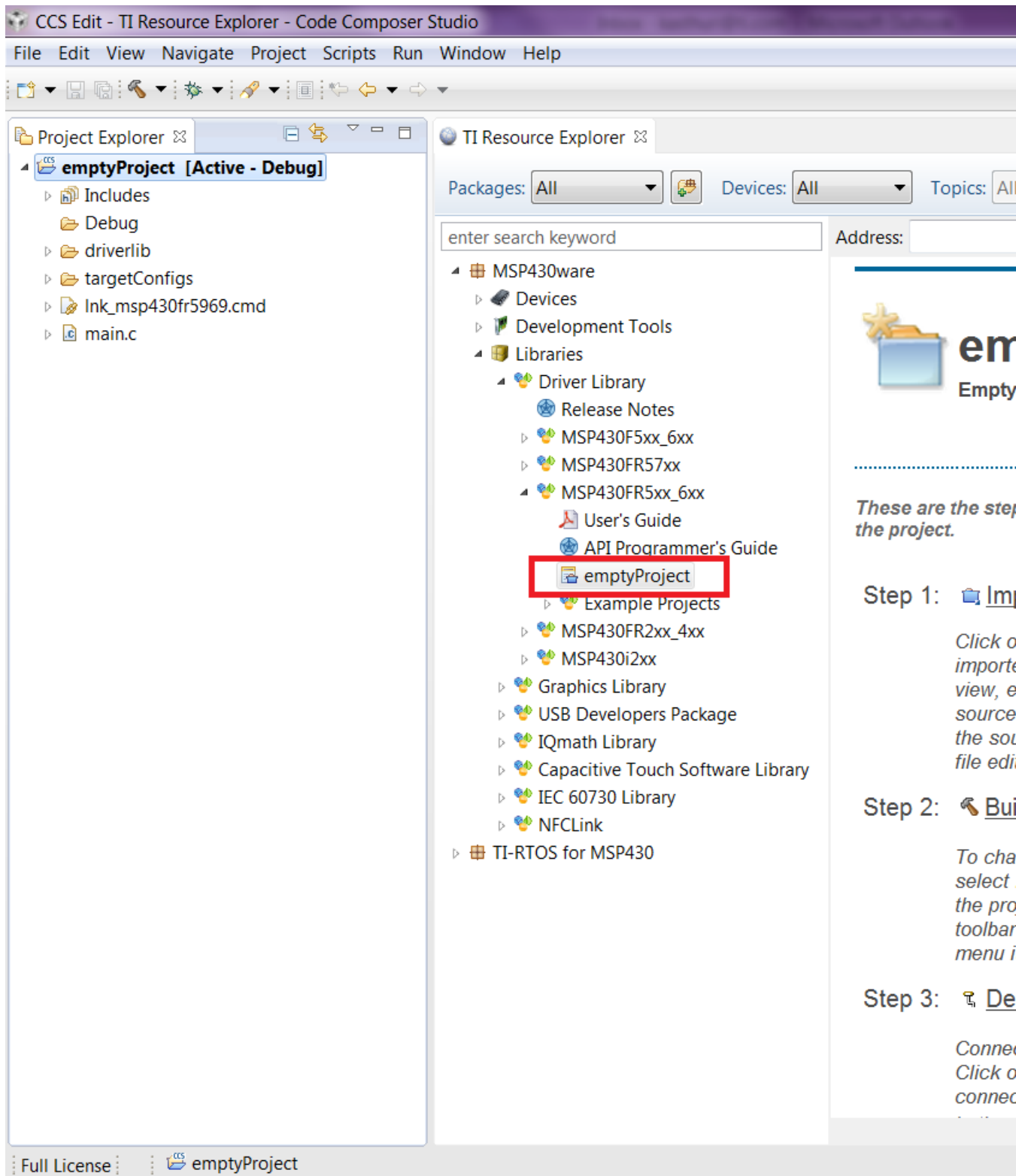
Connection: **TI MSP430 USB1**  
Click on the link above to change the device connection. Additionally, this option is also available in the project properties.

**Step 4: [Debug the imported project](#)**

Click on the link above to launch a debug session for the **framctl\_ex1\_write** project and switch to the **CCS Debugger Perspective**. Additionally, these are other methods to start a project debug session. Select the project in the **Project Explorer** view and click on the bug toolbar button. To relaunch a previous debug session, click on the session arrow beside the bug toolbar button and select one of the debug sessions from the history.

The COM port to download to can be changed using the Debugger Configuration option on the right if required.

To get started on a new project we recommend getting started on an empty project we provide. This project has all the driverlib source files, header files, project paths are set by default.

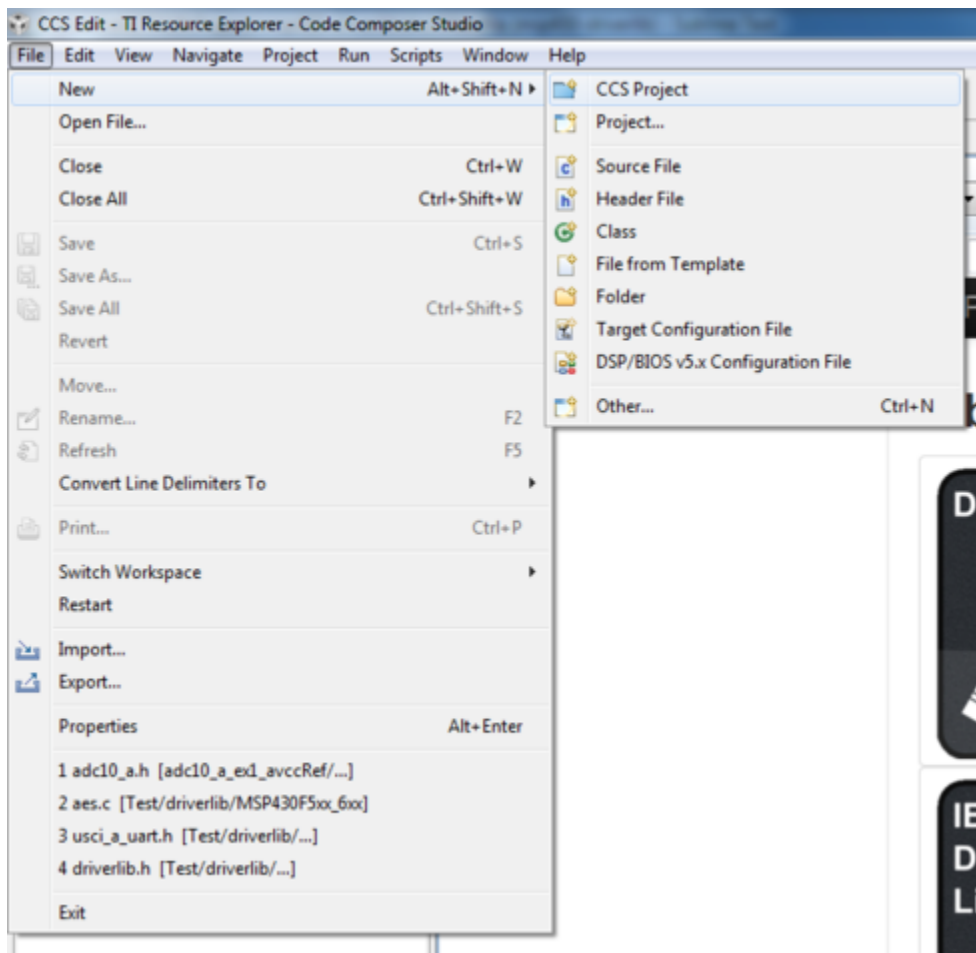


The main.c included with the empty project can be modified to include user code.

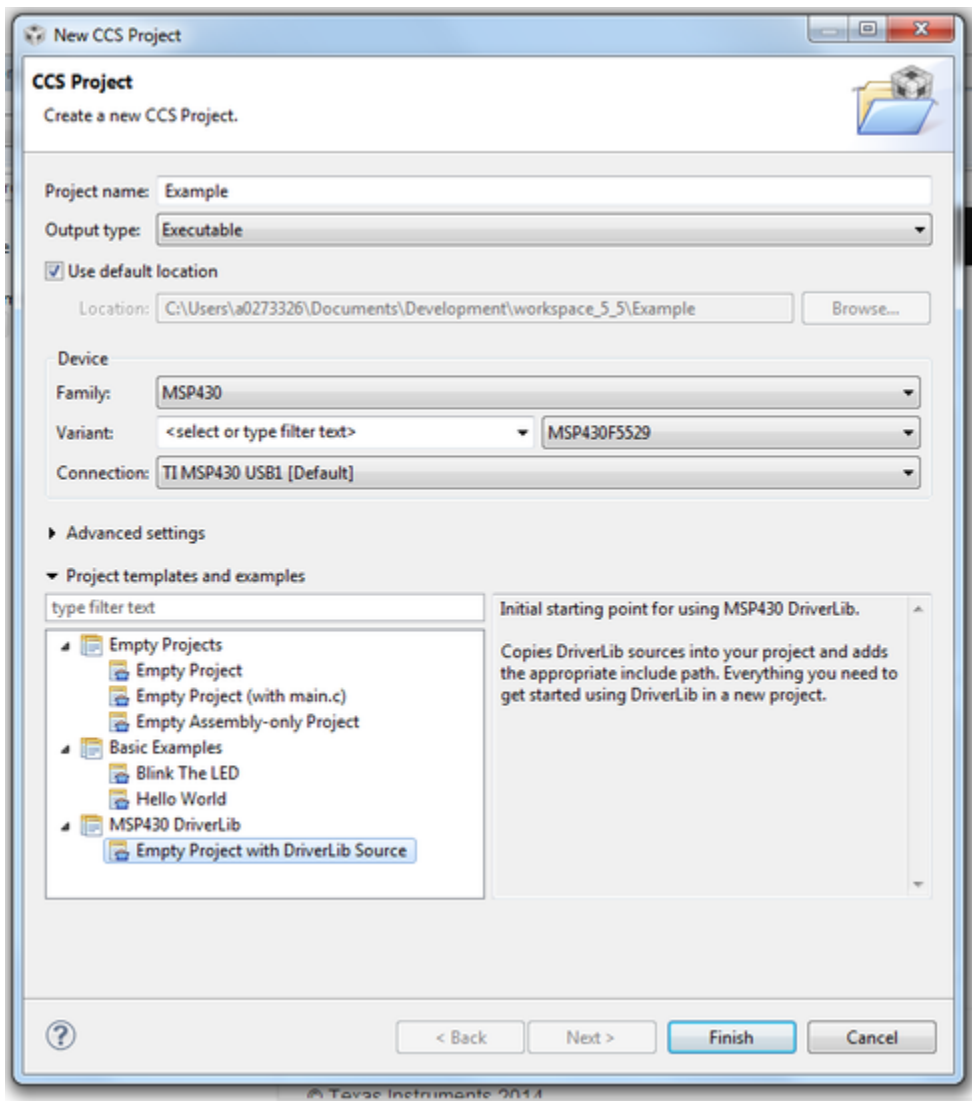
## 3 How to create a new CCS project that uses Driverlib

### 3.1 Introduction

To get started on a new project we recommend using the new project wizard. For driver library to work with the new project wizard CCS must have discovered the driver library RTSC product. For more information refer to the installation steps of the release notes. The new project wizard adds the needed driver library source files and adds the driver library include path. To open the new project wizard go to File -> New -> CCS Project as seen in the screenshot below.



Once the new project wizard has been opened name your project and choose the device you would like to create a Driver Library project for. The device must be supported by driver library. Then under "Project templates and examples" choose "Empty Project with DriverLib Source" as seen below.



Finally click "Finish" and begin developing with your Driver Library enabled project.

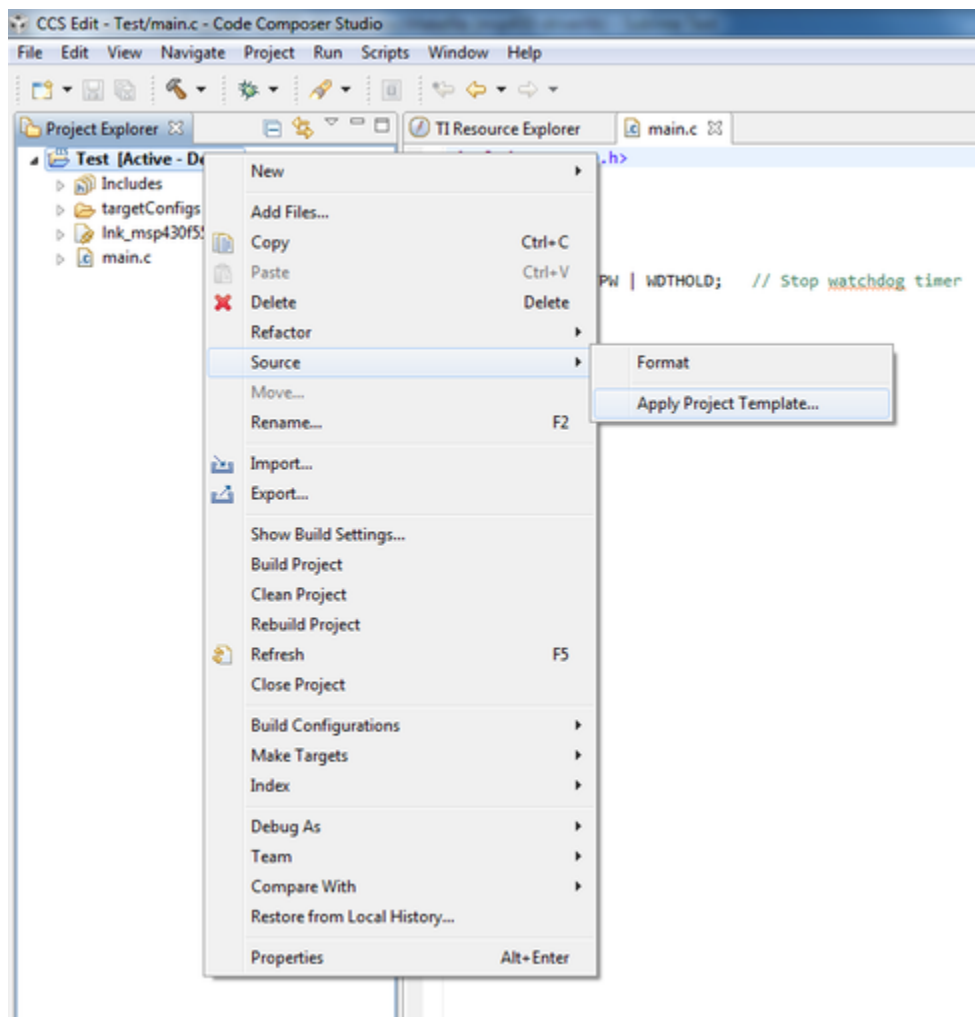
We recommend -O4 compiler settings for more efficient optimizations for projects using driverlib



# 4 How to include driverlib into your existing CCS project

## 4.1 Introduction

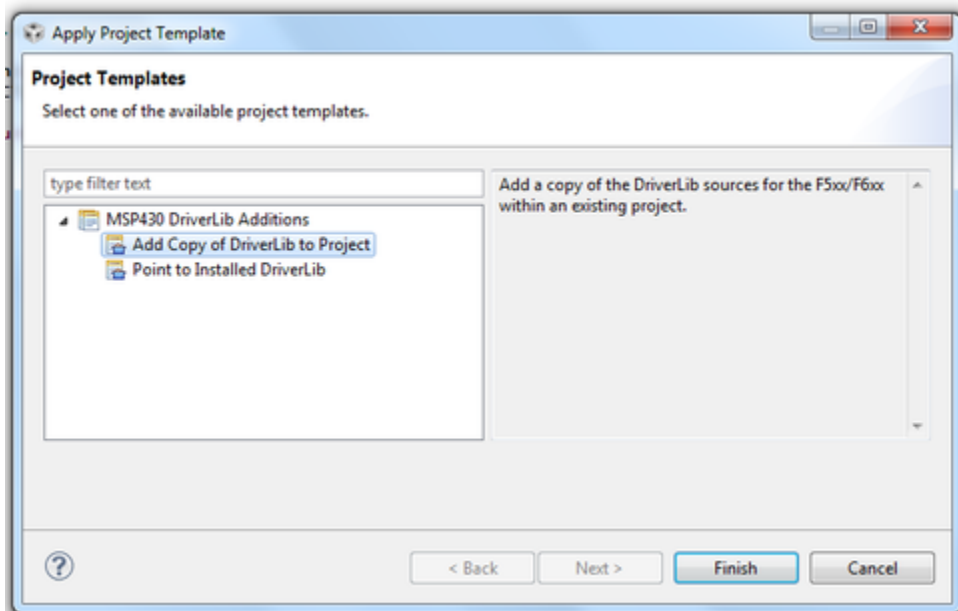
To add driver library to an existing project we recommend using CCS project templates. For driver library to work with project templates CCS must have discovered the driver library RTSC product. For more information refer to the installation steps of the release notes. CCS project templates adds the needed driver library source files and adds the driver library include path. To apply a project template right click on an existing project then go to Source -> Apply Project Template as seen in the screenshot below.



In the "Apply Project Template" dialog box under "MSP430 DriverLib Additions" choose either "Add Local Copy" or "Point to Installed DriverLib" as seen in the screenshot below. Most users will want to add a local copy which copies the DriverLib source into the project and sets the compiler

settings needed.

Pointing to an installed DriverLib is for advanced users who are including a static library in their project and want to add the DriverLib header files to their include path.

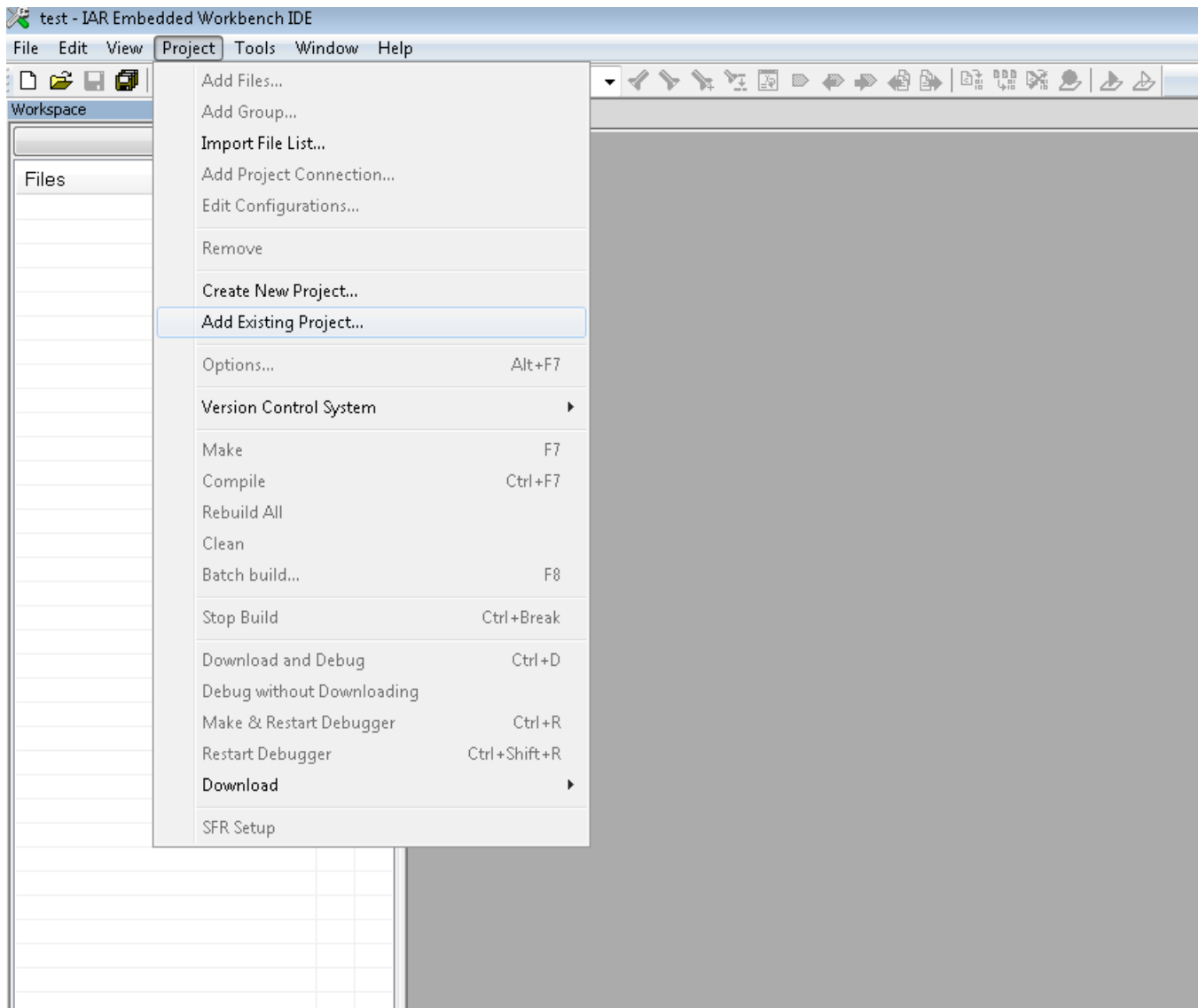


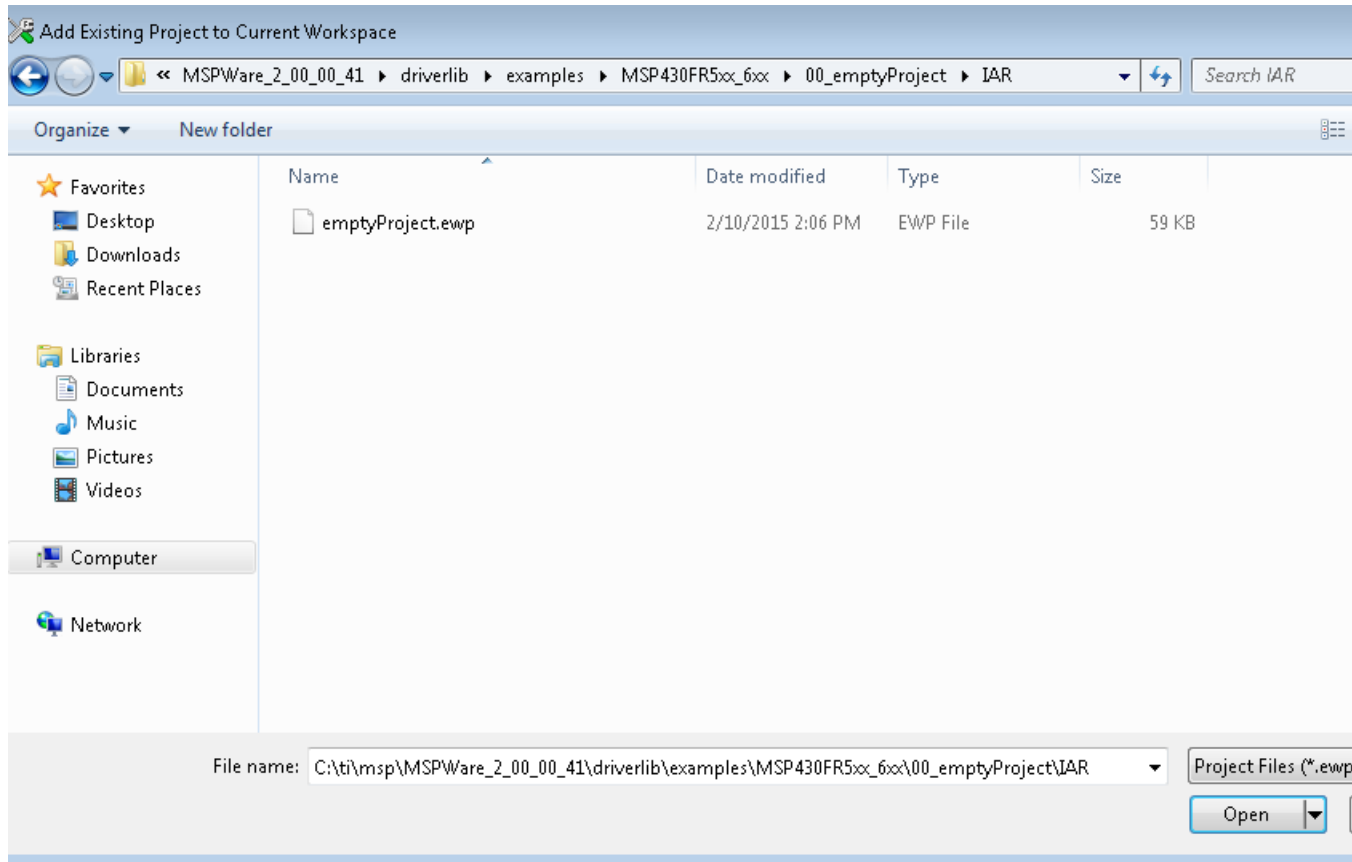
Click "Finish" and start developing with driver library in your project.

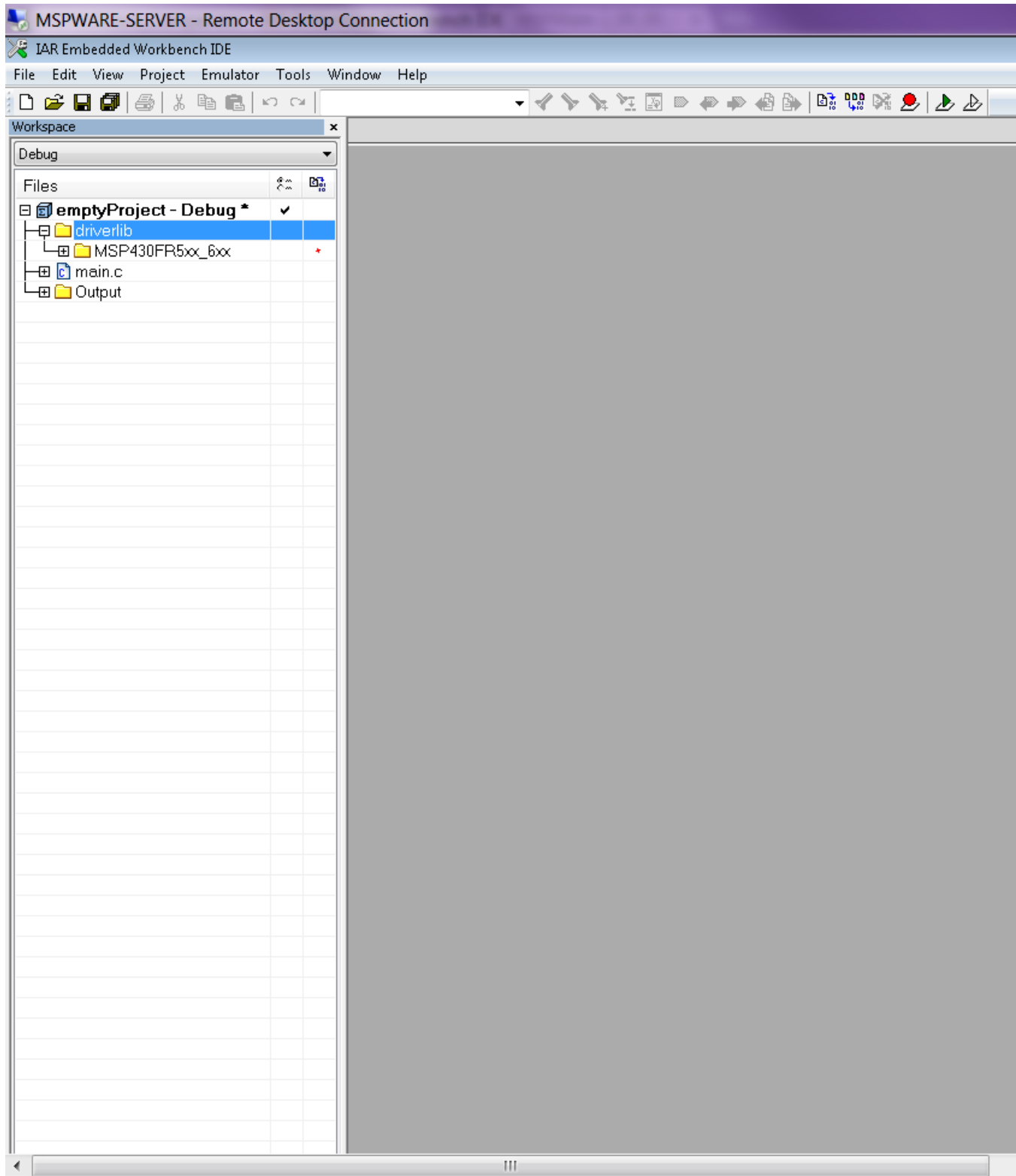
# 5 How to create a new IAR project that uses Driverlib

## 5.1 Introduction

It is recommended to get started with an Empty Driverlib Project. Browse to the empty project in your device's family. This is available in the driverlib instal folder\00\_emptyProject



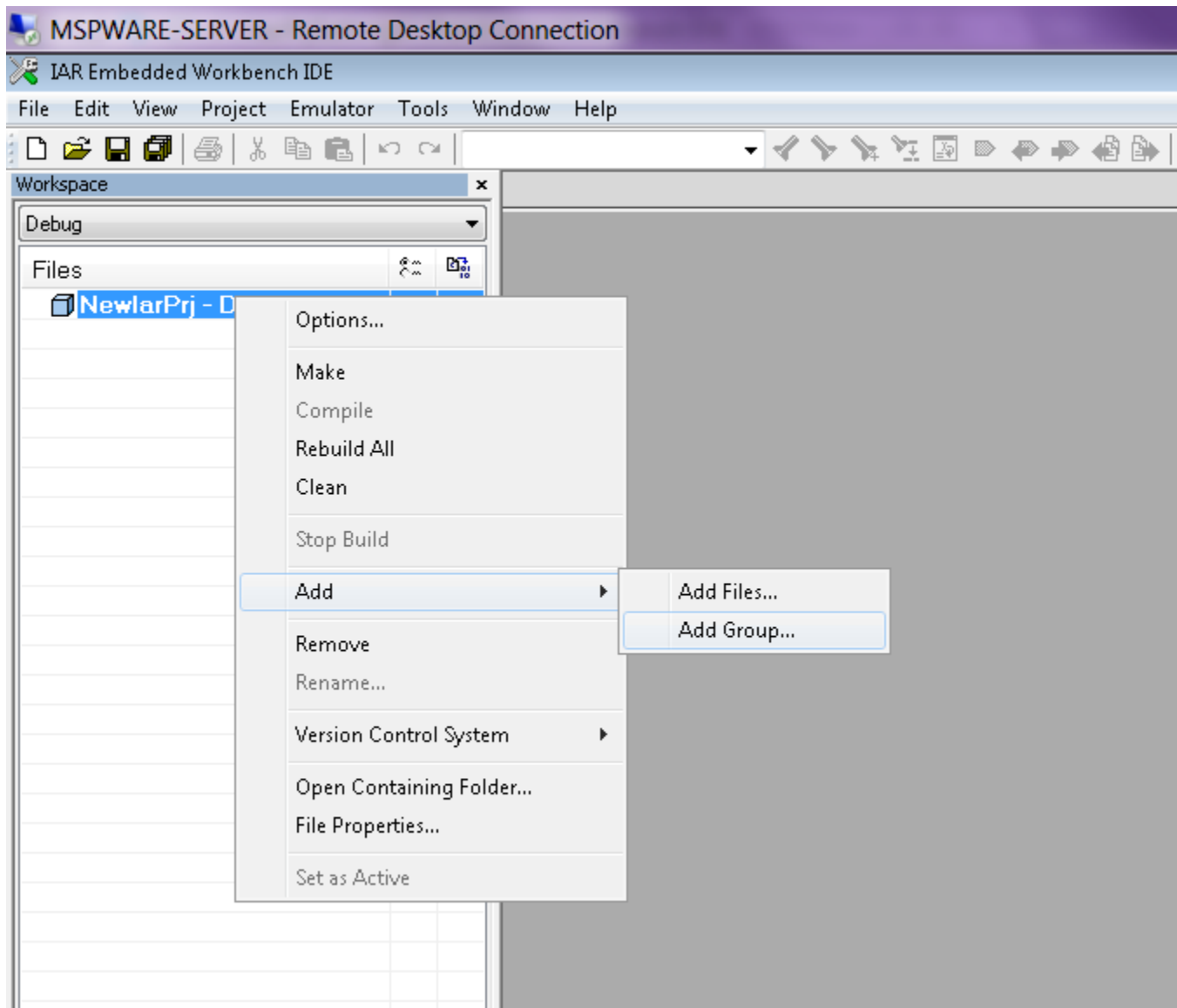




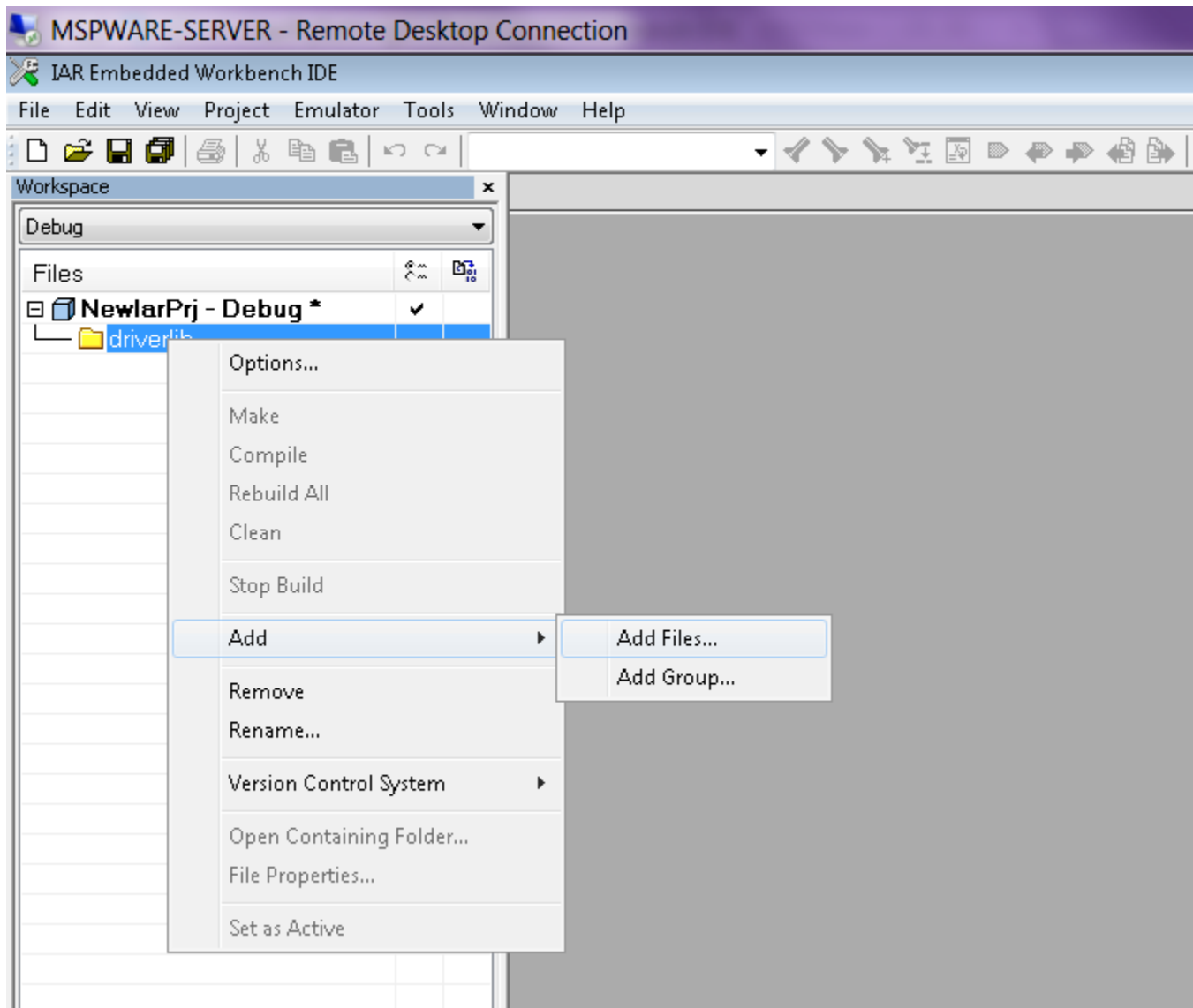
# 6 How to include driverlib into your existing IAR project

## 6.1 Introduction

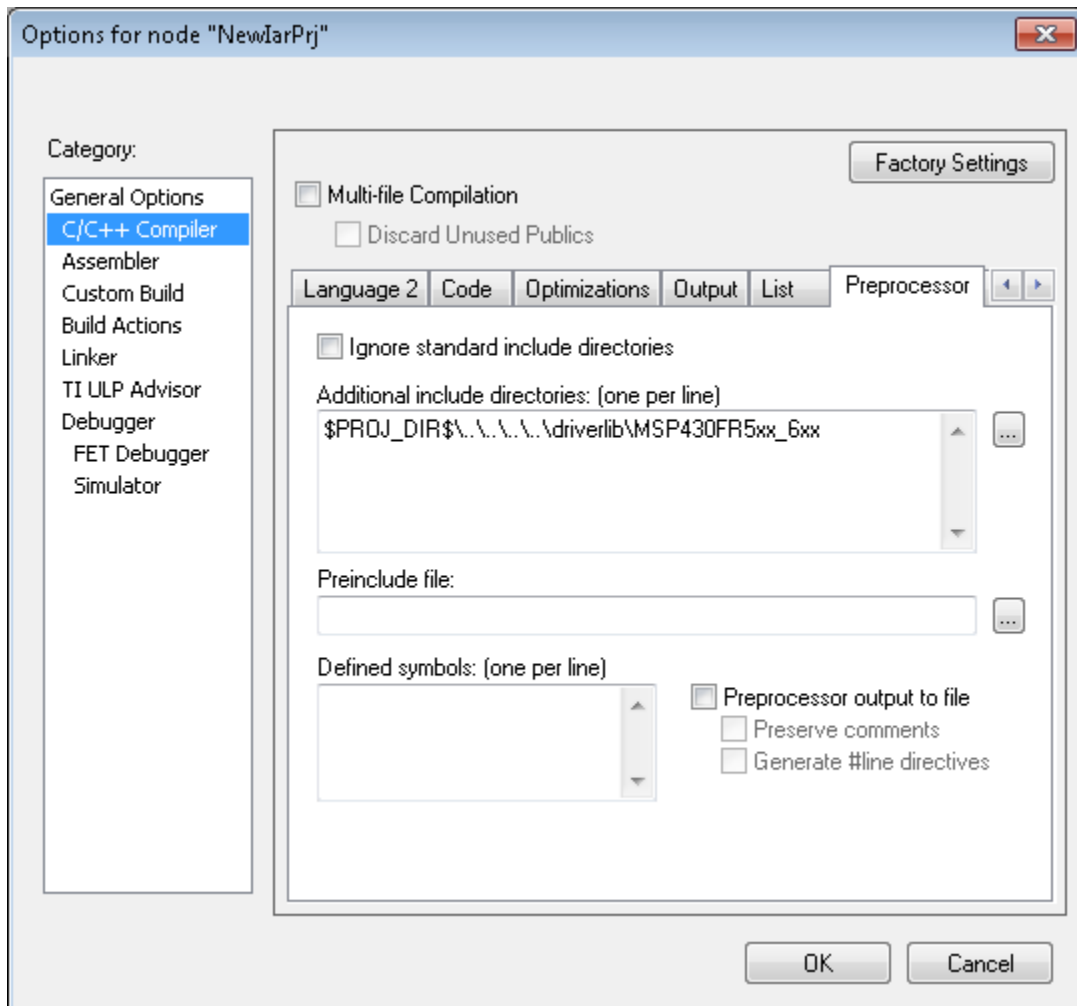
To add driver library to an existing project, right click project click on Add Group - "driverlib"



Now click Add files and browse through driverlib folder and add all source files of the family the device belongs to.



Add another group via "Add Group" and add inc folder. Add all files in the same driverlib family inc folder



Click "Finish" and start developing with driver library in your project.



# 7 10-Bit Analog-to-Digital Converter (ADC10\_B)

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## 7.1 Introduction

The 10-Bit Analog-to-Digital (ADC10.B) API provides a set of functions for using the MSP430Ware ADC10.B modules. Functions are provided to initialize the ADC10.B modules, setup signal sources and reference voltages, and manage interrupts for the ADC10.B modules.

The ADC10.B module supports fast 10-bit analog-to-digital conversions. The module implements a 10-bit SAR core together, sample select control and a window comparator.

ADC10.B features include:

- Greater than 200-ksps maximum conversion rate
- Monotonic 10-bit converter with no missing codes
- Sample-and-hold with programmable sampling periods controlled by software or timers
- Conversion initiation by software or different timers
- Software-selectable on chip reference using the REF module or external reference
- Twelve individually configurable external input channels
- Conversion channel for temperature sensor of the REF module
- Selectable conversion clock source
- Single-channel, repeat-single-channel, sequence, and repeat-sequence conversion modes
- Window comparator for low-power monitoring of input signals
- Interrupt vector register for fast decoding of six ADC interrupts (ADC10IFG0, ADC10TOVIFG, ADC10OVIFG, ADC10LOIFG, ADC10INIFG, ADC10HIIFG)

## 7.2 API Functions

### Functions

- bool [ADC10\\_B\\_init](#) (uint16\_t baseAddress, uint16\_t sampleHoldSignalSourceSelect, uint8\_t clockSourceSelect, uint16\_t clockSourceDivider)  
*Initializes the ADC10B Module.*
- void [ADC10\\_B\\_enable](#) (uint16\_t baseAddress)  
*Enables the ADC10B block.*
- void [ADC10\\_B\\_disable](#) (uint16\_t baseAddress)  
*Disables the ADC10B block.*
- void [ADC10\\_B\\_setupSamplingTimer](#) (uint16\_t baseAddress, uint16\_t clockCycleHoldCount, uint16\_t multipleSamplesEnabled)

- Sets up and enables the Sampling Timer Pulse Mode.*
- void [ADC10\\_B\\_disableSamplingTimer](#) (uint16\_t baseAddress)
  - Disables Sampling Timer Pulse Mode.*
- void [ADC10\\_B\\_configureMemory](#) (uint16\_t baseAddress, uint8\_t inputSourceSelect, uint8\_t positiveRefVoltageSourceSelect, uint8\_t negativeRefVoltageSourceSelect)
  - Configures the controls of the selected memory buffer.*
- void [ADC10\\_B\\_enableInterrupt](#) (uint16\_t baseAddress, uint8\_t interruptMask)
  - Enables selected ADC10B interrupt sources.*
- void [ADC10\\_B\\_disableInterrupt](#) (uint16\_t baseAddress, uint8\_t interruptMask)
  - Disables selected ADC10B interrupt sources.*
- void [ADC10\\_B\\_clearInterrupt](#) (uint16\_t baseAddress, uint8\_t interruptFlagMask)
  - Clears ADC10B selected interrupt flags.*
- uint8\_t [ADC10\\_B\\_getInterruptStatus](#) (uint16\_t baseAddress, uint8\_t interruptFlagMask)
  - Returns the status of the selected memory interrupt flags.*
- void [ADC10\\_B\\_startConversion](#) (uint16\_t baseAddress, uint8\_t conversionSequenceModeSelect)
  - Enables/Starts an Analog-to-Digital Conversion.*
- void [ADC10\\_B\\_disableConversions](#) (uint16\_t baseAddress, bool preempt)
  - Disables the ADC from converting any more signals.*
- uint16\_t [ADC10\\_B\\_getResults](#) (uint16\_t baseAddress)
  - Returns the raw contents of the specified memory buffer.*
- void [ADC10\\_B\\_setResolution](#) (uint16\_t baseAddress, uint8\_t resolutionSelect)
  - Use to change the resolution of the converted data.*
- void [ADC10\\_B\\_setSampleHoldSignalInversion](#) (uint16\_t baseAddress, uint16\_t invertedSignal)
  - Use to invert or un-invert the sample/hold signal.*
- void [ADC10\\_B\\_setDataReadBackFormat](#) (uint16\_t baseAddress, uint16\_t readBackFormat)
  - Use to set the read-back format of the converted data.*
- void [ADC10\\_B\\_enableReferenceBurst](#) (uint16\_t baseAddress)
  - Enables the reference buffer's burst ability.*
- void [ADC10\\_B\\_disableReferenceBurst](#) (uint16\_t baseAddress)
  - Disables the reference buffer's burst ability.*
- void [ADC10\\_B\\_setReferenceBufferSamplingRate](#) (uint16\_t baseAddress, uint16\_t samplingRateSelect)
  - Use to set the reference buffer's sampling rate.*
- void [ADC10\\_B\\_setWindowComp](#) (uint16\_t baseAddress, uint16\_t highThreshold, uint16\_t lowThreshold)
  - Sets the high and low threshold for the window comparator feature.*
- uint32\_t [ADC10\\_B\\_getMemoryAddressForDMA](#) (uint16\_t baseAddress)
  - Returns the address of the memory buffer for the DMA module.*
- uint8\_t [ADC10\\_B\\_isBusy](#) (uint16\_t baseAddress)
  - Returns the busy status of the ADC10B core.*

## 7.2.1 Detailed Description

The ADC10.B API is broken into three groups of functions: those that deal with initialization and conversions, those that handle interrupts, and those that handle Auxiliary features of the ADC10.

The ADC10.B initialization and conversion functions are

- [ADC10.B.init\(\)](#)
- [ADC10.B.configureMemory\(\)](#)

- `ADC10.B.setupSamplingTimer()`
- `ADC10.B.disableSamplingTimer()`
- `ADC10.B.setWindowComp()`
- `ADC10.B.startConversion()`
- `ADC10.B.disableConversions()`
- `ADC10.B.getResults()`
- `ADC10.B.isBusy()`

The ADC10.B interrupts are handled by

- `ADC10.B.enableInterrupt()`
- `ADC10.B.disableInterrupt()`
- `ADC10.B.clearInterrupt()`
- `ADC10.B.getInterruptStatus()`

Auxiliary features of the ADC10.B are handled by

- `ADC10.B.setResolution()`
- `ADC10.B.setSampleHoldSignalInversion()`
- `ADC10.B.setDataReadBackFormat()`
- `ADC10.B.enableReferenceBurst()`
- `ADC10.B.disableReferenceBurst()`
- `ADC10.B.setReferenceBufferSamplingRate()`
- `ADC10.B.getMemoryAddressForDMA()`
- `ADC10.B.enable()`
- `ADC10.B.disable()`

## 7.2.2 Function Documentation

```
void ADC10.B.clearInterrupt ( uint16_t baseAddress, uint8_t interruptFlagMask )
```

Clears ADC10B selected interrupt flags.

The selected ADC10B interrupt flags are cleared, so that it no longer asserts. The memory buffer interrupt flags are only cleared when the memory buffer is accessed.

## Parameters

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>interruptFlagMask</i>	is a bit mask of the interrupt flags to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_OVIFG</b> - Interrupt flag for when a new conversion is about to overwrite the previous one</li> <li>■ <b>ADC10_B_TOVIFG</b> - Interrupt flag for when a new conversion is starting before the previous one has finished</li> <li>■ <b>ADC10_B_HIIFG</b> - Interrupt flag for when the input signal has gone above the high threshold of the window comparator</li> <li>■ <b>ADC10_B_LOIFG</b> - Interrupt flag for when the input signal has gone below the low threshold of the window comparator</li> <li>■ <b>ADC10_B_INIFG</b> - Interrupt flag for when the input signal is in between the high and low thresholds of the window comparator</li> <li>■ <b>ADC10_B_IFGO</b> - Interrupt flag for new conversion data in the memory buffer</li> </ul>

Modified bits of **ADC10IFG** register.

## Returns

None

```
void ADC10_B_configureMemory ( uint16_t baseAddress, uint8_t inputSourceSelect, uint8_t
    positiveRefVoltageSourceSelect, uint8_t negativeRefVoltageSourceSelect )
```

Configures the controls of the selected memory buffer.

Maps an input signal conversion into the memory buffer, as well as the positive and negative reference voltages for each conversion being stored into the memory buffer. If the internal reference is used for the positive reference voltage, the internal REF module has to control the voltage level. Note that if a conversion has been started with the startConversion() function, then a call to disableConversions() is required before this function may be called. If conversion is not disabled, this function does nothing.

## Parameters

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>inputSource</i> ↔ <i>Select</i>	<p>is the input that will store the converted data into the specified memory buffer. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_B_INPUT_A0</b> [Default]</li> <li>■ <b>ADC10_B_INPUT_A1</b></li> <li>■ <b>ADC10_B_INPUT_A2</b></li> <li>■ <b>ADC10_B_INPUT_A3</b></li> <li>■ <b>ADC10_B_INPUT_A4</b></li> <li>■ <b>ADC10_B_INPUT_A5</b></li> <li>■ <b>ADC10_B_INPUT_A6</b></li> <li>■ <b>ADC10_B_INPUT_A7</b></li> <li>■ <b>ADC10_B_INPUT_VREF_P</b></li> <li>■ <b>ADC10_B_INPUT_VREF_N</b></li> <li>■ <b>ADC10_B_INPUT_TEMPSENSOR</b></li> <li>■ <b>ADC10_B_INPUT_BATTERYMONITOR</b></li> <li>■ <b>ADC10_B_INPUT_A12</b></li> <li>■ <b>ADC10_B_INPUT_A13</b></li> <li>■ <b>ADC10_B_INPUT_A14</b></li> <li>■ <b>ADC10_B_INPUT_A15</b></li> </ul> <p>Modified bits are <b>ADC10INCHx</b> of <b>ADC10MCTL0</b> register.</p>
<i>positiveRef</i> ↔ <i>Voltage</i> ↔ <i>SourceSelect</i>	<p>is the reference voltage source to set as the upper limit for the conversion that is to be stored in the specified memory buffer. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_B_VREFPOS_AVCC</b> [Default]</li> <li>■ <b>ADC10_B_VREFPOS_EXT</b></li> <li>■ <b>ADC10_B_VREFPOS_INT</b></li> </ul> <p>Modified bits are <b>ADC10SREF</b> of <b>ADC10MCTL0</b> register.</p>
<i>negativeRef</i> ↔ <i>Voltage</i> ↔ <i>SourceSelect</i>	<p>is the reference voltage source to set as the lower limit for the conversion that is to be stored in the specified memory buffer. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_B_VREFNEG_AVSS</b> [Default]</li> <li>■ <b>ADC10_B_VREFNEG_EXT</b></li> </ul> <p>Modified bits are <b>ADC10SREF</b> of <b>ADC10MCTL0</b> register.</p>

**Returns**

None

```
void ADC10_B_disable ( uint16_t baseAddress )
```

Disables the ADC10B block.

This will disable operation of the ADC10B block.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10B module.
--------------------	---

Modified bits are **ADC10ON** of **ADC10CTL0** register.

**Returns**

None

```
void ADC10_B_disableConversions ( uint16_t baseAddress, bool preempt )
```

Disables the ADC from converting any more signals.

Disables the ADC from converting any more signals. If there is a conversion in progress, this function can stop it immediately if the *preempt* parameter is set as **ADC10\_B\_PREEMPTCONVERSION**, by changing the conversion mode to single-channel, single-conversion and disabling conversions. If the conversion mode is set as single-channel, single-conversion and this function is called without preemption, then the ADC core conversion status is polled until the conversion is complete before disabling conversions to prevent unpredictable data. If the **ADC10CTL1** and **ADC10CTL0**

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>preempt</i>	<p>specifies if the current conversion should be pre-empted stopped before the end of the conversion Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_B_COMPLETECONVERSION</b> - Allows the ADC10B to end the current conversion before disabling conversions.</li> <li>■ <b>ADC10_B_PREEMPTCONVERSION</b> - Stops the ADC10B immediately, with unpredictable results of the current conversion. Cannot be used with repeated conversion.</li> </ul>

Modified bits of **ADC10CTL1** register and bits of **ADC10CTL0** register.

**Returns**

None

```
void ADC10_B_disableInterrupt ( uint16_t baseAddress, uint8_t interruptMask )
```

Disables selected ADC10B interrupt sources.

Disables the indicated ADC10B interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>interruptMask</i>	is the bit mask of the memory buffer interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_OVIE</b> - Interrupts when a new conversion is about to overwrite the previous one</li> <li>■ <b>ADC10_B_TOVIE</b> - Interrupts when a new conversion is starting before the previous one has finished</li> <li>■ <b>ADC10_B_HIIE</b> - Interrupts when the input signal has gone above the high threshold of the window comparator</li> <li>■ <b>ADC10_B_LOIE</b> - Interrupts when the input signal has gone below the low threshold of the low window comparator</li> <li>■ <b>ADC10_B_INIE</b> - Interrupts when the input signal is in between the high and low thresholds of the window comparator</li> <li>■ <b>ADC10_B_IE0</b> - Interrupt for new conversion data in the memory buffer</li> </ul>

Modified bits of **ADC10IE** register.

#### Returns

None

```
void ADC10_B_disableReferenceBurst ( uint16_t baseAddress )
```

Disables the reference buffer's burst ability.

Disables the reference buffer's burst ability, forcing the reference buffer to remain on continuously.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC10B module.
--------------------	---

Modified bits are **ADC10REFBURST** of **ADC10CTL2** register.

#### Returns

None

```
void ADC10_B_disableSamplingTimer ( uint16_t baseAddress )
```

Disables Sampling Timer Pulse Mode.

Disables the Sampling Timer Pulse Mode. Note that if a conversion has been started with the `startConversion()` function, then a call to `disableConversions()` is required before this function may

be called.



**Parameters**

<i>baseAddress</i>	is the base address of the ADC10B module.
--------------------	---

Modified bits are **ADC10SHP** of **ADC10CTL1** register.

**Returns**

None

```
void ADC10_B_enable ( uint16_t baseAddress )
```

Enables the ADC10B block.

This will enable operation of the ADC10B block.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10B module.
--------------------	---

Modified bits are **ADC10ON** of **ADC10CTL0** register.

**Returns**

None

```
void ADC10_B_enableInterrupt ( uint16_t baseAddress, uint8_t interruptMask )
```

Enables selected ADC10B interrupt sources.

Enables the indicated ADC10B interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>interruptMask</i>	is the bit mask of the memory buffer interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_OVIE</b> - Interrupts when a new conversion is about to overwrite the previous one</li> <li>■ <b>ADC10_B_TOVIE</b> - Interrupts when a new conversion is starting before the previous one has finished</li> <li>■ <b>ADC10_B_HIIE</b> - Interrupts when the input signal has gone above the high threshold of the window comparator</li> <li>■ <b>ADC10_B_LOIE</b> - Interrupts when the input signal has gone below the low threshold of the low window comparator</li> <li>■ <b>ADC10_B_INIE</b> - Interrupts when the input signal is in between the high and low thresholds of the window comparator</li> <li>■ <b>ADC10_B_IE0</b> - Interrupt for new conversion data in the memory buffer</li> </ul>

Modified bits of **ADC10IE** register.

**Returns**

None

```
void ADC10_B_enableReferenceBurst ( uint16_t baseAddress )
```

Enables the reference buffer's burst ability.

Enables the reference buffer's burst ability, allowing the reference buffer to turn off while the ADC is not converting, and automatically turning on when the ADC needs the generated reference voltage for a conversion.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10B module.
--------------------	---

Modified bits are **ADC10REFBURST** of **ADC10CTL2** register.

**Returns**

None

```
uint8_t ADC10_B_getInterruptStatus ( uint16_t baseAddress, uint8_t interruptFlagMask )
```

Returns the status of the selected memory interrupt flags.

Returns the status of the selected interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>interruptFlagMask</i>	is a bit mask of the interrupt flags status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_OVIFG</b> - Interrupt flag for when a new conversion is about to overwrite the previous one</li> <li>■ <b>ADC10_B_TOVIFG</b> - Interrupt flag for when a new conversion is starting before the previous one has finished</li> <li>■ <b>ADC10_B_HIIFG</b> - Interrupt flag for when the input signal has gone above the high threshold of the window comparator</li> <li>■ <b>ADC10_B_LOIFG</b> - Interrupt flag for when the input signal has gone below the low threshold of the window comparator</li> <li>■ <b>ADC10_B_INIFG</b> - Interrupt flag for when the input signal is in between the high and low thresholds of the window comparator</li> <li>■ <b>ADC10_B_IFG0</b> - Interrupt flag for new conversion data in the memory buffer</li> </ul>

Modified bits of **ADC10IFG** register.

**Returns**

The current interrupt flag status for the corresponding mask.

`uint32_t ADC10_B_getMemoryAddressForDMA ( uint16_t baseAddress )`

Returns the address of the memory buffer for the DMA module.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10B module.
--------------------	---

**Returns**

Returns the address of the memory buffer. This can be used in conjunction with the DMA to store the converted data directly to memory.

`uint16_t ADC10_B_getResults ( uint16_t baseAddress )`

Returns the raw contents of the specified memory buffer.

Returns the raw contents of the specified memory buffer. The format of the content depends on the read-back format of the data: if the data is in signed 2's complement format then the contents in the memory buffer will be left-justified with the least-significant bits as 0's, whereas if the data is in unsigned format then the contents in the memory buffer will be right-justified with the most-significant bits as 0's.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10B module.
--------------------	---

**Returns**

A Signed Integer of the contents of the specified memory buffer.

`bool ADC10_B_init ( uint16_t baseAddress, uint16_t sampleHoldSignalSourceSelect,  
uint8_t clockSourceSelect, uint16_t clockSourceDivider )`

Initializes the ADC10B Module.

This function initializes the ADC module to allow for analog-to-digital conversions. Specifically this function sets up the sample-and-hold signal and clock sources for the ADC core to use for conversions. Upon successful completion of the initialization all of the ADC control registers will be reset, excluding the memory controls and reference module bits, the given parameters will be set, and the ADC core will be turned on (Note, that the ADC core only draws power during conversions and remains off when not converting). Note that sample/hold signal sources are device dependent. Note that if re-initializing the ADC after starting a conversion with the `startConversion()` function, the `disableConversion()` must be called BEFORE this function can be called.

## Parameters

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>sampleHold</i> ↔ <i>SignalSource</i> ↔ <i>Select</i>	is the signal that will trigger a sample-and-hold for an input signal to be converted. This parameter is device specific and sources should be found in the device's datasheet. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_SAMPLEHOLDSOURCE_SC</b> [Default]</li> <li>■ <b>ADC10_B_SAMPLEHOLDSOURCE_1</b></li> <li>■ <b>ADC10_B_SAMPLEHOLDSOURCE_2</b></li> <li>■ <b>ADC10_B_SAMPLEHOLDSOURCE_3</b></li> </ul> Modified bits are <b>ADC10SHSx</b> of <b>ADC10CTL1</b> register.
<i>clockSource</i> ↔ <i>Select</i>	selects the clock that will be used by the ADC10B core and the sampling timer if a sampling pulse mode is enabled. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_CLOCKSOURCE_ADC10OSC</b> [Default] - MODOSC 5 MHz oscillator from the clock system</li> <li>■ <b>ADC10_B_CLOCKSOURCE_ACLK</b> - The Auxiliary Clock</li> <li>■ <b>ADC10_B_CLOCKSOURCE_MCLK</b> - The Master Clock</li> <li>■ <b>ADC10_B_CLOCKSOURCE_SMCLK</b> - The Sub-Master Clock</li> </ul> Modified bits are <b>ADC10SSELx</b> of <b>ADC10CTL1</b> register.
<i>clockSource</i> ↔ <i>Divider</i>	selects the amount that the clock will be divided. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_CLOCKDIVIDER_1</b> [Default]</li> <li>■ <b>ADC10_B_CLOCKDIVIDER_2</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_3</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_4</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_5</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_6</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_7</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_8</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_12</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_16</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_20</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_24</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_28</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_32</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_64</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_128</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_192</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_256</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_320</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_384</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_448</b></li> <li>■ <b>ADC10_B_CLOCKDIVIDER_512</b></li> </ul> Modified bits are <b>ADC10DIVx</b> of <b>ADC10CTL1</b> register; bits <b>ADC10PDIVx</b> of <b>ADC10CTL2</b> register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the initialization process.

```
uint8_t ADC10_B_isBusy ( uint16_t baseAddress )
```

Returns the busy status of the ADC10B core.

Returns the status of the ADC core if there is a conversion currently taking place.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10B module.
--------------------	---

**Returns**

ADC10\_B\_BUSY or ADC10\_B\_NOTBUSY dependent if there is a conversion currently taking place. Return one of the following:

- **ADC10\_B\_NOTBUSY**
- **ADC10\_B\_BUSY**

```
void ADC10_B_setDataReadBackFormat ( uint16_t baseAddress, uint16_t readBackFormat )
```

Use to set the read-back format of the converted data.

Sets the format of the converted data: how it will be stored into the memory buffer, and how it should be read back. The format can be set as right-justified (default), which indicates that the number will be unsigned, or left-justified, which indicates that the number will be signed in 2's complement format. This change affects all memory buffers for subsequent conversions.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>readBack↔ Format</i>	is the specified format to store the conversions in the memory buffer. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_UNSIGNED_BINARY</b> [Default]</li> <li>■ <b>ADC10_B_SIGNED_2SCOMPLEMENT</b> Modified bits are <b>ADC10DF</b> of <b>ADC10CTL2</b> register.</li> </ul>

**Returns**

None

```
void ADC10_B_setReferenceBufferSamplingRate ( uint16_t baseAddress, uint16_t samplingRateSelect )
```

Use to set the reference buffer's sampling rate.

Sets the reference buffer's sampling rate to the selected sampling rate. The default sampling rate is maximum of 200-kSPS, and can be reduced to a maximum of 50-kSPS to conserve power.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>samplingRateSelect</i>	is the specified maximum sampling rate. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_MAXSAMPLINGRATE_200KSPS</b> [Default]</li> <li>■ <b>ADC10_B_MAXSAMPLINGRATE_50KSPS</b></li> </ul> Modified bits are <b>ADC10SR</b> of <b>ADC10CTL2</b> register.

Modified bits of **ADC10CTL2** register.

#### Returns

None

```
void ADC10_B_setResolution ( uint16_t baseAddress, uint8_t resolutionSelect )
```

Use to change the resolution of the converted data.

This function can be used to change the resolution of the converted data from the default of 12-bits.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>resolutionSelect</i>	determines the resolution of the converted data. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_RESOLUTION_8BIT</b></li> <li>■ <b>ADC10_B_RESOLUTION_10BIT</b> [Default]</li> </ul> Modified bits are <b>ADC10RES</b> of <b>ADC10CTL2</b> register.

#### Returns

None

```
void ADC10_B_setSampleHoldSignalInversion ( uint16_t baseAddress, uint16_t invertedSignal )
```

Use to invert or un-invert the sample/hold signal.

This function can be used to invert or un-invert the sample/hold signal. Note that if a conversion has been started with the `startConversion()` function, then a call to `disableConversions()` is required before this function may be called.

**Parameters**

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>invertedSignal</i>	set if the sample/hold signal should be inverted Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_NONINVERTEDSIGNAL</b> [Default] - a sample-and-hold of an input signal for conversion will be started on a rising edge of the sample/hold signal.</li> <li>■ <b>ADC10_B_INVERTEDSIGNAL</b> - a sample-and-hold of an input signal for conversion will be started on a falling edge of the sample/hold signal.</li> </ul> Modified bits are <b>ADC10ISSH</b> of <b>ADC10CTL1</b> register.

**Returns**

None

```
void ADC10_B_setupSamplingTimer ( uint16_t baseAddress, uint16_t clockCycleHoldCount,
uint16_t multipleSamplesEnabled )
```

Sets up and enables the Sampling Timer Pulse Mode.

This function sets up the sampling timer pulse mode which allows the sample/hold signal to trigger a sampling timer to sample-and-hold an input signal for a specified number of clock cycles without having to hold the sample/hold signal for the entire period of sampling. Note that if a conversion has been started with the startConversion() function, then a call to disableConversions() is required before this function may be called.



## Parameters

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>clockCycle↔</i> <i>HoldCount</i>	sets the amount of clock cycles to sample-and- hold for the memory buffer. Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_CYCLEHOLD_4_CYCLES</b> [Default]</li> <li>■ <b>ADC10_B_CYCLEHOLD_8_CYCLES</b></li> <li>■ <b>ADC10_B_CYCLEHOLD_16_CYCLES</b></li> <li>■ <b>ADC10_B_CYCLEHOLD_32_CYCLES</b></li> <li>■ <b>ADC10_B_CYCLEHOLD_64_CYCLES</b></li> <li>■ <b>ADC10_B_CYCLEHOLD_96_CYCLES</b></li> <li>■ <b>ADC10_B_CYCLEHOLD_128_CYCLES</b></li> <li>■ <b>ADC10_B_CYCLEHOLD_192_CYCLES</b></li> <li>■ <b>ADC10_B_CYCLEHOLD_256_CYCLES</b></li> <li>■ <b>ADC10_B_CYCLEHOLD_384_CYCLES</b></li> <li>■ <b>ADC10_B_CYCLEHOLD_512_CYCLES</b></li> <li>■ <b>ADC10_B_CYCLEHOLD_768_CYCLES</b></li> <li>■ <b>ADC10_B_CYCLEHOLD_1024_CYCLES</b></li> </ul> Modified bits are <b>ADC10SHTx</b> of <b>ADC10CTL0</b> register.
<i>multiple↔</i> <i>Samples↔</i> <i>Enabled</i>	allows multiple conversions to start without a trigger signal from the sample/hold signal Valid values are: <ul style="list-style-type: none"> <li>■ <b>ADC10_B_MULTIPLESAMPLESDISABLE</b> - a timer trigger will be needed to start every ADC conversion.</li> <li>■ <b>ADC10_B_MULTIPLESAMPLESENABLE</b> - during a sequenced and/or repeated conversion mode, after the first conversion, no sample/hold signal is necessary to start subsequent samples.</li> </ul> Modified bits are <b>ADC10MSC</b> of <b>ADC10CTL0</b> register.

## Returns

None

```
void ADC10_B_setWindowComp ( uint16_t baseAddress, uint16_t highThreshold, uint16_t lowThreshold )
```

Sets the high and low threshold for the window comparator feature.

Sets the high and low threshold for the window comparator feature. Use the ADC10HIIE, ADC10INIE, ADC10LOIE interrupts to utilize this feature.

## Parameters

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>highThreshold</i>	is the upper bound that could trip an interrupt for the window comparator.
<i>lowThreshold</i>	is the lower bound that could trip on interrupt for the window comparator.

Modified bits of **ADC10LO** register and bits of **ADC10HI** register.

#### Returns

None

```
void ADC10_B_startConversion ( uint16_t baseAddress, uint8_t conversionSequence↵
ModeSelect )
```

Enables/Starts an Analog-to-Digital Conversion.

This function enables/starts the conversion process of the ADC. If the sample/hold signal source chosen during initialization was ADC10OSC, then the conversion is started immediately, otherwise the chosen sample/hold signal source starts the conversion by a rising edge of the signal. Keep in mind when selecting conversion modes, that for sequenced and/or repeated modes, to keep the sample/hold-and-convert process continuing without a trigger from the sample/hold signal source, the multiple samples must be enabled using the [ADC10\\_B\\_setupSamplingTimer\(\)](#) function. Also note that when a sequence conversion mode is selected, the first input channel is the one mapped to the memory buffer, the next input channel selected for conversion is one less than the input channel just converted (i.e. A1 comes after A2), until A0 is reached, and if in repeating mode, then the next input channel will again be the one mapped to the memory buffer. Note that after this function is called, the `ADC10_B_stopConversions()` has to be called to re-initialize the ADC, reconfigure a memory buffer control, enable/disable the sampling timer, or to change the internal reference voltage.

#### Parameters

<i>baseAddress</i>	is the base address of the ADC10B module.
<i>conversion↵ Sequence↵ ModeSelect</i>	<p>determines the ADC operating mode. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>ADC10_B_SINGLECHANNEL</b> [Default] - one-time conversion of a single channel into a single memory buffer</li> <li>■ <b>ADC10_B_SEQOFCHANNELS</b> - one time conversion of multiple channels into the specified starting memory buffer and each subsequent memory buffer up until the conversion is stored in a memory buffer dedicated as the end-of-sequence by the memory's control register</li> <li>■ <b>ADC10_B_REPEATED_SINGLECHANNEL</b> - repeated conversions of one channel into a single memory buffer</li> <li>■ <b>ADC10_B_REPEATED_SEQOFCHANNELS</b> - repeated conversions of multiple channels into the specified starting memory buffer and each subsequent memory buffer up until the conversion is stored in a memory buffer dedicated as the end-of-sequence by the memory's control register</li> </ul> <p>Modified bits are <b>ADC10CONSEQx</b> of <b>ADC10CTL1</b> register.</p>

#### Returns

None

## 7.3 Programming Example

The following example shows how to initialize and use the ADC10.B API to start a single channel, single conversion.

```
// Initialize ADC10.B with ADC10.B's built-in oscillator
ADC10_B_init (ADC10_B_BASE,
             ADC10_B_SAMPLEHOLDSOURCE_SC,
             ADC10_B_CLOCKSOURCE_ADC10OSC,
             ADC10_B_CLOCKDIVIDEBY_1);

//Switch ON ADC10.B
ADC10_B_enable(ADC10_B_BASE);

// Setup sampling timer to sample-and-hold for 16 clock cycles
ADC10_B_setupSamplingTimer (ADC10_B_BASE,
                           ADC10_B_CYCLEHOLD_16_CYCLES,
                           FALSE);

// Configure the Input to the Memory Buffer with the specified Reference Voltages
ADC10_B_configureMemory(ADC10_B_BASE,
                      ADC10_B_INPUT_A0,
                      ADC10_B_VREFPOS_AVCC, // Vref+ = AVcc
                      ADC10_B_VREFNEG_AVSS  // Vref- = AVss
                      );

while (1)
{
    // Start a single conversion, no repeating or sequences.
    ADC10_B_startConversion (ADC10_B_BASE,
                           ADC10_B_SINGLECHANNEL);

    // Wait for the Interrupt Flag to assert
    while( !(ADC10_B_getInterruptStatus(ADC10_B_BASE,ADC10_IFG0) ) );

    // Clear the Interrupt Flag and start another conversion
    ADC10_B_clearInterrupt(ADC10_B_BASE,ADC10_IFG0);
}
```

## 8 Comparator (COMP\_D)

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### 8.1 Introduction

The Comparator D (Comp\_D) API provides a set of functions for using the MSP430Ware Comp\_D modules. Functions are provided to initialize the Comp\_D modules, setup reference voltages for input, and manage interrupts for the Comp\_D modules.

The Comp\_D module provides the ability to compare two analog signals and use the output in software and on an output pin. The output represents whether the signal on the positive terminal is higher than the signal on the negative terminal. The Comp\_D may be used to generate a hysteresis. There are 16 different inputs that can be used, as well as the ability to short 2 input together. The Comp\_D module also has control over the REF module to generate a reference voltage as an input.

The Comp\_D module can generate multiple interrupts. An interrupt may be asserted for the output, with separate interrupts on whether the output rises, or falls.

### 8.2 API Functions

#### Functions

- bool [Comp\\_D\\_init](#) (uint16\_t baseAddress, [Comp\\_D\\_initParam](#) \*param)  
*Initializes the Comp\_D Module.*
- void [Comp\\_D\\_setReferenceVoltage](#) (uint16\_t baseAddress, uint16\_t supplyVoltageReferenceBase, uint16\_t lowerLimitSupplyVoltageFractionOf32, uint16\_t upperLimitSupplyVoltageFractionOf32)  
*Generates a Reference Voltage to the terminal selected during initialization.*
- void [Comp\\_D\\_setReferenceAccuracy](#) (uint16\_t baseAddress, uint16\_t referenceAccuracy)  
*Sets the reference accuracy.*
- void [Comp\\_D\\_enableInterrupt](#) (uint16\_t baseAddress, uint16\_t interruptMask)  
*Enables selected Comparator interrupt sources.*
- void [Comp\\_D\\_disableInterrupt](#) (uint16\_t baseAddress, uint16\_t interruptMask)  
*Disables selected Comparator interrupt sources.*
- void [Comp\\_D\\_clearInterrupt](#) (uint16\_t baseAddress, uint16\_t interruptFlagMask)  
*Clears Comparator interrupt flags.*
- uint8\_t [Comp\\_D\\_getInterruptStatus](#) (uint16\_t baseAddress, uint16\_t interruptFlagMask)  
*Gets the current Comparator interrupt status.*
- void [Comp\\_D\\_setInterruptEdgeDirection](#) (uint16\_t baseAddress, uint16\_t edgeDirection)  
*Explicitly sets the edge direction that would trigger an interrupt.*
- void [Comp\\_D\\_toggleInterruptEdgeDirection](#) (uint16\_t baseAddress)  
*Toggles the edge direction that would trigger an interrupt.*
- void [Comp\\_D\\_enable](#) (uint16\_t baseAddress)  
*Turns on the Comparator module.*

- void `Comp_D_disable` (uint16\_t baseAddress)  
*Turns off the Comparator module.*
- void `Comp_D_shortInputs` (uint16\_t baseAddress)  
*Shorts the two input pins chosen during initialization.*
- void `Comp_D_unshortInputs` (uint16\_t baseAddress)  
*Disables the short of the two input pins chosen during initialization.*
- void `Comp_D_disableInputBuffer` (uint16\_t baseAddress, uint8\_t inputPort)  
*Disables the input buffer of the selected input port to effectively allow for analog signals.*
- void `Comp_D_enableInputBuffer` (uint16\_t baseAddress, uint8\_t inputPort)  
*Enables the input buffer of the selected input port to allow for digital signals.*
- void `Comp_D_swapIO` (uint16\_t baseAddress)  
*Toggles the bit that swaps which terminals the inputs go to, while also inverting the output of the comparator.*
- uint16\_t `Comp_D_outputValue` (uint16\_t baseAddress)  
*Returns the output value of the Comp\_D module.*

### 8.2.1 Detailed Description

The API is broken into three groups of functions: those that deal with initialization and output, those that handle interrupts, and those that handle Auxiliary features of the Comp\_D.

The Comp\_D initialization and output functions are

- `Comp_D_init()`
- `Comp_D_setReferenceVoltage()`
- `Comp_D_enable()`
- `Comp_D_disable()`
- `Comp_D_outputValue()`

The Comp\_D interrupts are handled by

- `Comp_D_enableInterrupt()`
- `Comp_D_disableInterrupt()`
- `Comp_D_clearInterrupt()`
- `Comp_D_getInterruptStatus()`
- `Comp_D_setInterruptEdgeDirection()`
- `Comp_D_toggleInterruptEdgeDirection()`

Auxiliary features of the Comp\_D are handled by

- `Comp_D_enableShortOfInputs()`
- `Comp_D_disableShortOfInputs()`
- `Comp_D_disableInputBuffer()`
- `Comp_D_enableInputBuffer()`
- `Comp_D_swapIO()`
- `Comp_D_setReferenceAccuracy()`

## 8.2.2 Function Documentation

`void Comp_D_clearInterrupt ( uint16_t baseAddress, uint16_t interruptFlagMask )`

Clears Comparator interrupt flags.

The Comparator interrupt source is cleared, so that it no longer asserts. The highest interrupt flag is automatically cleared when an interrupt vector generator is used.

### Parameters

<i>baseAddress</i>	is the base address of the COMP_D module.
<i>interruptFlagMask</i>	Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>COMP_D_INTERRUPT_FLAG</b> - Output interrupt flag</li> <li>■ <b>COMP_D_INTERRUPT_FLAG_INVERTED_POLARITY</b> - Output interrupt flag inverted polarity</li> </ul>

`void Comp_D_disable ( uint16_t baseAddress )`

Turns off the Comparator module.

This function clears the CDON bit disabling the operation of the Comparator module, saving from excess power consumption.

### Parameters

<i>baseAddress</i>	is the base address of the COMP_D module.
--------------------	---

### Returns

None

`void Comp_D_disableInputBuffer ( uint16_t baseAddress, uint8_t inputPort )`

Disables the input buffer of the selected input port to effectively allow for analog signals.

This function sets the bit to disable the buffer for the specified input port to allow for analog signals from any of the comparator input pins. This bit is automatically set when the input is initialized to be used with the comparator module. This function should be used whenever an analog input is connected to one of these pins to prevent parasitic voltage from causing unexpected results.

## Parameters

<i>baseAddress</i>	is the base address of the COMP_D module.
<i>inputPort</i>	is the port in which the input buffer will be disabled. Valid values are: <ul style="list-style-type: none"> <li>■ <b>COMP_D.INPUT0</b> [Default]</li> <li>■ <b>COMP_D.INPUT1</b></li> <li>■ <b>COMP_D.INPUT2</b></li> <li>■ <b>COMP_D.INPUT3</b></li> <li>■ <b>COMP_D.INPUT4</b></li> <li>■ <b>COMP_D.INPUT5</b></li> <li>■ <b>COMP_D.INPUT6</b></li> <li>■ <b>COMP_D.INPUT7</b></li> <li>■ <b>COMP_D.INPUT8</b></li> <li>■ <b>COMP_D.INPUT9</b></li> <li>■ <b>COMP_D.INPUT10</b></li> <li>■ <b>COMP_D.INPUT11</b></li> <li>■ <b>COMP_D.INPUT12</b></li> <li>■ <b>COMP_D.INPUT13</b></li> <li>■ <b>COMP_D.INPUT14</b></li> <li>■ <b>COMP_D.INPUT15</b></li> <li>■ <b>COMP_D.VREF</b> Modified bits are <b>CDPDx</b> of <b>CDCTL3</b> register.</li> </ul>

## Returns

None

```
void Comp_D_disableInterrupt ( uint16_t baseAddress, uint16_t interruptMask )
```

Disables selected Comparator interrupt sources.

Disables the indicated Comparator interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

## Parameters

<i>baseAddress</i>	is the base address of the COMP_D module.
<i>interruptMask</i>	Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>COMP_D.INTERRUPT</b> - Output interrupt</li> <li>■ <b>COMP_D.INTERRUPT.INVERTED.POLARITY</b> - Output interrupt inverted polarity</li> </ul>

```
void Comp_D_enable ( uint16_t baseAddress )
```

Turns on the Comparator module.

This function sets the bit that enables the operation of the Comparator module.

#### Parameters

<i>baseAddress</i>	is the base address of the COMP_D module.
--------------------	---

#### Returns

None

```
void Comp_D_enableInputBuffer ( uint16_t baseAddress, uint8_t inputPort )
```

Enables the input buffer of the selected input port to allow for digital signals.

This function clears the bit to enable the buffer for the specified input port to allow for digital signals from any of the comparator input pins. This should not be reset if there is an analog signal connected to the specified input pin to prevent from unexpected results.

#### Parameters

<i>baseAddress</i>	is the base address of the COMP_D module.
<i>inputPort</i>	is the port in which the input buffer will be enabled. Valid values are: <ul style="list-style-type: none"> <li>■ <b>COMP_D.INPUT0</b> [Default]</li> <li>■ <b>COMP_D.INPUT1</b></li> <li>■ <b>COMP_D.INPUT2</b></li> <li>■ <b>COMP_D.INPUT3</b></li> <li>■ <b>COMP_D.INPUT4</b></li> <li>■ <b>COMP_D.INPUT5</b></li> <li>■ <b>COMP_D.INPUT6</b></li> <li>■ <b>COMP_D.INPUT7</b></li> <li>■ <b>COMP_D.INPUT8</b></li> <li>■ <b>COMP_D.INPUT9</b></li> <li>■ <b>COMP_D.INPUT10</b></li> <li>■ <b>COMP_D.INPUT11</b></li> <li>■ <b>COMP_D.INPUT12</b></li> <li>■ <b>COMP_D.INPUT13</b></li> <li>■ <b>COMP_D.INPUT14</b></li> <li>■ <b>COMP_D.INPUT15</b></li> <li>■ <b>COMP_D.VREF</b></li> </ul> Modified bits are <b>CDPDx</b> of <b>CDCTL3</b> register.



**Returns**

None

```
void Comp_D_enableInterrupt ( uint16_t baseAddress, uint16_t interruptMask )
```

Enables selected Comparator interrupt sources.

Enables the indicated Comparator interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. **Does not clear interrupt flags.**

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_D module.
<i>interruptMask</i>	Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>COMP_D.INTERRUPT</b> - Output interrupt</li> <li>■ <b>COMP_D.INTERRUPT.INVERTED.POLARITY</b> - Output interrupt inverted polarity</li> </ul>

```
uint8_t Comp_D_getInterruptStatus ( uint16_t baseAddress, uint16_t interruptFlagMask )
```

Gets the current Comparator interrupt status.

This returns the interrupt status for the Comparator module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_D module.
<i>interruptFlag↔ Mask</i>	Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>COMP_D.INTERRUPT.FLAG</b> - Output interrupt flag</li> <li>■ <b>COMP_D.INTERRUPT.FLAG.INVERTED.POLARITY</b> - Output interrupt flag inverted polarity</li> </ul>

```
bool Comp_D_init ( uint16_t baseAddress, Comp_D_initParam * param )
```

Initializes the Comp\_D Module.

Upon successful initialization of the Comp\_D module, this function will have reset all necessary register bits and set the given options in the registers. To actually use the Comp\_D module, the [Comp\\_D\\_enable\(\)](#) function must be explicitly called before use. If a Reference Voltage is set to a terminal, the Voltage should be set using the [setReferenceVoltage\(\)](#) function.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_D module.
<i>param</i>	is the pointer to struct for initialization.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the initialization process

References `Comp_D_initParam::invertedOutputPolarity`,  
`Comp_D_initParam::negativeTerminalInput`, `Comp_D_initParam::outputFilterEnableAndDelayLevel`,  
and `Comp_D_initParam::positiveTerminalInput`.

`uint16_t Comp_D_outputValue ( uint16_t baseAddress )`

Returns the output value of the Comp\_D module.

Returns the output value of the Comp\_D module.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_D module.
--------------------	---

**Returns**

`Comp_D_HIGH` or `Comp_D_LOW` as the output value of the Comparator module. Return one of the following:

- **Comp\_D\_HIGH**
- **Comp\_D\_LOW**  
indicates the output should be normal

`void Comp_D_setInterruptEdgeDirection ( uint16_t baseAddress, uint16_t edgeDirection )`

Explicitly sets the edge direction that would trigger an interrupt.

This function will set which direction the output will have to go, whether rising or falling, to generate an interrupt based on a non-inverted interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_D module.
<i>edgeDirection</i>	determines which direction the edge would have to go to generate an interrupt based on the non-inverted interrupt flag. Valid values are: <ul style="list-style-type: none"> <li>■ <b>COMP_D_FALLINGEDGE</b> [Default] - sets the bit to generate an interrupt when the output of the comparator falls from HIGH to LOW if the normal interrupt bit is set (and LOW to HIGH if the inverted interrupt enable bit is set).</li> <li>■ <b>COMP_D_RISINGEDGE</b> - sets the bit to generate an interrupt when the output of the comparator rises from LOW to HIGH if the normal interrupt bit is set (and HIGH to LOW if the inverted interrupt enable bit is set). Modified bits are <b>CDIES</b> of <b>CDCTL1</b> register.</li> </ul>

**Returns**

None

```
void Comp_D_setReferenceAccuracy ( uint16_t baseAddress, uint16_t referenceAccuracy )
```

Sets the reference accuracy.

The reference accuracy is set to the desired setting. Clocked is better for low power operations but has a lower accuracy.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_D module.
<i>referenceAccuracy</i>	is the reference accuracy setting of the comparator. Clocked is for low power/low accuracy. Valid values are: <ul style="list-style-type: none"> <li>■ <b>COMP_D_ACCURACY_STATIC</b></li> <li>■ <b>COMP_D_ACCURACY_CLOCKED</b></li> </ul> Modified bits are <b>CDREFACC</b> of <b>CDCTL2</b> register.

**Returns**

None

```
void Comp_D_setReferenceVoltage ( uint16_t baseAddress, uint16_t supplyVoltageReferenceBase, uint16_t lowerLimitSupplyVoltageFractionOf32, uint16_t upperLimitSupplyVoltageFractionOf32 )
```

Generates a Reference Voltage to the terminal selected during initialization.

Use this function to generate a voltage to serve as a reference to the terminal selected at initialization. The voltage is determined by the equation:  $V_{base} * (Numerator / 32)$ . If the upper and lower limit voltage numerators are equal, then a static reference is defined, whereas they are different then a hysteresis effect is generated. Note that the "limit" voltage is the voltage triggers a change in COMP\_D value.

## Parameters

<i>baseAddress</i>	is the base address of the COMP_D module.
<i>supplyVoltage</i> ↔ <i>ReferenceBase</i>	decides the source and max amount of Voltage that can be used as a reference. Valid values are: <ul style="list-style-type: none"> <li>■ <b>COMP_D.REFERENCE_AMPLIFIER_DISABLED</b></li> <li>■ <b>COMP_D.VREFBASE1_5V</b></li> <li>■ <b>COMP_D.VREFBASE2_0V</b></li> <li>■ <b>COMP_D.VREFBASE2_5V</b></li> </ul> Modified bits are <b>CDREFL</b> of <b>CDCTL2</b> register.
<i>lowerLimit</i> ↔ <i>SupplyVoltage</i> ↔ <i>FractionOf32</i>	is the numerator of the equation to generate the reference voltage for the lower limit reference voltage. Modified bits are <b>CDREF0</b> of <b>CDCTL2</b> register.
<i>upperLimit</i> ↔ <i>SupplyVoltage</i> ↔ <i>FractionOf32</i>	is the numerator of the equation to generate the reference voltage for the upper limit reference voltage. Modified bits are <b>CDREF1</b> of <b>CDCTL2</b> register.

## Returns

None

```
void Comp_D_shortInputs ( uint16_t baseAddress )
```

Shorts the two input pins chosen during initialization.

This function sets the bit that shorts the devices attached to the input pins chosen from the initialization of the comparator.

## Parameters

<i>baseAddress</i>	is the base address of the COMP_D module.
--------------------	---

## Returns

None

```
void Comp_D_swapIO ( uint16_t baseAddress )
```

Toggles the bit that swaps which terminals the inputs go to, while also inverting the output of the comparator.

This function toggles the bit that controls which input goes to which terminal. After initialization, this bit is set to 0, after toggling it once the inputs are routed to the opposite terminal and the output is inverted.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_D module.
--------------------	---

**Returns**

None

```
void Comp_D_toggleInterruptEdgeDirection ( uint16_t baseAddress )
```

Toggles the edge direction that would trigger an interrupt.

This function will toggle which direction the output will have to go, whether rising or falling, to generate an interrupt based on a non-inverted interrupt. If the direction was rising, it is now falling, if it was falling, it is now rising.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_D module.
--------------------	---

**Returns**

None

```
void Comp_D_unshortInputs ( uint16_t baseAddress )
```

Disables the short of the two input pins chosen during initialization.

This function clears the bit that shorts the devices attached to the input pins chosen from the initialization of the comparator.

**Parameters**

<i>baseAddress</i>	is the base address of the COMP_D module.
--------------------	---

**Returns**

None

## 8.3 Programming Example

The following example shows how to initialize and use the Comp\_D API to turn on an LED when the input to the positive terminal is higher than the input to the negative terminal.

```
// Initialize the Comparator D module
/* Base Address of Comparator D,
   Pin CD2 to Positive(+) Terminal,
   Reference Voltage to Negative(-) Terminal,
   Normal Power Mode,
   Output Filter On with minimal delay,
   Non-Inverted Output Polarity
*/
Comp_D_initParam param = {0};
```

```
param.positiveTerminalInput = COMP_D.INPUT2;
param.negativeTerminalInput = COMP_D.VREF;
param.outputFilterEnableAndDelayLevel = COMP_D.FILTEROUTPUT_OFF;
param.invertedOutputPolarity = COMP_D.NORMALOUTPUTPOLARITY;
CompD_init (COMP_D.BASE, &param);

// Set the reference voltage that is being supplied to the (-) terminal
/* Base Address of Comparator D,
   Reference Voltage of 2.0 V,
   Upper Limit of 2.0*(32/32) = 2.0V,
   Lower Limit of 2.0*(32/32) = 2.0V
*/
CompD_setReferenceVoltage (COMP_D.BASE,
    COMP_D.VREFBASE2_0V,
    32,
    32,
    COMP_D.ACCURACY_STATIC
);

//Disable Input Buffer on P1.2/CD2
/* Base Address of Comparator D,
   * Input Buffer port
   * Selecting the CDx input pin to the comparator
   * multiplexer with the CDx bits automatically
   * disables output driver and input buffer for
   * that pin, regardless of the state of the
   * associated CDPD.x bit
*/
CompD_disableInputBuffer (COMP_D.BASE,
    COMP_D.INPUT2);
// Allow power to Comparator module
CompD_enable (COMP_D.BASE);

_delay_cycles(400);           // delay for the reference to settle
```

# 9 Cyclical Redundancy Check (CRC)

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## 9.1 Introduction

The Cyclic Redundancy Check (CRC) API provides a set of functions for using the MSP430Ware CRC module. Functions are provided to initialize the CRC and create a CRC signature to check the validity of data. This is mostly useful in the communication of data, or as a startup procedure to as a more complex and accurate check of data.

The CRC module offers no interrupts and is used only to generate CRC signatures to verify against pre-made CRC signatures (Checksums).

## 9.2 API Functions

### Functions

- void [CRC\\_setSeed](#) (uint16\_t baseAddress, uint16\_t seed)  
*Sets the seed for the CRC.*
- void [CRC\\_set16BitData](#) (uint16\_t baseAddress, uint16\_t dataIn)  
*Sets the 16 bit data to add into the CRC module to generate a new signature.*
- void [CRC\\_set8BitData](#) (uint16\_t baseAddress, uint8\_t dataIn)  
*Sets the 8 bit data to add into the CRC module to generate a new signature.*
- void [CRC\\_set16BitDataReversed](#) (uint16\_t baseAddress, uint16\_t dataIn)  
*Translates the 16 bit data by reversing the bits in each byte and then sets this data to add into the CRC module to generate a new signature.*
- void [CRC\\_set8BitDataReversed](#) (uint16\_t baseAddress, uint8\_t dataIn)  
*Translates the 8 bit data by reversing the bits in each byte and then sets this data to add into the CRC module to generate a new signature.*
- uint16\_t [CRC\\_getData](#) (uint16\_t baseAddress)  
*Returns the value currently in the Data register.*
- uint16\_t [CRC\\_getResult](#) (uint16\_t baseAddress)  
*Returns the value of the Signature Result.*
- uint16\_t [CRC\\_getResultBitsReversed](#) (uint16\_t baseAddress)  
*Returns the bit-wise reversed format of the Signature Result.*

### 9.2.1 Detailed Description

The CRC API is one group that controls the CRC module. The APIs that are used to set the seed and data are

- [CRC\\_setSeed\(\)](#)
- [CRC\\_set16BitData\(\)](#)

- [CRC\\_set8BitData\(\)](#)
- [CRC\\_set16BitDataReversed\(\)](#)
- [CRC\\_set8BitDataReversed\(\)](#)
- [CRC\\_setSeed\(\)](#)

The APIs that are used to get the data and results are

- [CRC\\_getData\(\)](#)
- [CRC\\_getResult\(\)](#)
- [CRC\\_getResultBitsReversed\(\)](#)

## 9.2.2 Function Documentation

`uint16_t CRC_getData ( uint16_t baseAddress )`

Returns the value currently in the Data register.

This function returns the value currently in the data register. If set in byte bits reversed format, then the translated data would be returned.

### Parameters

<i>baseAddress</i>	is the base address of the CRC module.
--------------------	--

### Returns

The value currently in the data register

`uint16_t CRC_getResult ( uint16_t baseAddress )`

Returns the value of the Signature Result.

This function returns the value of the signature result generated by the CRC.

### Parameters

<i>baseAddress</i>	is the base address of the CRC module.
--------------------	--

### Returns

The value currently in the data register

`uint16_t CRC_getResultBitsReversed ( uint16_t baseAddress )`

Returns the bit-wise reversed format of the Signature Result.

This function returns the bit-wise reversed format of the Signature Result.



**Parameters**

<i>baseAddress</i>	is the base address of the CRC module.
--------------------	--

**Returns**

The bit-wise reversed format of the Signature Result

`void CRC_set16BitData ( uint16_t baseAddress, uint16_t dataIn )`

Sets the 16 bit data to add into the CRC module to generate a new signature.

This function sets the given data into the CRC module to generate the new signature from the current signature and new data.

**Parameters**

<i>baseAddress</i>	is the base address of the CRC module.
<i>dataIn</i>	is the data to be added, through the CRC module, to the signature. Modified bits are <b>CRCDI</b> of <b>CRCDI</b> register.

**Returns**

None

`void CRC_set16BitDataReversed ( uint16_t baseAddress, uint16_t dataIn )`

Translates the 16 bit data by reversing the bits in each byte and then sets this data to add into the CRC module to generate a new signature.

This function first reverses the bits in each byte of the data and then generates the new signature from the current signature and new translated data.

**Parameters**

<i>baseAddress</i>	is the base address of the CRC module.
<i>dataIn</i>	is the data to be added, through the CRC module, to the signature. Modified bits are <b>CRCDIRB</b> of <b>CRCDIRB</b> register.

**Returns**

None

`void CRC_set8BitData ( uint16_t baseAddress, uint8_t dataIn )`

Sets the 8 bit data to add into the CRC module to generate a new signature.

This function sets the given data into the CRC module to generate the new signature from the current signature and new data.

**Parameters**

<i>baseAddress</i>	is the base address of the CRC module.
<i>dataIn</i>	is the data to be added, through the CRC module, to the signature. Modified bits are <b>CRCDI</b> of <b>CRCDI</b> register.

**Returns**

None

```
void CRC_set8BitDataReversed ( uint16_t baseAddress, uint8_t dataIn )
```

Translates the 8 bit data by reversing the bits in each byte and then sets this data to add into the CRC module to generate a new signature.

This function first reverses the bits in each byte of the data and then generates the new signature from the current signature and new translated data.

**Parameters**

<i>baseAddress</i>	is the base address of the CRC module.
<i>dataIn</i>	is the data to be added, through the CRC module, to the signature. Modified bits are <b>CRCDIRB</b> of <b>CRCDIRB</b> register.

**Returns**

None

```
void CRC_setSeed ( uint16_t baseAddress, uint16_t seed )
```

Sets the seed for the CRC.

This function sets the seed for the CRC to begin generating a signature with the given seed and all passed data. Using this function resets the CRC signature.

**Parameters**

<i>baseAddress</i>	is the base address of the CRC module.
<i>seed</i>	is the seed for the CRC to start generating a signature from. Modified bits are <b>CRCINIRES</b> of <b>CRCINIRES</b> register.

**Returns**

None

## 9.3 Programming Example

The following example shows how to initialize and use the CRC API to generate a CRC signature on an array of data.

```
unsigned int crcSeed = 0xBEEF;
unsigned int data[] = {0x0123,
                      0x4567,
                      0x8910,
                      0x1112,
                      0x1314};

unsigned int crcResult;
int i;

// Stop WDT
WDT_hold(WDT_A.BASE);

// Set P1.0 as an output
GPIO_setAsOutputPin(GPIO_PORT_P1,
                    GPIO_PIN0);

// Set the CRC seed
CRC_setSeed(CRC_BASE,
            crcSeed);

for (i = 0; i < 5; i++)
{
    //Add all of the values into the CRC signature
    CRC_set16BitData(CRC_BASE,
                    data[i]);
}

// Save the current CRC signature checksum to be compared for later
crcResult = CRC_getResult(CRC_BASE);
```

# 10 Clock System (CS)

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## 10.1 Introduction

The clock system module supports low system cost and low power consumption. Using three internal clock signals, the user can select the best balance of performance and low power consumption. The clock module can be configured to operate without any external components, with one or two external crystals, or with resonators, under full software control.

The clock system module includes up to five clock sources:

- XT1CLK - Low-frequency/high-frequency oscillator that can be used either with low-frequency 32768-Hz watch crystals, standard crystals, resonators, or external clock sources in the 4 MHz to 24 MHz range. When optional XT2 is present, the XT1 high-frequency mode may or may not be available, depending on the device configuration. See the device-specific data sheet for supported functions.
- VLOCLK - Internal very-low-power low-frequency oscillator with 10-kHz typical frequency
- DCOCLK - Internal digitally controlled oscillator (DCO) with three selectable fixed frequencies
- XT2CLK - Optional high-frequency oscillator that can be used with standard crystals, resonators, or external clock sources in the 4 MHz to 24 MHz range. See device-specific data sheet for availability.

Four system clock signals are available from the clock module:

- ACLK - Auxiliary clock. The ACLK is software selectable as XT1CLK, VLOCLK, DCOCLK, and when available, XT2CLK. ACLK can be divided by 1, 2, 4, 8, 16, or 32. ACLK is software selectable by individual peripheral modules.
- MCLK - Master clock. MCLK is software selectable as XT1CLK, VLOCLK, DCOCLK, and when available, XT2CLK. MCLK can be divided by 1, 2, 4, 8, 16, or 32. MCLK is used by the CPU and system.
- SMCLK - Subsystem master clock. SMCLK is software selectable as XT1CLK, VLOCLK, DCOCLK, and when available, XT2CLK. SMCLK is software selectable by individual peripheral modules.
- MODCLK - Module clock. MODCLK is used by various peripheral modules and is sourced by MODOSC.

**Fail-Safe logic** The crystal oscillator faults are set if the corresponding crystal oscillator is turned on and not operating properly. Once set, the fault bits remain set until reset in software, regardless if the fault condition no longer exists. If the user clears the fault bits and the fault condition still exists, the fault bits are automatically set, otherwise they remain cleared.

The OFIFG oscillator-fault interrupt flag is set and latched at POR or when any oscillator fault is detected. When OFIFG is set and OFIE is set, the OFIFG requests a user NMI. When the interrupt is granted, the OFIE is not reset automatically as it is in previous MSP430 families. It is no longer required to reset the OFIE. NMI entry/exit circuitry removes this requirement. The OFIFG flag

must be cleared by software. The source of the fault can be identified by checking the individual fault bits.

If XT1 in LF mode is sourcing any system clock (ACLK, MCLK, or SMCLK), and a fault is detected, the system clock is automatically switched to the VLO for its clock source (VLOCLK). Similarly, if XT1 in HF mode is sourcing any system clock and a fault is detected, the system clock is automatically switched to MODOSC for its clock source (MODCLK).

When XT2 (if available) is sourcing any system clock and a fault is detected, the system clock is automatically switched to MODOSC for its clock source (MODCLK).

The fail-safe logic does not change the respective SELA, SELM, and SELS bit settings. The fail-safe mechanism behaves the same in normal and bypass modes.

## 10.2 API Functions

### Macros

- #define **CS\_DCO\_FREQ\_1** 5330000
- #define **CS\_DCO\_FREQ\_2** 6670000
- #define **CS\_DCO\_FREQ\_3** 8000000
- #define **CS\_DCO\_FREQ\_4** 16000000
- #define **CS\_DCO\_FREQ\_5** 20000000
- #define **CS\_DCO\_FREQ\_6** 24000000
- #define **CS\_VLOCLK\_FREQUENCY** 10000
- #define **CS\_MODCLK\_FREQUENCY** 5000000
- #define **CS\_LFMODCLK\_FREQUENCY** 39062
- #define **XT1\_FREQUENCY\_THRESHOLD** 50000

### Functions

- void **CS\_setExternalClockSource** (uint32\_t XT1CLK.frequency, uint32\_t XT2CLK.frequency)  
*Sets the external clock source.*
- void **CS\_initClockSignal** (uint8\_t selectedClockSignal, uint16\_t clockSource, uint16\_t clockSourceDivider)  
*Initializes clock signal.*
- void **CS\_turnOnXT1** (uint16\_t xt1drive)  
*Initializes the XT1 crystal oscillator in low frequency mode.*
- void **CS\_turnOnSMCLK** (void)  
*Turn on SMCLK.*
- void **CS\_turnOffSMCLK** (void)  
*Turn off SMCLK.*
- void **CS\_bypassXT1** (void)  
*Bypasses the XT1 crystal oscillator.*
- bool **CS\_turnOnXT1WithTimeout** (uint16\_t xt1drive, uint32\_t timeout)  
*Initializes the XT1 crystal oscillator in low frequency mode with timeout.*
- bool **CS\_bypassXT1WithTimeout** (uint32\_t timeout)  
*Bypasses the XT1 crystal oscillator with timeout.*
- void **CS\_turnOffXT1** (void)  
*Stops the XT1 oscillator using the XT1OFF bit.*
- void **CS\_turnOnXT2** (uint16\_t xt2drive)

- Starts the XT2 crystal.*
- void `CS_bypassXT2` (void)
- Bypasses the XT2 crystal oscillator.*
- bool `CS_turnOnXT2WithTimeout` (uint16\_t xt2drive, uint32\_t timeout)
- Initializes the XT2 crystal oscillator with timeout.*
- bool `CS_bypassXT2WithTimeout` (uint32\_t timeout)
- Bypasses the XT2 crystal oscillator with timeout.*
- void `CS_turnOffXT2` (void)
- Stops the XT2 oscillator using the XT2OFF bit.*
- void `CS_enableClockRequest` (uint8\_t selectClock)
- Enables conditional module requests.*
- void `CS_disableClockRequest` (uint8\_t selectClock)
- Disables conditional module requests.*
- uint8\_t `CS_getFaultFlagStatus` (uint8\_t mask)
- Gets the current CS fault flag status.*
- void `CS_clearFaultFlag` (uint8\_t mask)
- Clears the current CS fault flag status for the masked bit.*
- uint32\_t `CS_getACLK` (void)
- Get the current ACLK frequency.*
- uint32\_t `CS_getSMCLK` (void)
- Get the current SMCLK frequency.*
- uint32\_t `CS_getMCLK` (void)
- Get the current MCLK frequency.*
- uint16\_t `CS_clearAllOscFlagsWithTimeout` (uint32\_t timeout)
- Clears all the Oscillator Flags.*
- void `CS_setDCOFreq` (uint16\_t dcorssel, uint16\_t dcofsel)
- Set DCO frequency.*

## 10.2.1 Detailed Description

The CS API is broken into four groups of functions: an API that initializes the clock module, those that deal with clock configuration and control, and external crystal and bypass specific configuration and initialization, and those that handle interrupts.

General CS configuration and initialization are handled by the following API

- `CS_initClockSignal()`
- `CS_enableClockRequest()`
- `CS_disableClockRequest()`
- `CS_getACLK()`
- `CS_getSMCLK()`
- `CS_getMCLK()`
- `CS_setDCOFreq()`

The following external crystal and bypass specific configuration and initialization functions are available for FR57xx devices:

- `CS_turnOnXT1()`
- `CS_bypassXT1()`
- `CS_bypassXT1WithTimeout()`

- `CS_turnOnXT1WithTimeout()`
- `CS_turnOffXT1()`
- `CS_turnOnXT2()`
- `CS_bypassXT2()`
- `CS_turnOnXT2WithTimeout()`
- `CS_bypassXT2WithTimeout()`
- `CS_turnOffXT2()`
- `CS_turnOnSMCLK()`
- `CS_turnOffSMCLK()`

The CS interrupts are handled by

- `CS_enableClockRequest()`
- `CS_disableClockRequest()`
- `CS_getFaultFlagStatus()`
- `CS_clearFaultFlag()`
- `CS_clearAllOscFlagsWithTimeout()`

`CS_setExternalClockSource` must be called if an external crystal XT1 or XT2 is used and the user intends to call `CS_getMCLK`, `CS_getSMCLK` or `CS_getACLK` APIs and `turnOnXT1`, `XT1ByPass`, `turnOnXT1WithTimeout`, `XT1ByPassWithTimeout`. If not any of the previous API are going to be called, it is not necessary to invoke this API.

## 10.2.2 Function Documentation

```
void CS_bypassXT1 ( void )
```

Bypasses the XT1 crystal oscillator.

Loops until all oscillator fault flags are cleared, with no timeout. **IMPORTANT:** User must call `CS_setExternalClockSource` function to set frequency of external clocks before calling this function.

Modified bits of **CSCTL0** register, bits of **CSCTL5** register, bits of **CSCTL4** register and bits of **SFRIFG** register.

**Returns**

None

```
bool CS_bypassXT1WithTimeout ( uint32_t timeout )
```

Bypasses the XT1 crystal oscillator with timeout.

Loops until all oscillator fault flags are cleared or until a timeout counter is decremented and equals to zero. **IMPORTANT:** User must call `CS_setExternalClockSource` to set frequency of external clocks before calling this function

**Parameters**

<i>timeout</i>	is the count value that gets decremented every time the loop that clears oscillator fault flags gets executed.
----------------	--

Modified bits of **CSCTL0** register, bits of **CSCTL5** register, bits of **CSCTL4** register and bits of **SFRIFG** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAIL

void CS\_bypassXT2 ( void )

Bypasses the XT2 crystal oscillator.

Bypasses the XT2 crystal oscillator which supports crystal frequencies between 4 MHz and 32 MHz. Loops until all oscillator fault flags are cleared, with no timeout. NOTE: User must call CS\_setExternalClockSource to set frequency of external clocks before calling this function.

Modified bits of **CSCTL5** register, bits of **CSCTL4** register and bits of **SFRIFG** register.

**Returns**

None

bool CS\_bypassXT2WithTimeout ( uint32\_t *timeout* )

Bypasses the XT2 crystal oscillator with timeout.

Bypasses the XT2 crystal oscillator with timeout, which supports crystal frequencies between 4 MHz and 32 MHz. Loops until all oscillator fault flags are cleared or until a timeout counter is decremented and equals to zero. NOTE: User must call CS\_setExternalClockSource to set frequency of external clocks before calling this function.

**Parameters**

<i>timeout</i>	is the count value that gets decremented every time the loop that clears oscillator fault flags gets executed.
----------------	--

Modified bits of **CSCTL5** register, bits of **CSCTL4** register and bits of **SFRIFG1** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAIL

uint16\_t CS\_clearAllOscFlagsWithTimeout ( uint32\_t *timeout* )

Clears all the Oscillator Flags.



**Parameters**

<i>timeout</i>	is the count value that gets decremented every time the loop that clears oscillator fault flags gets executed.
----------------	--

Modified bits of **CSCTL5** register and bits of **SFRIFG1** register.

**Returns**

the mask of the oscillator flag status

```
void CS_clearFaultFlag ( uint8_t mask )
```

Clears the current CS fault flag status for the masked bit.

**Parameters**

<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>CS_XT2OFFG</b> - XT2 oscillator fault flag</li> <li>■ <b>CS_XT1OFFG</b> - XT2 oscillator fault flag (HF mode)</li> </ul>
-------------	--

Modified bits of **CSCTL5** register.

**Returns**

None

```
void CS_disableClockRequest ( uint8_t selectClock )
```

Disables conditional module requests.

**Parameters**

<i>selectClock</i>	selects specific request enables. Valid values are: <ul style="list-style-type: none"> <li>■ <b>CS_ACLK</b></li> <li>■ <b>CS_MCLK</b></li> <li>■ <b>CS_SMCLK</b></li> <li>■ <b>CS_MODOSC</b></li> </ul>
--------------------	---

Modified bits of **CSCTL6** register.

**Returns**

None

```
void CS_enableClockRequest ( uint8_t selectClock )
```

Enables conditional module requests.

**Parameters**

<i>selectClock</i>	selects specific request enables. Valid values are: <ul style="list-style-type: none"> <li>■ <b>CS_ACLK</b></li> <li>■ <b>CS_MCLK</b></li> <li>■ <b>CS_SMCLK</b></li> <li>■ <b>CS_MODOSC</b></li> </ul>
--------------------	---

Modified bits of **CSCTL6** register.

**Returns**

None

`uint32_t CS_getACLK ( void )`

Get the current ACLK frequency.

If a oscillator fault is set, the frequency returned will be based on the fail safe mechanism of CS module. The user of this API must ensure that `CS_externalClockSourceInit` API was invoked before in case XT1 or XT2 is being used.

**Returns**

Current ACLK frequency in Hz

`uint8_t CS_getFaultFlagStatus ( uint8_t mask )`

Gets the current CS fault flag status.

**Parameters**

<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>CS_XT2OFFG</b> - XT2 oscillator fault flag</li> <li>■ <b>CS_XT1OFFG</b> - XT2 oscillator fault flag (HF mode)</li> </ul>
-------------	--

**Returns**

Logical OR of any of the following:

- **CS\_XT2OFFG** XT2 oscillator fault flag
- **CS\_XT1OFFG** XT2 oscillator fault flag (HF mode)  
indicating the status of the masked interrupts

`uint32_t CS_getMCLK ( void )`

Get the current MCLK frequency.

If a oscillator fault is set, the frequency returned will be based on the fail safe mechanism of CS module. The user of this API must ensure that CS\_externalClockSourceInit API was invoked before in case XT1 or XT2 is being used.

**Returns**

Current MCLK frequency in Hz

uint32\_t CS\_getSMCLK ( void )

Get the current SMCLK frequency.

If a oscillator fault is set, the frequency returned will be based on the fail safe mechanism of CS module. The user of this API must ensure that CS\_externalClockSourceInit API was invoked before in case XT1 or XT2 is being used.

**Returns**

Current SMCLK frequency in Hz

void CS\_initClockSignal ( uint8\_t *selectedClockSignal*, uint16\_t *clockSource*, uint16\_t *clockSourceDivider* )

Initializes clock signal.

This function initializes each of the clock signals. The user must ensure that this function is called for each clock signal. If not, the default state is assumed for the particular clock signal. Refer to MSP430ware documentation for CS module or Device Family User's Guide for details of default clock signal states.

**Parameters**

<i>selectedClockSignal</i>	is the selected clock signal Valid values are: <ul style="list-style-type: none"> <li>■ CS_ACLK</li> <li>■ CS_MCLK</li> <li>■ CS_SMCLK</li> <li>■ CS_MODOSC</li> </ul>
<i>clockSource</i>	is Clock source for the selectedClock Valid values are: <ul style="list-style-type: none"> <li>■ CS_XT1CLK_SELECT</li> <li>■ CS_VLOCLK_SELECT</li> <li>■ CS_DCOCLK_SELECT</li> <li>■ CS_XT2CLK_SELECT</li> </ul>

<i>clockSource↔ Divider</i>	selects the clock divider to calculate clock signal from clock source. Valid values are: <ul style="list-style-type: none"> <li>■ <b>CS_CLOCK_DIVIDER_1</b> - [Default for ACLK]</li> <li>■ <b>CS_CLOCK_DIVIDER_2</b></li> <li>■ <b>CS_CLOCK_DIVIDER_4</b></li> <li>■ <b>CS_CLOCK_DIVIDER_8</b> - [Default for SMCLK and MCLK]</li> <li>■ <b>CS_CLOCK_DIVIDER_16</b></li> <li>■ <b>CS_CLOCK_DIVIDER_32</b></li> </ul>
---------------------------------	---

Modified bits of **CSCTL0** register, bits of **CSCTL3** register and bits of **CSCTL2** register.

#### Returns

None

```
void CS_setDCOFreq ( uint16_t dcorsel, uint16_t dcofsel )
```

Set DCO frequency.

#### Parameters

<i>dcorsel</i>	selects frequency range option. Valid options are: <b>CS_DCORSEL_0</b> [Default] <b>CS.DCO↔ RSEL_1</b> Valid values are: <ul style="list-style-type: none"> <li>■ <b>CS_DCORSEL_0</b></li> <li>■ <b>CS_DCORSEL_1</b></li> </ul>
<i>dcofsel</i>	selects valid frequency options based on dco frequency range selection ( <i>dcorsel</i> ). Valid values are: <ul style="list-style-type: none"> <li>■ <b>CS_DCOFSEL_0</b> - Low frequency option 5.33MHZ. High frequency option 16MHz.</li> <li>■ <b>CS_DCOFSEL_1</b> - Low frequency option 6.67MHZ. High frequency option 20MHz.</li> <li>■ <b>CS_DCOFSEL_2</b> - Low frequency option 5.33MHZ. High frequency option 16MHz.</li> <li>■ <b>CS_DCOFSEL_3</b> [Default] - Low frequency option 8MHZ. High frequency option 24↔ MHz.</li> </ul>

#### Returns

None

```
void CS_setExternalClockSource ( uint32_t XT1CLK_frequency, uint32_t XT2CLK_frequency  
 )
```

Sets the external clock source.

This function sets the external clock sources XT1 and XT2 crystal oscillator frequency values. This function must be called if an external crystal XT1 or XT2 is used and the user intends to call **CS\_getMCLK**, **CS\_getSMCLK**, **CS\_getACLK** and **turnOnXT1**, **XT1ByPass**, **turnOnXT1WithTimeout**, **XT1ByPassWithTimeout**.

**Parameters**

<i>XT1CLK_↔ frequency</i>	is the XT1 crystal frequencies in Hz
<i>XT2CLK_↔ frequency</i>	is the XT2 crystal frequencies in Hz

**Returns**

None

```
void CS_turnOffSMCLK ( void )
```

Turn off SMCLK.

Modified bits of **CSCTL4** register.**Returns**

None

```
void CS_turnOffXT1 ( void )
```

Stops the XT1 oscillator using the XT1OFF bit.

Modified bits of **CSCTL4** register.**Returns**

None

```
void CS_turnOffXT2 ( void )
```

Stops the XT2 oscillator using the XT2OFF bit.

Modified bits of **CSCTL4** register.**Returns**

None

```
void CS_turnOnSMCLK ( void )
```

Turn on SMCLK.

Modified bits of **CSCTL4** register.**Returns**

None

```
void CS_turnOnXT1 ( uint16_t xt1drive )
```

Initializes the XT1 crystal oscillator in low frequency mode.

Loops until all oscillator fault flags are cleared, with no timeout. See the device-specific data sheet for appropriate drive settings. **IMPORTANT:** User must call `CS_setExternalClockSource` function to set frequency of external clocks before calling this function.

#### Parameters

<i>xt1drive</i>	is the target drive strength for the XT1 crystal oscillator. Valid values are: <ul style="list-style-type: none"> <li>■ <b>CS_XT1_DRIVE_0</b></li> <li>■ <b>CS_XT1_DRIVE_1</b></li> <li>■ <b>CS_XT1_DRIVE_2</b></li> <li>■ <b>CS_XT1_DRIVE_3</b> [Default]</li> </ul>
-----------------	---

Modified bits of **CSCTL0** register, bits of **CSCTL5** register, bits of **CSCTL4** register and bits of **SFRIFG1** register.

#### Returns

None

```
bool CS_turnOnXT1WithTimeout ( uint16_t xt1drive, uint32_t timeout )
```

Initializes the XT1 crystal oscillator in low frequency mode with timeout.

Loops until all oscillator fault flags are cleared or until a timeout counter is decremented and equals to zero. See the device-specific datasheet for appropriate drive settings. **IMPORTANT:** User must call `CS_setExternalClockSource` function to set frequency of external clocks before calling this function.

#### Parameters

<i>xt1drive</i>	is the target drive strength for the XT1 crystal oscillator. Valid values are: <ul style="list-style-type: none"> <li>■ <b>CS_XT1_DRIVE_0</b></li> <li>■ <b>CS_XT1_DRIVE_1</b></li> <li>■ <b>CS_XT1_DRIVE_2</b></li> <li>■ <b>CS_XT1_DRIVE_3</b> [Default]</li> </ul>
-----------------	---

<i>timeout</i>	is the count value that gets decremented every time the loop that clears oscillator fault flags gets executed.
----------------	--

Modified bits of **CSCTL0** register, bits of **CSCTL5** register, bits of **CSCTL4** register and bits of **SFRIFG1** register.

#### Returns

STATUS\_SUCCESS or STATUS\_FAIL

`void CS_turnOnXT2 ( uint16_t xt2drive )`

Starts the XT2 crystal.

Initializes the XT2 crystal oscillator, which supports crystal frequencies between 4 MHz and 32 MHz, depending on the selected drive strength. Loops until all oscillator fault flags are cleared, with no timeout. See the device-specific data sheet for appropriate drive settings. NOTE: User must call `CS_setExternalClockSource` to set frequency of external clocks before calling this function.

#### Parameters

<i>xt2drive</i>	is the target drive strength for the XT2 crystal oscillator. Valid values are: <ul style="list-style-type: none"> <li>■ <b>CS_XT2_DRIVE_4MHZ_8MHZ</b></li> <li>■ <b>CS_XT2_DRIVE_8MHZ_16MHZ</b></li> <li>■ <b>CS_XT2_DRIVE_16MHZ_24MHZ</b></li> <li>■ <b>CS_XT2_DRIVE_24MHZ_32MHZ</b> [Default]</li> </ul>
-----------------	--

Modified bits of **CSCTL0** register, bits of **CSCTL5** register, bits of **CSCTL4** register and bits of **SFRIFG1** register.

#### Returns

None

`bool CS_turnOnXT2WithTimeout ( uint16_t xt2drive, uint32_t timeout )`

Initializes the XT2 crystal oscillator with timeout.

Initializes the XT2 crystal oscillator, which supports crystal frequencies between 4 MHz and 32 MHz, depending on the selected drive strength. Loops until all oscillator fault flags are cleared or until a timeout counter is decremented and equals to zero. See the device-specific data sheet for appropriate drive settings. NOTE: User must call `CS_setExternalClockSource` to set frequency of external clocks before calling this function.

**Parameters**

<i>xt2drive</i>	is the target drive strength for the XT2 crystal oscillator. Valid values are: <ul style="list-style-type: none"> <li>■ <b>CS_XT2_DRIVE_4MHZ_8MHZ</b></li> <li>■ <b>CS_XT2_DRIVE_8MHZ_16MHZ</b></li> <li>■ <b>CS_XT2_DRIVE_16MHZ_24MHZ</b></li> <li>■ <b>CS_XT2_DRIVE_24MHZ_32MHZ</b> [Default]</li> </ul>
<i>timeout</i>	is the count value that gets decremented every time the loop that clears oscillator fault flags gets executed.

Modified bits of **CSCTL5** register, bits of **CSCTL4** register and bits of **SFRIFG1** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAIL

## 10.3 Programming Example

The following example shows the configuration of the CS module that sets ACLK=SMCLK=MCLK=DCOCLK

```
//Set DCO Frequency to 8MHz
CS_setDCOFreq(CS_BASE, CS_DCORSEL_0, CS_DCOFSEL_3);

//configure MCLK, SMCLK and ACLK to be source by DCOCLK
CS_initClockSignal(CS_BASE, CS_ACLK, CS_DCOCLK_SELECT, CS_CLOCK_DIVIDER_1);
CS_initClockSignal(CS_BASE, CS_SMCLK, CS_DCOCLK_SELECT, CS_CLOCK_DIVIDER_1);
CS_initClockSignal(CS_BASE, CS_MCLK, CS_DCOCLK_SELECT, CS_CLOCK_DIVIDER_1);
```



# 11 Direct Memory Access (DMA)

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## 11.1 Introduction

The Direct Memory Access (DMA) API provides a set of functions for using the MSP430Ware DMA modules. Functions are provided to initialize and setup each DMA channel with the source and destination addresses, manage the interrupts for each channel, and set bits that affect all DMA channels.

The DMA module provides the ability to move data from one address in the device to another, and that includes other peripheral addresses to RAM or vice-versa, all without the actual use of the CPU. Please be advised, that the DMA module does halt the CPU for 2 cycles while transferring, but does not have to edit any registers or anything. The DMA can transfer by bytes or words at a time, and will automatically increment or decrement the source or destination address if desired. There are also 6 different modes to transfer by, including single-transfer, block-transfer, and burst-block-transfer, as well as repeated versions of those three different kinds which allows transfers to be repeated without having re-enable transfers.

The DMA settings that affect all DMA channels include prioritization, from a fixed priority to dynamic round-robin priority. Another setting that can be changed is when transfers occur, the CPU may be in a read-modify-write operation which can be disastrous to time sensitive material, so this can be disabled. And Non-Maskable-Interrupts can indeed be maskable to the DMA module if not enabled.

The DMA module can generate one interrupt per channel. The interrupt is only asserted when the specified amount of transfers has been completed. With single-transfer, this occurs when that many single transfers have occurred, while with block or burst-block transfers, once the block is completely transferred the interrupt is asserted.

## 11.2 API Functions

### Functions

- void `DMA_init` (`DMA_initParam *param`)  
*Initializes the specified DMA channel.*
- void `DMA_setTransferSize` (`uint8_t channelSelect`, `uint16_t transferSize`)  
*Sets the specified amount of transfers for the selected DMA channel.*
- `uint16_t DMA_getTransferSize` (`uint8_t channelSelect`)  
*Gets the amount of transfers for the selected DMA channel.*
- void `DMA_setSrcAddress` (`uint8_t channelSelect`, `uint32_t srcAddress`, `uint16_t directionSelect`)  
*Sets source address and the direction that the source address will move after a transfer.*
- void `DMA_setDstAddress` (`uint8_t channelSelect`, `uint32_t dstAddress`, `uint16_t directionSelect`)

- Sets the destination address and the direction that the destination address will move after a transfer.*
- void [DMA\\_enableTransfers](#) (uint8\_t channelSelect)  
*Enables transfers to be triggered.*
- void [DMA\\_disableTransfers](#) (uint8\_t channelSelect)  
*Disables transfers from being triggered.*
- void [DMA\\_startTransfer](#) (uint8\_t channelSelect)  
*Starts a transfer if using the default trigger source selected in initialization.*
- void [DMA\\_enableInterrupt](#) (uint8\_t channelSelect)  
*Enables the DMA interrupt for the selected channel.*
- void [DMA\\_disableInterrupt](#) (uint8\_t channelSelect)  
*Disables the DMA interrupt for the selected channel.*
- uint16\_t [DMA\\_getInterruptStatus](#) (uint8\_t channelSelect)  
*Returns the status of the interrupt flag for the selected channel.*
- void [DMA\\_clearInterrupt](#) (uint8\_t channelSelect)  
*Clears the interrupt flag for the selected channel.*
- uint16\_t [DMA\\_getNMIAbortStatus](#) (uint8\_t channelSelect)  
*Returns the status of the NMIAbort for the selected channel.*
- void [DMA\\_clearNMIAbort](#) (uint8\_t channelSelect)  
*Clears the status of the NMIAbort to proceed with transfers for the selected channel.*
- void [DMA\\_disableTransferDuringReadModifyWrite](#) (void)  
*Disables the DMA from stopping the CPU during a Read-Modify-Write Operation to start a transfer.*
- void [DMA\\_enableTransferDuringReadModifyWrite](#) (void)  
*Enables the DMA to stop the CPU during a Read-Modify-Write Operation to start a transfer.*
- void [DMA\\_enableRoundRobinPriority](#) (void)  
*Enables Round Robin prioritization.*
- void [DMA\\_disableRoundRobinPriority](#) (void)  
*Disables Round Robin prioritization.*
- void [DMA\\_enableNMIAbort](#) (void)  
*Enables a NMI to interrupt a DMA transfer.*
- void [DMA\\_disableNMIAbort](#) (void)  
*Disables any NMI from interrupting a DMA transfer.*

## 11.2.1 Detailed Description

The DMA API is broken into three groups of functions: those that deal with initialization and transfers, those that handle interrupts, and those that affect all DMA channels.

The DMA initialization and transfer functions are: [DMA\\_init\(\)](#) [DMA\\_setSrcAddress\(\)](#) [DMA\\_setDstAddress\(\)](#) [DMA\\_enableTransfers\(\)](#) [DMA\\_disableTransfers\(\)](#) [DMA\\_startTransfer\(\)](#) [DMA\\_setTransferSize\(\)](#) [DMA\\_getTransferSize\(\)](#)

The DMA interrupts are handled by: [DMA\\_enableInterrupt\(\)](#) [DMA\\_disableInterrupt\(\)](#) [DMA\\_getInterruptStatus\(\)](#) [DMA\\_clearInterrupt\(\)](#) [DMA\\_getNMIAbortStatus\(\)](#) [DMA\\_clearNMIAbort\(\)](#)

Features of the DMA that affect all channels are handled by: [DMA\\_disableTransferDuringReadModifyWrite\(\)](#) [DMA\\_enableTransferDuringReadModifyWrite\(\)](#) [DMA\\_enableRoundRobinPriority\(\)](#) [DMA\\_disableRoundRobinPriority\(\)](#) [DMA\\_enableNMIAbort\(\)](#) [DMA\\_disableNMIAbort\(\)](#)

## 11.2.2 Function Documentation

void DMA\_clearInterrupt ( uint8\_t *channelSelect* )

Clears the interrupt flag for the selected channel.

This function clears the DMA interrupt flag is cleared, so that it no longer asserts.

### Parameters

<i>channelSelect</i>	<p>is the specified channel to clear the interrupt flag for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	---

### Returns

None

void DMA\_clearNMIAbort ( uint8\_t *channelSelect* )

Clears the status of the NMIAbort to proceed with transfers for the selected channel.

This function clears the status of the NMI Abort flag for the selected channel to allow for transfers on the channel to continue.

### Parameters

<i>channelSelect</i>	<p>is the specified channel to clear the NMI Abort flag for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	---

### Returns

None

```
void DMA_disableInterrupt ( uint8_t channelSelect )
```

Disables the DMA interrupt for the selected channel.

Disables the DMA interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>channelSelect</i>	<p>is the specified channel to disable the interrupt for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	--

**Returns**

None

```
void DMA_disableNMIAbort ( void )
```

Disables any NMI from interrupting a DMA transfer.

This function disables NMI's from interrupting any DMA transfer currently in progress.

**Returns**

None

```
void DMA_disableRoundRobinPriority ( void )
```

Disables Round Robin prioritization.

This function disables Round Robin Prioritization, enabling static prioritization of the DMA channels. In static prioritization, the DMA channels are prioritized with the lowest DMA channel index having the highest priority (i.e. DMA Channel 0 has the highest priority).

**Returns**

None

```
void DMA_disableTransferDuringReadModifyWrite ( void )
```

Disables the DMA from stopping the CPU during a Read-Modify-Write Operation to start a transfer.

This function allows the CPU to finish any read-modify-write operations it may be in the middle of before transfers of and DMA channel stop the CPU.

**Returns**

None

```
void DMA_disableTransfers ( uint8_t channelSelect )
```

Disables transfers from being triggered.

This function disables transfer from being triggered for the selected channel. This function should be called before any re-initialization of the selected DMA channel.

**Parameters**

<i>channelSelect</i>	is the specified channel to disable transfers for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	---

**Returns**

None

```
void DMA_enableInterrupt ( uint8_t channelSelect )
```

Enables the DMA interrupt for the selected channel.

Enables the DMA interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>channelSelect</i>	<p>is the specified channel to enable the interrupt for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	---

**Returns**

None

```
void DMA_enableNMIAbort ( void )
```

Enables a NMI to interrupt a DMA transfer.

This function allow NMI's to interrupting any DMA transfer currently in progress and stops any future transfers to begin before the NMI is done processing.

**Returns**

None

```
void DMA_enableRoundRobinPriority ( void )
```

Enables Round Robin prioritization.

This function enables Round Robin Prioritization of DMA channels. In the case of Round Robin Prioritization, the last DMA channel to have transferred data then has the last priority, which comes into play when multiple DMA channels are ready to transfer at the same time.

**Returns**

None

```
void DMA_enableTransferDuringReadModifyWrite ( void )
```

Enables the DMA to stop the CPU during a Read-Modify-Write Operation to start a transfer.

This function allows the DMA to stop the CPU in the middle of a read- modify-write operation to transfer data.

**Returns**

None

```
void DMA_enableTransfers ( uint8_t channelSelect )
```

Enables transfers to be triggered.

This function enables transfers upon appropriate trigger of the selected trigger source for the selected channel.

**Parameters**

<i>channelSelect</i>	<p>is the specified channel to enable transfer for. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	--

**Returns**

None

```
uint16_t DMA_getInterruptStatus ( uint8_t channelSelect )
```

Returns the status of the interrupt flag for the selected channel.

Returns the status of the interrupt flag for the selected channel.

**Parameters**

<i>channelSelect</i>	<p>is the specified channel to return the interrupt flag status from. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	--

**Returns**

One of the following:

- **DMA\_INT\_INACTIVE**
- **DMA\_INT\_ACTIVE**  
indicating the status of the current interrupt flag

uint16\_t DMA\_getNMIAbortStatus ( uint8\_t *channelSelect* )

Returns the status of the NMIAbort for the selected channel.

This function returns the status of the NMI Abort flag for the selected channel. If this flag has been set, it is because a transfer on this channel was aborted due to a interrupt from an NMI.

**Parameters**

<i>channelSelect</i>	is the specified channel to return the status of the NMI Abort flag for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	---

**Returns**

One of the following:

- **DMA\_NOTABORTED**
- **DMA\_ABORTED**  
indicating the status of the NMIAbort for the selected channel

uint16\_t DMA\_getTransferSize ( uint8\_t *channelSelect* )

Gets the amount of transfers for the selected DMA channel.

This function gets the amount of transfers for the selected DMA channel without having to reinitialize the DMA channel.



**Parameters**

<i>channelSelect</i>	is the specified channel to set source address direction for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	--

**Returns**

the amount of transfers

```
void DMA_init ( DMA_initParam * param )
```

Initializes the specified DMA channel.

This function initializes the specified DMA channel. Upon successful completion of initialization of the selected channel the control registers will be cleared and the given variables will be set. Please note, if transfers have been enabled with the `enableTransfers()` function, then a call to `disableTransfers()` is necessary before re-initialization. Also note, that the trigger sources are device dependent and can be found in the device family data sheet. The amount of DMA channels available are also device specific.

**Parameters**

<i>param</i>	is the pointer to struct for initialization.
--------------	--

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the initialization process.

References `DMA_initParam::channelSelect`, `DMA_initParam::transferModeSelect`, `DMA_initParam::transferSize`, `DMA_initParam::transferUnitSelect`, `DMA_initParam::triggerSourceSelect`, and `DMA_initParam::triggerTypeSelect`.

```
void DMA_setDstAddress ( uint8_t channelSelect, uint32_t dstAddress, uint16_t directionSelect )
```

Sets the destination address and the direction that the destination address will move after a transfer.

This function sets the destination address and the direction that the destination address will move after a transfer is complete. It may be incremented, decremented, or unchanged.

## Parameters

<i>channelSelect</i>	is the specified channel to set the destination address direction for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
<i>dstAddress</i>	is the address of where the data will be transferred to. Modified bits are <b>DMAxDA</b> of <b>DMAxDA</b> register.
<i>directionSelect</i>	is the specified direction of the destination address after a transfer. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_DIRECTION_UNCHANGED</b></li> <li>■ <b>DMA_DIRECTION_DECREMENT</b></li> <li>■ <b>DMA_DIRECTION_INCREMENT</b></li> </ul> Modified bits are <b>DMADSTINCR</b> of <b>DMAxCTL</b> register.

## Returns

None

```
void DMA_setSrcAddress ( uint8_t channelSelect, uint32_t srcAddress, uint16_t
directionSelect )
```

Sets source address and the direction that the source address will move after a transfer.

This function sets the source address and the direction that the source address will move after a transfer is complete. It may be incremented, decremented or unchanged.

## Parameters

<i>channelSelect</i>	is the specified channel to set source address direction for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
<i>srcAddress</i>	is the address of where the data will be transferred from. Modified bits are <b>DMAxSA</b> of <b>DMAxSA</b> register.
<i>directionSelect</i>	is the specified direction of the source address after a transfer. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_DIRECTION_UNCHANGED</b></li> <li>■ <b>DMA_DIRECTION_DECREMENT</b></li> <li>■ <b>DMA_DIRECTION_INCREMENT</b></li> </ul> Modified bits are <b>DMASRCINCR</b> of <b>DMAxCTL</b> register.

## Returns

None

```
void DMA_setTransferSize ( uint8_t channelSelect, uint16_t transferSize )
```

Sets the specified amount of transfers for the selected DMA channel.

This function sets the specified amount of transfers for the selected DMA channel without having to reinitialize the DMA channel.

## Parameters

<i>channelSelect</i>	is the specified channel to set source address direction for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
<i>transferSize</i>	is the amount of transfers to complete in a block transfer mode, as well as how many transfers to complete before the interrupt flag is set. Valid value is between 1-65535, if 0, no transfers will occur. Modified bits are <b>DMAxSZ</b> of <b>DMAxSZ</b> register.

## Returns

None

```
void DMA_startTransfer ( uint8_t channelSelect )
```

Starts a transfer if using the default trigger source selected in initialization.

This functions triggers a transfer of data from source to destination if the trigger source chosen from initialization is the DMA\_TRIGGERSOURCE\_0. Please note, this function needs to be called for each (repeated-)single transfer, and when transferAmount of transfers have been complete in (repeated-)block transfers.

## Parameters

<i>channelSelect</i>	is the specified channel to start transfers for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>DMA_CHANNEL_0</b></li> <li>■ <b>DMA_CHANNEL_1</b></li> <li>■ <b>DMA_CHANNEL_2</b></li> <li>■ <b>DMA_CHANNEL_3</b></li> <li>■ <b>DMA_CHANNEL_4</b></li> <li>■ <b>DMA_CHANNEL_5</b></li> <li>■ <b>DMA_CHANNEL_6</b></li> <li>■ <b>DMA_CHANNEL_7</b></li> </ul>
----------------------	---

**Returns**

None

## 11.3 Programming Example

The following example shows how to initialize and use the DMA API to transfer words from one spot in RAM to another.

```
// Initialize and Setup DMA Channel 0
/*
 * Base Address of the DMA Module
 * Configure DMA channel 0
 * Configure channel for repeated block transfers
 * DMA interrupt flag will be set after every 16 transfers
 * Use DMA_startTransfer() function to trigger transfers
 * Transfer Word-to-Word
 * Trigger upon Rising Edge of Trigger Source Signal
 */
DMA_initParam param = {0};
param.channelSelect = DMA_CHANNEL_0;
param.transferModeSelect = DMA_TRANSFER_REPEATED_BLOCK;
param.transferSize = 16;
param.triggerSourceSelect = DMA_TRIGGERSOURCE_0;
param.transferUnitSelect = DMA_SIZE_SRCWORD_DSTWORD;
param.triggerTypeSelect = DMA_TRIGGER_RISINGEDGE;
DMA_init (&param);
/*
 * Base Address of the DMA Module
 * Configure DMA channel 0
 * Use 0x1C00 as source
 * Increment source address after every transfer
 */
DMA_setSrcAddress (DMA_CHANNEL_0,
                  0x1C00,
                  DMA_DIRECTION_INCREMENT);
/*
 * Base Address of the DMA Module
 * Configure DMA channel 0
 * Use 0x1C20 as destination
 * Increment destination address after every transfer
 */
DMA_setDstAddress (DMA_CHANNEL_0,
                  0x1C20,
                  DMA_DIRECTION_INCREMENT);

// Enable transfers on DMA channel 0
DMA_enableTransfers (DMA_CHANNEL_0);

while (1)
{
    // Start block transfer on DMA channel 0
    DMA_startTransfer (DMA_CHANNEL_0);
}
```

# 12 EUSCI Universal Asynchronous Receiver/Transmitter (EUSCI\_A\_UART)

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## 12.1 Introduction

The MSP430Ware library for UART mode features include:

- Odd, even, or non-parity
- Independent transmit and receive shift registers
- Separate transmit and receive buffer registers
- LSB-first or MSB-first data transmit and receive
- Built-in idle-line and address-bit communication protocols for multiprocessor systems
- Receiver start-edge detection for auto wake up from LPMx modes
- Status flags for error detection and suppression
- Status flags for address detection
- Independent interrupt capability for receive and transmit

In UART mode, the USCI transmits and receives characters at a bit rate asynchronous to another device. Timing for each character is based on the selected baud rate of the USCI. The transmit and receive functions use the same baud-rate frequency.

## 12.2 API Functions

### Functions

- bool [EUSCI\\_A\\_UART\\_init](#) (uint16\_t baseAddress, [EUSCI\\_A\\_UART\\_initParam](#) \*param)  
*Advanced initialization routine for the UART block. The values to be written into the clockPrescaler, firstModReg, secondModReg and overSampling parameters should be pre-computed and passed into the initialization function.*
- void [EUSCI\\_A\\_UART\\_transmitData](#) (uint16\_t baseAddress, uint8\_t transmitData)  
*Transmits a byte from the UART Module. Please note that if TX interrupt is disabled, this function manually polls the TX IFG flag waiting for an indication that it is safe to write to the transmit buffer and does not time-out.*
- uint8\_t [EUSCI\\_A\\_UART\\_receiveData](#) (uint16\_t baseAddress)  
*Receives a byte that has been sent to the UART Module.*
- void [EUSCI\\_A\\_UART\\_enableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Enables individual UART interrupt sources.*
- void [EUSCI\\_A\\_UART\\_disableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Disables individual UART interrupt sources.*
- uint8\_t [EUSCI\\_A\\_UART\\_getInterruptStatus](#) (uint16\_t baseAddress, uint8\_t mask)

- Gets the current UART interrupt status.*
- void [EUSCI\\_A\\_UART\\_clearInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)
  - Clears UART interrupt sources.*
- void [EUSCI\\_A\\_UART\\_enable](#) (uint16\_t baseAddress)
  - Enables the UART block.*
- void [EUSCI\\_A\\_UART\\_disable](#) (uint16\_t baseAddress)
  - Disables the UART block.*
- uint8\_t [EUSCI\\_A\\_UART\\_queryStatusFlags](#) (uint16\_t baseAddress, uint8\_t mask)
  - Gets the current UART status flags.*
- void [EUSCI\\_A\\_UART\\_setDormant](#) (uint16\_t baseAddress)
  - Sets the UART module in dormant mode.*
- void [EUSCI\\_A\\_UART\\_resetDormant](#) (uint16\_t baseAddress)
  - Re-enables UART module from dormant mode.*
- void [EUSCI\\_A\\_UART\\_transmitAddress](#) (uint16\_t baseAddress, uint8\_t transmitAddress)
  - Transmits the next byte to be transmitted marked as address depending on selected multiprocessor mode.*
- void [EUSCI\\_A\\_UART\\_transmitBreak](#) (uint16\_t baseAddress)
  - Transmit break.*
- uint32\_t [EUSCI\\_A\\_UART\\_getReceiveBufferAddress](#) (uint16\_t baseAddress)
  - Returns the address of the RX Buffer of the UART for the DMA module.*
- uint32\_t [EUSCI\\_A\\_UART\\_getTransmitBufferAddress](#) (uint16\_t baseAddress)
  - Returns the address of the TX Buffer of the UART for the DMA module.*
- void [EUSCI\\_A\\_UART\\_selectDeglitchTime](#) (uint16\_t baseAddress, uint16\_t deglitchTime)
  - Sets the deglitch time.*

## 12.2.1 Detailed Description

The EUSCI\_A\_UART API provides the set of functions required to implement an interrupt driven EUSCI\_A\_UART driver. The EUSCI\_A\_UART initialization with the various modes and features is done by the [EUSCI\\_A\\_UART\\_init\(\)](#). At the end of this function EUSCI\_A\_UART is initialized and stays disabled. [EUSCI\\_A\\_UART\\_enable\(\)](#) enables the EUSCI\_A\_UART and the module is now ready for transmit and receive. It is recommended to initialize the EUSCI\_A\_UART via [EUSCI\\_A\\_UART\\_init\(\)](#), enable the required interrupts and then enable EUSCI\_A\_UART via [EUSCI\\_A\\_UART\\_enable\(\)](#).

The EUSCI\_A\_UART API is broken into three groups of functions: those that deal with configuration and control of the EUSCI\_A\_UART modules, those used to send and receive data, and those that deal with interrupt handling and those dealing with DMA.

Configuration and control of the EUSCI\_UART are handled by the

- [EUSCI\\_A\\_UART\\_init\(\)](#)
- [EUSCI\\_A\\_UART\\_initAdvance\(\)](#)
- [EUSCI\\_A\\_UART\\_enable\(\)](#)
- [EUSCI\\_A\\_UART\\_disable\(\)](#)
- [EUSCI\\_A\\_UART\\_setDormant\(\)](#)
- [EUSCI\\_A\\_UART\\_resetDormant\(\)](#)
- [EUSCI\\_A\\_UART\\_selectDeglitchTime\(\)](#)

Sending and receiving data via the EUSCI\_UART is handled by the

- [EUSCI\\_A\\_UART\\_transmitData\(\)](#)

- EUSCI\_A\_UART\_receiveData()
- EUSCI\_A\_UART\_transmitAddress()
- EUSCI\_A\_UART\_transmitBreak()
- EUSCI\_A\_UART\_getTransmitBufferAddress()
- EUSCI\_A\_UART\_getTransmitBufferAddress()

Managing the EUSCI\_UART interrupts and status are handled by the

- EUSCI\_A\_UART\_enableInterrupt()
- EUSCI\_A\_UART\_disableInterrupt()
- EUSCI\_A\_UART\_getInterruptStatus()
- EUSCI\_A\_UART\_clearInterrupt()
- EUSCI\_A\_UART\_queryStatusFlags()

## 12.2.2 Function Documentation

`void EUSCI_A_UART_clearInterrupt ( uint16_t baseAddress, uint8_t mask )`

Clears UART interrupt sources.

The UART interrupt source is cleared, so that it no longer asserts. The highest interrupt flag is automatically cleared when an interrupt vector generator is used.

### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>mask</i>	is a bit mask of the interrupt sources to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ EUSCI_A_UART_RECEIVE_INTERRUPT_FLAG</li> <li>■ EUSCI_A_UART_TRANSMIT_INTERRUPT_FLAG</li> <li>■ EUSCI_A_UART_STARTBIT_INTERRUPT_FLAG</li> <li>■ EUSCI_A_UART_TRANSMIT_COMPLETE_INTERRUPT_FLAG</li> </ul>

Modified bits of **UCAxIFG** register.

### Returns

None

`void EUSCI_A_UART_disable ( uint16_t baseAddress )`

Disables the UART block.

This will disable operation of the UART block.



**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

Modified bits are **UCSWRST** of **UCAxCTL1** register.

**Returns**

None

`void EUSCI_A_UART_disableInterrupt ( uint16_t baseAddress, uint8_t mask )`

Disables individual UART interrupt sources.

Disables the indicated UART interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>mask</i>	is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_UART_RECEIVE_INTERRUPT</b> - Receive interrupt</li> <li>■ <b>EUSCI_A_UART_TRANSMIT_INTERRUPT</b> - Transmit interrupt</li> <li>■ <b>EUSCI_A_UART_RECEIVE_ERRONEOUSCHAR_INTERRUPT</b> - Receive erroneous-character interrupt enable</li> <li>■ <b>EUSCI_A_UART_BREAKCHAR_INTERRUPT</b> - Receive break character interrupt enable</li> <li>■ <b>EUSCI_A_UART_STARTBIT_INTERRUPT</b> - Start bit received interrupt enable</li> <li>■ <b>EUSCI_A_UART_TRANSMIT_COMPLETE_INTERRUPT</b> - Transmit complete interrupt enable</li> </ul>

Modified bits of **UCAxCTL1** register and bits of **UCAxIE** register.

**Returns**

None

`void EUSCI_A_UART_enable ( uint16_t baseAddress )`

Enables the UART block.

This will enable operation of the UART block.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

Modified bits are **UCSWRST** of **UCAxCTL1** register.

**Returns**

None

void EUSCI\_A\_UART\_enableInterrupt ( uint16\_t *baseAddress*, uint8\_t *mask* )

Enables individual UART interrupt sources.

Enables the indicated UART interrupt sources. The interrupt flag is first and then the corresponding interrupt is enabled. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>mask</i>	is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_UART_RECEIVE_INTERRUPT</b> - Receive interrupt</li> <li>■ <b>EUSCI_A_UART_TRANSMIT_INTERRUPT</b> - Transmit interrupt</li> <li>■ <b>EUSCI_A_UART_RECEIVE_ERRONEOUSCHAR_INTERRUPT</b> - Receive erroneous-character interrupt enable</li> <li>■ <b>EUSCI_A_UART_BREAKCHAR_INTERRUPT</b> - Receive break character interrupt enable</li> <li>■ <b>EUSCI_A_UART_STARTBIT_INTERRUPT</b> - Start bit received interrupt enable</li> <li>■ <b>EUSCI_A_UART_TRANSMIT_COMPLETE_INTERRUPT</b> - Transmit complete interrupt enable</li> </ul>

Modified bits of **UCAxCTL1** register and bits of **UCAxIE** register.

**Returns**

None

uint8\_t EUSCI\_A\_UART\_getInterruptStatus ( uint16\_t *baseAddress*, uint8\_t *mask* )

Gets the current UART interrupt status.

This returns the interrupt status for the UART module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_UART_RECEIVE_INTERRUPT_FLAG</b></li> <li>■ <b>EUSCI_A_UART_TRANSMIT_INTERRUPT_FLAG</b></li> <li>■ <b>EUSCI_A_UART_STARTBIT_INTERRUPT_FLAG</b></li> <li>■ <b>EUSCI_A_UART_TRANSMIT_COMPLETE_INTERRUPT_FLAG</b></li> </ul>

Modified bits of **UCAxIFG** register.

**Returns**

Logical OR of any of the following:

- **EUSCI\_A\_UART\_RECEIVE\_INTERRUPT\_FLAG**
  - **EUSCI\_A\_UART\_TRANSMIT\_INTERRUPT\_FLAG**
  - **EUSCI\_A\_UART\_STARTBIT\_INTERRUPT\_FLAG**
  - **EUSCI\_A\_UART\_TRANSMIT\_COMPLETE\_INTERRUPT\_FLAG**
- indicating the status of the masked flags

`uint32_t EUSCI_A_UART_getReceiveBufferAddress ( uint16_t baseAddress )`

Returns the address of the RX Buffer of the UART for the DMA module.

Returns the address of the UART RX Buffer. This can be used in conjunction with the DMA to store the received data directly to memory.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

**Returns**

Address of RX Buffer

`uint32_t EUSCI_A_UART_getTransmitBufferAddress ( uint16_t baseAddress )`

Returns the address of the TX Buffer of the UART for the DMA module.

Returns the address of the UART TX Buffer. This can be used in conjunction with the DMA to obtain transmitted data directly from memory.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

**Returns**

Address of TX Buffer

`bool EUSCI_A_UART_init ( uint16_t baseAddress, EUSCI_A_UART_initParam * param )`

Advanced initialization routine for the UART block. The values to be written into the `clockPrescalar`, `firstModReg`, `secondModReg` and `overSampling` parameters should be pre-computed and passed into the initialization function.

Upon successful initialization of the UART block, this function will have initialized the module, but the UART block still remains disabled and must be enabled with [EUSCI\\_A\\_UART\\_enable\(\)](#). To calculate values for `clockPrescalar`, `firstModReg`, `secondModReg` and `overSampling` please use the link below.

[http://software-dl.ti.com/msp430/msp430\\_public\\_sw/mcu/msp430/MSP430BaudRateConverter/index.html](http://software-dl.ti.com/msp430/msp430_public_sw/mcu/msp430/MSP430BaudRateConverter/index.html)

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>param</i>	is the pointer to struct for initialization.

Modified bits are **UCPEN**, **UCPAR**, **UCMSB**, **UC7BIT**, **UCSPB**, **UCMODEx** and **UCSYNC** of **UCAxCTL0** register; bits **UCSSELx** and **UCSWRST** of **UCAxCTL1** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAIL of the initialization process

References EUSCI\_A\_UART\_initParam::clockPrescalar, EUSCI\_A\_UART\_initParam::firstModReg, EUSCI\_A\_UART\_initParam::msborLsbFirst, EUSCI\_A\_UART\_initParam::numberOfStopBits, EUSCI\_A\_UART\_initParam::overSampling, EUSCI\_A\_UART\_initParam::parity, EUSCI\_A\_UART\_initParam::secondModReg, EUSCI\_A\_UART\_initParam::selectClockSource, and EUSCI\_A\_UART\_initParam::uartMode.

uint8\_t EUSCI\_A\_UART\_queryStatusFlags ( uint16\_t *baseAddress*, uint8\_t *mask* )

Gets the current UART status flags.

This returns the status for the UART module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ EUSCI_A_UART_LISTEN_ENABLE</li> <li>■ EUSCI_A_UART_FRAMING_ERROR</li> <li>■ EUSCI_A_UART_OVERRUN_ERROR</li> <li>■ EUSCI_A_UART_PARITY_ERROR</li> <li>■ EUSCI_A_UART_BREAK_DETECT</li> <li>■ EUSCI_A_UART_RECEIVE_ERROR</li> <li>■ EUSCI_A_UART_ADDRESS_RECEIVED</li> <li>■ EUSCI_A_UART_IDLELINE</li> <li>■ EUSCI_A_UART_BUSY</li> </ul>

Modified bits of **UCAxSTAT** register.

**Returns**

Logical OR of any of the following:

- EUSCI\_A\_UART\_LISTEN\_ENABLE
- EUSCI\_A\_UART\_FRAMING\_ERROR
- EUSCI\_A\_UART\_OVERRUN\_ERROR
- EUSCI\_A\_UART\_PARITY\_ERROR
- EUSCI\_A\_UART\_BREAK\_DETECT
- EUSCI\_A\_UART\_RECEIVE\_ERROR
- EUSCI\_A\_UART\_ADDRESS\_RECEIVED

- **EUSCI\_A\_UART\_IDLELINE**
- **EUSCI\_A\_UART\_BUSY**  
indicating the status of the masked interrupt flags

`uint8_t EUSCI_A_UART_receiveData ( uint16_t baseAddress )`

Receives a byte that has been sent to the UART Module.

This function reads a byte of data from the UART receive data Register.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

Modified bits of **UCAxRXBUF** register.

**Returns**

Returns the byte received from by the UART module, cast as an `uint8_t`.

`void EUSCI_A_UART_resetDormant ( uint16_t baseAddress )`

Re-enables UART module from dormant mode.

Not dormant. All received characters set UCRXIFG.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

Modified bits are **UCDORM** of **UCAxCTL1** register.

**Returns**

None

`void EUSCI_A_UART_selectDeglitchTime ( uint16_t baseAddress, uint16_t deglitchTime )`

Sets the deglitch time.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>deglitchTime</i>	is the selected deglitch time Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_UART_DEGLITCH_TIME_2ns</b></li> <li>■ <b>EUSCI_A_UART_DEGLITCH_TIME_50ns</b></li> <li>■ <b>EUSCI_A_UART_DEGLITCH_TIME_100ns</b></li> <li>■ <b>EUSCI_A_UART_DEGLITCH_TIME_200ns</b></li> </ul>

**Returns**

None

```
void EUSCI_A_UART_setDormant ( uint16_t baseAddress )
```

Sets the UART module in dormant mode.

Puts USCI in sleep mode Only characters that are preceded by an idle-line or with address bit set UCRXIFG. In UART mode with automatic baud-rate detection, only the combination of a break and sync field sets UCRXIFG.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

Modified bits of **UCAxCTL1** register.

**Returns**

None

```
void EUSCI_A_UART_transmitAddress ( uint16_t baseAddress, uint8_t transmitAddress )
```

Transmits the next byte to be transmitted marked as address depending on selected multiprocessor mode.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>transmitAddress</i>	is the next byte to be transmitted

Modified bits of **UCAxTXBUF** register and bits of **UCAxCTL1** register.

**Returns**

None

```
void EUSCI_A_UART_transmitBreak ( uint16_t baseAddress )
```

Transmit break.

Transmits a break with the next write to the transmit buffer. In UART mode with automatic baud-rate detection, EUSCI\_A\_UART\_AUTOMATICBAUDRATE\_SYNC(0x55) must be written into UCAxTXBUF to generate the required break/sync fields. Otherwise, DEFAULT\_SYNC(0x00) must be written into the transmit buffer. Also ensures module is ready for transmitting the next data.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
--------------------	---

Modified bits of **UCAxTXBUF** register and bits of **UCAxCTL1** register.

**Returns**

None

```
void EUSCI_A_UART_transmitData ( uint16_t baseAddress, uint8_t transmitData )
```

Transmits a byte from the UART Module. Please note that if TX interrupt is disabled, this function manually polls the TX IFG flag waiting for an indication that it is safe to write to the transmit buffer and does not time-out.

This function will place the supplied data into UART transmit data register to start transmission

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_UART module.
<i>transmitData</i>	data to be transmitted from the UART module

Modified bits of **UCAxTXBUF** register.

#### Returns

None

## 12.3 Programming Example

The following example shows how to use the EUSCI\_UART API to initialize the EUSCI\_UART, transmit characters, and receive characters.

```
// Configure UART
EUSCI_A_UART_initParam param = {0};
param.selectClockSource = EUSCI_A_UART_CLOCKSOURCE_ACLK;
param.clockPrescalar = 15;
param.firstModReg = 0;
param.secondModReg = 68;
param.parity = EUSCI_A_UART_NO_PARITY;
param.msborLsbFirst = EUSCI_A_UART_LSB_FIRST;
param.numberOfStopBits = EUSCI_A_UART_ONE_STOP_BIT;
param.uartMode = EUSCI_A_UART_MODE;
param.overSampling = EUSCI_A_UART_LOW_FREQUENCY_BAUDRATE_GENERATION;

if (STATUS_FAIL == EUSCI_A_UART_init (EUSCI_A0_BASE, &param) ) {
    return;
}

EUSCI_A_UART_enable (EUSCI_A0_BASE);

// Enable USCI_A0 RX interrupt
EUSCI_A_UART_enableInterrupt (EUSCI_A0_BASE,
    EUSCI_A_UART_RECEIVE_INTERRUPT);
```

# 13 EUSCI Synchronous Peripheral Interface (EUSCI\_A\_SPI)

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## 13.1 Introduction

The Serial Peripheral Interface Bus or SPI bus is a synchronous serial data link standard named by Motorola that operates in full duplex mode. Devices communicate in master/slave mode where the master device initiates the data frame.

This library provides the API for handling a SPI communication using EUSCI.

The SPI module can be configured as either a master or a slave device.

The SPI module also includes a programmable bit rate clock divider and prescaler to generate the output serial clock derived from the module's input clock.

## 13.2 Functions

### Functions

- void [EUSCI\\_A\\_SPI\\_initMaster](#) (uint16\_t baseAddress, [EUSCI\\_A\\_SPI\\_initMasterParam](#) \*param)  
*Initializes the SPI Master block.*
- void [EUSCI\\_A\\_SPI\\_select4PinFunctionality](#) (uint16\_t baseAddress, uint8\_t select4PinFunctionality)  
*Selects 4Pin Functionality.*
- void [EUSCI\\_A\\_SPI\\_changeMasterClock](#) (uint16\_t baseAddress, [EUSCI\\_A\\_SPI\\_changeMasterClockParam](#) \*param)  
*Initializes the SPI Master clock. At the end of this function call, SPI module is left enabled.*
- void [EUSCI\\_A\\_SPI\\_initSlave](#) (uint16\_t baseAddress, [EUSCI\\_A\\_SPI\\_initSlaveParam](#) \*param)  
*Initializes the SPI Slave block.*
- void [EUSCI\\_A\\_SPI\\_changeClockPhasePolarity](#) (uint16\_t baseAddress, uint16\_t clockPhase, uint16\_t clockPolarity)  
*Changes the SPI clock phase and polarity. At the end of this function call, SPI module is left enabled.*
- void [EUSCI\\_A\\_SPI\\_transmitData](#) (uint16\_t baseAddress, uint8\_t transmitData)  
*Transmits a byte from the SPI Module.*
- uint8\_t [EUSCI\\_A\\_SPI\\_receiveData](#) (uint16\_t baseAddress)  
*Receives a byte that has been sent to the SPI Module.*
- void [EUSCI\\_A\\_SPI\\_enableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Enables individual SPI interrupt sources.*
- void [EUSCI\\_A\\_SPI\\_disableInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)  
*Disables individual SPI interrupt sources.*
- uint8\_t [EUSCI\\_A\\_SPI\\_getInterruptStatus](#) (uint16\_t baseAddress, uint8\_t mask)



- Gets the current SPI interrupt status.*
- void [EUSCIA\\_SPI\\_clearInterrupt](#) (uint16\_t baseAddress, uint8\_t mask)
  - Clears the selected SPI interrupt status flag.*
- void [EUSCIA\\_SPI\\_enable](#) (uint16\_t baseAddress)
  - Enables the SPI block.*
- void [EUSCIA\\_SPI\\_disable](#) (uint16\_t baseAddress)
  - Disables the SPI block.*
- uint32\_t [EUSCIA\\_SPI\\_getReceiveBufferAddress](#) (uint16\_t baseAddress)
  - Returns the address of the RX Buffer of the SPI for the DMA module.*
- uint32\_t [EUSCIA\\_SPI\\_getTransmitBufferAddress](#) (uint16\_t baseAddress)
  - Returns the address of the TX Buffer of the SPI for the DMA module.*
- uint16\_t [EUSCIA\\_SPI\\_isBusy](#) (uint16\_t baseAddress)
  - Indicates whether or not the SPI bus is busy.*

## 13.2.1 Detailed Description

To use the module as a master, the user must call [EUSCIA\\_SPI\\_initMaster\(\)](#) to configure the SPI Master. This is followed by enabling the SPI module using [EUSCIA\\_SPI\\_enable\(\)](#). The interrupts are then enabled (if needed). It is recommended to enable the SPI module before enabling the interrupts. A data transmit is then initiated using [EUSCIA\\_SPI\\_transmitData\(\)](#) and then when the receive flag is set, the received data is read using [EUSCIA\\_SPI\\_receiveData\(\)](#) and this indicates that an RX/TX operation is complete.

To use the module as a slave, initialization is done using [EUSCIA\\_SPI\\_initSlave\(\)](#) and this is followed by enabling the module using [EUSCIA\\_SPI\\_enable\(\)](#). Following this, the interrupts may be enabled as needed. When the receive flag is set, data is first transmitted using [EUSCIA\\_SPI\\_transmitData\(\)](#) and this is followed by a data reception by [EUSCIA\\_SPI\\_receiveData\(\)](#)

The SPI API is broken into 3 groups of functions: those that deal with status and initialization, those that handle data, and those that manage interrupts.

The status and initialization of the SPI module are managed by

- [EUSCIA\\_SPI\\_initMaster\(\)](#)
- [EUSCIA\\_SPI\\_initSlave\(\)](#)
- [EUSCIA\\_SPI\\_disable\(\)](#)
- [EUSCIA\\_SPI\\_enable\(\)](#)
- [EUSCIA\\_SPI\\_masterChangeClock\(\)](#)
- [EUSCIA\\_SPI\\_isBusy\(\)](#)
- [EUSCIA\\_SPI\\_select4PinFunctionality\(\)](#)
- [EUSCIA\\_SPI\\_changeClockPhasePolarity\(\)](#)

Data handling is done by

- [EUSCIA\\_SPI\\_transmitData\(\)](#)
- [EUSCIA\\_SPI\\_receiveData\(\)](#)

Interrupts from the SPI module are managed using

- [EUSCIA\\_SPI\\_disableInterrupt\(\)](#)

- [EUSCI\\_A\\_SPI.enableInterrupt\(\)](#)
- [EUSCI\\_A\\_SPI.getInterruptStatus\(\)](#)
- [EUSCI\\_A\\_SPI.clearInterrupt\(\)](#)

DMA related

- [EUSCI\\_A\\_SPI.getReceiveBufferAddressForDMA\(\)](#)
- [EUSCI\\_A\\_SPI.getTransmitBufferAddressForDMA\(\)](#)

## 13.2.2 Function Documentation

```
void EUSCI_A_SPI_changeClockPhasePolarity ( uint16_t baseAddress, uint16_t clockPhase,
uint16_t clockPolarity )
```

Changes the SPI clock phase and polarity. At the end of this function call, SPI module is left enabled.

Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
<i>clockPhase</i>	is clock phase select. Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT</b> [Default]</li> <li>■ <b>EUSCI_A_SPI_PHASE_DATA_CAPTURED_ONFIRST_CHANGED_ON_NEXT</b></li> </ul>
<i>clockPolarity</i>	is clock polarity select Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_SPI_CLOCKPOLARITY_INACTIVITY_HIGH</b></li> <li>■ <b>EUSCI_A_SPI_CLOCKPOLARITY_INACTIVITY_LOW</b> [Default]</li> </ul>

Modified bits are **UCCKPL**, **UCCKPH** and **UCSWRST** of **UCAxCTLW0** register.

Returns

None

```
void EUSCI_A_SPI_changeMasterClock ( uint16_t baseAddress, EUSCI_A_SPI.changeMasterClockParam * param )
```

Initializes the SPI Master clock. At the end of this function call, SPI module is left enabled.

Parameters

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
<i>param</i>	is the pointer to struct for master clock setting.

Modified bits are **UCSWRST** of **UCAxCTLW0** register.

**Returns**

None

References EUSCI.A.SPI.changeMasterClockParam::clockSourceFrequency, and EUSCI.A.SPI.changeMasterClockParam::desiredSpiClock.

```
void EUSCI_A_SPI_clearInterrupt ( uint16_t baseAddress, uint8_t mask )
```

Clears the selected SPI interrupt status flag.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI module.
<i>mask</i>	is the masked interrupt flag to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ EUSCI.A.SPI_TRANSMIT_INTERRUPT</li> <li>■ EUSCI.A.SPI_RECEIVE_INTERRUPT</li> </ul>

Modified bits of **UCAxIFG** register.

**Returns**

None

```
void EUSCI_A_SPI_disable ( uint16_t baseAddress )
```

Disables the SPI block.

This will disable operation of the SPI block.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI module.
--------------------	--

Modified bits are **UCSWRST** of **UCAxCTLW0** register.

**Returns**

None

```
void EUSCI_A_SPI_disableInterrupt ( uint16_t baseAddress, uint8_t mask )
```

Disables individual SPI interrupt sources.

Disables the indicated SPI interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI module.
<i>mask</i>	is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI.A.SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI.A.SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIE** register.

**Returns**

None

```
void EUSCI_A_SPI_enable ( uint16_t baseAddress )
```

Enables the SPI block.

This will enable operation of the SPI block.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI module.
--------------------	--

Modified bits are **UCSWRST** of **UCAxCTLW0** register.

**Returns**

None

```
void EUSCI_A_SPI_enableInterrupt ( uint16_t baseAddress, uint8_t mask )
```

Enables individual SPI interrupt sources.

Enables the indicated SPI interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI module.
<i>mask</i>	is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI.A.SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI.A.SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIFG** register and bits of **UCAxIE** register.

**Returns**

None

```
uint8_t EUSCI_A_SPI_getInterruptStatus ( uint16_t baseAddress, uint8_t mask )
```

Gets the current SPI interrupt status.

This returns the interrupt status for the SPI module based on which flag is passed.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI module.
<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI_A_SPI_RECEIVE_INTERRUPT</b></li> </ul>

#### Returns

Logical OR of any of the following:

- **EUSCI\_A\_SPI\_TRANSMIT\_INTERRUPT**
- **EUSCI\_A\_SPI\_RECEIVE\_INTERRUPT**

indicating the status of the masked interrupts

```
uint32_t EUSCI_A_SPI_getReceiveBufferAddress ( uint16_t baseAddress )
```

Returns the address of the RX Buffer of the SPI for the DMA module.

Returns the address of the SPI RX Buffer. This can be used in conjunction with the DMA to store the received data directly to memory.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI module.
--------------------	--

#### Returns

the address of the RX Buffer

```
uint32_t EUSCI_A_SPI_getTransmitBufferAddress ( uint16_t baseAddress )
```

Returns the address of the TX Buffer of the SPI for the DMA module.

Returns the address of the SPI TX Buffer. This can be used in conjunction with the DMA to obtain transmitted data directly from memory.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI module.
--------------------	--

#### Returns

the address of the TX Buffer

```
void EUSCI_A_SPI_initMaster ( uint16_t baseAddress, EUSCI_A_SPI_initMasterParam *
    param )
```

Initializes the SPI Master block.

Upon successful initialization of the SPI master block, this function will have set the bus speed for the master, but the SPI Master block still remains disabled and must be enabled with [EUSCI\\_A\\_SPI\\_enable\(\)](#)

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI Master module.
<i>param</i>	is the pointer to struct for master initialization.

Modified bits are **UCCKPH**, **UCCKPL**, **UC7BIT**, **UCMSB**, **UCSSELx** and **UCSWRST** of **UCAxCTLW0** register.

#### Returns

STATUS\_SUCCESS

References EUSCI\_A\_SPI\_initMasterParam::clockPhase, EUSCI\_A\_SPI\_initMasterParam::clockPolarity, EUSCI\_A\_SPI\_initMasterParam::clockSourceFrequency, EUSCI\_A\_SPI\_initMasterParam::desiredSpiClock, EUSCI\_A\_SPI\_initMasterParam::msbFirst, EUSCI\_A\_SPI\_initMasterParam::selectClockSource, and EUSCI\_A\_SPI\_initMasterParam::spiMode.

```
void EUSCI_A_SPI_initSlave ( uint16_t baseAddress, EUSCI_A_SPI_initSlaveParam *
    param )
```

Initializes the SPI Slave block.

Upon successful initialization of the SPI slave block, this function will have initialized the slave block, but the SPI Slave block still remains disabled and must be enabled with [EUSCI\\_A\\_SPI\\_enable\(\)](#)

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI Slave module.
<i>param</i>	is the pointer to struct for slave initialization.

Modified bits are **UCMSB**, **UCMST**, **UC7BIT**, **UCCKPL**, **UCCKPH**, **UCMODE** and **UCSWRST** of **UCAxCTLW0** register.

#### Returns

STATUS\_SUCCESS

References EUSCI\_A\_SPI\_initSlaveParam::clockPhase, EUSCI\_A\_SPI\_initSlaveParam::clockPolarity, EUSCI\_A\_SPI\_initSlaveParam::msbFirst, and EUSCI\_A\_SPI\_initSlaveParam::spiMode.

```
uint16_t EUSCI_A_SPI_isBusy ( uint16_t baseAddress )
```

Indicates whether or not the SPI bus is busy.

This function returns an indication of whether or not the SPI bus is busy. This function checks the status of the bus via UCBBUSY bit

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI module.
--------------------	--

#### Returns

One of the following:

- **EUSCI\_A\_SPI\_BUSY**
- **EUSCI\_A\_SPI\_NOT\_BUSY**  
indicating if the EUSCI.A.SPI is busy

```
uint8_t EUSCI_A_SPI_receiveData ( uint16_t baseAddress )
```

Receives a byte that has been sent to the SPI Module.

This function reads a byte of data from the SPI receive data Register.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI module.
--------------------	--

#### Returns

Returns the byte received from by the SPI module, cast as an uint8\_t.

```
void EUSCI_A_SPI_select4PinFunctionality ( uint16_t baseAddress, uint8_t  
select4PinFunctionality )
```

Selects 4Pin Functionality.

This function should be invoked only in 4-wire mode. Invoking this function has no effect in 3-wire mode.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.A.SPI module.
<i>select4Pin↔ Functionality</i>	selects 4 pin functionality Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_A_SPI_PREVENT_CONFLICTS_WITH_OTHER_MASTERS</b></li> <li>■ <b>EUSCI_A_SPI_ENABLE_SIGNAL_FOR_4WIRE_SLAVE</b></li> </ul>

Modified bits are **UCSTEM** of **UCAxCTLW0** register.

#### Returns

None

```
void EUSCI_A_SPI_transmitData ( uint16_t baseAddress, uint8_t transmitData )
```

Transmits a byte from the SPI Module.

This function will place the supplied data into SPI transmit data register to start transmission.



**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI_A_SPI module.
<i>transmitData</i>	data to be transmitted from the SPI module

**Returns**

None

## 13.3 Programming Example

The following example shows how to use the SPI API to configure the SPI module as a master device, and how to do a simple send of data.

```
//Initialize slave to MSB first, inactive high clock polarity and 3 wire SPI
EUSCI_A_SPI_initSlaveParam param = {0};
param.msbFirst = EUSCI_A_SPI_MSB_FIRST;
param.clockPhase = EUSCI_A_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT;
param.clockPolarity = EUSCI_A_SPI_CLOCKPOLARITY_INACTIVITY_HIGH;
param.spiMode = EUSCI_A_SPI_3PIN;
EUSCI_A_SPI_initSlave(EUSCI_A0_BASE, &param);

//Enable SPI Module
EUSCI_A_SPI_enable(EUSCI_A0_BASE);

//Enable Receive interrupt
EUSCI_A_SPI_enableInterrupt(EUSCI_A0_BASE,
    EUSCI_A_SPI_RECEIVE_INTERRUPT
);
```

# 14 EUSCI Synchronous Peripheral Interface (EUSCI\_B\_SPI)

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## 14.1 Introduction

The Serial Peripheral Interface Bus or SPI bus is a synchronous serial data link standard named by Motorola that operates in full duplex mode. Devices communicate in master/slave mode where the master device initiates the data frame.

This library provides the API for handling a SPI communication using EUSCI.

The SPI module can be configured as either a master or a slave device.

The SPI module also includes a programmable bit rate clock divider and prescaler to generate the output serial clock derived from the module's input clock.

## 14.2 Functions

### Functions

- void `EUSCI_B_SPI_initMaster` (uint16\_t baseAddress, `EUSCI_B_SPI_initMasterParam` \*param)  
*Initializes the SPI Master block.*
- void `EUSCI_B_SPI_select4PinFunctionality` (uint16\_t baseAddress, uint8\_t select4PinFunctionality)  
*Selects 4Pin Functionality.*
- void `EUSCI_B_SPI_changeMasterClock` (uint16\_t baseAddress, `EUSCI_B_SPI_changeMasterClockParam` \*param)  
*Initializes the SPI Master clock. At the end of this function call, SPI module is left enabled.*
- void `EUSCI_B_SPI_initSlave` (uint16\_t baseAddress, `EUSCI_B_SPI_initSlaveParam` \*param)  
*Initializes the SPI Slave block.*
- void `EUSCI_B_SPI_changeClockPhasePolarity` (uint16\_t baseAddress, uint16\_t clockPhase, uint16\_t clockPolarity)  
*Changes the SPI clock phase and polarity. At the end of this function call, SPI module is left enabled.*
- void `EUSCI_B_SPI_transmitData` (uint16\_t baseAddress, uint8\_t transmitData)  
*Transmits a byte from the SPI Module.*
- uint8\_t `EUSCI_B_SPI_receiveData` (uint16\_t baseAddress)  
*Receives a byte that has been sent to the SPI Module.*
- void `EUSCI_B_SPI_enableInterrupt` (uint16\_t baseAddress, uint8\_t mask)  
*Enables individual SPI interrupt sources.*
- void `EUSCI_B_SPI_disableInterrupt` (uint16\_t baseAddress, uint8\_t mask)  
*Disables individual SPI interrupt sources.*
- uint8\_t `EUSCI_B_SPI_getInterruptStatus` (uint16\_t baseAddress, uint8\_t mask)

- Gets the current SPI interrupt status.*
- void `EUSCI_B_SPI_clearInterrupt` (uint16\_t baseAddress, uint8\_t mask)
  - Clears the selected SPI interrupt status flag.*
- void `EUSCI_B_SPI_enable` (uint16\_t baseAddress)
  - Enables the SPI block.*
- void `EUSCI_B_SPI_disable` (uint16\_t baseAddress)
  - Disables the SPI block.*
- uint32\_t `EUSCI_B_SPI_getReceiveBufferAddress` (uint16\_t baseAddress)
  - Returns the address of the RX Buffer of the SPI for the DMA module.*
- uint32\_t `EUSCI_B_SPI_getTransmitBufferAddress` (uint16\_t baseAddress)
  - Returns the address of the TX Buffer of the SPI for the DMA module.*
- uint16\_t `EUSCI_B_SPI_isBusy` (uint16\_t baseAddress)
  - Indicates whether or not the SPI bus is busy.*

## 14.2.1 Detailed Description

To use the module as a master, the user must call `EUSCI_B_SPI_masterInit()` to configure the SPI Master. This is followed by enabling the SPI module using `EUSCI_B_SPI_enable()`. The interrupts are then enabled (if needed). It is recommended to enable the SPI module before enabling the interrupts. A data transmit is then initiated using `EUSCI_B_SPI_transmitData()` and then when the receive flag is set, the received data is read using `EUSCI_B_SPI_receiveData()` and this indicates that an RX/TX operation is complete.

To use the module as a slave, initialization is done using `EUSCI_B_SPI_slaveInit()` and this is followed by enabling the module using `EUSCI_B_SPI_enable()`. Following this, the interrupts may be enabled as needed. When the receive flag is set, data is first transmitted using `EUSCI_B_SPI_transmitData()` and this is followed by a data reception by `EUSCI_B_SPI_receiveData()`

The SPI API is broken into 3 groups of functions: those that deal with status and initialization, those that handle data, and those that manage interrupts.

The status and initialization of the SPI module are managed by

- `EUSCI_B_SPI_masterInit()`
- `EUSCI_B_SPI_slaveInit()`
- `EUSCI_B_SPI_disable()`
- `EUSCI_B_SPI_enable()`
- `EUSCI_B_SPI_masterChangeClock()`
- `EUSCI_B_SPI_isBusy()`
- `EUSCI_B_SPI_select4PinFunctionality()`
- `EUSCI_B_SPI_changeClockPhasePolarity()`

Data handling is done by

- `EUSCI_B_SPI_transmitData()`
- `EUSCI_B_SPI_receiveData()`

Interrupts from the SPI module are managed using

- `EUSCI_B_SPI_disableInterrupt()`

- EUSCI\_B\_SPI\_enableInterrupt()
- EUSCI\_B\_SPI\_getInterruptStatus()
- EUSCI\_B\_SPI\_clearInterrupt()

DMA related

- EUSCI\_B\_SPI\_getReceiveBufferAddressForDMA()
- EUSCI\_B\_SPI\_getTransmitBufferAddressForDMA()

## 14.2.2 Function Documentation

```
void EUSCI_B_SPI_changeClockPhasePolarity ( uint16_t baseAddress, uint16_t clockPhase,
uint16_t clockPolarity )
```

Changes the SPI clock phase and polarity. At the end of this function call, SPI module is left enabled.

Parameters

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
<i>clockPhase</i>	is clock phase select. Valid values are: <ul style="list-style-type: none"> <li>■ EUSCI_B_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT [Default]</li> <li>■ EUSCI_B_SPI_PHASE_DATA_CAPTURED_ONFIRST_CHANGED_ON_NEXT</li> </ul>
<i>clockPolarity</i>	is clock polarity select Valid values are: <ul style="list-style-type: none"> <li>■ EUSCI_B_SPI_CLOCKPOLARITY_INACTIVITY_HIGH</li> <li>■ EUSCI_B_SPI_CLOCKPOLARITY_INACTIVITY_LOW [Default]</li> </ul>

Modified bits are **UCCKPL**, **UCCKPH** and **UCSWRST** of **UCAxCTLW0** register.

Returns

None

```
void EUSCI_B_SPI_changeMasterClock ( uint16_t baseAddress, EUSCI_B_SPI_changeMasterClockParam * param )
```

Initializes the SPI Master clock. At the end of this function call, SPI module is left enabled.

Parameters

<i>baseAddress</i>	is the base address of the EUSCI_B_SPI module.
<i>param</i>	is the pointer to struct for master clock setting.

Modified bits are **UCSWRST** of **UCAxCTLW0** register.

**Returns**

None

References EUSCI.B.SPI.changeMasterClockParam::clockSourceFrequency, and EUSCI.B.SPI.changeMasterClockParam::desiredSpiClock.

```
void EUSCI_B_SPI_clearInterrupt ( uint16_t baseAddress, uint8_t mask )
```

Clears the selected SPI interrupt status flag.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI module.
<i>mask</i>	is the masked interrupt flag to be cleared. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ EUSCI_B_SPI_TRANSMIT_INTERRUPT</li> <li>■ EUSCI_B_SPI_RECEIVE_INTERRUPT</li> </ul>

Modified bits of **UCAxIFG** register.

**Returns**

None

```
void EUSCI_B_SPI_disable ( uint16_t baseAddress )
```

Disables the SPI block.

This will disable operation of the SPI block.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI module.
--------------------	--

Modified bits are **UCSWRST** of **UCAxCTLW0** register.

**Returns**

None

```
void EUSCI_B_SPI_disableInterrupt ( uint16_t baseAddress, uint8_t mask )
```

Disables individual SPI interrupt sources.

Disables the indicated SPI interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI module.
<i>mask</i>	is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI.B.SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI.B.SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIE** register.

**Returns**

None

```
void EUSCI_B_SPI_enable ( uint16_t baseAddress )
```

Enables the SPI block.

This will enable operation of the SPI block.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI module.
--------------------	--

Modified bits are **UCSWRST** of **UCAxCTLW0** register.

**Returns**

None

```
void EUSCI_B_SPI_enableInterrupt ( uint16_t baseAddress, uint8_t mask )
```

Enables individual SPI interrupt sources.

Enables the indicated SPI interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI module.
<i>mask</i>	is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI.B.SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI.B.SPI_RECEIVE_INTERRUPT</b></li> </ul>

Modified bits of **UCAxIFG** register and bits of **UCAxIE** register.

**Returns**

None

```
uint8_t EUSCI_B_SPI_getInterruptStatus ( uint16_t baseAddress, uint8_t mask )
```

Gets the current SPI interrupt status.

This returns the interrupt status for the SPI module based on which flag is passed.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI module.
<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_SPI_TRANSMIT_INTERRUPT</b></li> <li>■ <b>EUSCI_B_SPI_RECEIVE_INTERRUPT</b></li> </ul>

#### Returns

Logical OR of any of the following:

- **EUSCI\_B\_SPI\_TRANSMIT\_INTERRUPT**
  - **EUSCI\_B\_SPI\_RECEIVE\_INTERRUPT**
- indicating the status of the masked interrupts

```
uint32_t EUSCI_B_SPI_getReceiveBufferAddress ( uint16_t baseAddress )
```

Returns the address of the RX Buffer of the SPI for the DMA module.

Returns the address of the SPI RX Buffer. This can be used in conjunction with the DMA to store the received data directly to memory.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI module.
--------------------	--

#### Returns

the address of the RX Buffer

```
uint32_t EUSCI_B_SPI_getTransmitBufferAddress ( uint16_t baseAddress )
```

Returns the address of the TX Buffer of the SPI for the DMA module.

Returns the address of the SPI TX Buffer. This can be used in conjunction with the DMA to obtain transmitted data directly from memory.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI module.
--------------------	--

#### Returns

the address of the TX Buffer

```
void EUSCI_B_SPI_initMaster ( uint16_t baseAddress, EUSCI_B_SPI_initMasterParam *  
    param )
```

Initializes the SPI Master block.

Upon successful initialization of the SPI master block, this function will have set the bus speed for the master, but the SPI Master block still remains disabled and must be enabled with [EUSCI\\_B\\_SPI.enable\(\)](#)

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_B.SPI Master module.
<i>param</i>	is the pointer to struct for master initialization.

Modified bits are **UCCKPH**, **UCCKPL**, **UC7BIT**, **UCMSB**, **UCSSELx** and **UCSWRST** of **UCAxCTLW0** register.

#### Returns

STATUS\_SUCCESS

References EUSCI\_B\_SPI\_initMasterParam::clockPhase, EUSCI\_B\_SPI\_initMasterParam::clockPolarity, EUSCI\_B\_SPI\_initMasterParam::clockSourceFrequency, EUSCI\_B\_SPI\_initMasterParam::desiredSpiClock, EUSCI\_B\_SPI\_initMasterParam::msbFirst, EUSCI\_B\_SPI\_initMasterParam::selectClockSource, and EUSCI\_B\_SPI\_initMasterParam::spiMode.

```
void EUSCI_B_SPI_initSlave ( uint16_t baseAddress, EUSCI_B_SPI_initSlaveParam *  
    param )
```

Initializes the SPI Slave block.

Upon successful initialization of the SPI slave block, this function will have initialized the slave block, but the SPI Slave block still remains disabled and must be enabled with [EUSCI\\_B\\_SPI.enable\(\)](#)

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI_B.SPI Slave module.
<i>param</i>	is the pointer to struct for slave initialization.

Modified bits are **UCMSB**, **UCMST**, **UC7BIT**, **UCCKPL**, **UCCKPH**, **UCMODE** and **UCSWRST** of **UCAxCTLW0** register.

#### Returns

STATUS\_SUCCESS

References EUSCI\_B\_SPI\_initSlaveParam::clockPhase, EUSCI\_B\_SPI\_initSlaveParam::clockPolarity, EUSCI\_B\_SPI\_initSlaveParam::msbFirst, and EUSCI\_B\_SPI\_initSlaveParam::spiMode.

```
uint16_t EUSCI_B_SPI_isBusy ( uint16_t baseAddress )
```

Indicates whether or not the SPI bus is busy.



This function returns an indication of whether or not the SPI bus is busy. This function checks the status of the bus via UCBBUSY bit

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI module.
--------------------	--

#### Returns

One of the following:

- **EUSCI\_B\_SPI\_BUSY**
- **EUSCI\_B\_SPI\_NOT\_BUSY**  
indicating if the EUSCI.B.SPI is busy

```
uint8_t EUSCI_B_SPI_receiveData ( uint16_t baseAddress )
```

Receives a byte that has been sent to the SPI Module.

This function reads a byte of data from the SPI receive data Register.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI module.
--------------------	--

#### Returns

Returns the byte received from by the SPI module, cast as an uint8\_t.

```
void EUSCI_B_SPI_select4PinFunctionality ( uint16_t baseAddress, uint8_t  
select4PinFunctionality )
```

Selects 4Pin Functionality.

This function should be invoked only in 4-wire mode. Invoking this function has no effect in 3-wire mode.

#### Parameters

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI module.
<i>select4Pin↔ Functionality</i>	selects 4 pin functionality Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_SPI_PREVENT_CONFLICTS_WITH_OTHER_MASTERS</b></li> <li>■ <b>EUSCI_B_SPI_ENABLE_SIGNAL_FOR_4WIRE_SLAVE</b></li> </ul>

Modified bits are **UCSTEM** of **UCAxCTLW0** register.

#### Returns

None

```
void EUSCI_B_SPI_transmitData ( uint16_t baseAddress, uint8_t transmitData )
```

Transmits a byte from the SPI Module.

This function will place the supplied data into SPI transmit data register to start transmission.

**Parameters**

<i>baseAddress</i>	is the base address of the EUSCI.B.SPI module.
<i>transmitData</i>	data to be transmitted from the SPI module

**Returns**

None

## 14.3 Programming Example

The following example shows how to use the SPI API to configure the SPI module as a master device, and how to do a simple send of data.

```
//Initialize slave to MSB first, inactive high clock polarity and 3 wire SPI
EUSCI_B_SPI_initSlaveParam param = {0};
param.msbFirst = EUSCI_B_SPI_MSB_FIRST;
param.clockPhase = EUSCI_B_SPI_PHASE_DATA_CHANGED_ONFIRST_CAPTURED_ON_NEXT;
param.clockPolarity = EUSCI_B_SPI_CLOCKPOLARITY_INACTIVITY_HIGH;
param.spiMode = EUSCI_B_SPI_3PIN;
EUSCI_B_SPI_initSlave(EUSCI_B0_BASE, &param);

//Enable SPI Module
EUSCI_B_SPI_enable(EUSCI_B0_BASE);

//Enable Receive interrupt
EUSCI_B_SPI_enableInterrupt(EUSCI_B0_BASE,
    EUSCI_B_SPI_RECEIVE_INTERRUPT
);
```

# 15 EUSCI Inter-Integrated Circuit (EUSCI\_B\_I2C)

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## 15.1 Introduction

In I2C mode, the eUSCI\_B module provides an interface between the device and I2C-compatible devices connected by the two-wire I2C serial bus. External components attached to the I2C bus serially transmit and/or receive serial data to/from the eUSCI\_B module through the 2-wire I2C interface. The Inter-Integrated Circuit (I2C) API provides a set of functions for using the MSP430Ware I2C modules. Functions are provided to initialize the I2C modules, to send and receive data, obtain status, and to manage interrupts for the I2C modules.

The I2C module provide the ability to communicate to other IC devices over an I2C bus. The I2C bus is specified to support devices that can both transmit and receive (write and read) data. Also, devices on the I2C bus can be designated as either a master or a slave. The MSP430Ware I2C modules support both sending and receiving data as either a master or a slave, and also support the simultaneous operation as both a master and a slave.

I2C module can generate interrupts. The I2C module configured as a master will generate interrupts when a transmit or receive operation is completed (or aborted due to an error). The I2C module configured as a slave will generate interrupts when data has been sent or requested by a master.

## 15.2 Master Operations

To drive the master module, the APIs need to be invoked in the following order

- **EUSCI\_B\_I2C.initMaster**
- **EUSCI\_B\_I2C.setSlaveAddress**
- **EUSCI\_B\_I2C.setMode**
- **EUSCI\_B\_I2C.enable**
- **EUSCI\_B\_I2C.enableInterrupt** ( if interrupts are being used ) This may be followed by the APIs for transmit or receive as required

The user must first initialize the I2C module and configure it as a master with a call to [EUSCI\\_B\\_I2C.initMaster\(\)](#). That function will set the clock and data rates. This is followed by a call to set the slave address with which the master intends to communicate with using [EUSCI\\_B\\_I2C.setSlaveAddress](#). Then the mode of operation (transmit or receive) is chosen using [EUSCI\\_B\\_I2C.setMode](#). The I2C module may now be enabled using [EUSCI\\_B\\_I2C.enable](#). It is recommended to enable the [EUSCI\\_B\\_I2C](#) module before enabling the interrupts. Any transmission or reception of data may be initiated at this point after interrupts are enabled (if any).

The transaction can then be initiated on the bus by calling the transmit or receive related APIs as listed below.

Master Single Byte Transmission

- [EUSCI\\_B\\_I2C.masterSendSingleByte\(\)](#)

Master Multiple Byte Transmission

- [EUSCI\\_B\\_I2C.masterSendMultiByteStart\(\)](#)
- [EUSCI\\_B\\_I2C.masterSendMultiByteNext\(\)](#)
- [EUSCI\\_B\\_I2C.masterSendMultiByteStop\(\)](#)

Master Single Byte Reception

- [EUSCI\\_B\\_I2C.masterReceiveSingleByte\(\)](#)

Master Multiple Byte Reception

- [EUSCI\\_B\\_I2C.masterMultiByteReceiveStart\(\)](#)
- [EUSCI\\_B\\_I2C.masterReceiveMultiByteNext\(\)](#)
- [EUSCI\\_B\\_I2C.masterReceiveMultiByteFinish\(\)](#)
- [EUSCI\\_B\\_I2C.masterReceiveMultiByteStop\(\)](#)

For the interrupt-driven transaction, the user must register an interrupt handler for the I2C devices and enable the I2C interrupt.

## 15.3 Slave Operations

To drive the slave module, the APIs need to be invoked in the following order

- [EUSCI\\_B\\_I2C.initSlave\(\)](#)
- [EUSCI\\_B\\_I2C.setMode\(\)](#)
- [EUSCI\\_B\\_I2C.enable\(\)](#)
- [EUSCI\\_B\\_I2C.enableInterrupt\(\)](#) ( if interrupts are being used ) This may be followed by the APIs for transmit or receive as required

The user must first call the [EUSCI\\_B\\_I2C.initSlave](#) to initialize the slave module in I2C mode and set the slave address. This is followed by a call to set the mode of operation ( transmit or receive ).The I2C module may now be enabled using [EUSCI\\_B\\_I2C.enable](#). It is recommended to enable the I2C module before enabling the interrupts. Any transmission or reception of data may be initiated at this point after interrupts are enabled (if any).

The transaction can then be initiated on the bus by calling the transmit or receive related APIs as listed below.

Slave Transmission API

- [EUSCI\\_B\\_I2C.slavePutData\(\)](#)

Slave Reception API

- [EUSCI\\_B\\_I2C\\_slaveGetData\(\)](#)

For the interrupt-driven transaction, the user must register an interrupt handler for the I2C devices and enable the I2C interrupt.

## 15.4 API Functions

### Functions

- void [EUSCI\\_B\\_I2C\\_initMaster](#) (uint16\_t baseAddress, [EUSCI\\_B\\_I2C\\_initMasterParam](#) \*param)  
*Initializes the I2C Master block.*
- void [EUSCI\\_B\\_I2C\\_initSlave](#) (uint16\_t baseAddress, [EUSCI\\_B\\_I2C\\_initSlaveParam](#) \*param)  
*Initializes the I2C Slave block.*
- void [EUSCI\\_B\\_I2C\\_enable](#) (uint16\_t baseAddress)  
*Enables the I2C block.*
- void [EUSCI\\_B\\_I2C\\_disable](#) (uint16\_t baseAddress)  
*Disables the I2C block.*
- void [EUSCI\\_B\\_I2C\\_setSlaveAddress](#) (uint16\_t baseAddress, uint8\_t slaveAddress)  
*Sets the address that the I2C Master will place on the bus.*
- void [EUSCI\\_B\\_I2C\\_setMode](#) (uint16\_t baseAddress, uint8\_t mode)  
*Sets the mode of the I2C device.*
- uint8\_t [EUSCI\\_B\\_I2C\\_getMode](#) (uint16\_t baseAddress)  
*Gets the mode of the I2C device.*
- void [EUSCI\\_B\\_I2C\\_slavePutData](#) (uint16\_t baseAddress, uint8\_t transmitData)  
*Transmits a byte from the I2C Module.*
- uint8\_t [EUSCI\\_B\\_I2C\\_slaveGetData](#) (uint16\_t baseAddress)  
*Receives a byte that has been sent to the I2C Module.*
- uint16\_t [EUSCI\\_B\\_I2C\\_isBusBusy](#) (uint16\_t baseAddress)  
*Indicates whether or not the I2C bus is busy.*
- uint16\_t [EUSCI\\_B\\_I2C\\_masterIsStopSent](#) (uint16\_t baseAddress)  
*Indicates whether STOP got sent.*
- uint16\_t [EUSCI\\_B\\_I2C\\_masterIsStartSent](#) (uint16\_t baseAddress)  
*Indicates whether Start got sent.*
- void [EUSCI\\_B\\_I2C\\_enableInterrupt](#) (uint16\_t baseAddress, uint16\_t mask)  
*Enables individual I2C interrupt sources.*
- void [EUSCI\\_B\\_I2C\\_disableInterrupt](#) (uint16\_t baseAddress, uint16\_t mask)  
*Disables individual I2C interrupt sources.*
- void [EUSCI\\_B\\_I2C\\_clearInterrupt](#) (uint16\_t baseAddress, uint16\_t mask)  
*Clears I2C interrupt sources.*
- uint16\_t [EUSCI\\_B\\_I2C\\_getInterruptStatus](#) (uint16\_t baseAddress, uint16\_t mask)  
*Gets the current I2C interrupt status.*
- void [EUSCI\\_B\\_I2C\\_masterSendSingleByte](#) (uint16\_t baseAddress, uint8\_t txData)  
*Does single byte transmission from Master to Slave.*
- uint8\_t [EUSCI\\_B\\_I2C\\_masterReceiveSingleByte](#) (uint16\_t baseAddress)  
*Does single byte reception from Slave.*
- bool [EUSCI\\_B\\_I2C\\_masterSendSingleByteWithTimeout](#) (uint16\_t baseAddress, uint8\_t txData, uint32\_t timeout)  
*Does single byte transmission from Master to Slave with timeout.*
- void [EUSCI\\_B\\_I2C\\_masterSendMultiByteStart](#) (uint16\_t baseAddress, uint8\_t txData)  
*Starts multi-byte transmission from Master to Slave.*

- bool `EUSCI_B_I2C_masterSendMultiByteStartWithTimeout` (uint16\_t baseAddress, uint8\_t txData, uint32\_t timeout)  
*Starts multi-byte transmission from Master to Slave with timeout.*
- void `EUSCI_B_I2C_masterSendMultiByteNext` (uint16\_t baseAddress, uint8\_t txData)  
*Continues multi-byte transmission from Master to Slave.*
- bool `EUSCI_B_I2C_masterSendMultiByteNextWithTimeout` (uint16\_t baseAddress, uint8\_t txData, uint32\_t timeout)  
*Continues multi-byte transmission from Master to Slave with timeout.*
- void `EUSCI_B_I2C_masterSendMultiByteFinish` (uint16\_t baseAddress, uint8\_t txData)  
*Finishes multi-byte transmission from Master to Slave.*
- bool `EUSCI_B_I2C_masterSendMultiByteFinishWithTimeout` (uint16\_t baseAddress, uint8\_t txData, uint32\_t timeout)  
*Finishes multi-byte transmission from Master to Slave with timeout.*
- void `EUSCI_B_I2C_masterSendStart` (uint16\_t baseAddress)  
*This function is used by the Master module to initiate START.*
- void `EUSCI_B_I2C_masterSendMultiByteStop` (uint16\_t baseAddress)  
*Send STOP byte at the end of a multi-byte transmission from Master to Slave.*
- bool `EUSCI_B_I2C_masterSendMultiByteStopWithTimeout` (uint16\_t baseAddress, uint32\_t timeout)  
*Send STOP byte at the end of a multi-byte transmission from Master to Slave with timeout.*
- void `EUSCI_B_I2C_masterReceiveStart` (uint16\_t baseAddress)  
*Starts reception at the Master end.*
- uint8\_t `EUSCI_B_I2C_masterReceiveMultiByteNext` (uint16\_t baseAddress)  
*Starts multi-byte reception at the Master end one byte at a time.*
- uint8\_t `EUSCI_B_I2C_masterReceiveMultiByteFinish` (uint16\_t baseAddress)  
*Finishes multi-byte reception at the Master end.*
- bool `EUSCI_B_I2C_masterReceiveMultiByteFinishWithTimeout` (uint16\_t baseAddress, uint8\_t \*txData, uint32\_t timeout)  
*Finishes multi-byte reception at the Master end with timeout.*
- void `EUSCI_B_I2C_masterReceiveMultiByteStop` (uint16\_t baseAddress)  
*Sends the STOP at the end of a multi-byte reception at the Master end.*
- void `EUSCI_B_I2C_enableMultiMasterMode` (uint16\_t baseAddress)  
*Enables Multi Master Mode.*
- void `EUSCI_B_I2C_disableMultiMasterMode` (uint16\_t baseAddress)  
*Disables Multi Master Mode.*
- uint8\_t `EUSCI_B_I2C_masterReceiveSingle` (uint16\_t baseAddress)  
*receives a byte that has been sent to the I2C Master Module.*
- uint32\_t `EUSCI_B_I2C_getReceiveBufferAddress` (uint16\_t baseAddress)  
*Returns the address of the RX Buffer of the I2C for the DMA module.*
- uint32\_t `EUSCI_B_I2C_getTransmitBufferAddress` (uint16\_t baseAddress)  
*Returns the address of the TX Buffer of the I2C for the DMA module.*

## 15.4.1 Detailed Description

The eUSCI I2C API is broken into three groups of functions: those that deal with interrupts, those that handle status and initialization, and those that deal with sending and receiving data.

The I2C master and slave interrupts are handled by

- `EUSCI_B_I2C_enableInterrupt`
- `EUSCI_B_I2C_disableInterrupt`

- EUSCI\_B\_I2C\_clearInterrupt
- EUSCI\_B\_I2C\_getInterruptStatus

Status and initialization functions for the I2C modules are

- EUSCI\_B\_I2C\_initMaster
- EUSCI\_B\_I2C\_enable
- EUSCI\_B\_I2C\_disable
- EUSCI\_B\_I2C\_isBusBusy
- EUSCI\_B\_I2C\_isBusy
- EUSCI\_B\_I2C\_initSlave
- EUSCI\_B\_I2C\_interruptStatus
- EUSCI\_B\_I2C\_setSlaveAddress
- EUSCI\_B\_I2C\_setMode
- EUSCI\_B\_I2C\_masterIsStopSent
- EUSCI\_B\_I2C\_masterIsStartSent
- EUSCI\_B\_I2C\_selectMasterEnvironmentSelect

Sending and receiving data from the I2C slave module is handled by

- EUSCI\_B\_I2C\_slavePutData
- EUSCI\_B\_I2C\_slaveGetData

Sending and receiving data from the I2C slave module is handled by

- EUSCI\_B\_I2C\_masterSendSingleByte
- EUSCI\_B\_I2C\_masterSendStart
- EUSCI\_B\_I2C\_masterSendMultiByteStart
- EUSCI\_B\_I2C\_masterSendMultiByteNext
- EUSCI\_B\_I2C\_masterSendMultiByteFinish
- EUSCI\_B\_I2C\_masterSendMultiByteStop
- EUSCI\_B\_I2C\_masterReceiveMultiByteNext
- EUSCI\_B\_I2C\_masterReceiveMultiByteFinish
- EUSCI\_B\_I2C\_masterReceiveMultiByteStop
- EUSCI\_B\_I2C\_masterReceiveStart
- EUSCI\_B\_I2C\_masterReceiveSingle

## 15.4.2 Function Documentation

```
void EUSCI_B_I2C_clearInterrupt ( uint16_t baseAddress, uint16_t mask )
```

Clears I2C interrupt sources.

The I2C interrupt source is cleared, so that it no longer asserts. The highest interrupt flag is automatically cleared when an interrupt vector generator is used.



## Parameters

<i>baseAddress</i>	is the base address of the I2C module.
<i>mask</i>	<p>is a bit mask of the interrupt sources to be cleared. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_I2C_NAK_INTERRUPT</b> - Not-acknowledge interrupt</li> <li>■ <b>EUSCI_B_I2C_ARBITRATIONLOST_INTERRUPT</b> - Arbitration lost interrupt</li> <li>■ <b>EUSCI_B_I2C_STOP_INTERRUPT</b> - STOP condition interrupt</li> <li>■ <b>EUSCI_B_I2C_START_INTERRUPT</b> - START condition interrupt</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT0</b> - Transmit interrupt0</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT1</b> - Transmit interrupt1</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT2</b> - Transmit interrupt2</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT3</b> - Transmit interrupt3</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT0</b> - Receive interrupt0</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT1</b> - Receive interrupt1</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT2</b> - Receive interrupt2</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT3</b> - Receive interrupt3</li> <li>■ <b>EUSCI_B_I2C_BIT9_POSITION_INTERRUPT</b> - Bit position 9 interrupt</li> <li>■ <b>EUSCI_B_I2C_CLOCK_LOW_TIMEOUT_INTERRUPT</b> - Clock low timeout interrupt enable</li> <li>■ <b>EUSCI_B_I2C_BYTE_COUNTER_INTERRUPT</b> - Byte counter interrupt enable</li> </ul>

Modified bits of **UCBxIFG** register.

## Returns

None

```
void EUSCI_B_I2C_disable ( uint16_t baseAddress )
```

Disables the I2C block.

This will disable operation of the I2C block.

## Parameters

<i>baseAddress</i>	is the base address of the USCI I2C module.
--------------------	---

Modified bits are **UCSWRST** of **UCBxCTLW0** register.

## Returns

None

```
void EUSCI_B_I2C_disableInterrupt ( uint16_t baseAddress, uint16_t mask )
```

Disables individual I2C interrupt sources.

Disables the indicated I2C interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

## Parameters

<i>baseAddress</i>	is the base address of the I2C module.
<i>mask</i>	is the bit mask of the interrupt sources to be disabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_I2C_NAK_INTERRUPT</b> - Not-acknowledge interrupt</li> <li>■ <b>EUSCI_B_I2C_ARBITRATIONLOST_INTERRUPT</b> - Arbitration lost interrupt</li> <li>■ <b>EUSCI_B_I2C_STOP_INTERRUPT</b> - STOP condition interrupt</li> <li>■ <b>EUSCI_B_I2C_START_INTERRUPT</b> - START condition interrupt</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT0</b> - Transmit interrupt0</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT1</b> - Transmit interrupt1</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT2</b> - Transmit interrupt2</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT3</b> - Transmit interrupt3</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT0</b> - Receive interrupt0</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT1</b> - Receive interrupt1</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT2</b> - Receive interrupt2</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT3</b> - Receive interrupt3</li> <li>■ <b>EUSCI_B_I2C_BIT9_POSITION_INTERRUPT</b> - Bit position 9 interrupt</li> <li>■ <b>EUSCI_B_I2C_CLOCK_LOW_TIMEOUT_INTERRUPT</b> - Clock low timeout interrupt enable</li> <li>■ <b>EUSCI_B_I2C_BYTE_COUNTER_INTERRUPT</b> - Byte counter interrupt enable</li> </ul>

Modified bits of **UCBxIE** register.

## Returns

None

```
void EUSCI_B_I2C_disableMultiMasterMode ( uint16_t baseAddress )
```

Disables Multi Master Mode.

At the end of this function, the I2C module is still disabled till `EUSCI_B_I2C_enable` is invoked

## Parameters

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

Modified bits are **UCSWRST** and **UCMM** of **UCBxCTLW0** register.

## Returns

None

```
void EUSCI_B_I2C_enable ( uint16_t baseAddress )
```

Enables the I2C block.

This will enable operation of the I2C block.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI I2C module.
--------------------	---

Modified bits are **UCSWRST** of **UCBxCTLW0** register.

**Returns**

None

```
void EUSCI_B_I2C_enableInterrupt ( uint16_t baseAddress, uint16_t mask )
```

Enables individual I2C interrupt sources.

Enables the indicated I2C interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
<i>mask</i>	is the bit mask of the interrupt sources to be enabled. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_I2C_NAK_INTERRUPT</b> - Not-acknowledge interrupt</li> <li>■ <b>EUSCI_B_I2C_ARBITRATIONLOST_INTERRUPT</b> - Arbitration lost interrupt</li> <li>■ <b>EUSCI_B_I2C_STOP_INTERRUPT</b> - STOP condition interrupt</li> <li>■ <b>EUSCI_B_I2C_START_INTERRUPT</b> - START condition interrupt</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT0</b> - Transmit interrupt0</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT1</b> - Transmit interrupt1</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT2</b> - Transmit interrupt2</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT3</b> - Transmit interrupt3</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT0</b> - Receive interrupt0</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT1</b> - Receive interrupt1</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT2</b> - Receive interrupt2</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT3</b> - Receive interrupt3</li> <li>■ <b>EUSCI_B_I2C_BIT9_POSITION_INTERRUPT</b> - Bit position 9 interrupt</li> <li>■ <b>EUSCI_B_I2C_CLOCK_LOW_TIMEOUT_INTERRUPT</b> - Clock low timeout interrupt enable</li> <li>■ <b>EUSCI_B_I2C_BYTE_COUNTER_INTERRUPT</b> - Byte counter interrupt enable</li> </ul>

Modified bits of **UCBxIE** register.

**Returns**

None

```
void EUSCI_B_I2C_enableMultiMasterMode ( uint16_t baseAddress )
```

Enables Multi Master Mode.

At the end of this function, the I2C module is still disabled till EUSCI.B\_I2C\_enable is invoked

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

Modified bits are **UCSWRST** and **UCMM** of **UCBxCTLW0** register.

**Returns**

None

`uint16_t EUSCI_B_I2C_getInterruptStatus ( uint16_t baseAddress, uint16_t mask )`

Gets the current I2C interrupt status.

This returns the interrupt status for the I2C module based on which flag is passed.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
<i>mask</i>	is the masked interrupt flag status to be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_I2C_NAK_INTERRUPT</b> - Not-acknowledge interrupt</li> <li>■ <b>EUSCI_B_I2C_ARBITRATIONLOST_INTERRUPT</b> - Arbitration lost interrupt</li> <li>■ <b>EUSCI_B_I2C_STOP_INTERRUPT</b> - STOP condition interrupt</li> <li>■ <b>EUSCI_B_I2C_START_INTERRUPT</b> - START condition interrupt</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT0</b> - Transmit interrupt0</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT1</b> - Transmit interrupt1</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT2</b> - Transmit interrupt2</li> <li>■ <b>EUSCI_B_I2C_TRANSMIT_INTERRUPT3</b> - Transmit interrupt3</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT0</b> - Receive interrupt0</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT1</b> - Receive interrupt1</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT2</b> - Receive interrupt2</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_INTERRUPT3</b> - Receive interrupt3</li> <li>■ <b>EUSCI_B_I2C_BIT9_POSITION_INTERRUPT</b> - Bit position 9 interrupt</li> <li>■ <b>EUSCI_B_I2C_CLOCK_LOW_TIMEOUT_INTERRUPT</b> - Clock low timeout interrupt enable</li> <li>■ <b>EUSCI_B_I2C_BYTE_COUNTER_INTERRUPT</b> - Byte counter interrupt enable</li> </ul>

**Returns**

Logical OR of any of the following:

- **EUSCI\_B\_I2C\_NAK\_INTERRUPT** Not-acknowledge interrupt
- **EUSCI\_B\_I2C\_ARBITRATIONLOST\_INTERRUPT** Arbitration lost interrupt
- **EUSCI\_B\_I2C\_STOP\_INTERRUPT** STOP condition interrupt
- **EUSCI\_B\_I2C\_START\_INTERRUPT** START condition interrupt
- **EUSCI\_B\_I2C\_TRANSMIT\_INTERRUPT0** Transmit interrupt0
- **EUSCI\_B\_I2C\_TRANSMIT\_INTERRUPT1** Transmit interrupt1

- **EUSCI\_B\_I2C\_TRANSMIT\_INTERRUPT2** Transmit interrupt2
- **EUSCI\_B\_I2C\_TRANSMIT\_INTERRUPT3** Transmit interrupt3
- **EUSCI\_B\_I2C\_RECEIVE\_INTERRUPT0** Receive interrupt0
- **EUSCI\_B\_I2C\_RECEIVE\_INTERRUPT1** Receive interrupt1
- **EUSCI\_B\_I2C\_RECEIVE\_INTERRUPT2** Receive interrupt2
- **EUSCI\_B\_I2C\_RECEIVE\_INTERRUPT3** Receive interrupt3
- **EUSCI\_B\_I2C\_BIT9\_POSITION\_INTERRUPT** Bit position 9 interrupt
- **EUSCI\_B\_I2C\_CLOCK\_LOW\_TIMEOUT\_INTERRUPT** Clock low timeout interrupt enable
- **EUSCI\_B\_I2C\_BYTE\_COUNTER\_INTERRUPT** Byte counter interrupt enable  
indicating the status of the masked interrupts

uint8\_t EUSCI\_B\_I2C\_getMode ( uint16\_t *baseAddress* )

Gets the mode of the I2C device.

Current I2C transmit/receive mode.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

Modified bits are **UCTR** of **UCBxCTLW0** register.

**Returns**

One of the following:

- **EUSCI\_B\_I2C\_TRANSMIT\_MODE**
  - **EUSCI\_B\_I2C\_RECEIVE\_MODE**
- indicating the current mode

uint32\_t EUSCI\_B\_I2C\_getReceiveBufferAddress ( uint16\_t *baseAddress* )

Returns the address of the RX Buffer of the I2C for the DMA module.

Returns the address of the I2C RX Buffer. This can be used in conjunction with the DMA to store the received data directly to memory.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

**Returns**

The address of the I2C RX Buffer

uint32\_t EUSCI\_B\_I2C\_getTransmitBufferAddress ( uint16\_t *baseAddress* )

Returns the address of the TX Buffer of the I2C for the DMA module.

Returns the address of the I2C TX Buffer. This can be used in conjunction with the DMA to obtain transmitted data directly from memory.



**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

**Returns**

The address of the I2C TX Buffer

```
void EUSCI_B_I2C_initMaster ( uint16_t baseAddress, EUSCI_B_I2C_initMasterParam *  
param )
```

Initializes the I2C Master block.

This function initializes operation of the I2C Master block. Upon successful initialization of the I2C block, this function will have set the bus speed for the master; however I2C module is still disabled till EUSCI\_B\_I2C.enable is invoked.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>param</i>	is the pointer to the struct for master initialization.

**Returns**

None

References EUSCI\_B\_I2C\_initMasterParam::autoSTOPGeneration, EUSCI\_B\_I2C\_initMasterParam::byteCounterThreshold, EUSCI\_B\_I2C\_initMasterParam::dataRate, EUSCI\_B\_I2C\_initMasterParam::i2cClk, and EUSCI\_B\_I2C\_initMasterParam::selectClockSource.

```
void EUSCI_B_I2C_initSlave ( uint16_t baseAddress, EUSCI_B_I2C_initSlaveParam *  
param )
```

Initializes the I2C Slave block.

This function initializes operation of the I2C as a Slave mode. Upon successful initialization of the I2C blocks, this function will have set the slave address but the I2C module is still disabled till EUSCI\_B\_I2C.enable is invoked.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Slave module.
<i>param</i>	is the pointer to the struct for slave initialization.

**Returns**

None

References EUSCI\_B\_I2C\_initSlaveParam::slaveAddress, EUSCI\_B\_I2C\_initSlaveParam::slaveAddressOffset, and EUSCI\_B\_I2C\_initSlaveParam::slaveOwnAddressEnable.

uint16\_t EUSCI\_B\_I2C\_isBusBusy ( uint16\_t *baseAddress* )

Indicates whether or not the I2C bus is busy.

This function returns an indication of whether or not the I2C bus is busy. This function checks the status of the bus via UCBBUSY bit in UCBxSTAT register.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C module.
--------------------	--

**Returns**

One of the following:

- **EUSCI\_B\_I2C\_BUS\_BUSY**
- **EUSCI\_B\_I2C\_BUS\_NOT\_BUSY**  
indicating whether the bus is busy

uint16\_t EUSCI\_B\_I2C\_masterIsStartSent ( uint16\_t *baseAddress* )

Indicates whether Start got sent.

This function returns an indication of whether or not Start got sent This function checks the status of the bus via UCTXSTT bit in UCBxCTL1 register.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

**Returns**

One of the following:

- **EUSCI\_B\_I2C\_START\_SEND\_COMPLETE**
- **EUSCI\_B\_I2C\_SENDING\_START**  
indicating whether the start was sent

uint16\_t EUSCI\_B\_I2C\_masterIsStopSent ( uint16\_t *baseAddress* )

Indicates whether STOP got sent.

This function returns an indication of whether or not STOP got sent This function checks the status of the bus via UCTXSTP bit in UCBxCTL1 register.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

**Returns**

One of the following:

- **EUSCI\_B\_I2C\_STOP\_SEND\_COMPLETE**
- **EUSCI\_B\_I2C\_SENDING\_STOP**  
indicating whether the stop was sent

`uint8_t EUSCI_B_I2C_masterReceiveMultiByteFinish ( uint16_t baseAddress )`

Finishes multi-byte reception at the Master end.

This function is used by the Master module to initiate completion of a multi-byte reception. This function receives the current byte and initiates the STOP from master to slave.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTP** of **UCBxCTLW0** register.

**Returns**

Received byte at Master end.

`bool EUSCI_B_I2C_masterReceiveMultiByteFinishWithTimeout ( uint16_t baseAddress,  
uint8_t * txData, uint32_t timeout )`

Finishes multi-byte reception at the Master end with timeout.

This function is used by the Master module to initiate completion of a multi-byte reception. This function receives the current byte and initiates the STOP from master to slave.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is a pointer to the location to store the received byte at master end
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits are **UCTXSTP** of **UCBxCTLW0** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the reception process

`uint8_t EUSCI_B_I2C_masterReceiveMultiByteNext ( uint16_t baseAddress )`

Starts multi-byte reception at the Master end one byte at a time.

This function is used by the Master module to receive each byte of a multi- byte reception. This function reads currently received byte.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

**Returns**

Received byte at Master end.

`void EUSCI_B_I2C_masterReceiveMultiByteStop ( uint16_t baseAddress )`

Sends the STOP at the end of a multi-byte reception at the Master end.

This function is used by the Master module to initiate STOP

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTP** of **UCBxCTLW0** register.

**Returns**

None

`uint8_t EUSCI_B_I2C_masterReceiveSingle ( uint16_t baseAddress )`

receives a byte that has been sent to the I2C Master Module.

This function reads a byte of data from the I2C receive data Register.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

**Returns**

Returns the byte received from by the I2C module, cast as an `uint8_t`.

`uint8_t EUSCI_B_I2C_masterReceiveSingleByte ( uint16_t baseAddress )`

Does single byte reception from Slave.

This function is used by the Master module to receive a single byte. This function sends start and stop, waits for data reception and then receives the data from the slave

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits of **UCBxTXBUF** register, bits of **UCBxCTLW0** register, bits of **UCBxIE** register and bits of **UCBxIFG** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

`void EUSCI_B_I2C_masterReceiveStart ( uint16_t baseAddress )`

Starts reception at the Master end.

This function is used by the Master module initiate reception of a single byte. This function sends a start.

**Parameters**


---

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTT** of **UCBxCTLW0** register.

#### Returns

None

```
void EUSCI_B_I2C_masterSendMultiByteFinish ( uint16_t baseAddress, uint8_t txData )
```

Finishes multi-byte transmission from Master to Slave.

This function is used by the Master module to send the last byte and STOP. This function transmits the last data byte of a multi-byte transmission to the slave and then sends a stop.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the last data byte to be transmitted in a multi-byte transmission

Modified bits of **UCBxTXBUF** register and bits of **UCBxCTLW0** register.

#### Returns

None

```
bool EUSCI_B_I2C_masterSendMultiByteFinishWithTimeout ( uint16_t baseAddress, uint8_t txData, uint32_t timeout )
```

Finishes multi-byte transmission from Master to Slave with timeout.

This function is used by the Master module to send the last byte and STOP. This function transmits the last data byte of a multi-byte transmission to the slave and then sends a stop.

#### Parameters

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the last data byte to be transmitted in a multi-byte transmission
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits of **UCBxTXBUF** register and bits of **UCBxCTLW0** register.

#### Returns

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

```
void EUSCI_B_I2C_masterSendMultiByteNext ( uint16_t baseAddress, uint8_t txData )
```

Continues multi-byte transmission from Master to Slave.

This function is used by the Master module continue each byte of a multi-byte transmission. This function transmits each data byte of a multi-byte transmission to the slave.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the next data byte to be transmitted

Modified bits of **UCBxTXBUF** register.

**Returns**

None

```
bool EUSCI_B_I2C_masterSendMultiByteNextWithTimeout ( uint16_t baseAddress, uint8_t txData, uint32_t timeout )
```

Continues multi-byte transmission from Master to Slave with timeout.

This function is used by the Master module continue each byte of a multi- byte transmission. This function transmits each data byte of a multi-byte transmission to the slave.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the next data byte to be transmitted
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits of **UCBxTXBUF** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

```
void EUSCI_B_I2C_masterSendMultiByteStart ( uint16_t baseAddress, uint8_t txData )
```

Starts multi-byte transmission from Master to Slave.

This function is used by the master module to start a multi byte transaction.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the first data byte to be transmitted

Modified bits of **UCBxTXBUF** register, bits of **UCBxCTLW0** register, bits of **UCBxIE** register and bits of **UCBxIFG** register.

**Returns**

None

```
bool EUSCI_B_I2C_masterSendMultiByteStartWithTimeout ( uint16_t baseAddress, uint8_t txData, uint32_t timeout )
```

Starts multi-byte transmission from Master to Slave with timeout.

This function is used by the master module to start a multi byte transaction.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the first data byte to be transmitted
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits of **UCBxTXBUF** register, bits of **UCBxCTLW0** register, bits of **UCBxIE** register and bits of **UCBxIFG** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

```
void EUSCI_B_I2C_masterSendMultiByteStop ( uint16_t baseAddress )
```

Send STOP byte at the end of a multi-byte transmission from Master to Slave.

This function is used by the Master module send STOP at the end of a multi- byte transmission. This function sends a stop after current transmission is complete.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTP** of **UCBxCTLW0** register.

**Returns**

None

```
bool EUSCI_B_I2C_masterSendMultiByteStopWithTimeout ( uint16_t baseAddress, uint32_t timeout )
```

Send STOP byte at the end of a multi-byte transmission from Master to Slave with timeout.

This function is used by the Master module send STOP at the end of a multi- byte transmission. This function sends a stop after current transmission is complete.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits are **UCTXSTP** of **UCBxCTLW0** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

```
void EUSCI_B_I2C_masterSendSingleByte ( uint16_t baseAddress, uint8_t txData )
```

Does single byte transmission from Master to Slave.

This function is used by the Master module to send a single byte. This function sends a start, then transmits the byte to the slave and then sends a stop.



**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the data byte to be transmitted

Modified bits of **UCBxTXBUF** register, bits of **UCBxCTLW0** register, bits of **UCBxIE** register and bits of **UCBxIFG** register.

**Returns**

None

```
bool EUSCI_B_I2C_masterSendSingleByteWithTimeout ( uint16_t baseAddress, uint8_t
txData, uint32_t timeout )
```

Does single byte transmission from Master to Slave with timeout.

This function is used by the Master module to send a single byte. This function sends a start, then transmits the byte to the slave and then sends a stop.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
<i>txData</i>	is the data byte to be transmitted
<i>timeout</i>	is the amount of time to wait until giving up

Modified bits of **UCBxTXBUF** register, bits of **UCBxCTLW0** register, bits of **UCBxIE** register and bits of **UCBxIFG** register.

**Returns**

STATUS\_SUCCESS or STATUS\_FAILURE of the transmission process.

```
void EUSCI_B_I2C_masterSendStart ( uint16_t baseAddress )
```

This function is used by the Master module to initiate START.

This function is used by the Master module to initiate START

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Master module.
--------------------	---

Modified bits are **UCTXSTT** of **UCBxCTLW0** register.

**Returns**

None

```
void EUSCI_B_I2C_setMode ( uint16_t baseAddress, uint8_t mode )
```

Sets the mode of the I2C device.

When the receive parameter is set to EUSCI\_B\_I2C\_TRANSMIT\_MODE, the address will indicate that the I2C module is in receive mode; otherwise, the I2C module is in send mode.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI I2C module.
<i>mode</i>	Mode for the EUSCI_B_I2C module Valid values are: <ul style="list-style-type: none"> <li>■ <b>EUSCI_B_I2C_TRANSMIT_MODE</b> [Default]</li> <li>■ <b>EUSCI_B_I2C_RECEIVE_MODE</b></li> </ul>

Modified bits are **UCTR** of **UCBxCTLW0** register.

**Returns**

None

```
void EUSCI_B_I2C_setSlaveAddress ( uint16_t baseAddress, uint8_t slaveAddress )
```

Sets the address that the I2C Master will place on the bus.

This function will set the address that the I2C Master will place on the bus when initiating a transaction.

**Parameters**

<i>baseAddress</i>	is the base address of the USCI I2C module.
<i>slaveAddress</i>	7-bit slave address

Modified bits of **UCBxI2CSA** register.

**Returns**

None

```
uint8_t EUSCI_B_I2C_slaveGetData ( uint16_t baseAddress )
```

Receives a byte that has been sent to the I2C Module.

This function reads a byte of data from the I2C receive data Register.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Slave module.
--------------------	--

**Returns**

Returns the byte received from by the I2C module, cast as an uint8\_t.

```
void EUSCI_B_I2C_slavePutData ( uint16_t baseAddress, uint8_t transmitData )
```

Transmits a byte from the I2C Module.

This function will place the supplied data into I2C transmit data register to start transmission.

**Parameters**

<i>baseAddress</i>	is the base address of the I2C Slave module.
<i>transmitData</i>	data to be transmitted from the I2C module

Modified bits of **UCBxTXBUF** register.

**Returns**

None

## 15.5 Programming Example

The following example shows how to use the I2C API to send data as a master.

```
//Initialize Slave
EUSCI_B_I2C_initSlaveParam param = {0};
param.slaveAddress = 0x48;
param.slaveAddressOffset = EUSCI_B_I2C_OWN_ADDRESS_OFFSET0;
param.slaveOwnAddressEnable = EUSCI_B_I2C_OWN_ADDRESS_ENABLE;
EUSCI_B_I2C_initSlave(EUSCI_B0_BASE, &param);

EUSCI_B_I2C_enable(EUSCI_B0_BASE);

EUSCI_B_I2C_enableInterrupt(EUSCI_B0_BASE,
    EUSCI_B_I2C_TRANSMIT_INTERRUPT0 +
    EUSCI_B_I2C_STOP_INTERRUPT);
```

# 16 FRAMCtl - FRAM Controller

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## 16.1 Introduction

FRAM memory is a non-volatile memory that reads and writes like standard SRAM. The MSP430 FRAM memory features include:

- Byte or word write access
- Automatic and programmable wait state control with independent wait state settings for access and cycle times
- Error Correction Code with bit error correction, extended bit error detection and flag indicators
- Cache for fast read

## 16.2 API Functions

`FRAMCtl.enableInterrupt` enables selected FRAM interrupt sources.

`FRAMCtl.getInterruptStatus` returns the status of the selected FRAM interrupt flags.

`FRAMCtl.disableInterrupt` disables selected FRAM interrupt sources.

Depending on the kind of writes being performed to the FRAM, this library provides APIs for FRAM writes.

`FRAMCtl.write8` facilitates writing into the FRAM memory in byte format. `FRAMCtl.write16` facilitates writing into the FRAM memory in word format. `FRAMCtl.write32` facilitates writing into the FRAM memory in long format, pass by reference. `FRAMCtl.memoryFill32` facilitates writing into the FRAM memory in long format, pass by value.

The FRAM API is broken into 3 groups of functions: those that write into FRAM, those that handle interrupts, and those that configure the wait state.

FRAM writes are managed by

- `FRAMCtl.write8()`
- `FRAMCtl.write16()`
- `FRAMCtl.write32()`
- `FRAMCtl.memoryFill32()`

The FRAM interrupts are handled by

- `FRAMCtl.enableInterrupt()`
- `FRAMCtl.getInterruptStatus()`
- `FRAMCtl.disableInterrupt()`

The FRAM wait state is handled by

- `FRAMCtl_configureWaitStateControl()`

## 16.3 Programming Example

The following example shows some FRAM operations using the APIs

```
//Writes the value of "data", 128 times to FRAM
FRAMCtl_memoryFill32(FRAM.BASE,data,
                    (unsigned long *)FRAMCTL.TEST_START,128);
```

# 17 GPIO

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## 17.1 Introduction

The Digital I/O (GPIO) API provides a set of functions for using the MSP430Ware GPIO modules. Functions are provided to setup and enable use of input/output pins, setting them up with or without interrupts and those that access the pin value.

The digital I/O features include:

- Independently programmable individual I/Os
- Any combination of input or output
- Individually configurable P1 and P2 interrupts. Some devices may include additional port interrupts.
- Independent input and output data registers
- Individually configurable pullup or pulldown resistors

Devices within the family may have up to twelve digital I/O ports implemented (P1 to P11 and PJ). Most ports contain eight I/O lines; however, some ports may contain less (see the device-specific data sheet for ports available). Each I/O line is individually configurable for input or output direction, and each can be individually read or written. Each I/O line is individually configurable for pullup or pulldown resistors. PJ contains only four I/O lines.

Ports P1 and P2 always have interrupt capability. Each interrupt for the P1 and P2 I/O lines can be individually enabled and configured to provide an interrupt on a rising or falling edge of an input signal. All P1 I/O lines source a single interrupt vector P1IV, and all P2 I/O lines source a different, single interrupt vector P2IV. On some devices, additional ports with interrupt capability may be available (see the device-specific data sheet for details) and contain their own respective interrupt vectors. Individual ports can be accessed as byte-wide ports or can be combined into word-wide ports and accessed via word formats. Port pairs P1/P2, P3/P4, P5/P6, P7/P8, etc., are associated with the names PA, PB, PC, PD, etc., respectively. All port registers are handled in this manner with this naming convention except for the interrupt vector registers, P1IV and P2IV; that is, PAIV does not exist. When writing to port PA with word operations, all 16 bits are written to the port. When writing to the lower byte of the PA port using byte operations, the upper byte remains unchanged. Similarly, writing to the upper byte of the PA port using byte instructions leaves the lower byte unchanged. When writing to a port that contains less than the maximum number of bits possible, the unused bits are a "don't care". Ports PB, PC, PD, PE, and PF behave similarly.

Reading of the PA port using word operations causes all 16 bits to be transferred to the destination. Reading the lower or upper byte of the PA port (P1 or P2) and storing to memory using byte operations causes only the lower or upper byte to be transferred to the destination, respectively. Reading of the PA port and storing to a general-purpose register using byte operations causes the byte transferred to be written to the least significant byte of the register. The upper significant byte of the destination register is cleared automatically. Ports PB, PC, PD, PE, and PF behave similarly. When reading from ports that contain less than the maximum bits possible, unused bits are read as zeros (similarly for port PJ).

The GPIO pin may be configured as an I/O pin with `GPIO_setAsOutputPin()`, `GPIO_setAsInputPin()`, `GPIO_setAsInputPinWithPullDownresistor()` or `GPIO_setAsInputPinWithPullUpresistor()`. The GPIO pin may instead be configured to operate in the Peripheral Module assigned function by configuring the GPIO using `GPIO_setAsPeripheralModuleFunctionOutputPin()` or `GPIO_setAsPeripheralModuleFunctionInputPin()`.

## 17.2 API Functions

### Functions

- void `GPIO_setAsOutputPin` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function configures the selected Pin as output pin.*
- void `GPIO_setAsInputPin` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function configures the selected Pin as input pin.*
- void `GPIO_setAsPeripheralModuleFunctionOutputPin` (uint8\_t selectedPort, uint16\_t selectedPins, uint8\_t mode)  
*This function configures the peripheral module function in the output direction for the selected pin.*
- void `GPIO_setAsPeripheralModuleFunctionInputPin` (uint8\_t selectedPort, uint16\_t selectedPins, uint8\_t mode)  
*This function configures the peripheral module function in the input direction for the selected pin.*
- void `GPIO_setOutputHighOnPin` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function sets output HIGH on the selected Pin.*
- void `GPIO_setOutputLowOnPin` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function sets output LOW on the selected Pin.*
- void `GPIO_toggleOutputOnPin` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function toggles the output on the selected Pin.*
- void `GPIO_setAsInputPinWithPullDownResistor` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function sets the selected Pin in input Mode with Pull Down resistor.*
- void `GPIO_setAsInputPinWithPullUpResistor` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function sets the selected Pin in input Mode with Pull Up resistor.*
- uint8\_t `GPIO_getInputPinValue` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function gets the input value on the selected pin.*
- void `GPIO_enableInterrupt` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function enables the port interrupt on the selected pin.*
- void `GPIO_disableInterrupt` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function disables the port interrupt on the selected pin.*
- uint16\_t `GPIO_getInterruptStatus` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function gets the interrupt status of the selected pin.*
- void `GPIO_clearInterrupt` (uint8\_t selectedPort, uint16\_t selectedPins)  
*This function clears the interrupt flag on the selected pin.*
- void `GPIO_selectInterruptEdge` (uint8\_t selectedPort, uint16\_t selectedPins, uint8\_t edgeSelect)  
*This function selects on what edge the port interrupt flag should be set for a transition.*

### 17.2.1 Detailed Description

The GPIO API is broken into three groups of functions: those that deal with configuring the GPIO pins, those that deal with interrupts, and those that access the pin value.

The GPIO pins are configured with

- `GPIO_setAsOutputPin()`
- `GPIO_setAsInputPin()`
- `GPIO_setAsInputPinWithPullDownResistor()`
- `GPIO_setAsInputPinWithPullUpResistor()`
- `GPIO_setAsPeripheralModuleFunctionOutputPin()`
- `GPIO_setAsPeripheralModuleFunctionInputPin()`

The GPIO interrupts are handled with

- `GPIO_enableInterrupt()`
- `GPIO_disableInterrupt()`
- `GPIO_clearInterrupt()`
- `GPIO_getInterruptStatus()`
- `GPIO_selectInterruptEdge()`

The GPIO pin state is accessed with

- `GPIO_setOutputHighOnPin()`
- `GPIO_setOutputLowOnPin()`
- `GPIO_toggleOutputOnPin()`
- `GPIO_getInputPinValue()`

## 17.2.2 Function Documentation

```
void GPIO_clearInterrupt ( uint8_t selectedPort, uint16_t selectedPins )
```

This function clears the interrupt flag on the selected pin.

This function clears the interrupt flag on the selected pin. Please refer to family user's guide for available ports with interrupt capability.



## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
<i>selectedPins</i>	is the specified pin in the selected port. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"><li>■ GPIO_PIN0</li><li>■ GPIO_PIN1</li><li>■ GPIO_PIN2</li><li>■ GPIO_PIN3</li><li>■ GPIO_PIN4</li><li>■ GPIO_PIN5</li><li>■ GPIO_PIN6</li><li>■ GPIO_PIN7</li><li>■ GPIO_PIN8</li><li>■ GPIO_PIN9</li><li>■ GPIO_PIN10</li><li>■ GPIO_PIN11</li><li>■ GPIO_PIN12</li><li>■ GPIO_PIN13</li><li>■ GPIO_PIN14</li><li>■ GPIO_PIN15</li><li>■ GPIO_PIN_ALL8</li><li>■ GPIO_PIN_ALL16</li></ul>

Modified bits of **PxIFG** register.

#### Returns

None

```
void GPIO_disableInterrupt ( uint8_t selectedPort, uint16_t selectedPins )
```

This function disables the port interrupt on the selected pin.

This function disables the port interrupt on the selected pin. Please refer to family user's guide for available ports with interrupt capability.

#### Parameters

<i>selectedPort</i>	<p>is the selected port. Valid values are:</p> <ul style="list-style-type: none"> <li>■ GPIO_PORT_P1</li> <li>■ GPIO_PORT_P2</li> <li>■ GPIO_PORT_P3</li> <li>■ GPIO_PORT_P4</li> <li>■ GPIO_PORT_P5</li> <li>■ GPIO_PORT_P6</li> <li>■ GPIO_PORT_P7</li> <li>■ GPIO_PORT_P8</li> <li>■ GPIO_PORT_P9</li> <li>■ GPIO_PORT_P10</li> <li>■ GPIO_PORT_P11</li> <li>■ GPIO_PORT_PA</li> <li>■ GPIO_PORT_PB</li> <li>■ GPIO_PORT_PC</li> <li>■ GPIO_PORT_PD</li> <li>■ GPIO_PORT_PE</li> <li>■ GPIO_PORT_PF</li> <li>■ GPIO_PORT_PJ</li> </ul>
---------------------	---

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
---------------------	---

Modified bits of **PxIE** register.

**Returns**

None

```
void GPIO_enableInterrupt ( uint8_t selectedPort, uint16_t selectedPins )
```

This function enables the port interrupt on the selected pin.

This function enables the port interrupt on the selected pin. Please refer to family user's guide for available ports with interrupt capability.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
<i>selectedPins</i>	is the specified pin in the selected port. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"><li>■ GPIO_PIN0</li><li>■ GPIO_PIN1</li><li>■ GPIO_PIN2</li><li>■ GPIO_PIN3</li><li>■ GPIO_PIN4</li><li>■ GPIO_PIN5</li><li>■ GPIO_PIN6</li><li>■ GPIO_PIN7</li><li>■ GPIO_PIN8</li><li>■ GPIO_PIN9</li><li>■ GPIO_PIN10</li><li>■ GPIO_PIN11</li><li>■ GPIO_PIN12</li><li>■ GPIO_PIN13</li><li>■ GPIO_PIN14</li><li>■ GPIO_PIN15</li><li>■ GPIO_PIN_ALL8</li><li>■ GPIO_PIN_ALL16</li></ul>

Modified bits of **PxIE** register.

**Returns**

None

`uint8_t GPIO_getInputPinValue ( uint8_t selectedPort, uint16_t selectedPins )`

This function gets the input value on the selected pin.

This function gets the input value on the selected pin.

**Parameters**

<i>selectedPort</i>	<p>is the selected port. Valid values are:</p> <ul style="list-style-type: none"> <li>■ GPIO_PORT_P1</li> <li>■ GPIO_PORT_P2</li> <li>■ GPIO_PORT_P3</li> <li>■ GPIO_PORT_P4</li> <li>■ GPIO_PORT_P5</li> <li>■ GPIO_PORT_P6</li> <li>■ GPIO_PORT_P7</li> <li>■ GPIO_PORT_P8</li> <li>■ GPIO_PORT_P9</li> <li>■ GPIO_PORT_P10</li> <li>■ GPIO_PORT_P11</li> <li>■ GPIO_PORT_PA</li> <li>■ GPIO_PORT_PB</li> <li>■ GPIO_PORT_PC</li> <li>■ GPIO_PORT_PD</li> <li>■ GPIO_PORT_PE</li> <li>■ GPIO_PORT_PF</li> <li>■ GPIO_PORT_PJ</li> </ul>
---------------------	---

<i>selectedPins</i>	<p>is the specified pin in the selected port. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>GPIO_PIN0</b></li> <li>■ <b>GPIO_PIN1</b></li> <li>■ <b>GPIO_PIN2</b></li> <li>■ <b>GPIO_PIN3</b></li> <li>■ <b>GPIO_PIN4</b></li> <li>■ <b>GPIO_PIN5</b></li> <li>■ <b>GPIO_PIN6</b></li> <li>■ <b>GPIO_PIN7</b></li> <li>■ <b>GPIO_PIN8</b></li> <li>■ <b>GPIO_PIN9</b></li> <li>■ <b>GPIO_PIN10</b></li> <li>■ <b>GPIO_PIN11</b></li> <li>■ <b>GPIO_PIN12</b></li> <li>■ <b>GPIO_PIN13</b></li> <li>■ <b>GPIO_PIN14</b></li> <li>■ <b>GPIO_PIN15</b></li> <li>■ <b>GPIO_PIN_ALL8</b></li> <li>■ <b>GPIO_PIN_ALL16</b></li> </ul>
---------------------	---

**Returns**

One of the following:

- **GPIO\_INPUT\_PIN\_HIGH**
- **GPIO\_INPUT\_PIN\_LOW**

indicating the status of the pin

```
uint16_t GPIO_getInterruptStatus ( uint8_t selectedPort, uint16_t selectedPins )
```

This function gets the interrupt status of the selected pin.

This function gets the interrupt status of the selected pin. Please refer to family user's guide for available ports with interrupt capability.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
<i>selectedPins</i>	is the specified pin in the selected port. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"><li>■ GPIO_PIN0</li><li>■ GPIO_PIN1</li><li>■ GPIO_PIN2</li><li>■ GPIO_PIN3</li><li>■ GPIO_PIN4</li><li>■ GPIO_PIN5</li><li>■ GPIO_PIN6</li><li>■ GPIO_PIN7</li><li>■ GPIO_PIN8</li><li>■ GPIO_PIN9</li><li>■ GPIO_PIN10</li><li>■ GPIO_PIN11</li><li>■ GPIO_PIN12</li><li>■ GPIO_PIN13</li><li>■ GPIO_PIN14</li><li>■ GPIO_PIN15</li><li>■ GPIO_PIN_ALL8</li><li>■ GPIO_PIN_ALL16</li></ul>

**Returns**

Logical OR of any of the following:

- **GPIO\_PIN0**
- **GPIO\_PIN1**
- **GPIO\_PIN2**
- **GPIO\_PIN3**
- **GPIO\_PIN4**
- **GPIO\_PIN5**
- **GPIO\_PIN6**
- **GPIO\_PIN7**
- **GPIO\_PIN8**
- **GPIO\_PIN9**
- **GPIO\_PIN10**
- **GPIO\_PIN11**
- **GPIO\_PIN12**
- **GPIO\_PIN13**
- **GPIO\_PIN14**
- **GPIO\_PIN15**
- **GPIO\_PIN\_ALL8**
- **GPIO\_PIN\_ALL16**

indicating the interrupt status of the selected pins [Default: 0]

```
void GPIO_selectInterruptEdge ( uint8_t selectedPort, uint16_t selectedPins, uint8_t  
edgeSelect )
```

This function selects on what edge the port interrupt flag should be set for a transition.

This function selects on what edge the port interrupt flag should be set for a transition. Values for *edgeSelect* should be `GPIO_LOW_TO_HIGH_TRANSITION` or `GPIO_HIGH_TO_LOW_TRANSITION`. Please refer to family user's guide for available ports with interrupt capability.



## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
<i>selectedPins</i>	is the specified pin in the selected port. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"><li>■ GPIO_PIN0</li><li>■ GPIO_PIN1</li><li>■ GPIO_PIN2</li><li>■ GPIO_PIN3</li><li>■ GPIO_PIN4</li><li>■ GPIO_PIN5</li><li>■ GPIO_PIN6</li><li>■ GPIO_PIN7</li><li>■ GPIO_PIN8</li><li>■ GPIO_PIN9</li><li>■ GPIO_PIN10</li><li>■ GPIO_PIN11</li><li>■ GPIO_PIN12</li><li>■ GPIO_PIN13</li><li>■ GPIO_PIN14</li><li>■ GPIO_PIN15</li><li>■ GPIO_PIN_ALL8</li><li>■ GPIO_PIN_ALL16</li></ul>

<i>edgeSelect</i>	specifies what transition sets the interrupt flag Valid values are: <ul style="list-style-type: none"> <li>■ <b>GPIO_HIGH_TO_LOW_TRANSITION</b></li> <li>■ <b>GPIO_LOW_TO_HIGH_TRANSITION</b></li> </ul>
-------------------	--

Modified bits of **PxIES** register.

#### Returns

None

```
void GPIO_setAsInputPin ( uint8_t selectedPort, uint16_t selectedPins )
```

This function configures the selected Pin as input pin.

This function selected pins on a selected port as input pins.

#### Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"> <li>■ <b>GPIO_PORT_P1</b></li> <li>■ <b>GPIO_PORT_P2</b></li> <li>■ <b>GPIO_PORT_P3</b></li> <li>■ <b>GPIO_PORT_P4</b></li> <li>■ <b>GPIO_PORT_P5</b></li> <li>■ <b>GPIO_PORT_P6</b></li> <li>■ <b>GPIO_PORT_P7</b></li> <li>■ <b>GPIO_PORT_P8</b></li> <li>■ <b>GPIO_PORT_P9</b></li> <li>■ <b>GPIO_PORT_P10</b></li> <li>■ <b>GPIO_PORT_P11</b></li> <li>■ <b>GPIO_PORT_PA</b></li> <li>■ <b>GPIO_PORT_PB</b></li> <li>■ <b>GPIO_PORT_PC</b></li> <li>■ <b>GPIO_PORT_PD</b></li> <li>■ <b>GPIO_PORT_PE</b></li> <li>■ <b>GPIO_PORT_PF</b></li> <li>■ <b>GPIO_PORT_PJ</b></li> </ul>
---------------------	--

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
---------------------	---

Modified bits of **PxDIR** register, bits of **PxREN** register and bits of **PxSEL** register.

#### Returns

None

```
void GPIO_setAsInputPinWithPullDownResistor ( uint8_t selectedPort, uint16_t
selectedPins )
```

This function sets the selected Pin in input Mode with Pull Down resistor.

This function sets the selected Pin in input Mode with Pull Down resistor.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
<i>selectedPins</i>	is the specified pin in the selected port. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"><li>■ GPIO_PIN0</li><li>■ GPIO_PIN1</li><li>■ GPIO_PIN2</li><li>■ GPIO_PIN3</li><li>■ GPIO_PIN4</li><li>■ GPIO_PIN5</li><li>■ GPIO_PIN6</li><li>■ GPIO_PIN7</li><li>■ GPIO_PIN8</li><li>■ GPIO_PIN9</li><li>■ GPIO_PIN10</li><li>■ GPIO_PIN11</li><li>■ GPIO_PIN12</li><li>■ GPIO_PIN13</li><li>■ GPIO_PIN14</li><li>■ GPIO_PIN15</li><li>■ GPIO_PIN_ALL8</li><li>■ GPIO_PIN_ALL16</li></ul>

Modified bits of **PxDIR** register, bits of **PxOUT** register and bits of **PxREN** register.

**Returns**

None

```
void GPIO_setAsInputPinWithPullUpResistor ( uint8_t selectedPort, uint16_t selectedPins )
```

This function sets the selected Pin in input Mode with Pull Up resistor.

This function sets the selected Pin in input Mode with Pull Up resistor.

**Parameters**

<i>selectedPort</i>	<p>is the selected port. Valid values are:</p> <ul style="list-style-type: none"> <li>■ GPIO_PORT_P1</li> <li>■ GPIO_PORT_P2</li> <li>■ GPIO_PORT_P3</li> <li>■ GPIO_PORT_P4</li> <li>■ GPIO_PORT_P5</li> <li>■ GPIO_PORT_P6</li> <li>■ GPIO_PORT_P7</li> <li>■ GPIO_PORT_P8</li> <li>■ GPIO_PORT_P9</li> <li>■ GPIO_PORT_P10</li> <li>■ GPIO_PORT_P11</li> <li>■ GPIO_PORT_PA</li> <li>■ GPIO_PORT_PB</li> <li>■ GPIO_PORT_PC</li> <li>■ GPIO_PORT_PD</li> <li>■ GPIO_PORT_PE</li> <li>■ GPIO_PORT_PF</li> <li>■ GPIO_PORT_PJ</li> </ul>
---------------------	---

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
---------------------	---

Modified bits of **PxDIR** register, bits of **PxOUT** register and bits of **PxREN** register.

**Returns**

None

```
void GPIO_setAsOutputPin ( uint8_t selectedPort, uint16_t selectedPins )
```

This function configures the selected Pin as output pin.

This function selected pins on a selected port as output pins.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
<i>selectedPins</i>	is the specified pin in the selected port. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"><li>■ GPIO_PIN0</li><li>■ GPIO_PIN1</li><li>■ GPIO_PIN2</li><li>■ GPIO_PIN3</li><li>■ GPIO_PIN4</li><li>■ GPIO_PIN5</li><li>■ GPIO_PIN6</li><li>■ GPIO_PIN7</li><li>■ GPIO_PIN8</li><li>■ GPIO_PIN9</li><li>■ GPIO_PIN10</li><li>■ GPIO_PIN11</li><li>■ GPIO_PIN12</li><li>■ GPIO_PIN13</li><li>■ GPIO_PIN14</li><li>■ GPIO_PIN15</li><li>■ GPIO_PIN_ALL8</li><li>■ GPIO_PIN_ALL16</li></ul>

Modified bits of **PxDIR** register and bits of **PxSEL** register.

#### Returns

None

```
void GPIO_setAsPeripheralModuleFunctionInputPin ( uint8_t selectedPort, uint16_t
selectedPins, uint8_t mode )
```

This function configures the peripheral module function in the input direction for the selected pin.

This function configures the peripheral module function in the input direction for the selected pin for either primary, secondary or ternary module function modes. Note that MSP430F5xx/6xx family doesn't support these function modes.

#### Parameters

<i>selectedPort</i>	<p>is the selected port. Valid values are:</p> <ul style="list-style-type: none"> <li>■ GPIO_PORT_P1</li> <li>■ GPIO_PORT_P2</li> <li>■ GPIO_PORT_P3</li> <li>■ GPIO_PORT_P4</li> <li>■ GPIO_PORT_P5</li> <li>■ GPIO_PORT_P6</li> <li>■ GPIO_PORT_P7</li> <li>■ GPIO_PORT_P8</li> <li>■ GPIO_PORT_P9</li> <li>■ GPIO_PORT_P10</li> <li>■ GPIO_PORT_P11</li> <li>■ GPIO_PORT_PA</li> <li>■ GPIO_PORT_PB</li> <li>■ GPIO_PORT_PC</li> <li>■ GPIO_PORT_PD</li> <li>■ GPIO_PORT_PE</li> <li>■ GPIO_PORT_PF</li> <li>■ GPIO_PORT_PJ</li> </ul>
---------------------	---



<i>selectedPins</i>	is the specified pin in the selected port. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"><li>■ GPIO_PIN0</li><li>■ GPIO_PIN1</li><li>■ GPIO_PIN2</li><li>■ GPIO_PIN3</li><li>■ GPIO_PIN4</li><li>■ GPIO_PIN5</li><li>■ GPIO_PIN6</li><li>■ GPIO_PIN7</li><li>■ GPIO_PIN8</li><li>■ GPIO_PIN9</li><li>■ GPIO_PIN10</li><li>■ GPIO_PIN11</li><li>■ GPIO_PIN12</li><li>■ GPIO_PIN13</li><li>■ GPIO_PIN14</li><li>■ GPIO_PIN15</li><li>■ GPIO_PIN_ALL8</li><li>■ GPIO_PIN_ALL16</li></ul>
---------------------	---

<i>mode</i>	<p>is the specified mode that the pin should be configured for the module function. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>GPIO_PRIMARY_MODULE_FUNCTION</b></li> <li>■ <b>GPIO_SECONDARY_MODULE_FUNCTION</b></li> <li>■ <b>GPIO_TERNARY_MODULE_FUNCTION</b></li> </ul>
-------------	---

Modified bits of **PxDIR** register and bits of **PxSEL** register.

#### Returns

None

```
void GPIO_setAsPeripheralModuleFunctionOutputPin ( uint8_t selectedPort, uint16_t
selectedPins, uint8_t mode )
```

This function configures the peripheral module function in the output direction for the selected pin.

This function configures the peripheral module function in the output direction for the selected pin for either primary, secondary or ternary module function modes. Note that MSP430F5xx/6xx family doesn't support these function modes.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
<i>selectedPins</i>	is the specified pin in the selected port. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"><li>■ GPIO_PIN0</li><li>■ GPIO_PIN1</li><li>■ GPIO_PIN2</li><li>■ GPIO_PIN3</li><li>■ GPIO_PIN4</li><li>■ GPIO_PIN5</li><li>■ GPIO_PIN6</li><li>■ GPIO_PIN7</li><li>■ GPIO_PIN8</li><li>■ GPIO_PIN9</li><li>■ GPIO_PIN10</li><li>■ GPIO_PIN11</li><li>■ GPIO_PIN12</li><li>■ GPIO_PIN13</li><li>■ GPIO_PIN14</li><li>■ GPIO_PIN15</li><li>■ GPIO_PIN_ALL8</li><li>■ GPIO_PIN_ALL16</li></ul>

<i>mode</i>	<p>is the specified mode that the pin should be configured for the module function. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>GPIO_PRIMARY_MODULE_FUNCTION</b></li> <li>■ <b>GPIO_SECONDARY_MODULE_FUNCTION</b></li> <li>■ <b>GPIO_TERNARY_MODULE_FUNCTION</b></li> </ul>
-------------	---

Modified bits of **PxDIR** register and bits of **PxSEL** register.

#### Returns

None

```
void GPIO_setOutputHighOnPin ( uint8_t selectedPort, uint16_t selectedPins )
```

This function sets output HIGH on the selected Pin.

This function sets output HIGH on the selected port's pin.

#### Parameters

<i>selectedPort</i>	<p>is the selected port. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>GPIO_PORT_P1</b></li> <li>■ <b>GPIO_PORT_P2</b></li> <li>■ <b>GPIO_PORT_P3</b></li> <li>■ <b>GPIO_PORT_P4</b></li> <li>■ <b>GPIO_PORT_P5</b></li> <li>■ <b>GPIO_PORT_P6</b></li> <li>■ <b>GPIO_PORT_P7</b></li> <li>■ <b>GPIO_PORT_P8</b></li> <li>■ <b>GPIO_PORT_P9</b></li> <li>■ <b>GPIO_PORT_P10</b></li> <li>■ <b>GPIO_PORT_P11</b></li> <li>■ <b>GPIO_PORT_PA</b></li> <li>■ <b>GPIO_PORT_PB</b></li> <li>■ <b>GPIO_PORT_PC</b></li> <li>■ <b>GPIO_PORT_PD</b></li> <li>■ <b>GPIO_PORT_PE</b></li> <li>■ <b>GPIO_PORT_PF</b></li> <li>■ <b>GPIO_PORT_PJ</b></li> </ul>
---------------------	---

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
---------------------	---

Modified bits of **PxOUT** register.

**Returns**

None

```
void GPIO_setOutputLowOnPin ( uint8_t selectedPort, uint16_t selectedPins )
```

This function sets output LOW on the selected Pin.

This function sets output LOW on the selected port's pin.

## Parameters

<i>selectedPort</i>	is the selected port. Valid values are: <ul style="list-style-type: none"><li>■ GPIO_PORT_P1</li><li>■ GPIO_PORT_P2</li><li>■ GPIO_PORT_P3</li><li>■ GPIO_PORT_P4</li><li>■ GPIO_PORT_P5</li><li>■ GPIO_PORT_P6</li><li>■ GPIO_PORT_P7</li><li>■ GPIO_PORT_P8</li><li>■ GPIO_PORT_P9</li><li>■ GPIO_PORT_P10</li><li>■ GPIO_PORT_P11</li><li>■ GPIO_PORT_PA</li><li>■ GPIO_PORT_PB</li><li>■ GPIO_PORT_PC</li><li>■ GPIO_PORT_PD</li><li>■ GPIO_PORT_PE</li><li>■ GPIO_PORT_PF</li><li>■ GPIO_PORT_PJ</li></ul>
<i>selectedPins</i>	is the specified pin in the selected port. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"><li>■ GPIO_PIN0</li><li>■ GPIO_PIN1</li><li>■ GPIO_PIN2</li><li>■ GPIO_PIN3</li><li>■ GPIO_PIN4</li><li>■ GPIO_PIN5</li><li>■ GPIO_PIN6</li><li>■ GPIO_PIN7</li><li>■ GPIO_PIN8</li><li>■ GPIO_PIN9</li><li>■ GPIO_PIN10</li><li>■ GPIO_PIN11</li><li>■ GPIO_PIN12</li><li>■ GPIO_PIN13</li><li>■ GPIO_PIN14</li><li>■ GPIO_PIN15</li><li>■ GPIO_PIN_ALL8</li><li>■ GPIO_PIN_ALL16</li></ul>

Modified bits of **PxOUT** register.

**Returns**

None

```
void GPIO_toggleOutputOnPin ( uint8_t selectedPort, uint16_t selectedPins )
```

This function toggles the output on the selected Pin.

This function toggles the output on the selected port's pin.

**Parameters**

<i>selectedPort</i>	<p>is the selected port. Valid values are:</p> <ul style="list-style-type: none"> <li>■ GPIO_PORT_P1</li> <li>■ GPIO_PORT_P2</li> <li>■ GPIO_PORT_P3</li> <li>■ GPIO_PORT_P4</li> <li>■ GPIO_PORT_P5</li> <li>■ GPIO_PORT_P6</li> <li>■ GPIO_PORT_P7</li> <li>■ GPIO_PORT_P8</li> <li>■ GPIO_PORT_P9</li> <li>■ GPIO_PORT_P10</li> <li>■ GPIO_PORT_P11</li> <li>■ GPIO_PORT_PA</li> <li>■ GPIO_PORT_PB</li> <li>■ GPIO_PORT_PC</li> <li>■ GPIO_PORT_PD</li> <li>■ GPIO_PORT_PE</li> <li>■ GPIO_PORT_PF</li> <li>■ GPIO_PORT_PJ</li> </ul>
---------------------	---

<i>selectedPins</i>	<p>is the specified pin in the selected port. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ GPIO_PIN0</li> <li>■ GPIO_PIN1</li> <li>■ GPIO_PIN2</li> <li>■ GPIO_PIN3</li> <li>■ GPIO_PIN4</li> <li>■ GPIO_PIN5</li> <li>■ GPIO_PIN6</li> <li>■ GPIO_PIN7</li> <li>■ GPIO_PIN8</li> <li>■ GPIO_PIN9</li> <li>■ GPIO_PIN10</li> <li>■ GPIO_PIN11</li> <li>■ GPIO_PIN12</li> <li>■ GPIO_PIN13</li> <li>■ GPIO_PIN14</li> <li>■ GPIO_PIN15</li> <li>■ GPIO_PIN_ALL8</li> <li>■ GPIO_PIN_ALL16</li> </ul>
---------------------	---

Modified bits of **PxOUT** register.

#### Returns

None

## 17.3 Programming Example

The following example shows how to use the GPIO API. A trigger is generated on a hi "TO" low transition on P1.4 (pulled-up input pin), which will generate P1\_ISR. In the ISR, we toggle P1.0 (output pin).

```

//Set P1.0 to output direction
GPIO_setAsOutputPin(
    GPIO_PORT_P1,
    GPIO_PIN0
);

//Enable P1.4 internal resistance as pull-Up resistance
GPIO_setAsInputPinWithPullUpresistor(

    GPIO_PORT_P1,
    GPIO_PIN4
);

//P1.4 interrupt enabled
GPIO_enableInterrupt(

```



```

        GPIO_PORT_P1,
        GPIO_PIN4
    );

    //P1.4 Hi/Lo edge
    GPIO_selectInterruptEdge(
        GPIO_PORT_P1,
        GPIO_PIN4,
        GPIO_HIGH_TO_LOW_TRANSITION
    );

    //P1.4 IFG cleared
    GPIO_clearInterrupt(

        GPIO_PORT_P1,
        GPIO_PIN4
    );

    //Enter LPM4 w/interrupt
    _bis_SR_register(LPM4_bits + GIE);

    //For debugger
    _no_operation();
}

//*****
//
//This is the PORT1_VECTOR interrupt vector service routine
//
//*****
#pragma vector=PORT1_VECTOR
__interrupt void Port_1 (void)
{
    //P1.0 = toggle
    GPIO_toggleOutputOnPin(
        GPIO_PORT_P1,
        GPIO_PIN0
    );

    //P1.4 IFG cleared
    GPIO_clearInterrupt(
        GPIO_PORT_P1,
        GPIO_PIN4
    );
}

```

# 18 Memory Protection Unit (MPU)

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## 18.1 Introduction

The MPU protects against accidental writes to designated read-only memory segments or execution of code from a constant memory segment memory. Clearing the MPUENA bit disables the MPU, making the complete memory accessible for read, write, and execute operations. After a BOR, the complete memory is accessible without restrictions for read, write, and execute operations.

MPU features include:

- Main memory can be configured up to three segments of variable size
- Access rights for each segment can be set independently
- Information memory can have its access rights set independently
- All MPU registers are protected from access by password

## 18.2 API Functions

### Macros

- `#define MPU_MAX_SEG_VALUE 20`

### Functions

- void `MPU_initTwoSegments` (uint16\_t baseAddress, uint16\_t seg1boundary, uint8\_t seg1accmask, uint8\_t seg2accmask)  
*Initializes MPU with two memory segments.*
- void `MPU_initThreeSegments` (uint16\_t baseAddress, `MPU_initThreeSegmentsParam` \*param)  
*Initializes MPU with three memory segments.*
- void `MPU_initInfoSegment` (uint16\_t baseAddress, uint8\_t accmask)  
*Initializes user information memory segment.*
- void `MPU_start` (uint16\_t baseAddress)  
*The following function enables the MPU module in the device.*
- void `MPU_enablePUCOnViolation` (uint16\_t baseAddress, uint16\_t segment)  
*The following function enables PUC generation when an access violation has occurred on the memory segment selected by the user.*
- void `MPU_disablePUCOnViolation` (uint16\_t baseAddress, uint16\_t segment)  
*The following function disables PUC generation when an access violation has occurred on the memory segment selected by the user.*
- uint16\_t `MPU_getInterruptStatus` (uint16\_t baseAddress, uint16\_t memAccFlag)



- **MPU\_SEG\_INFO\_ACCESS\_VIOLATION** is set if an access violation in User Information Memory Segment is detected indicating the status of the interrupt flags.

```
uint16_t MPU_clearInterrupt ( uint16_t baseAddress, uint16_t memAccFlag )
```

Clears the masked interrupt flags.

Returns the memory segment violation flag status requested by the user or if user is providing a bit mask value, the function will return a value indicating if all flags were cleared.

#### Parameters

<i>baseAddress</i>	is the base address of the MPU module.
<i>memAccFlag</i>	is the is the memory access violation flag. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>MPU_SEG_1_ACCESS_VIOLATION</b> - is set if an access violation in Main Memory Segment 1 is detected</li> <li>■ <b>MPU_SEG_2_ACCESS_VIOLATION</b> - is set if an access violation in Main Memory Segment 2 is detected</li> <li>■ <b>MPU_SEG_3_ACCESS_VIOLATION</b> - is set if an access violation in Main Memory Segment 3 is detected</li> <li>■ <b>MPU_SEG_INFO_ACCESS_VIOLATION</b> - is set if an access violation in User Information Memory Segment is detected</li> </ul>

#### Returns

Logical OR of any of the following:

- **MPU\_SEG\_1\_ACCESS\_VIOLATION** is set if an access violation in Main Memory Segment 1 is detected
- **MPU\_SEG\_2\_ACCESS\_VIOLATION** is set if an access violation in Main Memory Segment 2 is detected
- **MPU\_SEG\_3\_ACCESS\_VIOLATION** is set if an access violation in Main Memory Segment 3 is detected
- **MPU\_SEG\_INFO\_ACCESS\_VIOLATION** is set if an access violation in User Information Memory Segment is detected indicating the status of the masked flags.

```
void MPU_disablePUCOnViolation ( uint16_t baseAddress, uint16_t segment )
```

The following function disables PUC generation when an access violation has occurred on the memory segment selected by the user.

Note that only specified segments for PUC generation are disabled. Other segments for PUC generation are left untouched. Users may call [MPU\\_enablePUCOnViolation\(\)](#) and [MPU\\_disablePUCOnViolation\(\)](#) to assure that all the bits will be set and/or cleared.

**Parameters**

<i>baseAddress</i>	is the base address of the MPU module.
<i>segment</i>	is the bit mask of memory segment that will NOT generate a PUC when an access violation occurs. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>MPU_FIRST_SEG</b> - PUC generation on first memory segment</li> <li>■ <b>MPU_SECOND_SEG</b> - PUC generation on second memory segment</li> <li>■ <b>MPU_THIRD_SEG</b> - PUC generation on third memory segment</li> <li>■ <b>MPU_INFO_SEG</b> - PUC generation on user information memory segment</li> </ul>

Modified bits of **MPUSAM** register and bits of **MPUCTL0** register.

**Returns**

None

```
void MPU_enablePUCOnViolation ( uint16_t baseAddress, uint16_t segment )
```

The following function enables PUC generation when an access violation has occurred on the memory segment selected by the user.

Note that only specified segments for PUC generation are enabled. Other segments for PUC generation are left untouched. Users may call [MPU\\_enablePUCOnViolation\(\)](#) and [MPU\\_disablePUCOnViolation\(\)](#) to assure that all the bits will be set and/or cleared.

**Parameters**

<i>baseAddress</i>	is the base address of the MPU module.
<i>segment</i>	is the bit mask of memory segment that will generate a PUC when an access violation occurs. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>MPU_FIRST_SEG</b> - PUC generation on first memory segment</li> <li>■ <b>MPU_SECOND_SEG</b> - PUC generation on second memory segment</li> <li>■ <b>MPU_THIRD_SEG</b> - PUC generation on third memory segment</li> <li>■ <b>MPU_INFO_SEG</b> - PUC generation on user information memory segment</li> </ul>

Modified bits of **MPUSAM** register and bits of **MPUCTL0** register.

**Returns**

None

```
uint16_t MPU_getInterruptStatus ( uint16_t baseAddress, uint16_t memAccFlag )
```

Returns the memory segment violation flag status requested by the user.

## Parameters

<i>baseAddress</i>	is the base address of the MPU module.
<i>memAccFlag</i>	is the is the memory access violation flag. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>MPU_SEG_1_ACCESS_VIOLATION</b> - is set if an access violation in Main Memory Segment 1 is detected</li> <li>■ <b>MPU_SEG_2_ACCESS_VIOLATION</b> - is set if an access violation in Main Memory Segment 2 is detected</li> <li>■ <b>MPU_SEG_3_ACCESS_VIOLATION</b> - is set if an access violation in Main Memory Segment 3 is detected</li> <li>■ <b>MPU_SEG_INFO_ACCESS_VIOLATION</b> - is set if an access violation in User Information Memory Segment is detected</li> </ul>

## Returns

Logical OR of any of the following:

- **MPU\_SEG\_1\_ACCESS\_VIOLATION** is set if an access violation in Main Memory Segment 1 is detected
  - **MPU\_SEG\_2\_ACCESS\_VIOLATION** is set if an access violation in Main Memory Segment 2 is detected
  - **MPU\_SEG\_3\_ACCESS\_VIOLATION** is set if an access violation in Main Memory Segment 3 is detected
  - **MPU\_SEG\_INFO\_ACCESS\_VIOLATION** is set if an access violation in User Information Memory Segment is detected
- indicating the status of the masked flags.

```
void MPU_initInfoSegment ( uint16_t baseAddress, uint8_t accmask )
```

Initializes user information memory segment.

This function initializes user information memory segment with specified access rights.

## Parameters

<i>baseAddress</i>	is the base address of the MPU module.
<i>accmask</i>	is the bit mask of access right for user information memory segment. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>MPU_READ</b> - Read rights</li> <li>■ <b>MPU_WRITE</b> - Write rights</li> <li>■ <b>MPU_EXEC</b> - Execute rights</li> <li>■ <b>MPU_NO_READ_WRITE_EXEC</b> - no read/write/execute rights</li> </ul>

Modified bits of **MPUSAM** register and bits of **MPUCTL0** register.

## Returns

None

```
void MPU_initThreeSegments ( uint16_t baseAddress, MPU_initThreeSegmentsParam *
    param )
```

Initializes MPU with three memory segments.

This function creates three memory segments in FRAM allowing the user to set access right to each segment. To set the correct value for *seg1boundary*, the user must consult the Device Family User's Guide and provide the MPUSBx value corresponding to the memory address where the user wants to create the partition. Consult the "Segment Border Setting" section in the User's Guide to find the options available for MPUSBx.

#### Parameters

<i>baseAddress</i>	is the base address of the MPU module.
<i>param</i>	is the pointer to struct for initializing three segments.

Modified bits of **MPUSAM** register, bits of **MPUSEG** register and bits of **MPUCTL0** register.

#### Returns

None

References `MPU_initThreeSegmentsParam::seg1accmask`,  
`MPU_initThreeSegmentsParam::seg1boundary`, `MPU_initThreeSegmentsParam::seg2accmask`,  
`MPU_initThreeSegmentsParam::seg2boundary`, and  
`MPU_initThreeSegmentsParam::seg3accmask`.

```
void MPU_initTwoSegments ( uint16_t baseAddress, uint16_t seg1boundary, uint8_t
    seg1accmask, uint8_t seg2accmask )
```

Initializes MPU with two memory segments.

This function creates two memory segments in FRAM allowing the user to set access right to each segment. To set the correct value for *seg1boundary*, the user must consult the Device Family User's Guide and provide the MPUSBx value corresponding to the memory address where the user wants to create the partition. Consult the "Segment Border Setting" section in the User's Guide to find the options available for MPUSBx.

#### Parameters

<i>baseAddress</i>	is the base address of the MPU module.
<i>seg1boundary</i>	Valid values can be found in the Family User's Guide
<i>seg1accmask</i>	is the bit mask of access right for memory segment 1. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>MPU_READ</b> - Read rights</li> <li>■ <b>MPU_WRITE</b> - Write rights</li> <li>■ <b>MPU_EXEC</b> - Execute rights</li> <li>■ <b>MPU_NO_READ_WRITE_EXEC</b> - no read/write/execute rights</li> </ul>

<i>seg2accmask</i>	is the bit mask of access right for memory segment 2 Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>MPU_READ</b> - Read rights</li> <li>■ <b>MPU_WRITE</b> - Write rights</li> <li>■ <b>MPU_EXEC</b> - Execute rights</li> <li>■ <b>MPU_NO_READ_WRITE_EXEC</b> - no read/write/execute rights</li> </ul>
--------------------	---

Modified bits of **MPUSAM** register, bits of **MPUSEG** register and bits of **MPUCTL0** register.

#### Returns

None

```
void MPU_start ( uint16_t baseAddress )
```

The following function enables the MPU module in the device.

This function needs to be called once all memory segmentation has been done. If this function is not called the MPU module will not be activated.

#### Parameters

<i>baseAddress</i>	is the base address of the MPU module.
--------------------	--

Modified bits of **MPUCTL0** register.

#### Returns

None

## 18.3 Programming Example

The following example shows some MPU operations using the APIs

```
//Initialize struct for three segments configuration
MPU_initThreeSegmentsParam threeSegParam;
threeSegParam.seg1boundary = 0x04;
threeSegParam.seg1boundary = 0x08;
threeSegParam.seg1accmask = MPU_READ|MPU_WRITE|MPU_EXEC;
threeSegParam.seg2accmask = MPU_READ;
threeSegParam.seg3accmask = MPU_READ|MPU_WRITE|MPU_EXEC;

//Define memory segment boundaries and set access right for each memory segment
MPU_initThreeSegments(MPU_BASE, &threeSegParam);

// Configures MPU to generate a PUC on access violation on the second segment
MPU_enablePUCOnViolation(MPU_BASE, MPU_SECOND_SEG);

//Enables the MPU module
MPU_start(MPU_BASE);
```



# 19 32-Bit Hardware Multiplier (MPY32)

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## 19.1 Introduction

The 32-Bit Hardware Multiplier (MPY32) API provides a set of functions for using the MSP430Ware MPY32 modules. Functions are provided to setup the MPY32 modules, set the operand registers, and obtain the results.

The MPY32 Modules does not generate any interrupts.

## 19.2 API Functions

### Functions

- void [MPY32\\_setWriteDelay](#) (uint16\_t writeDelaySelect)  
*Sets the write delay setting for the MPY32 module.*
- void [MPY32\\_enableSaturationMode](#) (void)  
*Enables Saturation Mode.*
- void [MPY32\\_disableSaturationMode](#) (void)  
*Disables Saturation Mode.*
- uint8\_t [MPY32\\_getSaturationMode](#) (void)  
*Gets the Saturation Mode.*
- void [MPY32\\_enableFractionalMode](#) (void)  
*Enables Fraction Mode.*
- void [MPY32\\_disableFractionalMode](#) (void)  
*Disables Fraction Mode.*
- uint8\_t [MPY32\\_getFractionalMode](#) (void)  
*Gets the Fractional Mode.*
- void [MPY32\\_setOperandOne8Bit](#) (uint8\_t multiplicationType, uint8\_t operand)  
*Sets an 8-bit value into operand 1.*
- void [MPY32\\_setOperandOne16Bit](#) (uint8\_t multiplicationType, uint16\_t operand)  
*Sets an 16-bit value into operand 1.*
- void [MPY32\\_setOperandOne24Bit](#) (uint8\_t multiplicationType, uint32\_t operand)  
*Sets an 24-bit value into operand 1.*
- void [MPY32\\_setOperandOne32Bit](#) (uint8\_t multiplicationType, uint32\_t operand)  
*Sets an 32-bit value into operand 1.*
- void [MPY32\\_setOperandTwo8Bit](#) (uint8\_t operand)  
*Sets an 8-bit value into operand 2, which starts the multiplication.*
- void [MPY32\\_setOperandTwo16Bit](#) (uint16\_t operand)  
*Sets an 16-bit value into operand 2, which starts the multiplication.*
- void [MPY32\\_setOperandTwo24Bit](#) (uint32\_t operand)  
*Sets an 24-bit value into operand 2, which starts the multiplication.*
- void [MPY32\\_setOperandTwo32Bit](#) (uint32\_t operand)

- Sets an 32-bit value into operand 2, which starts the multiplication.*
- `uint64_t MPY32_getResult` (void)
  - Returns an 64-bit result of the last multiplication operation.*
- `uint16_t MPY32_getSumExtension` (void)
  - Returns the Sum Extension of the last multiplication operation.*
- `uint16_t MPY32_getCarryBitValue` (void)
  - Returns the Carry Bit of the last multiplication operation.*
- `void MPY32_clearCarryBitValue` (void)
  - Clears the Carry Bit of the last multiplication operation.*
- `void MPY32_preloadResult` (`uint64_t` result)
  - Preloads the result register.*

## 19.2.1 Detailed Description

The MPY32 API is broken into three groups of functions: those that control the settings, those that set the operand registers, and those that return the results, sum extension, and carry bit value.

The settings are handled by

- `MPY32_setWriteDelay()`
- `MPY32_enableSaturationMode()`
- `MPY32_disableSaturationMode()`
- `MPY32_enableFractionalMode()`
- `MPY32_disableFractionalMode()`
- `MPY32_preloadResult()`

The operand registers are set by

- `MPY32_setOperandOne8Bit()`
- `MPY32_setOperandOne16Bit()`
- `MPY32_setOperandOne24Bit()`
- `MPY32_setOperandOne32Bit()`
- `MPY32_setOperandTwo8Bit()`
- `MPY32_setOperandTwo16Bit()`
- `MPY32_setOperandTwo24Bit()`
- `MPY32_setOperandTwo32Bit()`

The results can be returned by

- `MPY32_getResult()`
- `MPY32_getSumExtension()`
- `MPY32_getCarryBitValue()`
- `MPY32_getSaturationMode()`
- `MPY32_getFractionalMode()`

## 19.2.2 Function Documentation

`void MPY32_clearCarryBitValue ( void )`

Clears the Carry Bit of the last multiplication operation.

This function clears the Carry Bit of the MPY module

**Returns**

The value of the MPY32 module Carry Bit 0x0 or 0x1.

`void MPY32_disableFractionalMode ( void )`

Disables Fraction Mode.

This function disables fraction mode.

**Returns**

None

`void MPY32_disableSaturationMode ( void )`

Disables Saturation Mode.

This function disables saturation mode, which allows the raw result of the MPY result registers to be returned.

**Returns**

None

`void MPY32_enableFractionalMode ( void )`

Enables Fraction Mode.

This function enables fraction mode.

**Returns**

None

`void MPY32_enableSaturationMode ( void )`

Enables Saturation Mode.

This function enables saturation mode. When this is enabled, the result read out from the MPY result registers is converted to the most-positive number in the case of an overflow, or the most-negative number in the case of an underflow. Please note, that the raw value in the registers does not reflect the result returned, and if the saturation mode is disabled, then the raw value of the registers will be returned instead.

**Returns**

None

uint16\_t MPY32\_getCarryBitValue ( void )

Returns the Carry Bit of the last multiplication operation.

This function returns the Carry Bit of the MPY module, which either gives the sign after a signed operation or shows a carry after a multiply- and- accumulate operation.

**Returns**

The value of the MPY32 module Carry Bit 0x0 or 0x1.

uint8\_t MPY32\_getFractionalMode ( void )

Gets the Fractional Mode.

This function gets the current fractional mode.

**Returns**

Gets the fractional mode Return one of the following:

- **MPY32\_FRACTIONAL\_MODE\_DISABLED**
- **MPY32\_FRACTIONAL\_MODE\_ENABLED**

Gets the Fractional Mode

uint64\_t MPY32\_getResult ( void )

Returns an 64-bit result of the last multiplication operation.

This function returns all 64 bits of the result registers

**Returns**

The 64-bit result is returned as a uint64\_t type

uint8\_t MPY32\_getSaturationMode ( void )

Gets the Saturation Mode.

This function gets the current saturation mode.

**Returns**

Gets the Saturation Mode Return one of the following:

- **MPY32\_SATURATION\_MODE\_DISABLED**
- **MPY32\_SATURATION\_MODE\_ENABLED**

Gets the Saturation Mode

uint16\_t MPY32\_getSumExtension ( void )

Returns the Sum Extension of the last multiplication operation.

This function returns the Sum Extension of the MPY module, which either gives the sign after a signed operation or shows a carry after a multiply- and-accumulate operation. The Sum Extension acts as a check for overflows or underflows.

**Returns**

The value of the MPY32 module Sum Extension.

void MPY32\_preloadResult ( uint64\_t *result* )

Preloads the result register.

This function Preloads the result register

**Parameters**

<i>result</i>	value to preload the result register to
---------------	---

**Returns**

None

void MPY32\_setOperandOne16Bit ( uint8\_t *multiplicationType*, uint16\_t *operand* )

Sets an 16-bit value into operand 1.

This function sets the first operand for multiplication and determines what type of operation should be performed. Once the second operand is set, then the operation will begin.

**Parameters**

<i>multiplicationType</i>	<p>is the type of multiplication to perform once the second operand is set. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>MPY32_MULTIPLY_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLY_SIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_SIGNED</b></li> </ul>
---------------------------	--

<i>operand</i>	is the 16-bit value to load into the 1st operand.
----------------	---

**Returns**

None

```
void MPY32_setOperandOne24Bit ( uint8_t multiplicationType, uint32_t operand )
```

Sets an 24-bit value into operand 1.

This function sets the first operand for multiplication and determines what type of operation should be performed. Once the second operand is set, then the operation will begin.

**Parameters**

<i>multiplicationType</i>	is the type of multiplication to perform once the second operand is set. Valid values are: <ul style="list-style-type: none"> <li>■ <b>MPY32_MULTIPLY_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLY_SIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_SIGNED</b></li> </ul>
<i>operand</i>	is the 24-bit value to load into the 1st operand.

**Returns**

None

```
void MPY32_setOperandOne32Bit ( uint8_t multiplicationType, uint32_t operand )
```

Sets an 32-bit value into operand 1.

This function sets the first operand for multiplication and determines what type of operation should be performed. Once the second operand is set, then the operation will begin.

**Parameters**

<i>multiplicationType</i>	is the type of multiplication to perform once the second operand is set. Valid values are: <ul style="list-style-type: none"> <li>■ <b>MPY32_MULTIPLY_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLY_SIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_SIGNED</b></li> </ul>
---------------------------	---

<i>operand</i>	is the 32-bit value to load into the 1st operand.
----------------	---

**Returns**

None

```
void MPY32_setOperandOne8Bit ( uint8_t multiplicationType, uint8_t operand )
```

Sets an 8-bit value into operand 1.

This function sets the first operand for multiplication and determines what type of operation should be performed. Once the second operand is set, then the operation will begin.

**Parameters**

<i>multiplicationType</i>	is the type of multiplication to perform once the second operand is set. Valid values are: <ul style="list-style-type: none"> <li>■ <b>MPY32_MULTIPLY_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLY_SIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_UNSIGNED</b></li> <li>■ <b>MPY32_MULTIPLYACCUMULATE_SIGNED</b></li> </ul>
<i>operand</i>	is the 8-bit value to load into the 1st operand.

**Returns**

None

```
void MPY32_setOperandTwo16Bit ( uint16_t operand )
```

Sets an 16-bit value into operand 2, which starts the multiplication.

This function sets the second operand of the multiplication operation and starts the operation.

**Parameters**

<i>operand</i>	is the 16-bit value to load into the 2nd operand.
----------------	---

**Returns**

None

```
void MPY32_setOperandTwo24Bit ( uint32_t operand )
```

Sets an 24-bit value into operand 2, which starts the multiplication.

This function sets the second operand of the multiplication operation and starts the operation.

**Parameters**

<i>operand</i>	is the 24-bit value to load into the 2nd operand.
----------------	---

**Returns**

None

```
void MPY32_setOperandTwo32Bit ( uint32_t operand )
```

Sets an 32-bit value into operand 2, which starts the multiplication.

This function sets the second operand of the multiplication operation and starts the operation.

**Parameters**

<i>operand</i>	is the 32-bit value to load into the 2nd operand.
----------------	---

**Returns**

None

```
void MPY32_setOperandTwo8Bit ( uint8_t operand )
```

Sets an 8-bit value into operand 2, which starts the multiplication.

This function sets the second operand of the multiplication operation and starts the operation.

**Parameters**

<i>operand</i>	is the 8-bit value to load into the 2nd operand.
----------------	--

**Returns**

None

```
void MPY32_setWriteDelay ( uint16_t writeDelaySelect )
```

Sets the write delay setting for the MPY32 module.

This function sets up a write delay to the MPY module's registers, which holds any writes to the registers until all calculations are complete. There are two different settings, one which waits for 32-bit results to be ready, and one which waits for 64-bit results to be ready. This prevents unpredictable results if registers are changed before the results are ready.





## 20 Power Management Module (PMM)

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### 20.1 Introduction

The PMM manages all functions related to the power supply and its supervision for the device. Its primary functions are first to generate a supply voltage for the core logic, and second, provide several mechanisms for the supervision of the voltage applied to the device (DVCC).

The PMM uses an integrated low-dropout voltage regulator (LDO) to produce a secondary core voltage (VCORE) from the primary one applied to the device (DVCC). In general, VCORE supplies the CPU, memories, and the digital modules, while DVCC supplies the I/Os and analog modules. The VCORE output is maintained using a dedicated voltage reference. The input or primary side of the regulator is referred to as its high side. The output or secondary side is referred to as its low side.

### 20.2 API Functions

#### Functions

- void [PMM.enableSVSL](#) (void)  
*Enables the low-side SVS circuitry.*
- void [PMM.disableSVSL](#) (void)  
*Disables the low-side SVS circuitry.*
- void [PMM.enableSVSH](#) (void)  
*Enables the high-side SVS circuitry.*
- void [PMM.disableSVSH](#) (void)  
*Disables the high-side SVS circuitry.*
- void [PMM.turnOnRegulator](#) (void)  
*Makes the low-dropout voltage regulator (LDO) remain ON when going into LPM 3/4.*
- void [PMM.turnOffRegulator](#) (void)  
*Turns OFF the low-dropout voltage regulator (LDO) when going into LPM3/4, thus the system will enter LPM3.5 or LPM4.5 respectively.*
- void [PMM.trigPOR](#) (void)  
*Calling this function will trigger a software Power On Reset (POR).*
- void [PMM.trigBOR](#) (void)  
*Calling this function will trigger a software Brown Out Rest (BOR).*
- void [PMM.clearInterrupt](#) (uint16\_t mask)  
*Clears interrupt flags for the PMM.*
- uint16\_t [PMM.getInterruptStatus](#) (uint16\_t mask)  
*Returns interrupt status.*
- void [PMM.unlockLPM5](#) (void)  
*Unlock LPM5.*

## 20.2.1 Detailed Description

**PMM\_enableSVSH()** / **PMM\_disableSVSH()** If disabled on FR57xx, High-side SVS (SVSH) is disabled in LPM4.5. SVSH is always enabled in active mode and LPM0/1/2/3/4 and LPM3.5. If enabled, SVSH is always enabled. Note: this API has different functionality depending on the part.

**PMM\_enableSVSL()** / **PMM\_disableSVSL()** If disabled, Low-side SVS (SVSL) is disabled in low power modes. SVSL is always enabled in active mode and LPM0. If enabled, SVSL is enabled in LPM0/1/2. SVSL is always enabled in AM and always disabled in LPM3/4 and LPM3.5/4.5.

**PMM\_turnOffRegulator()** / **PMM\_turnOnRegulator()** If off, Regulator is turned off when going to LPM3/4. System enters LPM3.5 or LPM4.5, respectively. If on, Regulator remains on when going into LPM3/4

**PMM\_clearInterrupt()** Clear selected or all interrupt flags for the PMM

**PMM\_getInterruptStatus()** Returns interrupt status of the selected flag in the PMM module

**PMM\_lockLPM5()** / **PMM\_unlockLPM5()** If unlocked, LPMx.5 configuration is not locked and defaults to its reset condition. if locked, LPMx.5 configuration remains locked. Pin state is held during LPMx.5 entry and exit.

## 20.2.2 Function Documentation

```
void PMM_clearInterrupt ( uint16_t mask )
```

Clears interrupt flags for the PMM.

### Parameters

<i>mask</i>	<p>is the mask for specifying the required flag Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>PMM_BOR_INTERRUPT</b> - Software BOR interrupt</li> <li>■ <b>PMM_RST_INTERRUPT</b> - RESET pin interrupt</li> <li>■ <b>PMM_POR_INTERRUPT</b> - Software POR interrupt</li> <li>■ <b>PMM_SVSH_INTERRUPT</b> - SVS high side interrupt</li> <li>■ <b>PMM_SVSL_INTERRUPT</b> - SVS low side interrupt, not available for FR58xx/59xx</li> <li>■ <b>PMM_LPM5_INTERRUPT</b> - LPM5 indication</li> <li>■ <b>PMM_ALL</b> - All interrupts</li> </ul>
-------------	--

Modified bits of **PMMCTLO** register and bits of **PMMIFG** register.

### Returns

None

```
void PMM_disableSVSH ( void )
```

Disables the high-side SVS circuitry.

Modified bits of **PMMCTLO** register.

**Returns**

None

```
void PMM_disableSVSL ( void )
```

Disables the low-side SVS circuitry.  
Modified bits of **PMMCTLO** register.

**Returns**

None

```
void PMM_enableSVSH ( void )
```

Enables the high-side SVS circuitry.  
Modified bits of **PMMCTLO** register.

**Returns**

None

```
void PMM_enableSVSL ( void )
```

Enables the low-side SVS circuitry.  
Modified bits of **PMMCTLO** register.

**Returns**

None

```
uint16_t PMM_getInterruptStatus ( uint16_t mask )
```

Returns interrupt status.

**Parameters**

<i>mask</i>	<p>is the mask for specifying the required flag Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>PMM.BOR.INTERRUPT</b> - Software BOR interrupt</li> <li>■ <b>PMM.RST.INTERRUPT</b> - RESET pin interrupt</li> <li>■ <b>PMM.POR.INTERRUPT</b> - Software POR interrupt</li> <li>■ <b>PMM.SVSH.INTERRUPT</b> - SVS high side interrupt</li> <li>■ <b>PMM.SVSL.INTERRUPT</b> - SVS low side interrupt, not available for FR58xx/59xx</li> <li>■ <b>PMM.LPM5.INTERRUPT</b> - LPM5 indication</li> <li>■ <b>PMM.ALL</b> - All interrupts</li> </ul>
-------------	--

**Returns**

Logical OR of any of the following:

- **PMM\_BOR\_INTERRUPT** Software BOR interrupt
- **PMM\_RST\_INTERRUPT** RESET pin interrupt
- **PMM\_POR\_INTERRUPT** Software POR interrupt
- **PMM\_SVSH\_INTERRUPT** SVS high side interrupt
- **PMM\_SVSL\_INTERRUPT** SVS low side interrupt, not available for FR58xx/59xx
- **PMM\_LPM5\_INTERRUPT** LPM5 indication
- **PMM\_ALL** All interrupts  
indicating the status of the selected interrupt flags

`void PMM_trigBOR ( void )`

Calling this function will trigger a software Brown Out Rest (BOR).

Modified bits of **PMMCTLO** register.

**Returns**

None

`void PMM_trigPOR ( void )`

Calling this function will trigger a software Power On Reset (POR).

Modified bits of **PMMCTLO** register.

**Returns**

None

`void PMM_turnOffRegulator ( void )`

Turns OFF the low-dropout voltage regulator (LDO) when going into LPM3/4, thus the system will enter LPM3.5 or LPM4.5 respectively.

Modified bits of **PMMCTLO** register.

**Returns**

None

`void PMM_turnOnRegulator ( void )`

Makes the low-dropout voltage regulator (LDO) remain ON when going into LPM 3/4.

Modified bits of **PMMCTLO** register.

**Returns**

None

```
void PMM_unlockLPM5 ( void )
```

Unlock LPM5.

LPMx.5 configuration is not locked and defaults to its reset condition. Disable the GPIO power-on default high-impedance mode to activate previously configured port settings.

**Returns**

None

## 20.3 Programming Example

The following example shows some pmm operations using the APIs

```
//Unlock the GPIO pins.
/*
 * By default, the pins are unlocked unless waking
 * up from an LPMx.5 state in which case all GPIO
 * are previously locked.
 */
PMM_unlockLPM5();

//Get Interrupt Status from the PMMIFG register.
/* mask:
 * PMM_BOR_INTERRUPT
 * PMM_RST_INTERRUPT,
 * PMM_POR_INTERRUPT,
 * PMM_SVSL_INTERRUPT,
 * PMM_SVSH_INTERRUPT
 * PMM_LPM5_INTERRUPT,
 * return STATUS_SUCCESS (0x01) or STATUS_FAIL (0x00)
 */
if (PMM_getInterruptStatus(PMM_LPM5_INTERRUPT)) // Was this device in LPMx.5 mode
    before the reset was triggered?
{
    //Clear Interrupt Flag from the PMMIFG register.
    /* mask:
     * PMM_BOR_INTERRUPT
     * PMM_RST_INTERRUPT,
     * PMM_POR_INTERRUPT,
     * PMM_SVSL_INTERRUPT,
     * PMM_SVSH_INTERRUPT
     * PMM_LPM5_INTERRUPT,
     * PMM_ALL
     */
    PMM_clearInterrupt(PMM_LPM5_INTERRUPT); // Clear the LPMx.5 flag
}

if (PMM_getInterruptStatus(PMM_RST_INTERRUPT)) // Was this reset triggered by the
    Reset flag?
{
    PMM_clearInterrupt(PMM_RST_INTERRUPT); // Clear reset flag

    //Trigger a software Brown Out Reset (BOR)
    /*
     * Base Address of PMM,
     * Forces the devices to perform a BOR.
     */
    PMM_trigBOR(); // Software trigger a BOR.
}
```

```
if (PMM.getInterruptStatus(PMM.BOR_INTERRUPT)) // Was this reset triggered by the
    BOR flag?
{
    PMM.clearInterrupt(PMM.BOR_INTERRUPT); // Clear BOR flag

    //Disable SVSH
    /*
    * High-side SVS (SVSH) is disabled in LPM4.5. SVSH is
    * always enabled in active mode and LPM0/1/2/3/4 and LPM3.5.
    */
    PMM.disableSVSH();
    //Disable SVSL
    /*
    * Low-side SVS (SVSL) is disabled in low power modes.
    * SVSL is always enabled in active mode and LPM0.
    */
    PMM.disableSVSL();
    //Disable Regulator
    /*
    * Regulator is turned off when going to LPM3/4.
    * System enters LPM3.5 or LPM4.5, respectively.
    */
    PMM.turnOffRegulator();
    _bis_SR_register(LPM4_bits); // Enter LPM4.5, This automatically locks
                                // (if not locked already) all GPIO pins.
                                // and will set the LPM5 flag and set the LOCKLPM5 bit
                                // in the PM5CTL0 register upon wake up.
}

while (1)
{
    _no_operation(); // Don't sleep
}
```

# 21 Internal Reference (REF)

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## 21.1 Introduction

The Internal Reference (REF) API provides a set of functions for using the MSP430Ware REF modules. Functions are provided to setup and enable use of the Reference voltage, enable or disable the internal temperature sensor, and view the status of the inner workings of the REF module.

The reference module (REF) is responsible for generation of all critical reference voltages that can be used by various analog peripherals in a given device. These include but are not limited to the ADC12\_B and COMP\_B modules, dependent upon the particular device. The heart of the reference system is the bandgap from which all other references are derived by unity or non-inverting gain stages. The REFGEN sub-system consists of the bandgap, the bandgap bias, and the non-inverting buffer stage which generates the three primary voltage reference available in the system, namely 1.5 V, 2.0 V, and 2.5 V. In addition, when enabled, a buffered bandgap voltage is also available.

## 21.2 API Functions

### Functions

- void [Ref\\_setReferenceVoltage](#) (uint16\_t baseAddress, uint8\_t referenceVoltageSelect)  
*Sets the reference voltage for the voltage generator.*
- void [Ref\\_disableTempSensor](#) (uint16\_t baseAddress)  
*Disables the internal temperature sensor to save power consumption.*
- void [Ref\\_enableTempSensor](#) (uint16\_t baseAddress)  
*Enables the internal temperature sensor.*
- void [Ref\\_enableReferenceVoltage](#) (uint16\_t baseAddress)  
*Enables the reference voltage to be used by peripherals.*
- void [Ref\\_disableReferenceVoltage](#) (uint16\_t baseAddress)  
*Disables the reference voltage.*
- uint16\_t [Ref\\_getBandgapMode](#) (uint16\_t baseAddress)  
*Returns the bandgap mode of the Ref module.*
- bool [Ref\\_isBandgapActive](#) (uint16\_t baseAddress)  
*Returns the active status of the bandgap in the Ref module.*
- uint16\_t [Ref\\_isRefGenBusy](#) (uint16\_t baseAddress)  
*Returns the busy status of the reference generator in the Ref module.*
- bool [Ref\\_isRefGenActive](#) (uint16\_t baseAddress)  
*Returns the active status of the reference generator in the Ref module.*



## 21.2.1 Detailed Description

The REF API is broken into three groups of functions: those that deal with the reference voltage, those that handle the internal temperature sensor, and those that return the status of the REF module.

The reference voltage of the REF module is handled by

- [Ref\\_setReferenceVoltage\(\)](#)
- [Ref\\_enableReferenceVoltage\(\)](#)
- [Ref\\_disableReferenceVoltage\(\)](#)

The internal temperature sensor is handled by

- [Ref\\_disableTempSensor\(\)](#)
- [Ref\\_enableTempSensor\(\)](#)

The status of the REF module is handled by

- [Ref\\_getBandgapMode\(\)](#)
- [Ref\\_isBandgapActive\(\)](#)
- [Ref\\_isRefGenBusy\(\)](#)
- [Ref\\_isRefGen\(\)](#)

## 21.2.2 Function Documentation

```
void Ref_disableReferenceVoltage ( uint16_t baseAddress )
```

Disables the reference voltage.

This function is used to disable the generated reference voltage. Please note, if the

### Parameters

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

Modified bits are **REFON** of **REFCTL0** register.

### Returns

None

```
void Ref_disableTempSensor ( uint16_t baseAddress )
```

Disables the internal temperature sensor to save power consumption.

This function is used to turn off the internal temperature sensor to save on power consumption. The temperature sensor is enabled by default. Please note, that giving ADC12 module control over the Ref module, the state of the temperature sensor is dependent on the controls of the ADC12 module. Please note, if the [Ref\\_isRefGenBusy\(\)](#) returns Ref\_BUSY, this function will have no effect.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

Modified bits are **REFTCOFF** of **REFCTL0** register.

**Returns**

None

```
void Ref_enableReferenceVoltage ( uint16_t baseAddress )
```

Enables the reference voltage to be used by peripherals.

This function is used to enable the generated reference voltage to be used other peripherals or by an output pin, if enabled. Please note, that giving ADC12 module control over the Ref module, the state of the reference voltage is dependent on the controls of the ADC12 module. Please note, if the [Ref\\_isRefGenBusy\(\)](#) returns Ref\_BUSY, this function will have no effect.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

Modified bits are **REFON** of **REFCTL0** register.

**Returns**

None

```
void Ref_enableTempSensor ( uint16_t baseAddress )
```

Enables the internal temperature sensor.

This function is used to turn on the internal temperature sensor to use by other peripherals. The temperature sensor is enabled by default. Please note, if the [Ref\\_isRefGenBusy\(\)](#) returns Ref\_BUSY, this function will have no effect.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

Modified bits are **REFTCOFF** of **REFCTL0** register.

**Returns**

None

```
uint16_t Ref_getBandgapMode ( uint16_t baseAddress )
```

Returns the bandgap mode of the Ref module.

This function is used to return the bandgap mode of the Ref module, requested by the peripherals using the bandgap. If a peripheral requests static mode, then the bandgap mode will be static for all modules, whereas if all of the peripherals using the bandgap request sample mode, then that will be the mode returned. Sample mode allows the bandgap to be active only when necessary to

save on power consumption, static mode requires the bandgap to be active until no peripherals are using it anymore.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

**Returns**

One of the following:

- **Ref.STATICMODE** if the bandgap is operating in static mode
- **Ref.SAMPLEMODE** if the bandgap is operating in sample mode bandgap mode of the Ref module

`bool Ref_isBandgapActive ( uint16_t baseAddress )`

Returns the active status of the bandgap in the Ref module.

This function is used to return the active status of the bandgap in the Ref module. If the bandgap is in use by a peripheral, then the status will be seen as active.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

**Returns**

One of the following:

- **Ref.ACTIVE** if active
- **Ref.INACTIVE** if not active indicating the bandgap active status of the module

`bool Ref_isRefGenActive ( uint16_t baseAddress )`

Returns the active status of the reference generator in the Ref module.

This function is used to return the active status of the reference generator in the Ref module. If the ref. generator is on and ready to use, then the status will be seen as active.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

**Returns**

One of the following:

- **Ref.ACTIVE** if active
- **Ref.INACTIVE** if not active indicating the reference generator active status of the module

`uint16_t Ref_isRefGenBusy ( uint16_t baseAddress )`

Returns the busy status of the reference generator in the Ref module.

This function is used to return the busy status of the reference generator in the Ref module. If the ref. generator is in use by a peripheral, then the status will be seen as busy.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
--------------------	--

**Returns**

One of the following:

- **Ref\_NOTBUSY** if the reference generator is not being used
- **Ref\_BUSY** if the reference generator is being used, disallowing changes to be made to the REF module controls indicating the reference generator busy status of the module

```
void Ref_setReferenceVoltage ( uint16_t baseAddress, uint8_t referenceVoltageSelect )
```

Sets the reference voltage for the voltage generator.

This function sets the reference voltage generated by the voltage generator to be used by other peripherals. This reference voltage will only be valid while the Ref module is in control. Please note, if the [Ref\\_isRefGenBusy\(\)](#) returns Ref\_BUSY, this function will have no effect.

**Parameters**

<i>baseAddress</i>	is the base address of the REF module.
<i>referenceVoltageSelect</i>	is the desired voltage to generate for a reference voltage. Valid values are: <ul style="list-style-type: none"> <li>■ <b>REF_VREF1_5V</b> [Default]</li> <li>■ <b>REF_VREF2_0V</b></li> <li>■ <b>REF_VREF2_5V</b></li> </ul> Modified bits are <b>REFVSEL</b> of <b>REFCTL0</b> register.

**Returns**

None

## 21.3 Programming Example

The following example shows how to initialize and use the REF API with the ADC12 module to use as a positive reference to the analog signal input.

```
// By default, REFSTR=1 => REFCTL is used to configure the internal reference

// If ref generator busy, WAIT
while(Ref_refGenBusyStatus(REF_BASE));
// Select internal ref = 2.5V
Ref_setReferenceVoltage(REF_BASE,
                       REF_VREF2_5V);
// Internal Reference ON
Ref_enableReferenceVoltage(REF_BASE);

__delay_cycles(75); // Delay (~75us) for Ref to settle
```

## 22 Real-Time Clock (RTC\_B)

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### 22.1 Introduction

The Real Time Clock (RTC\_B) API provides a set of functions for using the MSP430Ware RTC\_B modules. Functions are provided to calibrate the clock, initialize the RTC modules in calendar mode, and setup conditions for, and enable, interrupts for the RTC modules. If an RTC\_B module is used, then prescale counters are also initialized.

The RTC\_B module provides the ability to keep track of the current time and date in calendar mode.

The RTC\_B module generates multiple interrupts. There are 2 interrupts that can be defined in calendar mode, and 1 interrupt for user-configured event, as well as an interrupt for each prescaler.

### 22.2 API Functions

#### Functions

- void [RTC\\_B\\_startClock](#) (uint16\_t baseAddress)  
*Starts the RTC.*
- void [RTC\\_B\\_holdClock](#) (uint16\_t baseAddress)  
*Holds the RTC.*
- void [RTC\\_B\\_setCalibrationFrequency](#) (uint16\_t baseAddress, uint16\_t frequencySelect)  
*Allows and Sets the frequency output to RTCCLK pin for calibration measurement.*
- void [RTC\\_B\\_setCalibrationData](#) (uint16\_t baseAddress, uint8\_t offsetDirection, uint8\_t offsetValue)  
*Sets the specified calibration for the RTC.*
- void [RTC\\_B\\_initCalendar](#) (uint16\_t baseAddress, [Calendar](#) \*CalendarTime, uint16\_t formatSelect)  
*Initializes the settings to operate the RTC in calendar mode.*
- [Calendar](#) [RTC\\_B\\_getCalendarTime](#) (uint16\_t baseAddress)  
*Returns the [Calendar](#) Time stored in the [Calendar](#) registers of the RTC.*
- void [RTC\\_B\\_configureCalendarAlarm](#) (uint16\_t baseAddress, [RTC\\_B\\_configureCalendarAlarmParam](#) \*param)  
*Sets and Enables the desired [Calendar](#) Alarm settings.*
- void [RTC\\_B\\_setCalendarEvent](#) (uint16\_t baseAddress, uint16\_t eventSelect)  
*Sets a single specified [Calendar](#) interrupt condition.*
- void [RTC\\_B\\_definePrescaleEvent](#) (uint16\_t baseAddress, uint8\_t prescaleSelect, uint8\_t prescaleEventDivider)  
*Sets up an interrupt condition for the selected Prescaler.*
- uint8\_t [RTC\\_B\\_getPrescaleValue](#) (uint16\_t baseAddress, uint8\_t prescaleSelect)  
*Returns the selected prescaler value.*

- void `RTC_B_setPrescaleValue` (uint16\_t baseAddress, uint8\_t prescaleSelect, uint8\_t prescaleCounterValue)  
*Sets the selected prescaler value.*
- void `RTC_B_enableInterrupt` (uint16\_t baseAddress, uint8\_t interruptMask)  
*Enables selected RTC interrupt sources.*
- void `RTC_B_disableInterrupt` (uint16\_t baseAddress, uint8\_t interruptMask)  
*Disables selected RTC interrupt sources.*
- uint8\_t `RTC_B_getInterruptStatus` (uint16\_t baseAddress, uint8\_t interruptFlagMask)  
*Returns the status of the selected interrupts flags.*
- void `RTC_B_clearInterrupt` (uint16\_t baseAddress, uint8\_t interruptFlagMask)  
*Clears selected RTC interrupt flags.*
- uint16\_t `RTC_B_convertBCDToBinary` (uint16\_t baseAddress, uint16\_t valueToConvert)  
*Convert the given BCD value to binary format.*
- uint16\_t `RTC_B_convertBinaryToBCD` (uint16\_t baseAddress, uint16\_t valueToConvert)  
*Convert the given binary value to BCD format.*

## 22.2.1 Detailed Description

The RTC\_B API is broken into 5 groups of functions: clock settings, calendar mode, prescale counter, interrupt condition setup/enable functions and data conversion.

The RTC\_B clock settings are handled by

- `RTC_B_startClock()`
- `RTC_B_holdClock()`
- `RTC_B_setCalibrationFrequency()`
- `RTC_B_setCalibrationData()`

The RTC\_B calendar mode is initialized and handled by

- `RTC_B_initCalendar()`
- `RTC_B_configureCalendarAlarm()`
- `RTC_B_getCalendarTime()`

The RTC\_B prescale counter is handled by

- `RTC_B_getPrescaleValue()`
- `RTC_B_setPrescaleValue()`

The RTC\_B interrupts are handled by

- `RTC_B_definePrescaleEvent()`
- `RTC_B_setCalendarEvent()`
- `RTC_B_enableInterrupt()`
- `RTC_B_disableInterrupt()`
- `RTC_B_getInterruptStatus()`
- `RTC_B_clearInterrupt()`

The RTC\_B conversions are handled by

- `RTC_B_convertBCDToBinary()`
- `RTC_B_convertBinaryToBCD()`



## 22.2.2 Function Documentation

```
void RTC_B_clearInterrupt ( uint16_t baseAddress, uint8_t interruptFlagMask )
```

Clears selected RTC interrupt flags.

This function clears the RTC interrupt flag is cleared, so that it no longer asserts.

### Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>interruptFlagMask</i> <i>Mask</i>	<p>is a bit mask of the interrupt flags to be cleared. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>RTC_B_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_B_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_B_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_B_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_B_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> <li>■ <b>RTC_B_OSCILLATOR_FAULT_INTERRUPT</b> - asserts if there is a problem with the 32kHz oscillator, while the RTC is running.</li> </ul>

### Returns

None

```
void RTC_B_configureCalendarAlarm ( uint16_t baseAddress, RTC_B_configureCalendarAlarmParam * param )
```

Sets and Enables the desired [Calendar](#) Alarm settings.

This function sets a [Calendar](#) interrupt condition to assert the RTCAIFG interrupt flag. The condition is a logical and of all of the parameters. For example if the minutes and hours alarm is set, then the interrupt will only assert when the minutes AND the hours change to the specified setting. Use the `RTC_B_ALARM_OFF` for any alarm settings that should not be apart of the alarm condition.

### Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>param</i>	is the pointer to struct for calendar alarm configuration.

**Returns**

None

References RTC\_B\_configureCalendarAlarmParam::dayOfMonthAlarm, RTC\_B\_configureCalendarAlarmParam::dayOfWeekAlarm, RTC\_B\_configureCalendarAlarmParam::hoursAlarm, and RTC\_B\_configureCalendarAlarmParam::minutesAlarm.

`uint16_t RTC_B_convertBCDToBinary ( uint16_t baseAddress, uint16_t valueToConvert )`

Convert the given BCD value to binary format.

This function converts BCD values to binary format. This API uses the hardware registers to perform the conversion rather than a software method.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>valueToConvert</i>	is the raw value in BCD format to convert to Binary. Modified bits are <b>BCD2BIN</b> of <b>BCD2BIN</b> register.

**Returns**

The binary version of the input parameter

`uint16_t RTC_B_convertBinaryToBCD ( uint16_t baseAddress, uint16_t valueToConvert )`

Convert the given binary value to BCD format.

This function converts binary values to BCD format. This API uses the hardware registers to perform the conversion rather than a software method.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>valueToConvert</i>	is the raw value in Binary format to convert to BCD. Modified bits are <b>BIN2BCD</b> of <b>BIN2BCD</b> register.

**Returns**

The BCD version of the valueToConvert parameter

`void RTC_B_definePrescaleEvent ( uint16_t baseAddress, uint8_t prescaleSelect, uint8_t prescaleEventDivider )`

Sets up an interrupt condition for the selected Prescaler.

This function sets the condition for an interrupt to assert based on the individual prescalers.

## Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>prescaleSelect</i>	is the prescaler to define an interrupt for. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B_PRESCALE_0</b></li> <li>■ <b>RTC_B_PRESCALE_1</b></li> </ul>
<i>prescaleEvent</i> ↔ <i>Divider</i>	is a divider to specify when an interrupt can occur based on the clock source of the selected prescaler. (Does not affect timer of the selected prescaler). Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B_PSEVENTDIVIDER_2</b> [Default]</li> <li>■ <b>RTC_B_PSEVENTDIVIDER_4</b></li> <li>■ <b>RTC_B_PSEVENTDIVIDER_8</b></li> <li>■ <b>RTC_B_PSEVENTDIVIDER_16</b></li> <li>■ <b>RTC_B_PSEVENTDIVIDER_32</b></li> <li>■ <b>RTC_B_PSEVENTDIVIDER_64</b></li> <li>■ <b>RTC_B_PSEVENTDIVIDER_128</b></li> <li>■ <b>RTC_B_PSEVENTDIVIDER_256</b></li> </ul> Modified bits are <b>RTxIP</b> of <b>RTCPSxCTL</b> register.

## Returns

None

```
void RTC_B_disableInterrupt ( uint16_t baseAddress, uint8_t interruptMask )
```

Disables selected RTC interrupt sources.

This function disables the selected RTC interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

## Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>interruptMask</i>	is a bit mask of the interrupts to disable. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>RTC_B_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_B_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_B_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_B_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_B_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> <li>■ <b>RTC_B_OSCILLATOR_FAULT_INTERRUPT</b> - asserts if there is a problem with the 32kHz oscillator, while the RTC is running.</li> </ul>

## Returns

None

```
void RTC_B_enableInterrupt ( uint16_t baseAddress, uint8_t interruptMask )
```

Enables selected RTC interrupt sources.

This function enables the selected RTC interrupt source. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

## Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>interruptMask</i>	is a bit mask of the interrupts to enable. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>RTC_B_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <a href="#">Calendar</a> event condition defined by <code>defineCalendarEvent()</code> is met.</li> <li>■ <b>RTC_B_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <a href="#">Calendar</a> mode is met.</li> <li>■ <b>RTC_B_CLOCK_READ_READY_INTERRUPT</b> - asserts when <a href="#">Calendar</a> registers are settled.</li> <li>■ <b>RTC_B_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_B_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> <li>■ <b>RTC_B_OSCILLATOR_FAULT_INTERRUPT</b> - asserts if there is a problem with the 32kHz oscillator, while the RTC is running.</li> </ul>

**Returns**

None

**Calendar** RTC\_B\_getCalendarTime ( uint16\_t *baseAddress* )

Returns the **Calendar** Time stored in the **Calendar** registers of the RTC.

This function returns the current **Calendar** time in the form of a **Calendar** structure. The RTCRDY polling is used in this function to prevent reading invalid time.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
--------------------	--

**Returns**

A **Calendar** structure containing the current time.

References Calendar::DayOfMonth, Calendar::DayOfWeek, Calendar::Hours, Calendar::Minutes, Calendar::Month, Calendar::Seconds, and Calendar::Year.

uint8\_t RTC\_B\_getInterruptStatus ( uint16\_t *baseAddress*, uint8\_t *interruptFlagMask* )

Returns the status of the selected interrupts flags.

This function returns the status of the interrupt flag for the selected channel.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>interruptFlagMask</i>	is a bit mask of the interrupt flags to return the status of. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>RTC_B_TIME_EVENT_INTERRUPT</b> - asserts when counter overflows in counter mode or when <b>Calendar</b> event condition defined by defineCalendarEvent() is met.</li> <li>■ <b>RTC_B_CLOCK_ALARM_INTERRUPT</b> - asserts when alarm condition in <b>Calendar</b> mode is met.</li> <li>■ <b>RTC_B_CLOCK_READ_READY_INTERRUPT</b> - asserts when <b>Calendar</b> registers are settled.</li> <li>■ <b>RTC_B_PRESCALE_TIMER0_INTERRUPT</b> - asserts when Prescaler 0 event condition is met.</li> <li>■ <b>RTC_B_PRESCALE_TIMER1_INTERRUPT</b> - asserts when Prescaler 1 event condition is met.</li> <li>■ <b>RTC_B_OSCILLATOR_FAULT_INTERRUPT</b> - asserts if there is a problem with the 32kHz oscillator, while the RTC is running.</li> </ul>

**Returns**

Logical OR of any of the following:

- **RTC\_B\_TIME\_EVENT\_INTERRUPT** asserts when counter overflows in counter mode or when [Calendar](#) event condition defined by `defineCalendarEvent()` is met.
- **RTC\_B\_CLOCK\_ALARM\_INTERRUPT** asserts when alarm condition in [Calendar](#) mode is met.
- **RTC\_B\_CLOCK\_READ\_READY\_INTERRUPT** asserts when [Calendar](#) registers are settled.
- **RTC\_B\_PRESCALE\_TIMER0\_INTERRUPT** asserts when Prescaler 0 event condition is met.
- **RTC\_B\_PRESCALE\_TIMER1\_INTERRUPT** asserts when Prescaler 1 event condition is met.
- **RTC\_B\_OSCILLATOR\_FAULT\_INTERRUPT** asserts if there is a problem with the 32kHz oscillator, while the RTC is running.  
indicating the status of the masked interrupts

```
uint8_t RTC_B_getPrescaleValue ( uint16_t baseAddress, uint8_t prescaleSelect )
```

Returns the selected prescaler value.

This function returns the value of the selected prescale counter register. Note that the counter value should be held by calling [RTC\\_B\\_holdClock\(\)](#) before calling this API.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>prescaleSelect</i>	is the prescaler to obtain the value of. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B_PRESCALE_0</b></li> <li>■ <b>RTC_B_PRESCALE_1</b></li> </ul>

**Returns**

The value of the specified prescaler count register

```
void RTC_B_holdClock ( uint16_t baseAddress )
```

Holds the RTC.

This function sets the RTC main hold bit to disable RTC functionality.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
--------------------	--

**Returns**

None

```
void RTC_B_initCalendar ( uint16_t baseAddress, Calendar * CalendarTime, uint16_t
    formatSelect )
```

Initializes the settings to operate the RTC in calendar mode.

This function initializes the **Calendar** mode of the RTC module. To prevent potential erroneous alarm conditions from occurring, the alarm should be disabled by clearing the RTCAIE, RTCAIFG and AE bits with APIs: [RTC\\_B\\_disableInterrupt\(\)](#), [RTC\\_B\\_clearInterrupt\(\)](#) and [RTC\\_B\\_configureCalendarAlarm\(\)](#) before calendar initialization.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>CalendarTime</i>	is the pointer to the structure containing the values for the <b>Calendar</b> to be initialized to. Valid values should be of type pointer to <b>Calendar</b> and should contain the following members and corresponding values: <b>Seconds</b> between 0-59 <b>Minutes</b> between 0-59 <b>Hours</b> between 0-23 <b>DayOfWeek</b> between 0-6 <b>DayOfMonth</b> between 1-31 <b>Year</b> between 0-4095 NOTE: Values beyond the ones specified may result in erratic behavior.
<i>formatSelect</i>	is the format for the <b>Calendar</b> registers to use. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B_FORMAT_BINARY</b> [Default]</li> <li>■ <b>RTC_B_FORMAT_BCD</b> Modified bits are <b>RTCBCD</b> of <b>RTCCTL1</b> register.</li> </ul>

#### Returns

None

References `Calendar::DayOfMonth`, `Calendar::DayOfWeek`, `Calendar::Hours`, `Calendar::Minutes`, `Calendar::Month`, `Calendar::Seconds`, and `Calendar::Year`.

```
void RTC_B_setCalendarEvent ( uint16_t baseAddress, uint16_t eventSelect )
```

Sets a single specified **Calendar** interrupt condition.

This function sets a specified event to assert the RTCTEVIFG interrupt. This interrupt is independent from the **Calendar** alarm interrupt.

#### Parameters

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>eventSelect</i>	is the condition selected. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B_CALENDAREVENT_MINUTECHANGE</b> - assert interrupt on every minute</li> <li>■ <b>RTC_B_CALENDAREVENT_HOURLCHANGE</b> - assert interrupt on every hour</li> <li>■ <b>RTC_B_CALENDAREVENT_NOON</b> - assert interrupt when hour is 12</li> <li>■ <b>RTC_B_CALENDAREVENT_MIDNIGHT</b> - assert interrupt when hour is 0</li> </ul> Modified bits are <b>RTCTEV</b> of <b>RTCCTL</b> register.

**Returns**

None

```
void RTC_B_setCalibrationData ( uint16_t baseAddress, uint8_t offsetDirection, uint8_t
offsetValue )
```

Sets the specified calibration for the RTC.

This function sets the calibration offset to make the RTC as accurate as possible. The offsetDirection can be either +4-ppm or -2-ppm, and the offsetValue should be from 1-63 and is multiplied by the direction setting (i.e. +4-ppm \* 8 (offsetValue) = +32-ppm). Please note, when measuring the frequency after setting the calibration, you will only see a change on the 1Hz frequency.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>offsetDirection</i>	is the direction that the calibration offset will go. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B_CALIBRATION_DOWN2PPM</b> - calibrate at steps of -2</li> <li>■ <b>RTC_B_CALIBRATION_UP4PPM</b> - calibrate at steps of +4</li> </ul> Modified bits are <b>RTCCALS</b> of <b>RTCCTL2</b> register.
<i>offsetValue</i>	is the value that the offset will be a factor of; a valid value is any integer from 1-63. Modified bits are <b>RTCCAL</b> of <b>RTCCTL2</b> register.

**Returns**

None

```
void RTC_B_setCalibrationFrequency ( uint16_t baseAddress, uint16_t frequencySelect )
```

Allows and Sets the frequency output to RTCCLK pin for calibration measurement.

This function sets a frequency to measure at the RTCCLK output pin. After testing the set frequency, the calibration could be set accordingly.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>frequencySelect</i>	is the frequency output to RTCCLK. Valid values are: <ul style="list-style-type: none"> <li>■ <b>RTC_B_CALIBRATIONFREQ_OFF</b> [Default] - turn off calibration output</li> <li>■ <b>RTC_B_CALIBRATIONFREQ_512HZ</b> - output signal at 512Hz for calibration</li> <li>■ <b>RTC_B_CALIBRATIONFREQ_256HZ</b> - output signal at 256Hz for calibration</li> <li>■ <b>RTC_B_CALIBRATIONFREQ_1HZ</b> - output signal at 1Hz for calibration</li> </ul> Modified bits are <b>RTCCALF</b> of <b>RTCCTL3</b> register.



**Returns**

None

```
void RTC_B_setPrescaleValue ( uint16_t baseAddress, uint8_t prescaleSelect, uint8_t
prescaleCounterValue )
```

Sets the selected prescaler value.

This function sets the prescale counter value. Before setting the prescale counter, it should be held by calling `RTC_B_holdClock()`.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
<i>prescaleSelect</i>	is the prescaler to set the value for. Valid values are: <ul style="list-style-type: none"> <li>■ <code>RTC_B_PRESCALE_0</code></li> <li>■ <code>RTC_B_PRESCALE_1</code></li> </ul>
<i>prescaleCounterValue</i>	is the specified value to set the prescaler to. Valid values are any integer between 0-255. Modified bits are <code>RTxPS</code> of <code>RTxPS</code> register.

**Returns**

None

```
void RTC_B_startClock ( uint16_t baseAddress )
```

Starts the RTC.

This function clears the RTC main hold bit to allow the RTC to function.

**Parameters**

<i>baseAddress</i>	is the base address of the RTC_B module.
--------------------	--

**Returns**

None

## 22.3 Programming Example

The following example shows how to initialize and use the RTC API to setup Calendar Mode with the current time and various interrupts.

```
//Initialize calendar struct
Calendar currentTime;
currentTime.Seconds = 0x00;
currentTime.Minutes = 0x26;
currentTime.Hours = 0x13;
currentTime.DayOfWeek = 0x03;
currentTime.DayOfMonth = 0x20;
```

```
currentTime.Month      = 0x07;
currentTime.Year       = 0x2011;

//Initialize alarm struct
RTC_B.configureCalendarAlarmParam alarmParam;
alarmParam.minutesAlarm = 0x00;
alarmParam.hoursAlarm = 0x17;
alarmParam.dayOfWeekAlarm = RTC_B.ALARMCONDITION_OFF;
alarmParam.dayOfMonthAlarm = 0x05;

//Initialize Calendar Mode of RTC_B
/*
 * Base Address of the RTC_B
 * Pass in current time, initialized above
 * Use BCD as Calendar Register Format
 */
RTC_B.initCalendar(RTC_B.BASE,
                  &currentTime,
                  RTC_B.FORMAT_BCD);

//Setup Calendar Alarm for 5:00pm on the 5th day of the month.
//Note: Does not specify day of the week.
RTC_B.setCalendarAlarm(RTC_B.BASE, &alarmParam);

//Specify an interrupt to assert every minute
RTC_B.setCalendarEvent(RTC_B.BASE,
                      RTC_B.CALENDAREVENT_MINUTECHANGE);

//Enable interrupt for RTC_B Ready Status, which asserts when the RTC_B
//Calendar registers are ready to read.
//Also, enable interrupts for the Calendar alarm and Calendar event.
RTC_B.enableInterrupt(RTC_B.BASE,
                     RTC_B.CLOCK_READ_READY_INTERRUPT +
                     RTC_B.TIME_EVENT_INTERRUPT +
                     RTC_B.CLOCK_ALARM_INTERRUPT);

//Start RTC_B Clock
RTC_B.startClock(RTC_B.BASE);

//Enter LPM3 mode with interrupts enabled
__bis_SR_register(LPM3_bits + GIE);
__no_operation();
```

## 23 SFR Module

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### 23.1 Introduction

The Special Function Registers API provides a set of functions for using the MSP430Ware SFR module. Functions are provided to enable and disable interrupts and control the  $\sim$ RST/NMI pin

The SFR module can enable interrupts to be generated from other peripherals of the device.

### 23.2 API Functions

#### Functions

- void [SFR\\_enableInterrupt](#) (uint8\_t interruptMask)  
*Enables selected SFR interrupt sources.*
- void [SFR\\_disableInterrupt](#) (uint8\_t interruptMask)  
*Disables selected SFR interrupt sources.*
- uint8\_t [SFR\\_getInterruptStatus](#) (uint8\_t interruptFlagMask)  
*Returns the status of the selected SFR interrupt flags.*
- void [SFR\\_clearInterrupt](#) (uint8\_t interruptFlagMask)  
*Clears the selected SFR interrupt flags.*
- void [SFR\\_setResetPinPullResistor](#) (uint16\_t pullResistorSetup)  
*Sets the pull-up/down resistor on the  $\sim$ RST/NMI pin.*
- void [SFR\\_setNMIEdge](#) (uint16\_t edgeDirection)  
*Sets the edge direction that will assert an NMI from a signal on the  $\sim$ RST/NMI pin if NMI function is active.*
- void [SFR\\_setResetNMIPinFunction](#) (uint8\_t resetPinFunction)  
*Sets the function of the  $\sim$ RST/NMI pin.*

#### 23.2.1 Detailed Description

The SFR API is broken into 2 groups: the SFR interrupts and the SFR  $\sim$ RST/NMI pin control

The SFR interrupts are handled by

- [SFR\\_enableInterrupt\(\)](#)
- [SFR\\_disableInterrupt\(\)](#)
- [SFR\\_getInterruptStatus\(\)](#)
- [SFR\\_clearInterrupt\(\)](#)

The SFR  $\sim$ RST/NMI pin is controlled by

- [SFR\\_setResetPinPullResistor\(\)](#)
- [SFR\\_setNMIEdge\(\)](#)
- [SFR\\_setResetNMIPinFunction\(\)](#)

## 23.2.2 Function Documentation

```
void SFR_clearInterrupt ( uint8_t interruptFlagMask )
```

Clears the selected SFR interrupt flags.

This function clears the status of the selected SFR interrupt flags.

### Parameters

<i>interruptFlagMask</i>	<p>is the bit mask of interrupt flags that will be cleared. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>SFR_JTAG_OUTBOX_INTERRUPT</b> - JTAG outbox interrupt</li> <li>■ <b>SFR_JTAG_INBOX_INTERRUPT</b> - JTAG inbox interrupt</li> <li>■ <b>SFR_NMI_PIN_INTERRUPT</b> - NMI pin interrupt, if NMI function is chosen</li> <li>■ <b>SFR_VACANT_MEMORY_ACCESS_INTERRUPT</b> - Vacant memory access interrupt</li> <li>■ <b>SFR_OSCILLATOR_FAULT_INTERRUPT</b> - Oscillator fault interrupt</li> <li>■ <b>SFR_WATCHDOG_INTERVAL_TIMER_INTERRUPT</b> - Watchdog interval timer interrupt</li> </ul>
--------------------------	---

### Returns

None

```
void SFR_disableInterrupt ( uint8_t interruptMask )
```

Disables selected SFR interrupt sources.

This function disables the selected SFR interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor.

### Parameters

<i>interruptMask</i>	<p>is the bit mask of interrupts that will be disabled. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>SFR_JTAG_OUTBOX_INTERRUPT</b> - JTAG outbox interrupt</li> <li>■ <b>SFR_JTAG_INBOX_INTERRUPT</b> - JTAG inbox interrupt</li> <li>■ <b>SFR_NMI_PIN_INTERRUPT</b> - NMI pin interrupt, if NMI function is chosen</li> <li>■ <b>SFR_VACANT_MEMORY_ACCESS_INTERRUPT</b> - Vacant memory access interrupt</li> <li>■ <b>SFR_OSCILLATOR_FAULT_INTERRUPT</b> - Oscillator fault interrupt</li> <li>■ <b>SFR_WATCHDOG_INTERVAL_TIMER_INTERRUPT</b> - Watchdog interval timer interrupt</li> </ul>
----------------------	---

**Returns**

None

```
void SFR_enableInterrupt ( uint8_t interruptMask )
```

Enables selected SFR interrupt sources.

This function enables the selected SFR interrupt sources. Only the sources that are enabled can be reflected to the processor interrupt; disabled sources have no effect on the processor. Does not clear interrupt flags.

**Parameters**

<i>interruptMask</i>	<p>is the bit mask of interrupts that will be enabled. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>SFR_JTAG_OUTBOX_INTERRUPT</b> - JTAG outbox interrupt</li> <li>■ <b>SFR_JTAG_INBOX_INTERRUPT</b> - JTAG inbox interrupt</li> <li>■ <b>SFR_NMI_PIN_INTERRUPT</b> - NMI pin interrupt, if NMI function is chosen</li> <li>■ <b>SFR_VACANT_MEMORY_ACCESS_INTERRUPT</b> - Vacant memory access interrupt</li> <li>■ <b>SFR_OSCILLATOR_FAULT_INTERRUPT</b> - Oscillator fault interrupt</li> <li>■ <b>SFR_WATCHDOG_INTERVAL_TIMER_INTERRUPT</b> - Watchdog interval timer interrupt</li> </ul>
----------------------	--

**Returns**

None

```
uint8_t SFR_getInterruptStatus ( uint8_t interruptFlagMask )
```

Returns the status of the selected SFR interrupt flags.

This function returns the status of the selected SFR interrupt flags in a bit mask format matching that passed into the *interruptFlagMask* parameter.

**Parameters**

<i>interruptFlagMask</i>	<p>is the bit mask of interrupt flags that the status of should be returned. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>SFR_JTAG_OUTBOX_INTERRUPT</b> - JTAG outbox interrupt</li> <li>■ <b>SFR_JTAG_INBOX_INTERRUPT</b> - JTAG inbox interrupt</li> <li>■ <b>SFR_NMI_PIN_INTERRUPT</b> - NMI pin interrupt, if NMI function is chosen</li> <li>■ <b>SFR_VACANT_MEMORY_ACCESS_INTERRUPT</b> - Vacant memory access interrupt</li> <li>■ <b>SFR_OSCILLATOR_FAULT_INTERRUPT</b> - Oscillator fault interrupt</li> <li>■ <b>SFR_WATCHDOG_INTERVAL_TIMER_INTERRUPT</b> - Watchdog interval timer interrupt</li> </ul>
--------------------------	--

**Returns**

A bit mask of the status of the selected interrupt flags. Return Logical OR of any of the following:

- **SFR\_JTAG\_OUTBOX\_INTERRUPT** JTAG outbox interrupt
- **SFR\_JTAG\_INBOX\_INTERRUPT** JTAG inbox interrupt
- **SFR\_NMI\_PIN\_INTERRUPT** NMI pin interrupt, if NMI function is chosen
- **SFR\_VACANT\_MEMORY\_ACCESS\_INTERRUPT** Vacant memory access interrupt
- **SFR\_OSCILLATOR\_FAULT\_INTERRUPT** Oscillator fault interrupt
- **SFR\_WATCHDOG\_INTERVAL\_TIMER\_INTERRUPT** Watchdog interval timer interrupt indicating the status of the masked interrupts

```
void SFR_setNMIEdge ( uint16_t edgeDirection )
```

Sets the edge direction that will assert an NMI from a signal on the  $\sim$ RST/NMI pin if NMI function is active.

This function sets the edge direction that will assert an NMI from a signal on the  $\sim$ RST/NMI pin if the NMI function is active. To activate the NMI function of the  $\sim$ RST/NMI use the [SFR\\_setResetNMIPinFunction\(\)](#) passing SFR\_RESETPINFUNC\_NMI into the resetPinFunction parameter.

**Parameters**

<i>edgeDirection</i>	is the direction that the signal on the $\sim$ RST/NMI pin should go to signal an interrupt, if enabled. Valid values are: <ul style="list-style-type: none"> <li>■ <b>SFR_NMI_RISINGEDGE</b> [Default]</li> <li>■ <b>SFR_NMI_FALLINGEDGE</b></li> </ul> Modified bits are <b>SYSNMIIES</b> of <b>SFRRPCR</b> register.
----------------------	---

**Returns**

None

```
void SFR_setResetNMIPinFunction ( uint8_t resetPinFunction )
```

Sets the function of the  $\sim$ RST/NMI pin.

This function sets the functionality of the  $\sim$ RST/NMI pin, whether in reset mode which will assert a reset if a low signal is observed on that pin, or an NMI which will assert an interrupt from an edge of the signal dependent on the setting of the edgeDirection parameter in [SFR\\_setNMIEdge\(\)](#).

**Parameters**

<i>resetPin↔ Function</i>	is the function that the $\sim$ RST/NMI pin should take on. Valid values are: <ul style="list-style-type: none"> <li>■ <b>SFR_RESETPINFUNC_RESET</b> [Default]</li> <li>■ <b>SFR_RESETPINFUNC_NMI</b></li> </ul> Modified bits are <b>SYSNMI</b> of <b>SFRRPCR</b> register.
-------------------------------	--

**Returns**

None

```
void SFR_setResetPinPullResistor ( uint16_t pullResistorSetup )
```

Sets the pull-up/down resistor on the  $\sim$ RST/NMI pin.

This function sets the pull-up/down resistors on the  $\sim$ RST/NMI pin to the settings from the pullResistorSetup parameter.

**Parameters**

<i>pullResistorSetup</i>	<p>is the selection of how the pull-up/down resistor on the <math>\sim</math>RST/NMI pin should be setup or disabled. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SFR_RESISTORDISABLE</b></li> <li>■ <b>SFR_RESISTORENABLE_PULLUP</b> [Default]</li> <li>■ <b>SFR_RESISTORENABLE_PULLDOWN</b></li> </ul> <p>Modified bits are <b>SYSRSTUP</b> and <b>SYSRSTRE</b> of <b>SFRRPCR</b> register.</p>
--------------------------	---

**Returns**

None

## 23.3 Programming Example

The following example shows how to initialize and use the SFR API

```
do
{
    // Clear SFR Fault Flag
    SFR_clearInterrupt(SFR_BASE,
                      OFIFG);

    // Test oscillator fault flag
}while (SFR_getInterruptStatus(SFR_BASE, OFIFG));
```

## 24 System Control Module

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### 24.1 Introduction

The System Control (SYS) API provides a set of functions for using the MSP430Ware SYS module. Functions are provided to control various SYS controls, setup the BSL, and control the JTAG Mailbox.

### 24.2 API Functions

#### Functions

- void [SysCtl.enableDedicatedJTAGPins](#) (void)  
*Sets the JTAG pins to be exclusively for JTAG until a BOR occurs.*
- uint8\_t [SysCtl.getBSEntryIndication](#) (void)  
*Returns the indication of a BSL entry sequence from the Spy-Bi-Wire.*
- void [SysCtl.enablePMMAccessProtect](#) (void)  
*Enables PMM Access Protection.*
- void [SysCtl.enableRAMBasedInterruptVectors](#) (void)  
*Enables RAM-based Interrupt Vectors.*
- void [SysCtl.disableRAMBasedInterruptVectors](#) (void)  
*Disables RAM-based Interrupt Vectors.*
- void [SysCtl.initJTAGMailbox](#) (uint8\_t mailboxSizeSelect, uint8\_t autoClearInboxFlagSelect)  
*Initializes JTAG Mailbox with selected properties.*
- uint8\_t [SysCtl.getJTAGMailboxFlagStatus](#) (uint8\_t mailboxFlagMask)  
*Returns the status of the selected JTAG Mailbox flags.*
- void [SysCtl.clearJTAGMailboxFlagStatus](#) (uint8\_t mailboxFlagMask)  
*Clears the status of the selected JTAG Mailbox flags.*
- uint16\_t [SysCtl.getJTAGInboxMessage16Bit](#) (uint8\_t inboxSelect)  
*Returns the contents of the selected JTAG Inbox in a 16 bit format.*
- uint32\_t [SysCtl.getJTAGInboxMessage32Bit](#) (void)  
*Returns the contents of JTAG Inboxes in a 32 bit format.*
- void [SysCtl.setJTAGOutgoingMessage16Bit](#) (uint8\_t outboxSelect, uint16\_t outgoingMessage)  
*Sets a 16 bit outgoing message in to the selected JTAG Outbox.*
- void [SysCtl.setJTAGOutgoingMessage32Bit](#) (uint32\_t outgoingMessage)  
*Sets a 32 bit message in to both JTAG Outboxes.*

#### 24.2.1 Detailed Description

The SYS API is broken into 2 groups: the various SYS controls and the JTAG mailbox controls. The various SYS controls are handled by



- `SysCtl_enableDedicatedJTAGPins()`
- `SysCtl_getBSLEntryIndication()`
- `SysCtl_enablePMMAccessProtect()`
- `SysCtl_enableRAMBasedInterruptVectors()`
- `SysCtl_disableRAMBasedInterruptVectors()`

The JTAG Mailbox controls are handled by

- `SysCtl_initJTAGMailbox()`
- `SysCtl_getJTAGMailboxFlagStatus()`
- `SysCtl_getJTAGInboxMessage16Bit()`
- `SysCtl_getJTAGInboxMessage32Bit()`
- `SysCtl_setJTAGOutgoingMessage16Bit()`
- `SysCtl_setJTAGOutgoingMessage32Bit()`
- `SysCtl_clearJTAGMailboxFlagStatus()`

## 24.2.2 Function Documentation

`void SysCtl_clearJTAGMailboxFlagStatus ( uint8_t mailboxFlagMask )`

Clears the status of the selected JTAG Mailbox flags.

This function clears the selected JTAG Mailbox flags.

### Parameters

<i>mailboxFlagMask</i>	<p>is the bit mask of JTAG mailbox flags that the status of should be cleared. Mask value is the logical OR of any of the following:</p> <ul style="list-style-type: none"> <li>■ <b>SYSCTL_JTAGOUTBOX_FLAG0</b> - flag for JTAG outbox 0</li> <li>■ <b>SYSCTL_JTAGOUTBOX_FLAG1</b> - flag for JTAG outbox 1</li> <li>■ <b>SYSCTL_JTAGINBOX_FLAG0</b> - flag for JTAG inbox 0</li> <li>■ <b>SYSCTL_JTAGINBOX_FLAG1</b> - flag for JTAG inbox 1</li> </ul>
------------------------	---

### Returns

None

`void SysCtl_disableRAMBasedInterruptVectors ( void )`

Disables RAM-based Interrupt Vectors.

This function disables the interrupt vectors from being generated at the top of the RAM.

### Returns

None

`void SysCtl_enableDedicatedJTAGPins ( void )`

Sets the JTAG pins to be exclusively for JTAG until a BOR occurs.

This function sets the JTAG pins to be exclusively used for the JTAG, and not to be shared with the GPIO pins. This setting can only be cleared when a BOR occurs.

**Returns**

None

`void SysCtl_enablePMMAccessProtect ( void )`

Enables PMM Access Protection.

This function enables the PMM Access Protection, which will lock any changes on the PMM control registers until a BOR occurs.

**Returns**

None

`void SysCtl_enableRAMBasedInterruptVectors ( void )`

Enables RAM-based Interrupt Vectors.

This function enables RAM-base Interrupt Vectors, which means that interrupt vectors are generated with the end address at the top of RAM, instead of the top of the lower 64kB of flash.

**Returns**

None

`uint8_t SysCtl_getBSLEntryIndication ( void )`

Returns the indication of a BSL entry sequence from the Spy-Bi-Wire.

This function returns the indication of a BSL entry sequence from the Spy- Bi-Wire.

**Returns**

One of the following:

- **SysCtl\_BSLENTY\_INDICATED**
- **SysCtl\_BSLENTY\_NOTINDICATED**  
indicating if a BSL entry sequence was detected

`uint16_t SysCtl_getJTAGInboxMessage16Bit ( uint8_t inboxSelect )`

Returns the contents of the selected JTAG Inbox in a 16 bit format.

This function returns the message contents of the selected JTAG inbox. If the auto clear settings for the Inbox flags were set, then using this function will automatically clear the corresponding JTAG inbox flag.

**Parameters**

<i>inboxSelect</i>	is the chosen JTAG inbox that the contents of should be returned Valid values are: <ul style="list-style-type: none"> <li>■ <b>SYSCTL_JTAGINBOX_0</b> - return contents of JTAG inbox 0</li> <li>■ <b>SYSCTL_JTAGINBOX_1</b> - return contents of JTAG inbox 1</li> </ul>
--------------------	---

**Returns**

The contents of the selected JTAG inbox in a 16 bit format.

`uint32_t SysCtl_getJTAGInboxMessage32Bit ( void )`

Returns the contents of JTAG Inboxes in a 32 bit format.

This function returns the message contents of both JTAG inboxes in a 32 bit format. This function should be used if 32-bit messaging has been set in the `SYS_initJTAGMailbox()` function. If the auto clear settings for the Inbox flags were set, then using this function will automatically clear both JTAG inbox flags.

**Returns**

The contents of both JTAG messages in a 32 bit format.

`uint8_t SysCtl_getJTAGMailboxFlagStatus ( uint8_t mailboxFlagMask )`

Returns the status of the selected JTAG Mailbox flags.

This function will return the status of the selected JTAG Mailbox flags in bit mask format matching that passed into the `mailboxFlagMask` parameter.

**Parameters**

<i>mailboxFlagMask</i>	is the bit mask of JTAG mailbox flags that the status of should be returned. Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>SYSCTL_JTAGOUTBOX_FLAG0</b> - flag for JTAG outbox 0</li> <li>■ <b>SYSCTL_JTAGOUTBOX_FLAG1</b> - flag for JTAG outbox 1</li> <li>■ <b>SYSCTL_JTAGINBOX_FLAG0</b> - flag for JTAG inbox 0</li> <li>■ <b>SYSCTL_JTAGINBOX_FLAG1</b> - flag for JTAG inbox 1</li> </ul>
------------------------	---

**Returns**

A bit mask of the status of the selected mailbox flags.

`void SysCtl_initJTAGMailbox ( uint8_t mailboxSizeSelect, uint8_t autoClearInboxFlagSelect )`

Initializes JTAG Mailbox with selected properties.

This function sets the specified settings for the JTAG Mailbox system. The settings that can be set are the size of the JTAG messages, and the auto-clearing of the inbox flags. If the inbox flags are set to auto-clear, then the inbox flags will be cleared upon reading of the inbox message buffer, otherwise they will have to be reset by software using the `SYS_clearJTAGMailboxFlagStatus()` function.

#### Parameters

<i>mailboxSize</i> ↔ <i>Select</i>	<p>is the size of the JTAG Mailboxes, whether 16- or 32-bits. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SYSCTL_JTAGMBSIZE_16BIT</b> [Default] - the JTAG messages will take up only one JTAG mailbox (i. e. an outgoing message will take up only 1 outbox of the JTAG mailboxes)</li> <li>■ <b>SYSCTL_JTAGMBSIZE_32BIT</b> - the JTAG messages will be contained within both JTAG mailboxes (i. e. an outgoing message will take up both Outboxes of the JTAG mailboxes)</li> </ul> <p>Modified bits are <b>JMBMODE</b> of <b>SYSJMBC</b> register.</p>
<i>autoClear</i> ↔ <i>InboxFlagSelect</i>	<p>decides how the JTAG inbox flags should be cleared, whether automatically after the corresponding outbox has been written to, or manually by software. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SYSCTL_JTAGINBOX0AUTO_JTAGINBOX1AUTO</b> [Default] - both JTAG inbox flags will be reset automatically when the corresponding inbox is read from.</li> <li>■ <b>SYSCTL_JTAGINBOX0AUTO_JTAGINBOX1SW</b> - only JTAG inbox 0 flag is reset automatically, while JTAG inbox 1 is reset with the</li> <li>■ <b>SYSCTL_JTAGINBOX0SW_JTAGINBOX1AUTO</b> - only JTAG inbox 1 flag is reset automatically, while JTAG inbox 0 is reset with the</li> <li>■ <b>SYSCTL_JTAGINBOX0SW_JTAGINBOX1SW</b> - both JTAG inbox flags will need to be reset manually by the</li> </ul> <p>Modified bits are <b>JMBCLR0OFF</b> and <b>JMBCLR1OFF</b> of <b>SYSJMBC</b> register.</p>

#### Returns

None

```
void SysCtl_setJTAGOutgoingMessage16Bit ( uint8_t outboxSelect, uint16_t
    outgoingMessage )
```

Sets a 16 bit outgoing message in to the selected JTAG Outbox.

This function sets the outgoing message in the selected JTAG outbox. The corresponding JTAG outbox flag is cleared after this function, and set after the JTAG has read the message.

#### Parameters

<i>outboxSelect</i>	<p>is the chosen JTAG outbox that the message should be set it. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>SYSCTL_JTAGOUTBOX_0</b> - set the contents of JTAG outbox 0</li> <li>■ <b>SYSCTL_JTAGOUTBOX_1</b> - set the contents of JTAG outbox 1</li> </ul>
---------------------	--

<i>outgoing</i> ↔ <i>Message</i>	is the message to send to the JTAG. Modified bits are <b>MSGHI</b> and <b>MSGLO</b> of <b>SYSJMBOX</b> register.
-------------------------------------	---

**Returns**

None

```
void SysCtl_setJTAGOutgoingMessage32Bit ( uint32_t outgoingMessage )
```

Sets a 32 bit message in to both JTAG Outboxes.

This function sets the 32-bit outgoing message in both JTAG outboxes. The JTAG outbox flags are cleared after this function, and set after the JTAG has read the message.

**Parameters**

<i>outgoing</i> ↔ <i>Message</i>	is the message to send to the JTAG. Modified bits are <b>MSGHI</b> and <b>MSGLO</b> of <b>SYSJMBOX</b> register.
-------------------------------------	---

**Returns**

None

## 24.3 Programming Example

The following example shows how to initialize and use the SYS API

```
SysCtl_enableRAMBasedInterruptVectors();
```

## 25 16-Bit Timer\_A (TIMER\_A)

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### 25.1 Introduction

TIMER\_A is a 16-bit timer/counter with multiple capture/compare registers. TIMER\_A can support multiple capture/compares, PWM outputs, and interval timing. TIMER\_A also has extensive interrupt capabilities. Interrupts may be generated from the counter on overflow conditions and from each of the capture/compare registers.

This peripheral API handles Timer A hardware peripheral.

TIMER\_A features include:

- Asynchronous 16-bit timer/counter with four operating modes
- Selectable and configurable clock source
- Up to seven configurable capture/compare registers
- Configurable outputs with pulse width modulation (PWM) capability
- Asynchronous input and output latching
- Interrupt vector register for fast decoding of all Timer interrupts

TIMER\_A can operate in 3 modes

- Continuous Mode
- Up Mode
- Down Mode

TIMER\_A Interrupts may be generated on counter overflow conditions and during capture compare events.

The TIMER\_A may also be used to generate PWM outputs. PWM outputs can be generated by initializing the compare mode with `TIMER_A_initCompare()` and the necessary parameters. The PWM may be customized by selecting a desired timer mode (continuous/up/upDown), duty cycle, output mode, timer period etc. The library also provides a simpler way to generate PWM using `Timer_A_generatePWM()` API. However the level of customization and the kinds of PWM generated are limited in this API. Depending on how complex the PWM is and what level of customization is required, the user can use `Timer_A_generatePWM()` or a combination of `Timer_initCompare()` and timer start APIs

The TIMER\_A API provides a set of functions for dealing with the TIMER\_A module. Functions are provided to configure and control the timer, along with functions to modify timer/counter values, and to manage interrupt handling for the timer.

Control is also provided over interrupt sources and events. Interrupts can be generated to indicate that an event has been captured.

## 25.2 API Functions

### Functions

- void `Timer_A_startCounter` (uint16\_t baseAddress, uint16\_t timerMode)  
*Starts Timer\_A counter.*
- void `Timer_A_initContinuousMode` (uint16\_t baseAddress, `Timer_A_initContinuousModeParam` \*param)  
*Configures Timer\_A in continuous mode.*
- void `Timer_A_initUpMode` (uint16\_t baseAddress, `Timer_A_initUpModeParam` \*param)  
*Configures Timer\_A in up mode.*
- void `Timer_A_initUpDownMode` (uint16\_t baseAddress, `Timer_A_initUpDownModeParam` \*param)  
*Configures Timer\_A in up down mode.*
- void `Timer_A_initCaptureMode` (uint16\_t baseAddress, `Timer_A_initCaptureModeParam` \*param)  
*Initializes Capture Mode.*
- void `Timer_A_initCompareMode` (uint16\_t baseAddress, `Timer_A_initCompareModeParam` \*param)  
*Initializes Compare Mode.*
- void `Timer_A_enableInterrupt` (uint16\_t baseAddress)  
*Enable timer interrupt.*
- void `Timer_A_disableInterrupt` (uint16\_t baseAddress)  
*Disable timer interrupt.*
- uint32\_t `Timer_A_getInterruptStatus` (uint16\_t baseAddress)  
*Get timer interrupt status.*
- void `Timer_A_enableCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Enable capture compare interrupt.*
- void `Timer_A_disableCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Disable capture compare interrupt.*
- uint32\_t `Timer_A_getCaptureCompareInterruptStatus` (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint16\_t mask)  
*Return capture compare interrupt status.*
- void `Timer_A_clear` (uint16\_t baseAddress)  
*Reset/Clear the timer clock divider, count direction, count.*
- uint8\_t `Timer_A_getSynchronizedCaptureCompareInput` (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint16\_t synchronized)  
*Get synchronized capturecompare input.*
- uint8\_t `Timer_A_getOutputForOutputModeOutBitValue` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Get output bit for output mode.*
- uint16\_t `Timer_A_getCaptureCompareCount` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Get current capturecompare count.*
- void `Timer_A_setOutputForOutputModeOutBitValue` (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint8\_t outputModeOutBitValue)  
*Set output bit for output mode.*
- void `Timer_A_outputPWM` (uint16\_t baseAddress, `Timer_A_outputPWMPParam` \*param)  
*Generate a PWM with timer running in up mode.*
- void `Timer_A_stop` (uint16\_t baseAddress)



- Stops the timer.*
- void `Timer_A_setCompareValue` (uint16\_t baseAddress, uint16\_t compareRegister, uint16\_t compareValue)
- Sets the value of the capture-compare register.*
- void `Timer_A_setOutputMode` (uint16\_t baseAddress, uint16\_t compareRegister, uint16\_t compareOutputMode)
- Sets the output mode.*
- void `Timer_A_clearTimerInterrupt` (uint16\_t baseAddress)
- Clears the Timer TAIFG interrupt flag.*
- void `Timer_A_clearCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)
- Clears the capture-compare interrupt flag.*
- uint16\_t `Timer_A_getCounterValue` (uint16\_t baseAddress)
- Reads the current timer count value.*

## 25.2.1 Detailed Description

The TIMER\_A API is broken into three groups of functions: those that deal with timer configuration and control, those that deal with timer contents, and those that deal with interrupt handling.

TIMER\_A configuration and initialization is handled by

- `Timer_A_startCounter()`
- `Timer_A_initUpMode()`
- `Timer_A_initUpDownMode()`
- `Timer_A_initContinuousMode()`
- `Timer_A_initCaptureMode()`
- `Timer_A_initCompareMode()`
- `Timer_A_clear()`
- `Timer_A_stop()`

TIMER\_A outputs are handled by

- `Timer_A_getSynchronizedCaptureCompareInput()`
- `Timer_A_getOutputForOutputModeOutBitValue()`
- `Timer_A_setOutputForOutputModeOutBitValue()`
- `Timer_A_outputPWM()`
- `Timer_A_getCaptureCompareCount()`
- `Timer_A_setCompareValue()`
- `Timer_A_getCounterValue()`

The interrupt handler for the TIMER\_A interrupt is managed with

- `Timer_A_enableInterrupt()`
- `Timer_A_disableInterrupt()`
- `Timer_A_getInterruptStatus()`
- `Timer_A_enableCaptureCompareInterrupt()`

- [Timer\\_A.disableCaptureCompareInterrupt\(\)](#)
- [Timer\\_A.getCaptureCompareInterruptStatus\(\)](#)
- [Timer\\_A.clearCaptureCompareInterrupt\(\)](#)
- [Timer\\_A.clearTimerInterrupt\(\)](#)

## 25.2.2 Function Documentation

`void Timer_A_clear ( uint16_t baseAddress )`

Reset/Clear the timer clock divider, count direction, count.

### Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

Modified bits of **TAxCTL** register.

### Returns

None

`void Timer_A_clearCaptureCompareInterrupt ( uint16_t baseAddress, uint16_t captureCompareRegister )`

Clears the capture-compare interrupt flag.

### Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>capture↔ Compare↔ Register</i>	selects the Capture-compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

Modified bits are **CCIFG** of **TAxCCTLn** register.

### Returns

None

`void Timer_A_clearTimerInterrupt ( uint16_t baseAddress )`

Clears the Timer TAIFG interrupt flag.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

Modified bits are **TAIFG** of **TAxCTL** register.

**Returns**

None

```
void Timer_A_disableCaptureCompareInterrupt ( uint16_t baseAddress, uint16_t
captureCompareRegister )
```

Disable capture compare interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>capture↔ Compare↔ Register</i>	is the selected capture compare register Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

Modified bits of **TAxCTLn** register.

**Returns**

None

```
void Timer_A_disableInterrupt ( uint16_t baseAddress )
```

Disable timer interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

Modified bits of **TAxCTL** register.

**Returns**

None

```
void Timer_A_enableCaptureCompareInterrupt ( uint16_t baseAddress, uint16_t
captureCompareRegister )
```

Enable capture compare interrupt.

Does not clear interrupt flags

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>capture</i> ↔ <i>Compare</i> ↔ <i>Register</i>	is the selected capture compare register Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

Modified bits of **TAxCTLn** register.

**Returns**

None

```
void Timer_A_enableInterrupt ( uint16_t baseAddress )
```

Enable timer interrupt.

Does not clear interrupt flags

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

Modified bits of **TAxCTL** register.

**Returns**

None

```
uint16_t Timer_A_getCaptureCompareCount ( uint16_t baseAddress, uint16_t
captureCompareRegister )
```

Get current capturecompare count.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>capture</i> ↔ <i>Compare</i> ↔ <i>Register</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

## Returns

Current count as an uint16\_t

```
uint32_t Timer_A_getCaptureCompareInterruptStatus ( uint16_t baseAddress, uint16_t
captureCompareRegister, uint16_t mask )
```

Return capture compare interrupt status.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>capture</i> ↔ <i>Compare</i> ↔ <i>Register</i>	is the selected capture compare register Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

<i>mask</i>	is the mask for the interrupt status Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURE_OVERFLOW</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_INTERRUPT_FLAG</b></li> </ul>
-------------	--

**Returns**

Logical OR of any of the following:

- **Timer\_A\_CAPTURE\_OVERFLOW**
- **Timer\_A\_CAPTURECOMPARE\_INTERRUPT\_FLAG**  
indicating the status of the masked interrupts

`uint16_t Timer_A_getCounterValue ( uint16_t baseAddress )`

Reads the current timer count value.

Reads the current count value of the timer. There is a majority vote system in place to confirm an accurate value is returned. The `TIMER_A_THRESHOLD` #define in the corresponding header file can be modified so that the votes must be closer together for a consensus to occur.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

**Returns**

Majority vote of timer count value

`uint32_t Timer_A_getInterruptStatus ( uint16_t baseAddress )`

Get timer interrupt status.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

**Returns**

One of the following:

- **Timer\_A\_INTERRUPT\_NOT\_PENDING**
- **Timer\_A\_INTERRUPT\_PENDING**  
indicating the Timer\_A interrupt status

`uint8_t Timer_A_getOutputForOutputModeOutBitValue ( uint16_t baseAddress, uint16_t captureCompareRegister )`

Get output bit for output mode.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>capture</i> ↔ <i>Compare</i> ↔ <i>Register</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

## Returns

One of the following:

- **Timer\_A\_OUTPUTMODE\_OUTBITVALUE\_HIGH**
- **Timer\_A\_OUTPUTMODE\_OUTBITVALUE\_LOW**

`uint8_t Timer_A_getSynchronizedCaptureCompareInput ( uint16_t baseAddress, uint16_t captureCompareRegister, uint16_t synchronized )`

Get synchronized capturecompare input.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>capture</i> ↔ <i>Compare</i> ↔ <i>Register</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>



<i>synchronized</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_READ_SYNCHRONIZED_CAPTURECOMPAREINPUT</b></li> <li>■ <b>TIMER_A_READ_CAPTURE_COMPARE_INPUT</b></li> </ul>
---------------------	---

**Returns**

One of the following:

- **Timer\_A\_CAPTURECOMPARE\_INPUT\_HIGH**
- **Timer\_A\_CAPTURECOMPARE\_INPUT\_LOW**

```
void Timer_A_initCaptureMode ( uint16_t baseAddress, Timer_A_initCaptureModeParam *  
param )
```

Initializes Capture Mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>param</i>	is the pointer to struct for capture mode initialization.

Modified bits of **TaxCCTLn** register.

**Returns**

None

References `Timer_A_initCaptureModeParam::captureInputSelect`,  
`Timer_A_initCaptureModeParam::captureInterruptEnable`,  
`Timer_A_initCaptureModeParam::captureMode`,  
`Timer_A_initCaptureModeParam::captureOutputMode`,  
`Timer_A_initCaptureModeParam::captureRegister`, and  
`Timer_A_initCaptureModeParam::synchronizeCaptureSource`.

```
void Timer_A_initCompareMode ( uint16_t baseAddress, Timer_A_initCompareModeParam  
* param )
```

Initializes Compare Mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>param</i>	is the pointer to struct for compare mode initialization.

Modified bits of **TaxCCRn** register and bits of **TaxCCTLn** register.

**Returns**

None

References `Timer_A_initCompareModeParam::compareInterruptEnable`,  
`Timer_A_initCompareModeParam::compareOutputMode`,  
`Timer_A_initCompareModeParam::compareRegister`, and  
`Timer_A_initCompareModeParam::compareValue`.

```
void Timer_A_initContinuousMode ( uint16_t baseAddress, Timer_A_initContinuous↔  
ModeParam * param )
```

Configures Timer\_A in continuous mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>param</i>	is the pointer to struct for continuous mode initialization.

Modified bits of **TAxCTL** register.

**Returns**

None

References Timer\_A\_initContinuousModeParam::clockSource, Timer\_A\_initContinuousModeParam::clockSourceDivider, Timer\_A\_initContinuousModeParam::startTimer, Timer\_A\_initContinuousModeParam::timerClear, and Timer\_A\_initContinuousModeParam::timerInterruptEnable\_TAIE.

```
void Timer_A_initUpDownMode ( uint16_t baseAddress, Timer_A_initUpDownModeParam
* param )
```

Configures Timer\_A in up down mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>param</i>	is the pointer to struct for up-down mode initialization.

Modified bits of **TAxCTL** register, bits of **TAxCCTL0** register and bits of **TAxCCR0** register.

**Returns**

None

References Timer\_A\_initUpDownModeParam::captureCompareInterruptEnable\_CCR0\_CCIE, Timer\_A\_initUpDownModeParam::clockSource, Timer\_A\_initUpDownModeParam::clockSourceDivider, Timer\_A\_initUpDownModeParam::startTimer, Timer\_A\_initUpDownModeParam::timerClear, Timer\_A\_initUpDownModeParam::timerInterruptEnable\_TAIE, and Timer\_A\_initUpDownModeParam::timerPeriod.

```
void Timer_A_initUpMode ( uint16_t baseAddress, Timer_A_initUpModeParam * param )
```

Configures Timer\_A in up mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>param</i>	is the pointer to struct for up mode initialization.

Modified bits of **TAxCTL** register, bits of **TAxCCTL0** register and bits of **TAxCCR0** register.

**Returns**

None

References Timer\_A\_initUpModeParam::captureCompareInterruptEnable\_CCR0\_CCIE, Timer\_A\_initUpModeParam::clockSource, Timer\_A\_initUpModeParam::clockSourceDivider,

Timer\_A\_initUpModeParam::startTimer, Timer\_A\_initUpModeParam::timerClear,  
 Timer\_A\_initUpModeParam::timerInterruptEnable\_TAIE, and  
 Timer\_A\_initUpModeParam::timerPeriod.

```
void Timer_A_outputPWM ( uint16_t baseAddress, Timer_A_outputPWMPParam * param )
```

Generate a PWM with timer running in up mode.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>param</i>	is the pointer to struct for PWM configuration.

Modified bits of **TAxCTL** register, bits of **TAxCCTL0** register, bits of **TAxCCR0** register and bits of **TAxCCTLn** register.

#### Returns

None

References Timer\_A\_outputPWMPParam::clockSource,  
 Timer\_A\_outputPWMPParam::clockSourceDivider,  
 Timer\_A\_outputPWMPParam::compareOutputMode, Timer\_A\_outputPWMPParam::compareRegister,  
 Timer\_A\_outputPWMPParam::dutyCycle, and Timer\_A\_outputPWMPParam::timerPeriod.

```
void Timer_A_setCompareValue ( uint16_t baseAddress, uint16_t compareRegister,  

  uint16_t compareValue )
```

Sets the value of the capture-compare register.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>compareRegister</i>	selects the Capture register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

<i>compareValue</i>	is the count to be compared with in compare mode
---------------------	--

Modified bits of **TAxCCRn** register.

**Returns**

None

```
void Timer_A_setOutputForOutputModeOutBitValue ( uint16_t baseAddress, uint16_t
captureCompareRegister, uint8_t outputModeOutBitValue )
```

Set output bit for output mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
<i>capture← Compare← Register</i>	Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>outputMode← OutBitValue</i>	is the value to be set for out bit Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_A_OUTPUTMODE_OUTBITVALUE_HIGH</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_OUTBITVALUE_LOW</b></li> </ul>

Modified bits of **TAxCTLn** register.

**Returns**

None

```
void Timer_A_setOutputMode ( uint16_t baseAddress, uint16_t compareRegister, uint16_t
compareOutputMode )
```

Sets the output mode.

Sets the output mode for the timer even the timer is already running.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

<p><i>compare← Register</i></p>	<p>selects the compare register being used. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_A_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<p><i>compare← OutputMode</i></p>	<p>specifies the output mode. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>TIMER_A_OUTPUTMODE_OUTBITVALUE</b> [Default]</li> <li>■ <b>TIMER_A_OUTPUTMODE_SET</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_TOGGLE_RESET</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_SET_RESET</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_TOGGLE</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_RESET</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_TOGGLE_SET</b></li> <li>■ <b>TIMER_A_OUTPUTMODE_RESET_SET</b></li> </ul>

Modified bits are **OUTMOD** of **TAXCCTLn** register.

**Returns**

None

`void Timer_A_startCounter ( uint16_t baseAddress, uint16_t timerMode )`

Starts `Timer_A` counter.

This function assumes that the timer has been previously configured using `Timer_A_initContinuousMode`, `Timer_A_initUpMode` or `Timer_A_initUpDownMode`.

**Parameters**

<p><i>baseAddress</i></p>	<p>is the base address of the <code>TIMER_A</code> module.</p>
<p><i>timerMode</i></p>	<p>mode to put the timer in Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>TIMER_A_STOP_MODE</b></li> <li>■ <b>TIMER_A_UP_MODE</b></li> <li>■ <b>TIMER_A_CONTINUOUS_MODE</b> [Default]</li> <li>■ <b>TIMER_A_UPDOWN_MODE</b></li> </ul>

Modified bits of **TAxCTL** register.

**Returns**

None

```
void Timer_A_stop ( uint16_t baseAddress )
```

Stops the timer.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_A module.
--------------------	--

Modified bits of **TAxCTL** register.

**Returns**

None

## 25.3 Programming Example

The following example shows some TIMER\_A operations using the APIs

```

{
    //Start TIMER_A
    Timer_A_initContinuousModeParam initContParam = {0};
    initContParam.clockSource = TIMER_A_CLOCKSOURCE_SMCLK;
    initContParam.clockSourceDivider = TIMER_A_CLOCKSOURCE_DIVIDER_1;
    initContParam.timerInterruptEnable.TAIE = TIMER_A_TAIE_INTERRUPT_DISABLE;
    initContParam.timerClear = TIMER_A_DO_CLEAR;
    initContParam.startTimer = false;
    Timer_A_initContinuousMode(TIMER_A1_BASE, &initContParam);

    //Initiaze compare mode
    Timer_A_clearCaptureCompareInterrupt(TIMER_A1_BASE,
        TIMER_A_CAPTURECOMPARE_REGISTER_0
    );

    Timer_A_initCompareModeParam initCompParam = {0};
    initCompParam.compareRegister = TIMER_A_CAPTURECOMPARE_REGISTER_0;
    initCompParam.compareInterruptEnable = TIMER_A_CAPTURECOMPARE_INTERRUPT_ENABLE;
    initCompParam.compareOutputMode = TIMER_A_OUTPUTMODE_OUTBITVALUE;
    initCompParam.compareValue = COMPARE_VALUE;
    Timer_A_initCompareMode(TIMER_A1_BASE, &initCompParam);

    Timer_A_startCounter( TIMER_A1_BASE,
        TIMER_A_CONTINUOUS_MODE
    );

    //Enter LPM0
    _bis_SR_register(LPM0_bits);

    //For debugger
    __no_operation();
}

```



## 26 16-Bit Timer\_B (TIMER\_B)

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### 26.1 Introduction

TIMER\_B is a 16-bit timer/counter with multiple capture/compare registers. TIMER\_B can support multiple capture/compares, PWM outputs, and interval timing. TIMER\_B also has extensive interrupt capabilities. Interrupts may be generated from the counter on overflow conditions and from each of the capture/compare registers.

This peripheral API handles Timer B hardware peripheral.

TIMER\_B features include:

- Asynchronous 16-bit timer/counter with four operating modes
- Selectable and configurable clock source
- Up to seven configurable capture/compare registers
- Configurable outputs with pulse width modulation (PWM) capability
- Asynchronous input and output latching
- Interrupt vector register for fast decoding of all Timer\_B interrupts

Differences From Timer\_A Timer\_B is identical to Timer\_A with the following exceptions:

- The length of Timer\_B is programmable to be 8, 10, 12, or 16 bits
- Timer\_B TBxCCRn registers are double-buffered and can be grouped
- All Timer\_B outputs can be put into a high-impedance state
- The SCCI bit function is not implemented in Timer\_B

TIMER\_B can operate in 3 modes

- Continuous Mode
- Up Mode
- Down Mode

TIMER\_B Interrupts may be generated on counter overflow conditions and during capture compare events.

The TIMER\_B may also be used to generate PWM outputs. PWM outputs can be generated by initializing the compare mode with `TIMER_B_initCompare()` and the necessary parameters. The PWM may be customized by selecting a desired timer mode (continuous/up/upDown), duty cycle, output mode, timer period etc. The library also provides a simpler way to generate PWM using `TIMER_B_generatePWM()` API. However the level of customization and the kinds of PWM generated are limited in this API. Depending on how complex the PWM is and what level of customization is required, the user can use `TIMER_B_generatePWM()` or a combination of `Timer_initCompare()` and timer start APIs

The TIMER\_B API provides a set of functions for dealing with the TIMER\_B module. Functions are provided to configure and control the timer, along with functions to modify timer/counter values, and to manage interrupt handling for the timer.

Control is also provided over interrupt sources and events. Interrupts can be generated to indicate that an event has been captured.

## 26.2 API Functions

### Functions

- void `Timer_B_startCounter` (uint16\_t baseAddress, uint16\_t timerMode)  
*Starts Timer\_B counter.*
- void `Timer_B_initContinuousMode` (uint16\_t baseAddress, `Timer_B_initContinuousModeParam` \*param)  
*Configures Timer\_B in continuous mode.*
- void `Timer_B_initUpMode` (uint16\_t baseAddress, `Timer_B_initUpModeParam` \*param)  
*Configures Timer\_B in up mode.*
- void `Timer_B_initUpDownMode` (uint16\_t baseAddress, `Timer_B_initUpDownModeParam` \*param)  
*Configures Timer\_B in up down mode.*
- void `Timer_B_initCaptureMode` (uint16\_t baseAddress, `Timer_B_initCaptureModeParam` \*param)  
*Initializes Capture Mode.*
- void `Timer_B_initCompareMode` (uint16\_t baseAddress, `Timer_B_initCompareModeParam` \*param)  
*Initializes Compare Mode.*
- void `Timer_B_enableInterrupt` (uint16\_t baseAddress)  
*Enable Timer\_B interrupt.*
- void `Timer_B_disableInterrupt` (uint16\_t baseAddress)  
*Disable Timer\_B interrupt.*
- uint32\_t `Timer_B_getInterruptStatus` (uint16\_t baseAddress)  
*Get Timer\_B interrupt status.*
- void `Timer_B_enableCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Enable capture compare interrupt.*
- void `Timer_B_disableCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Disable capture compare interrupt.*
- uint32\_t `Timer_B_getCaptureCompareInterruptStatus` (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint16\_t mask)  
*Return capture compare interrupt status.*
- void `Timer_B_clear` (uint16\_t baseAddress)  
*Reset/Clear the Timer\_B clock divider, count direction, count.*
- uint8\_t `Timer_B_getSynchronizedCaptureCompareInput` (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint16\_t synchronized)  
*Get synchronized capturecompare input.*
- uint8\_t `Timer_B_getOutputForOutputModeOutBitValue` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Get output bit for output mode.*

- uint16\_t `Timer_B.getCaptureCompareCount` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Get current capturecompare count.*
- void `Timer_B.setOutputForOutputModeOutBitValue` (uint16\_t baseAddress, uint16\_t captureCompareRegister, uint8\_t outputModeOutBitValue)  
*Set output bit for output mode.*
- void `Timer_B.outputPWM` (uint16\_t baseAddress, `Timer_B.outputPWMPParam` \*param)  
*Generate a PWM with Timer\_B running in up mode.*
- void `Timer_B.stop` (uint16\_t baseAddress)  
*Stops the Timer\_B.*
- void `Timer_B.setCompareValue` (uint16\_t baseAddress, uint16\_t compareRegister, uint16\_t compareValue)  
*Sets the value of the capture-compare register.*
- void `Timer_B.clearTimerInterrupt` (uint16\_t baseAddress)  
*Clears the Timer\_B TBIFG interrupt flag.*
- void `Timer_B.clearCaptureCompareInterrupt` (uint16\_t baseAddress, uint16\_t captureCompareRegister)  
*Clears the capture-compare interrupt flag.*
- void `Timer_B.selectCounterLength` (uint16\_t baseAddress, uint16\_t counterLength)  
*Selects Timer\_B counter length.*
- void `Timer_B.selectLatchingGroup` (uint16\_t baseAddress, uint16\_t groupLatch)  
*Selects Timer\_B Latching Group.*
- void `Timer_B.initCompareLatchLoadEvent` (uint16\_t baseAddress, uint16\_t compareRegister, uint16\_t compareLatchLoadEvent)  
*Selects Compare Latch Load Event.*
- uint16\_t `Timer_B.getCounterValue` (uint16\_t baseAddress)  
*Reads the current timer count value.*
- void `Timer_B.setOutputMode` (uint16\_t baseAddress, uint16\_t compareRegister, uint16\_t compareOutputMode)  
*Sets the output mode.*

## 26.2.1 Detailed Description

The TIMER\_B API is broken into three groups of functions: those that deal with timer configuration and control, those that deal with timer contents, and those that deal with interrupt handling.

TIMER\_B configuration and initialization is handled by

- `Timer_B.startCounter()`
- `Timer_B.initUpMode()`
- `Timer_B.initUpDownMode()`
- `Timer_B.initContinuousMode()`
- `Timer_B.initCapture()`
- `Timer_B.initCompare()`
- `Timer_B.clear()`
- `Timer_B.stop()`
- `Timer_B.initCompareLatchLoadEvent()`
- `Timer_B.selectLatchingGroup()`
- `Timer_B.selectCounterLength()`

TIMER\_B outputs are handled by

- `Timer_B_getSynchronizedCaptureCompareInput()`
- `Timer_B_getOutputForOutputModeOutBitValue()`
- `Timer_B_setOutputForOutputModeOutBitValue()`
- `Timer_B_generatePWM()`
- `Timer_B_getCaptureCompareCount()`
- `Timer_B_setCompareValue()`
- `Timer_B_getCounterValue()`

The interrupt handler for the TIMER\_B interrupt is managed with

- `Timer_B_enableInterrupt()`
- `Timer_B_disableInterrupt()`
- `Timer_B_getInterruptStatus()`
- `Timer_B_enableCaptureCompareInterrupt()`
- `Timer_B_disableCaptureCompareInterrupt()`
- `Timer_B_getCaptureCompareInterruptStatus()`
- `Timer_B_clearCaptureCompareInterrupt()`
- `Timer_B_clearTimerInterrupt()`

## 26.2.2 Function Documentation

`void Timer_B_clear ( uint16_t baseAddress )`

Reset/Clear the Timer\_B clock divider, count direction, count.

### Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

Modified bits of **TBxCTL** register.

### Returns

None

`void Timer_B_clearCaptureCompareInterrupt ( uint16_t baseAddress, uint16_t captureCompareRegister )`

Clears the capture-compare interrupt flag.

### Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>capture← Compare← Register</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

Modified bits are **CCIFG** of **TBxCCTLn** register.

#### Returns

None

```
void Timer_B_clearTimerInterrupt ( uint16_t baseAddress )
```

Clears the Timer\_B TBIFG interrupt flag.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

Modified bits are **TBIFG** of **TBxCTL** register.

#### Returns

None

```
void Timer_B_disableCaptureCompareInterrupt ( uint16_t baseAddress, uint16_t  
captureCompareRegister )
```

Disable capture compare interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>capture← Compare← Register</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

Modified bits of **TBxCCTLn** register.

**Returns**

None

```
void Timer_B_disableInterrupt ( uint16_t baseAddress )
```

Disable Timer\_B interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

Modified bits of **TBxCTL** register.

**Returns**

None

```
void Timer_B_enableCaptureCompareInterrupt ( uint16_t baseAddress, uint16_t  
captureCompareRegister )
```

Enable capture compare interrupt.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>capture</i> ↔ <i>Compare</i> ↔ <i>Register</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

Modified bits of **TBxCCTLn** register.

**Returns**

None

```
void Timer_B_enableInterrupt ( uint16_t baseAddress )
```

Enable Timer\_B interrupt.

Enables Timer\_B interrupt. Does not clear interrupt flags.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

Modified bits of **TBxCTL** register.

**Returns**

None

uint16\_t Timer\_B\_getCaptureCompareCount ( uint16\_t *baseAddress*, uint16\_t *captureCompareRegister* )

Get current capturecompare count.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>captureCompareRegister</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

**Returns**

Current count as uint16\_t

uint32\_t Timer\_B\_getCaptureCompareInterruptStatus ( uint16\_t *baseAddress*, uint16\_t *captureCompareRegister*, uint16\_t *mask* )

Return capture compare interrupt status.



## Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>capture</i> ↔ <i>Compare</i> ↔ <i>Register</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>mask</i>	is the mask for the interrupt status Mask value is the logical OR of any of the following: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURE_OVERFLOW</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_INTERRUPT_FLAG</b></li> </ul>

## Returns

Logical OR of any of the following:

- **Timer\_B\_CAPTURE\_OVERFLOW**
- **Timer\_B\_CAPTURECOMPARE\_INTERRUPT\_FLAG**  
indicating the status of the masked interrupts

`uint16_t Timer_B_getCounterValue ( uint16_t baseAddress )`

Reads the current timer count value.

Reads the current count value of the timer. There is a majority vote system in place to confirm an accurate value is returned. The `Timer_B_THRESHOLD` #define in the associated header file can be modified so that the votes must be closer together for a consensus to occur.

## Parameters

<i>baseAddress</i>	is the base address of the Timer module.
--------------------	--

## Returns

Majority vote of timer count value

`uint32_t Timer_B_getInterruptStatus ( uint16_t baseAddress )`

Get Timer\_B interrupt status.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

**Returns**

One of the following:

- **Timer\_B\_INTERRUPT\_NOT\_PENDING**
- **Timer\_B\_INTERRUPT\_PENDING**  
indicating the status of the Timer\_B interrupt

uint8\_t Timer\_B\_getOutputForOutputModeOutBitValue ( uint16\_t *baseAddress*, uint16\_t *captureCompareRegister* )

Get output bit for output mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>captureCompareRegister</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

**Returns**

One of the following:

- **Timer\_B\_OUTPUTMODE\_OUTBITVALUE\_HIGH**
- **Timer\_B\_OUTPUTMODE\_OUTBITVALUE\_LOW**

uint8\_t Timer\_B\_getSynchronizedCaptureCompareInput ( uint16\_t *baseAddress*, uint16\_t *captureCompareRegister*, uint16\_t *synchronized* )

Get synchronized capturecompare input.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

<i>capture← Compare← Register</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>synchronized</i>	selects the type of capture compare input Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_READ_SYNCHRONIZED_CAPTURECOMPAREINPUT</b></li> <li>■ <b>TIMER_B_READ_CAPTURE_COMPARE_INPUT</b></li> </ul>

**Returns**

One of the following:

- **Timer\_B\_CAPTURECOMPARE\_INPUT\_HIGH**
- **Timer\_B\_CAPTURECOMPARE\_INPUT\_LOW**

```
void Timer_B_initCaptureMode ( uint16_t baseAddress, Timer_B_initCaptureModeParam *  
param )
```

Initializes Capture Mode.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>param</i>	is the pointer to struct for capture mode initialization.

Modified bits of **TBxCCTLn** register.

**Returns**

None

References `Timer_B_initCaptureModeParam::captureInputSelect`,  
`Timer_B_initCaptureModeParam::captureInterruptEnable`,  
`Timer_B_initCaptureModeParam::captureMode`,  
`Timer_B_initCaptureModeParam::captureOutputMode`,  
`Timer_B_initCaptureModeParam::captureRegister`, and  
`Timer_B_initCaptureModeParam::synchronizeCaptureSource`.

```
void Timer_B_initCompareLatchLoadEvent ( uint16_t baseAddress, uint16_t  
compareRegister, uint16_t compareLatchLoadEvent )
```

Selects Compare Latch Load Event.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>compare</i> ↔ <i>Register</i>	selects the compare register being used. Refer to datasheet to ensure the device has the compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>compareLatch</i> ↔ <i>LoadEvent</i>	selects the latch load event Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_LATCH_ON_WRITE_TO_TBxCCRn_COMPARE_REGISTER</b> [Default]</li> <li>■ <b>TIMER_B_LATCH_WHEN_COUNTER_COUNTS_TO_0_IN_UP_OR_CONT_MODE</b></li> <li>■ <b>TIMER_B_LATCH_WHEN_COUNTER_COUNTS_TO_0_IN_UPDOWN_MODE</b></li> <li>■ <b>TIMER_B_LATCH_WHEN_COUNTER_COUNTS_TO_CURRENT_COMPARE_LAT</b>↔ <b>CH_VALUE</b></li> </ul>

Modified bits are **CLLD** of **TBxCCTLn** register.

## Returns

None

```
void Timer_B_initCompareMode ( uint16_t baseAddress, Timer_B_initCompareModeParam
* param )
```

Initializes Compare Mode.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>param</i>	is the pointer to struct for compare mode initialization.

Modified bits of **TBxCCTLn** register and bits of **TBxCCRn** register.

**Returns**

None

References `Timer_B_initCompareModeParam::compareInterruptEnable`,  
`Timer_B_initCompareModeParam::compareOutputMode`,  
`Timer_B_initCompareModeParam::compareRegister`, and  
`Timer_B_initCompareModeParam::compareValue`.

```
void Timer_B_initContinuousMode ( uint16_t baseAddress, Timer_B_initContinuous↔  

ModeParam * param )
```

Configures Timer\_B in continuous mode.

This API does not start the timer. Timer needs to be started when required using the `Timer_B_startCounter` API.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>param</i>	is the pointer to struct for continuous mode initialization.

Modified bits of **TBxCTL** register.

**Returns**

None

References `Timer_B_initContinuousModeParam::clockSource`,  
`Timer_B_initContinuousModeParam::clockSourceDivider`,  
`Timer_B_initContinuousModeParam::startTimer`, `Timer_B_initContinuousModeParam::timerClear`,  
and `Timer_B_initContinuousModeParam::timerInterruptEnable_TBIE`.

```
void Timer_B_initUpDownMode ( uint16_t baseAddress, Timer_B_initUpDownModeParam  

* param )
```

Configures Timer\_B in up down mode.

This API does not start the timer. Timer needs to be started when required using the `Timer_B_startCounter` API.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>param</i>	is the pointer to struct for up-down mode initialization.

Modified bits of **TBxCTL** register, bits of **TBxCCTL0** register and bits of **TBxCCR0** register.

**Returns**

None

References `Timer_B_initUpDownModeParam::captureCompareInterruptEnable_CCR0_CCIE`,  
`Timer_B_initUpDownModeParam::clockSource`,  
`Timer_B_initUpDownModeParam::clockSourceDivider`,  
`Timer_B_initUpDownModeParam::startTimer`, `Timer_B_initUpDownModeParam::timerClear`,

Timer\_B\_initUpDownModeParam::timerInterruptEnable\_TBIE, and  
Timer\_B\_initUpDownModeParam::timerPeriod.

```
void Timer_B_initUpMode ( uint16_t baseAddress, Timer_B_initUpModeParam * param )
```

Configures Timer\_B in up mode.

This API does not start the timer. Timer needs to be started when required using the  
Timer\_B.startCounter API.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>param</i>	is the pointer to struct for up mode initialization.

Modified bits of **TBxCTL** register, bits of **TBxCCTL0** register and bits of **TBxCCR0** register.

#### Returns

None

References Timer\_B\_initUpModeParam::captureCompareInterruptEnable\_CCR0\_CCIE,  
Timer\_B\_initUpModeParam::clockSource, Timer\_B\_initUpModeParam::clockSourceDivider,  
Timer\_B\_initUpModeParam::startTimer, Timer\_B\_initUpModeParam::timerClear,  
Timer\_B\_initUpModeParam::timerInterruptEnable\_TBIE, and  
Timer\_B\_initUpModeParam::timerPeriod.

```
void Timer_B_outputPWM ( uint16_t baseAddress, Timer_B_outputPWMPParam * param )
```

Generate a PWM with Timer\_B running in up mode.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>param</i>	is the pointer to struct for PWM configuration.

Modified bits of **TBxCCTLn** register, bits of **TBxCTL** register, bits of **TBxCCTL0** register and bits  
of **TBxCCR0** register.

#### Returns

None

References Timer\_B\_outputPWMPParam::clockSource,  
Timer\_B\_outputPWMPParam::clockSourceDivider,  
Timer\_B\_outputPWMPParam::compareOutputMode, Timer\_B\_outputPWMPParam::compareRegister,  
Timer\_B\_outputPWMPParam::dutyCycle, and Timer\_B\_outputPWMPParam::timerPeriod.

```
void Timer_B_selectCounterLength ( uint16_t baseAddress, uint16_t counterLength )
```

Selects Timer\_B counter length.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>counterLength</i>	selects the value of counter length. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_COUNTER_16BIT</b> [Default]</li> <li>■ <b>TIMER_B_COUNTER_12BIT</b></li> <li>■ <b>TIMER_B_COUNTER_10BIT</b></li> <li>■ <b>TIMER_B_COUNTER_8BIT</b></li> </ul>

Modified bits are **CNTL** of **TBxCTL** register.

**Returns**

None

```
void Timer_B_selectLatchingGroup ( uint16_t baseAddress, uint16_t groupLatch )
```

Selects Timer\_B Latching Group.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>groupLatch</i>	selects the latching group. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_GROUP_NONE</b> [Default]</li> <li>■ <b>TIMER_B_GROUP_CL12_CL23_CL56</b></li> <li>■ <b>TIMER_B_GROUP_CL123_CL456</b></li> <li>■ <b>TIMER_B_GROUP_ALL</b></li> </ul>

Modified bits are **TBCLGRP** of **TBxCTL** register.

**Returns**

None

```
void Timer_B_setCompareValue ( uint16_t baseAddress, uint16_t compareRegister,
                               uint16_t compareValue )
```

Sets the value of the capture-compare register.

**Parameters**

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>compareRegister</i>	selects the compare register being used. Refer to datasheet to ensure the device has the compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>compareValue</i>	is the count to be compared with in compare mode

Modified bits of **TBxCCRn** register.

**Returns**

None

```
void Timer_B_setOutputForOutputModeOutBitValue ( uint16_t baseAddress, uint16_t
                                                    captureCompareRegister, uint8_t outputModeOutBitValue )
```

Set output bit for output mode.



## Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>capture</i> ↔ <i>Compare</i> ↔ <i>Register</i>	selects the capture compare register being used. Refer to datasheet to ensure the device has the capture compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>
<i>outputMode</i> ↔ <i>OutBitValue</i>	the value to be set for out bit Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_OUTPUTMODE_OUTBITVALUE_HIGH</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_OUTBITVALUE_LOW</b></li> </ul>

Modified bits of **TBxCCTLn** register.

## Returns

None

```
void Timer_B_setOutputMode ( uint16_t baseAddress, uint16_t compareRegister, uint16_t
compareOutputMode )
```

Sets the output mode.

Sets the output mode for the timer even the timer is already running.

## Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
<i>compare</i> ↔ <i>Register</i>	selects the compare register being used. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_0</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_1</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_2</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_3</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_4</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_5</b></li> <li>■ <b>TIMER_B_CAPTURECOMPARE_REGISTER_6</b></li> </ul>

<i>compare</i> ↔ <i>OutputMode</i>	specifies the output mode. Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_OUTPUTMODE_OUTBITVALUE</b> [Default]</li> <li>■ <b>TIMER_B_OUTPUTMODE_SET</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_TOGGLE_RESET</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_SET_RESET</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_TOGGLE</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_RESET</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_TOGGLE_SET</b></li> <li>■ <b>TIMER_B_OUTPUTMODE_RESET_SET</b></li> </ul>
---------------------------------------	--

Modified bits are **OUTMOD** of **TBxCCTLn** register.

#### Returns

None

```
void Timer_B_startCounter ( uint16_t baseAddress, uint16_t timerMode )
```

Starts Timer\_B counter.

This function assumes that the timer has been previously configured using `Timer_B_initContinuousMode`, `Timer_B_initUpMode` or `Timer_B_initUpDownMode`.

#### Parameters

<i>baseAddress</i>	is the base address of the <b>TIMER_B</b> module.
<i>timerMode</i>	selects the mode of the timer Valid values are: <ul style="list-style-type: none"> <li>■ <b>TIMER_B_STOP_MODE</b></li> <li>■ <b>TIMER_B_UP_MODE</b></li> <li>■ <b>TIMER_B_CONTINUOUS_MODE</b> [Default]</li> <li>■ <b>TIMER_B_UPDOWN_MODE</b></li> </ul>

Modified bits of **TBxCTL** register.

#### Returns

None

```
void Timer_B_stop ( uint16_t baseAddress )
```

Stops the Timer\_B.

#### Parameters

<i>baseAddress</i>	is the base address of the TIMER_B module.
--------------------	--

Modified bits of **TBxCTL** register.

Returns

None

## 26.3 Programming Example

The following example shows some TIMER\_B operations using the APIs

```
{ //Start timer in continuous mode sourced by SMCLK
Timer_B_initContinuousModeParam initContParam = {0};
initContParam.clockSource = TIMER_B_CLOCKSOURCE_SMCLK;
initContParam.clockSourceDivider = TIMER_B_CLOCKSOURCE_DIVIDER_1;
initContParam.timerInterruptEnable_TBIE = TIMER_B_TBIE_INTERRUPT_DISABLE;
initContParam.timerClear = TIMER_B_DO_CLEAR;
initContParam.startTimer = false;
Timer_B_initContinuousMode(TIMER_B0_BASE, &initContParam);

//Initiaze compare mode
Timer_B_clearCaptureCompareInterrupt(TIMER_B0_BASE,
    TIMER_B_CAPTURECOMPARE_REGISTER_0);

Timer_B_initCompareModeParam initCompParam = {0};
initCompParam.compareRegister = TIMER_B_CAPTURECOMPARE_REGISTER_0;
initCompParam.compareInterruptEnable = TIMER_B_CAPTURECOMPARE_INTERRUPT_ENABLE;
initCompParam.compareOutputMode = TIMER_B_OUTPUTMODE_OUTBITVALUE;
initCompParam.compareValue = COMPARE_VALUE;
Timer_B_initCompareMode(TIMER_B0_BASE, &initCompParam);

Timer_B_startCounter( TIMER_B0_BASE,
    TIMER_B_CONTINUOUS_MODE
);
}
```

## 27 Tag Length Value

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### 27.1 Introduction

The TLV structure is a table stored in flash memory that contains device-specific information. This table is read-only and is write-protected. It contains important information for using and calibrating the device. A list of the contents of the TLV is available in the device-specific data sheet (in the Device Descriptors section), and an explanation on its functionality is available in the MSP430x5xx/MSP430x6xx Family User's Guide

### 27.2 API Functions

#### Functions

- void [TLV\\_getInfo](#) (uint8\_t tag, uint8\_t instance, uint8\_t \*length, uint16\_t \*\*data\_address)  
*Gets TLV Info.*
- uint16\_t [TLV\\_getDeviceType](#) ()  
*Retrieves the unique device ID from the TLV structure.*
- uint16\_t [TLV\\_getMemory](#) (uint8\_t instance)  
*Gets memory information.*
- uint16\_t [TLV\\_getPeripheral](#) (uint8\_t tag, uint8\_t instance)  
*Gets peripheral information from the TLV.*
- uint8\_t [TLV\\_getInterrupt](#) (uint8\_t tag)  
*Get interrupt information from the TLV.*

#### 27.2.1 Detailed Description

The APIs that help in querying the information in the TLV structure are listed

- [TLV\\_getInfo\(\)](#) This function retrieves the value of a tag and the length of the tag.
- [TLV\\_getDeviceType\(\)](#) This function retrieves the unique device ID from the TLV structure.
- [TLV\\_getMemory\(\)](#) The returned value is zero if the end of the memory list is reached.
- [TLV\\_getPeripheral\(\)](#) The returned value is zero if the specified tag value (peripheral) is not available in the device.
- [TLV\\_getInterrupt\(\)](#) The returned value is zero if the specified interrupt vector is not defined.

## 27.2.2 Function Documentation

`uint16_t TLV_getDeviceType ( void )`

Retrieves the unique device ID from the TLV structure.

### Returns

The device ID is returned as type `uint16_t`.

`void TLV_getInfo ( uint8_t tag, uint8_t instance, uint8_t * length, uint16_t ** data_address )`

Gets TLV Info.

The TLV structure uses a tag or base address to identify segments of the table where information is stored. Some examples of TLV tags are Peripheral Descriptor, Interrupts, Info Block and Die Record. This function retrieves the value of a tag and the length of the tag.

### Parameters

<i>tag</i>	<p>represents the tag for which the information needs to be retrieved. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>TLV_TAG_LDRTAG</b></li> <li>■ <b>TLV_TAG_PDTAG</b></li> <li>■ <b>TLV_TAG_Reserved3</b></li> <li>■ <b>TLV_TAG_Reserved4</b></li> <li>■ <b>TLV_TAG_BLANK</b></li> <li>■ <b>TLV_TAG_Reserved6</b></li> <li>■ <b>TLV_TAG_Reserved7</b></li> <li>■ <b>TLV_TAG_TAGEND</b></li> <li>■ <b>TLV_TAG_TAGEXT</b></li> <li>■ <b>TLV_TAG_TIMER.D.CAL</b></li> <li>■ <b>TLV_DEVICE.ID_0</b></li> <li>■ <b>TLV_DEVICE.ID_1</b></li> <li>■ <b>TLV_TAG_DIERECORD</b></li> <li>■ <b>TLV_TAG_ADCCAL</b></li> <li>■ <b>TLV_TAG_ADC12CAL</b></li> <li>■ <b>TLV_TAG_ADC10CAL</b></li> <li>■ <b>TLV_TAG_REFCAL</b></li> <li>■ <b>TLV_TAG_CTSD16CAL</b></li> </ul>
------------	--

<i>instance</i>	In some cases a specific tag may have more than one instance. For example there may be multiple instances of timer calibration data present under a single Timer Cal tag. This variable specifies the instance for which information is to be retrieved (0, 1, etc.). When only one instance exists; 0 is passed.
<i>length</i>	Acts as a return through indirect reference. The function retrieves the value of the TLV tag length. This value is pointed to by *length and can be used by the application level once the function is called. If the specified tag is not found then the pointer is null 0.
<i>data_address</i>	acts as a return through indirect reference. Once the function is called data_address points to the pointer that holds the value retrieved from the specified TLV tag. If the specified tag is not found then the pointer is null 0.

**Returns**

None

Referenced by TLV\_getInterrupt(), TLV\_getMemory(), and TLV\_getPeripheral().

**uint8\_t TLV\_getInterrupt ( uint8\_t tag )**

Get interrupt information from the TLV.

This function is used to retrieve information on available interrupt vectors. It allows the user to check if a specific interrupt vector is defined in a given device.

**Parameters**

<i>tag</i>	represents the tag for the interrupt vector. Interrupt vector tags number from 0 to N depending on the number of available interrupts. Refer to the device datasheet for a list of available interrupts.
------------	--

**Returns**

The returned value is zero if the specified interrupt vector is not defined.

References TLV\_getInfo(), and TLV\_getMemory().

**uint16\_t TLV\_getMemory ( uint8\_t instance )**

Gets memory information.

The Peripheral Descriptor tag is split into two portions a list of the available flash memory blocks followed by a list of available peripherals. This function is used to parse through the first portion and calculate the total flash memory available in a device. The typical usage is to call the TLV\_getMemory which returns a non-zero value until the entire memory list has been parsed. When a zero is returned, it indicates that all the memory blocks have been counted and the next address holds the beginning of the device peripheral list.

**Parameters**

<i>instance</i>	In some cases a specific tag may have more than one instance. This variable specifies the instance for which information is to be retrieved (0, 1 etc). When only one instance exists; 0 is passed.
-----------------	---

**Returns**

The returned value is zero if the end of the memory list is reached.

References TLV\_getInfo().

Referenced by TLV\_getInterrupt(), and TLV\_getPeripheral().

uint16\_t TLV\_getPeripheral ( uint8\_t *tag*, uint8\_t *instance* )

Gets peripheral information from the TLV.

The Peripheral Descriptor tag is split into two portions a list of the available flash memory blocks followed by a list of available peripherals. This function is used to parse through the second portion and can be used to check if a specific peripheral is present in a device. The function calls [TLV\\_getPeripheral\(\)](#) recursively until the end of the memory list and consequently the beginning of the peripheral list is reached. <

## Parameters

<i>tag</i>	<p>represents represents the tag for a specific peripheral for which the information needs to be retrieved. In the header file <code>tlv.h</code> specific peripheral tags are pre-defined, for example <code>USCIA_B</code> and <code>TA0</code> are defined as <code>TLV_PID_USCIA_AB</code> and <code>TLV_PID_TA2</code> respectively. Valid values are:</p> <ul style="list-style-type: none"> <li>■ <b>TLV_PID_NO_MODULE</b> - No Module</li> <li>■ <b>TLV_PID_PORTMAPPING</b> - Port Mapping</li> <li>■ <b>TLV_PID_MSP430CPUXV2</b> - MSP430CPUXV2</li> <li>■ <b>TLV_PID_JTAG</b> - JTAG</li> <li>■ <b>TLV_PID_SBW</b> - SBW</li> <li>■ <b>TLV_PID_EEM_XS</b> - EEM X-Small</li> <li>■ <b>TLV_PID_EEM_S</b> - EEM Small</li> <li>■ <b>TLV_PID_EEM_M</b> - EEM Medium</li> <li>■ <b>TLV_PID_EEM_L</b> - EEM Large</li> <li>■ <b>TLV_PID_PMM</b> - PMM</li> <li>■ <b>TLV_PID_PMM_FR</b> - PMM FRAM</li> <li>■ <b>TLV_PID_FCTL</b> - Flash</li> <li>■ <b>TLV_PID_CRC16</b> - CRC16</li> <li>■ <b>TLV_PID_CRC16_RB</b> - CRC16 Reverse</li> <li>■ <b>TLV_PID_WDT_A</b> - WDT_A</li> <li>■ <b>TLV_PID_SFR</b> - SFR</li> <li>■ <b>TLV_PID_SYS</b> - SYS</li> <li>■ <b>TLV_PID_RAMCTL</b> - RAMCTL</li> <li>■ <b>TLV_PID_DMA_1</b> - DMA 1</li> <li>■ <b>TLV_PID_DMA_3</b> - DMA 3</li> <li>■ <b>TLV_PID_UCS</b> - UCS</li> <li>■ <b>TLV_PID_DMA_6</b> - DMA 6</li> <li>■ <b>TLV_PID_DMA_2</b> - DMA 2</li> <li>■ <b>TLV_PID_PORT1_2</b> - Port 1 + 2 / A</li> <li>■ <b>TLV_PID_PORT3_4</b> - Port 3 + 4 / B</li> <li>■ <b>TLV_PID_PORT5_6</b> - Port 5 + 6 / C</li> <li>■ <b>TLV_PID_PORT7_8</b> - Port 7 + 8 / D</li> <li>■ <b>TLV_PID_PORT9_10</b> - Port 9 + 10 / E</li> <li>■ <b>TLV_PID_PORT11_12</b> - Port 11 + 12 / F</li> <li>■ <b>TLV_PID_PORTU</b> - Port U</li> <li>■ <b>TLV_PID_PORTJ</b> - Port J</li> <li>■ <b>TLV_PID_TA2</b> - Timer A2</li> <li>■ <b>TLV_PID_TA3</b> - Timer A1</li> <li>■ <b>TLV_PID_TA5</b> - Timer A5</li> <li>■ <b>TLV_PID_TA7</b> - Timer A7</li> <li>■ <b>TLV_PID_TB3</b> - Timer B3</li> <li>■ <b>TLV_PID_TB5</b> - Timer B5</li> <li>■ <b>TLV_PID_TB7</b> - Timer B7</li> <li>■ <b>TLV_PID_RTC</b> - RTC</li> <li>■ <b>TLV_PID_BT_RTC</b> - BT + RTC</li> </ul>
------------	--



**Returns**

The returned value is zero if the specified tag value (peripheral) is not available in the device.

References TLV\_getInfo(), and TLV\_getMemory().

## 27.3 Programming Example

The following example shows some tlv operations using the APIs

```
struct s_TLV_Die_Record * pDIEREC;  
unsigned char bDieRecord.bytes;  
  
TLV_getInfo(TLV_TAG_DIERECORD,  
            0,  
            &bDieRecord.bytes,  
            (unsigned int **)&pDIEREC  
            );
```

## 28 WatchDog Timer (WDT\_A)

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### 28.1 Introduction

The Watchdog Timer (WDT\_A) API provides a set of functions for using the MSP430Ware WDT\_A modules. Functions are provided to initialize the Watchdog in either timer interval mode, or watchdog mode, with selectable clock sources and dividers to define the timer interval.

The WDT\_A module can generate only 1 kind of interrupt in timer interval mode. If in watchdog mode, then the WDT\_A module will assert a reset once the timer has finished.

### 28.2 API Functions

#### Functions

- void [WDT\\_A\\_hold](#) (uint16\_t baseAddress)  
*Holds the Watchdog Timer.*
- void [WDT\\_A\\_start](#) (uint16\_t baseAddress)  
*Starts the Watchdog Timer.*
- void [WDT\\_A\\_resetTimer](#) (uint16\_t baseAddress)  
*Resets the timer counter of the Watchdog Timer.*
- void [WDT\\_A\\_initWatchdogTimer](#) (uint16\_t baseAddress, uint8\_t clockSelect, uint8\_t clockDivider)  
*Sets the clock source for the Watchdog Timer in watchdog mode.*
- void [WDT\\_A\\_initIntervalTimer](#) (uint16\_t baseAddress, uint8\_t clockSelect, uint8\_t clockDivider)  
*Sets the clock source for the Watchdog Timer in timer interval mode.*

#### 28.2.1 Detailed Description

The WDT\_A API is one group that controls the WDT\_A module.

- [WDT\\_A\\_hold\(\)](#)
- [WDT\\_A\\_start\(\)](#)
- [WDT\\_A\\_clearCounter\(\)](#)
- [WDT\\_A\\_initWatchdogTimer\(\)](#)
- [WDT\\_A\\_initIntervalTimer\(\)](#)

## 28.2.2 Function Documentation

void WDT\_A\_hold ( uint16\_t *baseAddress* )

Holds the Watchdog Timer.

This function stops the watchdog timer from running, that way no interrupt or PUC is asserted.

### Parameters

<i>baseAddress</i>	is the base address of the WDT_A module.
--------------------	--

### Returns

None

void WDT\_A\_initIntervalTimer ( uint16\_t *baseAddress*, uint8\_t *clockSelect*, uint8\_t *clockDivider* )

Sets the clock source for the Watchdog Timer in timer interval mode.

This function sets the watchdog timer as timer interval mode, which will assert an interrupt without causing a PUC.

### Parameters

<i>baseAddress</i>	is the base address of the WDT_A module.
<i>clockSelect</i>	is the clock source that the watchdog timer will use. Valid values are: <ul style="list-style-type: none"> <li>■ <b>WDT_A_CLOCKSOURCE_SMCLK</b> [Default]</li> <li>■ <b>WDT_A_CLOCKSOURCE_ACLK</b></li> <li>■ <b>WDT_A_CLOCKSOURCE_VLOCLK</b></li> <li>■ <b>WDT_A_CLOCKSOURCE_XCLK</b></li> </ul> Modified bits are <b>WDTSSSEL</b> of <b>WDTCTL</b> register.
<i>clockDivider</i>	is the divider of the clock source, in turn setting the watchdog timer interval. Valid values are: <ul style="list-style-type: none"> <li>■ <b>WDT_A_CLOCKDIVIDER_2G</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_128M</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_8192K</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_512K</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_32K</b> [Default]</li> <li>■ <b>WDT_A_CLOCKDIVIDER_8192</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_512</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_64</b></li> </ul> Modified bits are <b>WDTIS</b> and <b>WDTHOLD</b> of <b>WDTCTL</b> register.

**Returns**

None

```
void WDT_A_initWatchdogTimer ( uint16_t baseAddress, uint8_t clockSelect, uint8_t
clockDivider )
```

Sets the clock source for the Watchdog Timer in watchdog mode.

This function sets the watchdog timer in watchdog mode, which will cause a PUC when the timer overflows. When in the mode, a PUC can be avoided with a call to [WDT\\_A\\_resetTimer\(\)](#) before the timer runs out.

**Parameters**

<i>baseAddress</i>	is the base address of the WDT_A module.
<i>clockSelect</i>	is the clock source that the watchdog timer will use. Valid values are: <ul style="list-style-type: none"> <li>■ <b>WDT_A_CLOCKSOURCE_SMCLK</b> [Default]</li> <li>■ <b>WDT_A_CLOCKSOURCE_ACLK</b></li> <li>■ <b>WDT_A_CLOCKSOURCE_VLOCLK</b></li> <li>■ <b>WDT_A_CLOCKSOURCE_XCLK</b></li> </ul> Modified bits are <b>WDTSSSEL</b> of <b>WDTCTL</b> register.
<i>clockDivider</i>	is the divider of the clock source, in turn setting the watchdog timer interval. Valid values are: <ul style="list-style-type: none"> <li>■ <b>WDT_A_CLOCKDIVIDER_2G</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_128M</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_8192K</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_512K</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_32K</b> [Default]</li> <li>■ <b>WDT_A_CLOCKDIVIDER_8192</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_512</b></li> <li>■ <b>WDT_A_CLOCKDIVIDER_64</b></li> </ul> Modified bits are <b>WDTIS</b> and <b>WDTHOLD</b> of <b>WDTCTL</b> register.

**Returns**

None

```
void WDT_A_resetTimer ( uint16_t baseAddress )
```

Resets the timer counter of the Watchdog Timer.

This function resets the watchdog timer to 0x0000h.

**Parameters**

<i>baseAddress</i>	is the base address of the WDT_A module.
--------------------	--

**Returns**

None

```
void WDT_A_start ( uint16_t baseAddress )
```

Starts the Watchdog Timer.

This function starts the watchdog timer functionality to start counting again.

**Parameters**

<i>baseAddress</i>	is the base address of the WDT_A module.
--------------------	--

**Returns**

None

## 28.3 Programming Example

The following example shows how to initialize and use the WDT\_A API to interrupt about every 32 ms, toggling the LED in the ISR.

```
//Initialize WDT_A module in timer interval mode,
//with SMCLK as source at an interval of 32 ms.
WDT_A_initIntervalTimer(WDT_A.BASE,
    WDT_A.CLOCKSOURCE_SMCLK,
    WDT_A.CLOCKDIVIDER_32K);

//Enable Watchdog Interrupt
SFR_enableInterrupt (SFR.WATCHDOG_INTERVAL_TIMER_INTERRUPT);

//Set P1.0 to output direction
GPIO_setAsOutputPin(
    GPIO_PORT_P1,
    GPIO_PIN0
);

//Enter LPM0, enable interrupts
__bis_SR_register(LPM0_bits + GIE);
//For debugger
__no_operation();
```

## 29 Data Structure Documentation

### 29.1 Data Structures

Here are the data structures with brief descriptions:

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<a href="#">s_TLV_Die_Record</a>	. . . . .	??
<a href="#">s_TLV_REF_Cal_Data</a>	. . . . .	??
<a href="#">s_TLV_Timer_D_Cal_Data</a>	. . . . .	??
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## 29.2 Calendar Struct Reference

Used in the [RTC\\_B\\_initCalendar\(\)](#) function as the CalendarTime parameter.

```
#include <rtc_b.h>
```

### Data Fields

- `uint8_t` [Seconds](#)  
*Seconds of minute between 0-59.*
- `uint8_t` [Minutes](#)  
*Minutes of hour between 0-59.*
- `uint8_t` [Hours](#)  
*Hour of day between 0-23.*
- `uint8_t` [DayOfWeek](#)  
*Day of week between 0-6.*
- `uint8_t` [DayOfMonth](#)  
*Day of month between 1-31.*
- `uint8_t` [Month](#)  
*Month between 0-11.*
- `uint16_t` [Year](#)  
*Year between 0-4095.*

### 29.2.1 Detailed Description

Used in the [RTC\\_B\\_initCalendar\(\)](#) function as the CalendarTime parameter.

The documentation for this struct was generated from the following file:

- `rtc_b.h`

## 29.3 Comp\_D\_initParam Struct Reference

Used in the [Comp\\_D\\_init\(\)](#) function as the param parameter.

```
#include <comp_d.h>
```

## Data Fields

- uint8\_t [positiveTerminalInput](#)
- uint8\_t [negativeTerminalInput](#)
- uint8\_t [outputFilterEnableAndDelayLevel](#)
- uint16\_t [invertedOutputPolarity](#)

### 29.3.1 Detailed Description

Used in the [Comp\\_D\\_init\(\)](#) function as the param parameter.

### 29.3.2 Field Documentation

#### uint16\_t Comp\_D\_initParam::invertedOutputPolarity

Controls if the output will be inverted or not  
Valid values are:

- **COMP\_D\_NORMALOUTPUTPOLARITY** [Default]
- **COMP\_D\_INVERTEDOUTPUTPOLARITY**

Referenced by [Comp\\_D\\_init\(\)](#).

#### uint8\_t Comp\_D\_initParam::negativeTerminalInput

Selects the input to the negative terminal.  
Valid values are:

- **COMP\_D\_INPUT0** [Default]
- **COMP\_D\_INPUT1**
- **COMP\_D\_INPUT2**
- **COMP\_D\_INPUT3**
- **COMP\_D\_INPUT4**
- **COMP\_D\_INPUT5**
- **COMP\_D\_INPUT6**
- **COMP\_D\_INPUT7**
- **COMP\_D\_INPUT8**
- **COMP\_D\_INPUT9**
- **COMP\_D\_INPUT10**
- **COMP\_D\_INPUT11**
- **COMP\_D\_INPUT12**



- **COMP\_D\_INPUT13**
- **COMP\_D\_INPUT14**
- **COMP\_D\_INPUT15**
- **COMP\_D\_VREF**

Referenced by `Comp_D_init()`.

#### `uint8_t Comp_D_initParam::outputFilterEnableAndDelayLevel`

Controls the output filter delay state, which is either off or enabled with a specified delay level. This parameter is device specific and delay levels should be found in the device's datasheet.

Valid values are:

- **COMP\_D\_FILTEROUTPUT\_OFF** [Default]
- **COMP\_D\_FILTEROUTPUT\_DLYLVL1**
- **COMP\_D\_FILTEROUTPUT\_DLYLVL2**
- **COMP\_D\_FILTEROUTPUT\_DLYLVL3**
- **COMP\_D\_FILTEROUTPUT\_DLYLVL4**

Referenced by `Comp_D_init()`.

#### `uint8_t Comp_D_initParam::positiveTerminalInput`

Selects the input to the positive terminal.

Valid values are:

- **COMP\_D\_INPUT0** [Default]
- **COMP\_D\_INPUT1**
- **COMP\_D\_INPUT2**
- **COMP\_D\_INPUT3**
- **COMP\_D\_INPUT4**
- **COMP\_D\_INPUT5**
- **COMP\_D\_INPUT6**
- **COMP\_D\_INPUT7**
- **COMP\_D\_INPUT8**
- **COMP\_D\_INPUT9**
- **COMP\_D\_INPUT10**
- **COMP\_D\_INPUT11**
- **COMP\_D\_INPUT12**
- **COMP\_D\_INPUT13**
- **COMP\_D\_INPUT14**
- **COMP\_D\_INPUT15**
- **COMP\_D\_VREF**

Referenced by `Comp_D_init()`.

The documentation for this struct was generated from the following file:

- `comp_d.h`

## 29.4 DMA\_initParam Struct Reference

Used in the `DMA_init()` function as the `param` parameter.

```
#include <dma.h>
```

### Data Fields

- `uint8_t channelSelect`
- `uint16_t transferModeSelect`
- `uint16_t transferSize`
- `uint8_t triggerSourceSelect`
- `uint8_t transferUnitSelect`
- `uint8_t triggerTypeSelect`

### 29.4.1 Detailed Description

Used in the `DMA_init()` function as the `param` parameter.

### 29.4.2 Field Documentation

#### `uint8_t DMA_initParam::channelSelect`

Is the specified channel to initialize.  
Valid values are:

- `DMA_CHANNEL_0`
- `DMA_CHANNEL_1`
- `DMA_CHANNEL_2`
- `DMA_CHANNEL_3`
- `DMA_CHANNEL_4`
- `DMA_CHANNEL_5`
- `DMA_CHANNEL_6`
- `DMA_CHANNEL_7`

Referenced by `DMA_init()`.

### uint16\_t DMA\_initParam::transferModeSelect

Is the transfer mode of the selected channel.

Valid values are:

- **DMA\_TRANSFER\_SINGLE** [Default] - Single transfer, transfers disabled after transferAmount of transfers.
- **DMA\_TRANSFER\_BLOCK** - Multiple transfers of transferAmount, transfers disabled once finished.
- **DMA\_TRANSFER\_BURSTBLOCK** - Multiple transfers of transferAmount interleaved with CPU activity, transfers disabled once finished.
- **DMA\_TRANSFER\_REPEATED\_SINGLE** - Repeated single transfer by trigger.
- **DMA\_TRANSFER\_REPEATED\_BLOCK** - Multiple transfers of transferAmount by trigger.
- **DMA\_TRANSFER\_REPEATED\_BURSTBLOCK** - Multiple transfers of transferAmount by trigger interleaved with CPU activity.

Referenced by DMA\_init().

### uint16\_t DMA\_initParam::transferSize

Is the amount of transfers to complete in a block transfer mode, as well as how many transfers to complete before the interrupt flag is set. Valid value is between 1-65535, if 0, no transfers will occur.

Referenced by DMA\_init().

### uint8\_t DMA\_initParam::transferUnitSelect

Is the specified size of transfers.

Valid values are:

- **DMA\_SIZE\_SRCWORD\_DSTWORD** [Default]
- **DMA\_SIZE\_SRCBYTE\_DSTWORD**
- **DMA\_SIZE\_SRCWORD\_DSTBYTE**
- **DMA\_SIZE\_SRCBYTE\_DSTBYTE**

Referenced by DMA\_init().

### uint8\_t DMA\_initParam::triggerSourceSelect

Is the source that will trigger the start of each transfer, note that the sources are device specific.

Valid values are:

- **DMA\_TRIGGERSOURCE\_0** [Default]
- **DMA\_TRIGGERSOURCE\_1**
- **DMA\_TRIGGERSOURCE\_2**
- **DMA\_TRIGGERSOURCE\_3**

- **DMA\_TRIGGERSOURCE\_4**
- **DMA\_TRIGGERSOURCE\_5**
- **DMA\_TRIGGERSOURCE\_6**
- **DMA\_TRIGGERSOURCE\_7**
- **DMA\_TRIGGERSOURCE\_8**
- **DMA\_TRIGGERSOURCE\_9**
- **DMA\_TRIGGERSOURCE\_10**
- **DMA\_TRIGGERSOURCE\_11**
- **DMA\_TRIGGERSOURCE\_12**
- **DMA\_TRIGGERSOURCE\_13**
- **DMA\_TRIGGERSOURCE\_14**
- **DMA\_TRIGGERSOURCE\_15**
- **DMA\_TRIGGERSOURCE\_16**
- **DMA\_TRIGGERSOURCE\_17**
- **DMA\_TRIGGERSOURCE\_18**
- **DMA\_TRIGGERSOURCE\_19**
- **DMA\_TRIGGERSOURCE\_20**
- **DMA\_TRIGGERSOURCE\_21**
- **DMA\_TRIGGERSOURCE\_22**
- **DMA\_TRIGGERSOURCE\_23**
- **DMA\_TRIGGERSOURCE\_24**
- **DMA\_TRIGGERSOURCE\_25**
- **DMA\_TRIGGERSOURCE\_26**
- **DMA\_TRIGGERSOURCE\_27**
- **DMA\_TRIGGERSOURCE\_28**
- **DMA\_TRIGGERSOURCE\_29**
- **DMA\_TRIGGERSOURCE\_30**
- **DMA\_TRIGGERSOURCE\_31**

Referenced by `DMA_init()`.

#### `uint8_t DMA_initParam::triggerTypeSelect`

Is the type of trigger that the trigger signal needs to be to start a transfer.  
Valid values are:

- **DMA\_TRIGGER\_RISINGEDGE** [Default]
- **DMA\_TRIGGER\_HIGH** - A trigger would be a high signal from the trigger source, to be held high through the length of the transfer(s).

Referenced by `DMA_init()`.

The documentation for this struct was generated from the following file:

- `dma.h`

## 29.5 EUSCI\_A\_SPI\_changeMasterClockParam Struct Reference

Used in the [EUSCI\\_A\\_SPI\\_changeMasterClock\(\)](#) function as the param parameter.

```
#include <eusci_a_spi.h>
```

### Data Fields

- `uint32_t` [clockSourceFrequency](#)  
*Is the frequency of the selected clock source.*
- `uint32_t` [desiredSpiClock](#)  
*Is the desired clock rate for SPI communication.*

### 29.5.1 Detailed Description

Used in the [EUSCI\\_A\\_SPI\\_changeMasterClock\(\)](#) function as the param parameter.

The documentation for this struct was generated from the following file:

- `eusci_a_spi.h`

## 29.6 EUSCI\_A\_SPI\_initMasterParam Struct Reference

Used in the [EUSCI\\_A\\_SPI\\_initMaster\(\)](#) function as the param parameter.

```
#include <eusci_a_spi.h>
```

### Data Fields

- `uint8_t` [selectClockSource](#)
- `uint32_t` [clockSourceFrequency](#)  
*Is the frequency of the selected clock source.*
- `uint32_t` [desiredSpiClock](#)  
*Is the desired clock rate for SPI communication.*
- `uint16_t` [msbFirst](#)
- `uint16_t` [clockPhase](#)
- `uint16_t` [clockPolarity](#)
- `uint16_t` [spiMode](#)

### 29.6.1 Detailed Description

Used in the [EUSCI\\_A\\_SPI\\_initMaster\(\)](#) function as the param parameter.

## 29.6.2 Field Documentation

### uint16\_t EUSCI\_A\_SPI\_initMasterParam::clockPhase

Is clock phase select.

Valid values are:

- **EUSCI\_A\_SPI\_PHASE\_DATA\_CHANGED\_ONFIRST\_CAPTURED\_ON\_NEXT** [Default]
- **EUSCI\_A\_SPI\_PHASE\_DATA\_CAPTURED\_ONFIRST\_CHANGED\_ON\_NEXT**

Referenced by EUSCI\_A\_SPI\_initMaster().

### uint16\_t EUSCI\_A\_SPI\_initMasterParam::clockPolarity

Is clock polarity select

Valid values are:

- **EUSCI\_A\_SPI\_CLOCKPOLARITY\_INACTIVITY\_HIGH**
- **EUSCI\_A\_SPI\_CLOCKPOLARITY\_INACTIVITY\_LOW** [Default]

Referenced by EUSCI\_A\_SPI\_initMaster().

### uint16\_t EUSCI\_A\_SPI\_initMasterParam::msbFirst

Controls the direction of the receive and transmit shift register.

Valid values are:

- **EUSCI\_A\_SPI\_MSB\_FIRST**
- **EUSCI\_A\_SPI\_LSB\_FIRST** [Default]

Referenced by EUSCI\_A\_SPI\_initMaster().

### uint8\_t EUSCI\_A\_SPI\_initMasterParam::selectClockSource

Selects Clock source. Refer to device specific datasheet for available options.

Valid values are:

- **EUSCI\_A\_SPI\_CLOCKSOURCE\_ACLK**
- **EUSCI\_A\_SPI\_CLOCKSOURCE\_SMCLK**

Referenced by EUSCI\_A\_SPI\_initMaster().

### uint16\_t EUSCI\_A\_SPI\_initMasterParam::spiMode

Is SPI mode select

Valid values are:

- **EUSCI\_A\_SPI\_3PIN**

- **EUSCI\_A\_SPI\_4PIN\_UCxSTE\_ACTIVE\_HIGH**
- **EUSCI\_A\_SPI\_4PIN\_UCxSTE\_ACTIVE\_LOW**

Referenced by `EUSCI_A_SPI_initMaster()`.

The documentation for this struct was generated from the following file:

- `eusci_a_spi.h`

## 29.7 EUSCI\_A\_SPI\_initSlaveParam Struct Reference

Used in the `EUSCI_A_SPI_initSlave()` function as the param parameter.

```
#include <eusci_a_spi.h>
```

### Data Fields

- `uint16_t` [msbFirst](#)
- `uint16_t` [clockPhase](#)
- `uint16_t` [clockPolarity](#)
- `uint16_t` [spiMode](#)

### 29.7.1 Detailed Description

Used in the `EUSCI_A_SPI_initSlave()` function as the param parameter.

### 29.7.2 Field Documentation

`uint16_t` `EUSCI_A_SPI_initSlaveParam::clockPhase`

Is clock phase select.  
Valid values are:

- **EUSCI\_A\_SPI\_PHASE\_DATA\_CHANGED\_ONFIRST\_CAPTURED\_ON\_NEXT** [Default]
- **EUSCI\_A\_SPI\_PHASE\_DATA\_CAPTURED\_ONFIRST\_CHANGED\_ON\_NEXT**

Referenced by `EUSCI_A_SPI_initSlave()`.

`uint16_t` `EUSCI_A_SPI_initSlaveParam::clockPolarity`

Is clock polarity select  
Valid values are:

- **EUSCI\_A\_SPI\_CLOCKPOLARITY\_INACTIVITY\_HIGH**
- **EUSCI\_A\_SPI\_CLOCKPOLARITY\_INACTIVITY\_LOW** [Default]

Referenced by `EUSCI_A_SPI_initSlave()`.

**uint16\_t EUSCI\_A\_SPI\_initSlaveParam::msbFirst**

Controls the direction of the receive and transmit shift register.  
Valid values are:

- **EUSCI\_A\_SPI\_MSB\_FIRST**
- **EUSCI\_A\_SPI\_LSB\_FIRST** [Default]

Referenced by `EUSCI_A_SPI_initSlave()`.

**uint16\_t EUSCI\_A\_SPI\_initSlaveParam::spiMode**

Is SPI mode select  
Valid values are:

- **EUSCI\_A\_SPI\_3PIN**
- **EUSCI\_A\_SPI\_4PIN\_UCxSTE\_ACTIVE\_HIGH**
- **EUSCI\_A\_SPI\_4PIN\_UCxSTE\_ACTIVE\_LOW**

Referenced by `EUSCI_A_SPI_initSlave()`.

The documentation for this struct was generated from the following file:

- `eusci_a_spi.h`

## 29.8 EUSCI\_A\_UART\_initParam Struct Reference

Used in the `EUSCI_A_UART_init()` function as the param parameter.

```
#include <eusci_a_uart.h>
```

### Data Fields

- `uint8_t selectClockSource`
- `uint16_t clockPrescaler`  
*Is the value to be written into UCBRx bits.*
- `uint8_t firstModReg`
- `uint8_t secondModReg`
- `uint8_t parity`
- `uint16_t msborLsbFirst`
- `uint16_t numberOfStopBits`
- `uint16_t uartMode`
- `uint8_t overSampling`

### 29.8.1 Detailed Description

Used in the `EUSCI_A_UART_init()` function as the param parameter.



## 29.8.2 Field Documentation

### uint8\_t EUSCI\_A\_UART\_initParam::firstModReg

Is First modulation stage register setting. This value is a pre- calculated value which can be obtained from the Device Users Guide. This value is written into UCBRFx bits of UCAXMCTLW.

Referenced by EUSCI\_A\_UART\_init().

### uint16\_t EUSCI\_A\_UART\_initParam::msborLsbFirst

Controls direction of receive and transmit shift register.

Valid values are:

- **EUSCI\_A\_UART\_MSB\_FIRST**
- **EUSCI\_A\_UART\_LSB\_FIRST** [Default]

Referenced by EUSCI\_A\_UART\_init().

### uint16\_t EUSCI\_A\_UART\_initParam::numberOfStopBits

Indicates one/two STOP bits

Valid values are:

- **EUSCI\_A\_UART\_ONE\_STOP\_BIT** [Default]
- **EUSCI\_A\_UART\_TWO\_STOP\_BITS**

Referenced by EUSCI\_A\_UART\_init().

### uint8\_t EUSCI\_A\_UART\_initParam::overSampling

Indicates low frequency or oversampling baud generation

Valid values are:

- **EUSCI\_A\_UART\_OVERSAMPLING\_BAUDRATE\_GENERATION**
- **EUSCI\_A\_UART\_LOW\_FREQUENCY\_BAUDRATE\_GENERATION**

Referenced by EUSCI\_A\_UART\_init().

### uint8\_t EUSCI\_A\_UART\_initParam::parity

Is the desired parity.

Valid values are:

- **EUSCI\_A\_UART\_NO\_PARITY** [Default]
- **EUSCI\_A\_UART\_ODD\_PARITY**
- **EUSCI\_A\_UART\_EVEN\_PARITY**

Referenced by EUSCI\_A\_UART\_init().

**uint8\_t EUSCI\_A\_UART\_initParam::secondModReg**

Is Second modulation stage register setting. This value is a pre- calculated value which can be obtained from the Device Users Guide. This value is written into UCBSx bits of UCAXMCTLW.

Referenced by EUSCI\_A\_UART\_init().

**uint8\_t EUSCI\_A\_UART\_initParam::selectClockSource**

Selects Clock source. Refer to device specific datasheet for available options.  
Valid values are:

- **EUSCI\_A\_UART\_CLOCKSOURCE\_SMCLK**
- **EUSCI\_A\_UART\_CLOCKSOURCE\_ACLK**

Referenced by EUSCI\_A\_UART\_init().

**uint16\_t EUSCI\_A\_UART\_initParam::uartMode**

Selects the mode of operation  
Valid values are:

- **EUSCI\_A\_UART\_MODE** [Default]
- **EUSCI\_A\_UART\_IDLE\_LINE\_MULTI\_PROCESSOR\_MODE**
- **EUSCI\_A\_UART\_ADDRESS\_BIT\_MULTI\_PROCESSOR\_MODE**
- **EUSCI\_A\_UART\_AUTOMATIC\_BAUDRATE\_DETECTION\_MODE**

Referenced by EUSCI\_A\_UART\_init().

The documentation for this struct was generated from the following file:

- `eusci_a_uart.h`

## 29.9 EUSCI\_B\_I2C\_initMasterParam Struct Reference

Used in the [EUSCI\\_B\\_I2C\\_initMaster\(\)](#) function as the param parameter.

```
#include <eusci_b_i2c.h>
```

### Data Fields

- [uint8\\_t selectClockSource](#)
- [uint32\\_t i2cClk](#)
- [uint32\\_t dataRate](#)
- [uint8\\_t byteCounterThreshold](#)  
*Sets threshold for automatic STOP or UCSTPIFG.*
- [uint8\\_t autoSTOPGeneration](#)

## 29.9.1 Detailed Description

Used in the [EUSCI\\_B\\_I2C\\_initMaster\(\)](#) function as the param parameter.

## 29.9.2 Field Documentation

### uint8\_t EUSCI\_B\_I2C\_initMasterParam::autoSTOPGeneration

Sets up the STOP condition generation.

Valid values are:

- **EUSCI\_B\_I2C\_NO\_AUTO\_STOP**
- **EUSCI\_B\_I2C\_SET\_BYTECOUNT\_THRESHOLD\_FLAG**
- **EUSCI\_B\_I2C\_SEND\_STOP\_AUTOMATICALLY\_ON\_BYTECOUNT\_THRESHOLD**

Referenced by [EUSCI\\_B\\_I2C\\_initMaster\(\)](#).

### uint32\_t EUSCI\_B\_I2C\_initMasterParam::dataRate

Setup for selecting data transfer rate.

Valid values are:

- **EUSCI\_B\_I2C\_SET\_DATA\_RATE\_400KBPS**
- **EUSCI\_B\_I2C\_SET\_DATA\_RATE\_100KBPS**

Referenced by [EUSCI\\_B\\_I2C\\_initMaster\(\)](#).

### uint32\_t EUSCI\_B\_I2C\_initMasterParam::i2cClk

Is the rate of the clock supplied to the I2C module (the frequency in Hz of the clock source specified in [selectClockSource](#)).

Referenced by [EUSCI\\_B\\_I2C\\_initMaster\(\)](#).

### uint8\_t EUSCI\_B\_I2C\_initMasterParam::selectClockSource

Selects the clocksource. Refer to device specific datasheet for available options.

Valid values are:

- **EUSCI\_B\_I2C\_CLOCKSOURCE\_ACLK**
- **EUSCI\_B\_I2C\_CLOCKSOURCE\_SMCLK**

Referenced by [EUSCI\\_B\\_I2C\\_initMaster\(\)](#).

The documentation for this struct was generated from the following file:

- `eusci_b_i2c.h`

## 29.10 EUSCI\_B\_I2C\_initSlaveParam Struct Reference

Used in the [EUSCI\\_B\\_I2C\\_initSlave\(\)](#) function as the param parameter.

```
#include <eusci_b_i2c.h>
```

### Data Fields

- `uint8_t slaveAddress`  
*7-bit slave address*
- `uint8_t slaveAddressOffset`
- `uint32_t slaveOwnAddressEnable`

### 29.10.1 Detailed Description

Used in the [EUSCI\\_B\\_I2C\\_initSlave\(\)](#) function as the param parameter.

### 29.10.2 Field Documentation

`uint8_t EUSCI_B_I2C_initSlaveParam::slaveAddressOffset`

Own address Offset referred to- 'x' value of UCBxI2COAx.  
Valid values are:

- **EUSCI\_B\_I2C\_OWN\_ADDRESS\_OFFSET0**
- **EUSCI\_B\_I2C\_OWN\_ADDRESS\_OFFSET1**
- **EUSCI\_B\_I2C\_OWN\_ADDRESS\_OFFSET2**
- **EUSCI\_B\_I2C\_OWN\_ADDRESS\_OFFSET3**

Referenced by [EUSCI\\_B\\_I2C\\_initSlave\(\)](#).

`uint32_t EUSCI_B_I2C_initSlaveParam::slaveOwnAddressEnable`

Selects if the specified address is enabled or disabled.  
Valid values are:

- **EUSCI\_B\_I2C\_OWN\_ADDRESS\_DISABLE**
- **EUSCI\_B\_I2C\_OWN\_ADDRESS\_ENABLE**

Referenced by [EUSCI\\_B\\_I2C\\_initSlave\(\)](#).

The documentation for this struct was generated from the following file:

- `eusci_b_i2c.h`

## 29.11 EUSCI\_B\_SPI\_changeMasterClockParam Struct Reference

Used in the [EUSCI\\_B\\_SPI\\_changeMasterClock\(\)](#) function as the param parameter.

```
#include <eusci_b_spi.h>
```

### Data Fields

- `uint32_t` [clockSourceFrequency](#)  
*Is the frequency of the selected clock source.*
- `uint32_t` [desiredSpiClock](#)  
*Is the desired clock rate for SPI communication.*

### 29.11.1 Detailed Description

Used in the [EUSCI\\_B\\_SPI\\_changeMasterClock\(\)](#) function as the param parameter.

The documentation for this struct was generated from the following file:

- `eusci_b_spi.h`

## 29.12 EUSCI\_B\_SPI\_initMasterParam Struct Reference

Used in the [EUSCI\\_B\\_SPI\\_initMaster\(\)](#) function as the param parameter.

```
#include <eusci_b_spi.h>
```

### Data Fields

- `uint8_t` [selectClockSource](#)
- `uint32_t` [clockSourceFrequency](#)  
*Is the frequency of the selected clock source.*
- `uint32_t` [desiredSpiClock](#)  
*Is the desired clock rate for SPI communication.*
- `uint16_t` [msbFirst](#)
- `uint16_t` [clockPhase](#)
- `uint16_t` [clockPolarity](#)
- `uint16_t` [spiMode](#)

### 29.12.1 Detailed Description

Used in the [EUSCI\\_B\\_SPI\\_initMaster\(\)](#) function as the param parameter.

## 29.12.2 Field Documentation

### uint16\_t EUSCI\_B\_SPI\_initMasterParam::clockPhase

Is clock phase select.

Valid values are:

- **EUSCI\_B\_SPI\_PHASE\_DATA\_CHANGED\_ONFIRST\_CAPTURED\_ON\_NEXT** [Default]
- **EUSCI\_B\_SPI\_PHASE\_DATA\_CAPTURED\_ONFIRST\_CHANGED\_ON\_NEXT**

Referenced by EUSCI\_B\_SPI\_initMaster().

### uint16\_t EUSCI\_B\_SPI\_initMasterParam::clockPolarity

Is clock polarity select

Valid values are:

- **EUSCI\_B\_SPI\_CLOCKPOLARITY\_INACTIVITY\_HIGH**
- **EUSCI\_B\_SPI\_CLOCKPOLARITY\_INACTIVITY\_LOW** [Default]

Referenced by EUSCI\_B\_SPI\_initMaster().

### uint16\_t EUSCI\_B\_SPI\_initMasterParam::msbFirst

Controls the direction of the receive and transmit shift register.

Valid values are:

- **EUSCI\_B\_SPI\_MSB\_FIRST**
- **EUSCI\_B\_SPI\_LSB\_FIRST** [Default]

Referenced by EUSCI\_B\_SPI\_initMaster().

### uint8\_t EUSCI\_B\_SPI\_initMasterParam::selectClockSource

Selects Clock source. Refer to device specific datasheet for available options.

Valid values are:

- **EUSCI\_B\_SPI\_CLOCKSOURCE\_ACLK**
- **EUSCI\_B\_SPI\_CLOCKSOURCE\_SMCLK**

Referenced by EUSCI\_B\_SPI\_initMaster().

### uint16\_t EUSCI\_B\_SPI\_initMasterParam::spiMode

Is SPI mode select

Valid values are:

- **EUSCI\_B\_SPI\_3PIN**

- **EUSCI\_B\_SPI\_4PIN\_UCxSTE\_ACTIVE\_HIGH**
- **EUSCI\_B\_SPI\_4PIN\_UCxSTE\_ACTIVE\_LOW**

Referenced by `EUSCI_B_SPI_initMaster()`.

The documentation for this struct was generated from the following file:

- `eusci_b_spi.h`

## 29.13 EUSCI\_B\_SPI\_initSlaveParam Struct Reference

Used in the `EUSCI_B_SPI_initSlave()` function as the param parameter.

```
#include <eusci_b_spi.h>
```

### Data Fields

- `uint16_t` [msbFirst](#)
- `uint16_t` [clockPhase](#)
- `uint16_t` [clockPolarity](#)
- `uint16_t` [spiMode](#)

### 29.13.1 Detailed Description

Used in the `EUSCI_B_SPI_initSlave()` function as the param parameter.

### 29.13.2 Field Documentation

`uint16_t` `EUSCI_B_SPI_initSlaveParam::clockPhase`

Is clock phase select.  
Valid values are:

- **EUSCI\_B\_SPI\_PHASE\_DATA\_CHANGED\_ONFIRST\_CAPTURED\_ON\_NEXT** [Default]
- **EUSCI\_B\_SPI\_PHASE\_DATA\_CAPTURED\_ONFIRST\_CHANGED\_ON\_NEXT**

Referenced by `EUSCI_B_SPI_initSlave()`.

`uint16_t` `EUSCI_B_SPI_initSlaveParam::clockPolarity`

Is clock polarity select  
Valid values are:

- **EUSCI\_B\_SPI\_CLOCKPOLARITY\_INACTIVITY\_HIGH**
- **EUSCI\_B\_SPI\_CLOCKPOLARITY\_INACTIVITY\_LOW** [Default]

Referenced by `EUSCI_B_SPI_initSlave()`.

**uint16\_t EUSCI\_B\_SPI\_initSlaveParam::msbFirst**

Controls the direction of the receive and transmit shift register.  
Valid values are:

- **EUSCI\_B\_SPI\_MSB\_FIRST**
- **EUSCI\_B\_SPI\_LSB\_FIRST** [Default]

Referenced by EUSCI\_B\_SPI\_initSlave().

**uint16\_t EUSCI\_B\_SPI\_initSlaveParam::spiMode**

Is SPI mode select  
Valid values are:

- **EUSCI\_B\_SPI\_3PIN**
- **EUSCI\_B\_SPI\_4PIN\_UCxSTE\_ACTIVE\_HIGH**
- **EUSCI\_B\_SPI\_4PIN\_UCxSTE\_ACTIVE\_LOW**

Referenced by EUSCI\_B\_SPI\_initSlave().

The documentation for this struct was generated from the following file:

- eusci\_b\_spi.h

## 29.14 MPU\_initThreeSegmentsParam Struct Reference

Used in the [MPU\\_initThreeSegments\(\)](#) function as the param parameter.

```
#include <mpu.h>
```

### Data Fields

- uint16\_t [seg1boundary](#)  
*Valid values can be found in the Family User's Guide.*
- uint16\_t [seg2boundary](#)  
*Valid values can be found in the Family User's Guide.*
- uint8\_t [seg1accmask](#)
- uint8\_t [seg2accmask](#)
- uint8\_t [seg3accmask](#)

#### 29.14.1 Detailed Description

Used in the [MPU\\_initThreeSegments\(\)](#) function as the param parameter.



## 29.14.2 Field Documentation

uint8\_t MPU\_initThreeSegmentsParam::seg1accmask

Is the bit mask of access right for memory segment 1.  
Logical OR of any of the following:

- **MPU\_READ** - Read rights
- **MPU\_WRITE** - Write rights
- **MPU\_EXEC** - Execute rights
- **MPU\_NO\_READ\_WRITE\_EXEC** - no read/write/execute rights

Referenced by MPU\_initThreeSegments().

uint8\_t MPU\_initThreeSegmentsParam::seg2accmask

Is the bit mask of access right for memory segment 2.  
Logical OR of any of the following:

- **MPU\_READ** - Read rights
- **MPU\_WRITE** - Write rights
- **MPU\_EXEC** - Execute rights
- **MPU\_NO\_READ\_WRITE\_EXEC** - no read/write/execute rights

Referenced by MPU\_initThreeSegments().

uint8\_t MPU\_initThreeSegmentsParam::seg3accmask

Is the bit mask of access right for memory segment 3.  
Logical OR of any of the following:

- **MPU\_READ** - Read rights
- **MPU\_WRITE** - Write rights
- **MPU\_EXEC** - Execute rights
- **MPU\_NO\_READ\_WRITE\_EXEC** - no read/write/execute rights

Referenced by MPU\_initThreeSegments().

The documentation for this struct was generated from the following file:

- mpu.h

## 29.15 RTC\_B\_configureCalendarAlarmParam Struct Reference

Used in the [RTC\\_B\\_configureCalendarAlarm\(\)](#) function as the param parameter.

```
#include <rtc_b.h>
```

## Data Fields

- uint8\_t [minutesAlarm](#)
- uint8\_t [hoursAlarm](#)
- uint8\_t [dayOfWeekAlarm](#)
- uint8\_t [dayOfMonthAlarm](#)

### 29.15.1 Detailed Description

Used in the [RTC\\_B\\_configureCalendarAlarm\(\)](#) function as the param parameter.

### 29.15.2 Field Documentation

#### uint8\_t RTC\_B\_configureCalendarAlarmParam::dayOfMonthAlarm

Is the alarm condition for the day of the month.  
Valid values are:

- **RTC\_B\_ALARMCONDITION\_OFF** [Default]

Referenced by [RTC\\_B\\_configureCalendarAlarm\(\)](#).

#### uint8\_t RTC\_B\_configureCalendarAlarmParam::dayOfWeekAlarm

Is the alarm condition for the day of week.  
Valid values are:

- **RTC\_B\_ALARMCONDITION\_OFF** [Default]

Referenced by [RTC\\_B\\_configureCalendarAlarm\(\)](#).

#### uint8\_t RTC\_B\_configureCalendarAlarmParam::hoursAlarm

Is the alarm condition for the hours.  
Valid values are:

- **RTC\_B\_ALARMCONDITION\_OFF** [Default]

Referenced by [RTC\\_B\\_configureCalendarAlarm\(\)](#).

#### uint8\_t RTC\_B\_configureCalendarAlarmParam::minutesAlarm

Is the alarm condition for the minutes.  
Valid values are:

- **RTC\_B\_ALARMCONDITION\_OFF** [Default]

Referenced by `RTC_B_configureCalendarAlarm()`.

The documentation for this struct was generated from the following file:

- `rtc_b.h`

## 29.16 Timer\_A\_initCaptureModeParam Struct Reference

Used in the `Timer_A_initCaptureMode()` function as the `param` parameter.

```
#include <timer_a.h>
```

### Data Fields

- `uint16_t captureRegister`
- `uint16_t captureMode`
- `uint16_t captureInputSelect`
- `uint16_t synchronizeCaptureSource`
- `uint16_t captureInterruptEnable`
- `uint16_t captureOutputMode`

### 29.16.1 Detailed Description

Used in the `Timer_A_initCaptureMode()` function as the `param` parameter.

### 29.16.2 Field Documentation

`uint16_t Timer_A_initCaptureModeParam::captureInputSelect`

Decides the Input Select  
Valid values are:

- `TIMER_A_CAPTURE_INPUTSELECT_CC1xA`
- `TIMER_A_CAPTURE_INPUTSELECT_CC1xB`
- `TIMER_A_CAPTURE_INPUTSELECT_GND`
- `TIMER_A_CAPTURE_INPUTSELECT_Vcc`

Referenced by `Timer_A_initCaptureMode()`.

`uint16_t Timer_A_initCaptureModeParam::captureInterruptEnable`

Is to enable or disable timer captureCompare interrupt.  
Valid values are:

- `TIMER_A_CAPTURECOMPARE_INTERRUPT_DISABLE` [Default]

- **TIMER\_A\_CAPTURECOMPARE\_INTERRUPT\_ENABLE**

Referenced by `Timer_A_initCaptureMode()`.

#### `uint16_t Timer_A_initCaptureModeParam::captureMode`

Is the capture mode selected.

Valid values are:

- **TIMER\_A\_CAPTUREMODE\_NO\_CAPTURE** [Default]
- **TIMER\_A\_CAPTUREMODE\_RISING\_EDGE**
- **TIMER\_A\_CAPTUREMODE\_FALLING\_EDGE**
- **TIMER\_A\_CAPTUREMODE\_RISING\_AND\_FALLING\_EDGE**

Referenced by `Timer_A_initCaptureMode()`.

#### `uint16_t Timer_A_initCaptureModeParam::captureOutputMode`

Specifies the output mode.

Valid values are:

- **TIMER\_A\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_A\_OUTPUTMODE\_SET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_A\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE**
- **TIMER\_A\_OUTPUTMODE\_RESET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_A\_OUTPUTMODE\_RESET\_SET**

Referenced by `Timer_A_initCaptureMode()`.

#### `uint16_t Timer_A_initCaptureModeParam::captureRegister`

Selects the Capture register being used. Refer to datasheet to ensure the device has the capture compare register being used.

Valid values are:

- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_0**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_1**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_2**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_3**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_4**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_5**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_6**

Referenced by `Timer_A_initCaptureMode()`.

## uint16\_t Timer\_A\_initCaptureModeParam::synchronizeCaptureSource

Decides if capture source should be synchronized with timer clock  
Valid values are:

- **TIMER\_A\_CAPTURE\_ASYNCHRONOUS** [Default]
- **TIMER\_A\_CAPTURE\_SYNCHRONOUS**

Referenced by `Timer_A_initCaptureMode()`.

The documentation for this struct was generated from the following file:

- `timer_a.h`

## 29.17 Timer\_A\_initCompareModeParam Struct Reference

Used in the `Timer_A_initCompareMode()` function as the param parameter.

```
#include <timer_a.h>
```

### Data Fields

- uint16\_t [compareRegister](#)
- uint16\_t [compareInterruptEnable](#)
- uint16\_t [compareOutputMode](#)
- uint16\_t [compareValue](#)

*Is the count to be compared with in compare mode.*

### 29.17.1 Detailed Description

Used in the `Timer_A_initCompareMode()` function as the param parameter.

### 29.17.2 Field Documentation

#### uint16\_t Timer\_A\_initCompareModeParam::compareInterruptEnable

Is to enable or disable timer captureComapre interrupt.  
Valid values are:

- **TIMER\_A\_CAPTURECOMPARE\_INTERRUPT\_DISABLE** [Default]
- **TIMER\_A\_CAPTURECOMPARE\_INTERRUPT\_ENABLE**

Referenced by `Timer_A_initCompareMode()`.

### uint16\_t Timer\_A\_initCompareModeParam::compareOutputMode

Specifies the output mode.

Valid values are:

- **TIMER\_A\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_A\_OUTPUTMODE\_SET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_A\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE**
- **TIMER\_A\_OUTPUTMODE\_RESET**
- **TIMER\_A\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_A\_OUTPUTMODE\_RESET\_SET**

Referenced by `Timer_A_initCompareMode()`.

### uint16\_t Timer\_A\_initCompareModeParam::compareRegister

Selects the Capture register being used. Refer to datasheet to ensure the device has the capture compare register being used.

Valid values are:

- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_0**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_1**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_2**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_3**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_4**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_5**
- **TIMER\_A\_CAPTURECOMPARE\_REGISTER\_6**

Referenced by `Timer_A_initCompareMode()`.

The documentation for this struct was generated from the following file:

- `timer_a.h`

## 29.18 Timer\_A\_initContinuousModeParam Struct Reference

Used in the `Timer_A_initContinuousMode()` function as the param parameter.

```
#include <timer_a.h>
```

### Data Fields

- `uint16_t` [clockSource](#)

- uint16\_t [clockSourceDivider](#)
- uint16\_t [timerInterruptEnable\\_TAIE](#)
- uint16\_t [timerClear](#)
- bool [startTimer](#)

*Whether to start the timer immediately.*

## 29.18.1 Detailed Description

Used in the [Timer\\_A\\_initContinuousMode\(\)](#) function as the param parameter.

## 29.18.2 Field Documentation

### uint16\_t Timer\_A\_initContinuousModeParam::clockSource

Selects Clock source.

Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_A\_CLOCKSOURCE\_ACLK**
- **TIMER\_A\_CLOCKSOURCE\_SMCLK**
- **TIMER\_A\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by [Timer\\_A\\_initContinuousMode\(\)](#).

### uint16\_t Timer\_A\_initContinuousModeParam::clockSourceDivider

Is the desired divider for the clock source

Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_24**

- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_A_initContinuousMode()`.

`uint16_t Timer_A_initContinuousModeParam::timerClear`

Decides if `Timer_A` clock divider, count direction, count need to be reset.  
Valid values are:

- **TIMER\_A\_DO\_CLEAR**
- **TIMER\_A\_SKIP\_CLEAR** [Default]

Referenced by `Timer_A_initContinuousMode()`.

`uint16_t Timer_A_initContinuousModeParam::timerInterruptEnable_TAIE`

Is to enable or disable `Timer_A` interrupt  
Valid values are:

- **TIMER\_A\_TAIE\_INTERRUPT\_ENABLE**
- **TIMER\_A\_TAIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_A_initContinuousMode()`.

The documentation for this struct was generated from the following file:

- `timer_a.h`

## 29.19 Timer\_A\_initUpDownModeParam Struct Reference

Used in the `Timer_A_initUpDownMode()` function as the `param` parameter.

```
#include <timer_a.h>
```

### Data Fields

- `uint16_t clockSource`
- `uint16_t clockSourceDivider`
- `uint16_t timerPeriod`  
*Is the specified `Timer_A` period.*
- `uint16_t timerInterruptEnable_TAIE`
- `uint16_t captureCompareInterruptEnable_CCR0_CCIE`



- uint16\_t [timerClear](#)
- bool [startTimer](#)

*Whether to start the timer immediately.*

## 29.19.1 Detailed Description

Used in the [Timer\\_A\\_initUpDownMode\(\)](#) function as the param parameter.

## 29.19.2 Field Documentation

uint16\_t [Timer\\_A\\_initUpDownModeParam::captureCompareInterruptEnable\\_CCR0\\_CCIE](#)

Is to enable or disable Timer\_A CCR0 captureCompare interrupt.  
Valid values are:

- **TIMER\_A\_CCIE\_CCR0\_INTERRUPT\_ENABLE**
- **TIMER\_A\_CCIE\_CCR0\_INTERRUPT\_DISABLE** [Default]

Referenced by [Timer\\_A\\_initUpDownMode\(\)](#).

uint16\_t [Timer\\_A\\_initUpDownModeParam::clockSource](#)

Selects Clock source.  
Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_A\_CLOCKSOURCE\_ACLK**
- **TIMER\_A\_CLOCKSOURCE\_SMCLK**
- **TIMER\_A\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by [Timer\\_A\\_initUpDownMode\(\)](#).

uint16\_t [Timer\\_A\\_initUpDownModeParam::clockSourceDivider](#)

Is the desired divider for the clock source  
Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_7**

- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_A_initUpDownMode()`.

`uint16_t Timer_A_initUpDownModeParam::timerClear`

Decides if `Timer_A` clock divider, count direction, count need to be reset.  
Valid values are:

- **TIMER\_A\_DO\_CLEAR**
- **TIMER\_A\_SKIP\_CLEAR** [Default]

Referenced by `Timer_A_initUpDownMode()`.

`uint16_t Timer_A_initUpDownModeParam::timerInterruptEnable_TAIE`

Is to enable or disable `Timer_A` interrupt  
Valid values are:

- **TIMER\_A\_TAIE\_INTERRUPT\_ENABLE**
- **TIMER\_A\_TAIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_A_initUpDownMode()`.

The documentation for this struct was generated from the following file:

- `timer_a.h`

## 29.20 `Timer_A_initUpModeParam` Struct Reference

Used in the [`Timer\_A\_initUpMode\(\)`](#) function as the `param` parameter.

```
#include <timer_a.h>
```

## Data Fields

- uint16\_t [clockSource](#)
- uint16\_t [clockSourceDivider](#)
- uint16\_t [timerPeriod](#)
- uint16\_t [timerInterruptEnable\\_TAIE](#)
- uint16\_t [captureCompareInterruptEnable\\_CCR0\\_CCIE](#)
- uint16\_t [timerClear](#)
- bool [startTimer](#)

*Whether to start the timer immediately.*

### 29.20.1 Detailed Description

Used in the [Timer\\_A\\_initUpMode\(\)](#) function as the param parameter.

### 29.20.2 Field Documentation

#### uint16\_t Timer\_A\_initUpModeParam::captureCompareInterruptEnable\_CCR0\_CCIE

Is to enable or disable Timer\_A CCR0 captureComapre interrupt.  
Valid values are:

- **TIMER\_A\_CCIE\_CCR0\_INTERRUPT\_ENABLE**
- **TIMER\_A\_CCIE\_CCR0\_INTERRUPT\_DISABLE** [Default]

Referenced by [Timer\\_A\\_initUpMode\(\)](#).

#### uint16\_t Timer\_A\_initUpModeParam::clockSource

Selects Clock source.  
Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_A\_CLOCKSOURCE\_ACLK**
- **TIMER\_A\_CLOCKSOURCE\_SMCLK**
- **TIMER\_A\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by [Timer\\_A\\_initUpMode\(\)](#).

#### uint16\_t Timer\_A\_initUpModeParam::clockSourceDivider

Is the desired divider for the clock source  
Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_2**

- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_A\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_A_initUpMode()`.

#### `uint16_t Timer_A_initUpModeParam::timerClear`

Decides if `Timer_A` clock divider, count direction, count need to be reset.  
Valid values are:

- **TIMER\_A\_DO\_CLEAR**
- **TIMER\_A\_SKIP\_CLEAR** [Default]

Referenced by `Timer_A_initUpMode()`.

#### `uint16_t Timer_A_initUpModeParam::timerInterruptEnable_TAIE`

Is to enable or disable `Timer_A` interrupt  
Valid values are:

- **TIMER\_A\_TAIE\_INTERRUPT\_ENABLE**
- **TIMER\_A\_TAIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_A_initUpMode()`.

**uint16\_t Timer\_A\_initUpModeParam::timerPeriod**

Is the specified Timer\_A period. This is the value that gets written into the CCR0. Limited to 16 bits[uint16\_t]

Referenced by Timer\_A\_initUpMode().

The documentation for this struct was generated from the following file:

- timer\_a.h

## 29.21 Timer\_A\_outputPWMParam Struct Reference

Used in the [Timer\\_A\\_outputPWM\(\)](#) function as the param parameter.

```
#include <timer_a.h>
```

### Data Fields

- uint16\_t [clockSource](#)
- uint16\_t [clockSourceDivider](#)
- uint16\_t [timerPeriod](#)
  - Selects the desired timer period.*
- uint16\_t [compareRegister](#)
- uint16\_t [compareOutputMode](#)
- uint16\_t [dutyCycle](#)
  - Specifies the dutycycle for the generated waveform.*

### 29.21.1 Detailed Description

Used in the [Timer\\_A\\_outputPWM\(\)](#) function as the param parameter.

### 29.21.2 Field Documentation

**uint16\_t Timer\_A\_outputPWMParam::clockSource**

Selects Clock source.

Valid values are:

- **TIMER\_A\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_A\_CLOCKSOURCE\_ACLK**
- **TIMER\_A\_CLOCKSOURCE\_SMCLK**
- **TIMER\_A\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by [Timer\\_A\\_outputPWM\(\)](#).

`uint16_t Timer_A_outputPWMParam::clockSourceDivider`

Is the desired divider for the clock source  
Valid values are:

- `TIMER_A_CLOCKSOURCE_DIVIDER_1` [Default]
- `TIMER_A_CLOCKSOURCE_DIVIDER_2`
- `TIMER_A_CLOCKSOURCE_DIVIDER_3`
- `TIMER_A_CLOCKSOURCE_DIVIDER_4`
- `TIMER_A_CLOCKSOURCE_DIVIDER_5`
- `TIMER_A_CLOCKSOURCE_DIVIDER_6`
- `TIMER_A_CLOCKSOURCE_DIVIDER_7`
- `TIMER_A_CLOCKSOURCE_DIVIDER_8`
- `TIMER_A_CLOCKSOURCE_DIVIDER_10`
- `TIMER_A_CLOCKSOURCE_DIVIDER_12`
- `TIMER_A_CLOCKSOURCE_DIVIDER_14`
- `TIMER_A_CLOCKSOURCE_DIVIDER_16`
- `TIMER_A_CLOCKSOURCE_DIVIDER_20`
- `TIMER_A_CLOCKSOURCE_DIVIDER_24`
- `TIMER_A_CLOCKSOURCE_DIVIDER_28`
- `TIMER_A_CLOCKSOURCE_DIVIDER_32`
- `TIMER_A_CLOCKSOURCE_DIVIDER_40`
- `TIMER_A_CLOCKSOURCE_DIVIDER_48`
- `TIMER_A_CLOCKSOURCE_DIVIDER_56`
- `TIMER_A_CLOCKSOURCE_DIVIDER_64`

Referenced by `Timer_A_outputPWM()`.

`uint16_t Timer_A_outputPWMParam::compareOutputMode`

Specifies the output mode.  
Valid values are:

- `TIMER_A_OUTPUTMODE_OUTBITVALUE` [Default]
- `TIMER_A_OUTPUTMODE_SET`
- `TIMER_A_OUTPUTMODE_TOGGLE_RESET`
- `TIMER_A_OUTPUTMODE_SET_RESET`
- `TIMER_A_OUTPUTMODE_TOGGLE`
- `TIMER_A_OUTPUTMODE_RESET`
- `TIMER_A_OUTPUTMODE_TOGGLE_SET`
- `TIMER_A_OUTPUTMODE_RESET_SET`

Referenced by `Timer_A_outputPWM()`.

## uint16\_t Timer\_A\_outputPWMParm::compareRegister

Selects the compare register being used. Refer to datasheet to ensure the device has the capture compare register being used.

Valid values are:

- `TIMER_A_CAPTURECOMPARE_REGISTER_0`
- `TIMER_A_CAPTURECOMPARE_REGISTER_1`
- `TIMER_A_CAPTURECOMPARE_REGISTER_2`
- `TIMER_A_CAPTURECOMPARE_REGISTER_3`
- `TIMER_A_CAPTURECOMPARE_REGISTER_4`
- `TIMER_A_CAPTURECOMPARE_REGISTER_5`
- `TIMER_A_CAPTURECOMPARE_REGISTER_6`

Referenced by `Timer_A_outputPWM()`.

The documentation for this struct was generated from the following file:

- `timer_a.h`

## 29.22 Timer\_B\_initCaptureModeParam Struct Reference

Used in the `Timer_B_initCaptureMode()` function as the param parameter.

```
#include <timer_b.h>
```

### Data Fields

- `uint16_t captureRegister`
- `uint16_t captureMode`
- `uint16_t captureInputSelect`
- `uint16_t synchronizeCaptureSource`
- `uint16_t captureInterruptEnable`
- `uint16_t captureOutputMode`

### 29.22.1 Detailed Description

Used in the `Timer_B_initCaptureMode()` function as the param parameter.

### 29.22.2 Field Documentation

#### uint16\_t Timer\_B\_initCaptureModeParam::captureInputSelect

Decides the Input Select

Valid values are:

- **TIMER\_B\_CAPTURE\_INPUTSELECT\_CCIxA** [Default]
- **TIMER\_B\_CAPTURE\_INPUTSELECT\_CCIxB**
- **TIMER\_B\_CAPTURE\_INPUTSELECT\_GND**
- **TIMER\_B\_CAPTURE\_INPUTSELECT\_Vcc**

Referenced by `Timer_B_initCaptureMode()`.

#### `uint16_t Timer_B_initCaptureModeParam::captureInterruptEnable`

Is to enable or disable `Timer_B` capture compare interrupt.  
Valid values are:

- **TIMER\_B\_CAPTURECOMPARE\_INTERRUPT\_DISABLE** [Default]
- **TIMER\_B\_CAPTURECOMPARE\_INTERRUPT\_ENABLE**

Referenced by `Timer_B_initCaptureMode()`.

#### `uint16_t Timer_B_initCaptureModeParam::captureMode`

Is the capture mode selected.  
Valid values are:

- **TIMER\_B\_CAPTUREMODE\_NO\_CAPTURE** [Default]
- **TIMER\_B\_CAPTUREMODE\_RISING\_EDGE**
- **TIMER\_B\_CAPTUREMODE\_FALLING\_EDGE**
- **TIMER\_B\_CAPTUREMODE\_RISING\_AND\_FALLING\_EDGE**

Referenced by `Timer_B_initCaptureMode()`.

#### `uint16_t Timer_B_initCaptureModeParam::captureOutputMode`

Specifies the output mode.  
Valid values are:

- **TIMER\_B\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_B\_OUTPUTMODE\_SET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_B\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE**
- **TIMER\_B\_OUTPUTMODE\_RESET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_B\_OUTPUTMODE\_RESET\_SET**

Referenced by `Timer_B_initCaptureMode()`.



**uint16\_t Timer\_B\_initCaptureModeParam::captureRegister**

Selects the capture register being used. Refer to datasheet to ensure the device has the capture register being used.

Valid values are:

- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_0**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_1**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_2**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_3**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_4**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_5**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_6**

Referenced by `Timer_B_initCaptureMode()`.

**uint16\_t Timer\_B\_initCaptureModeParam::synchronizeCaptureSource**

Decides if capture source should be synchronized with Timer\_B clock

Valid values are:

- **TIMER\_B\_CAPTURE\_ASYNCHRONOUS** [Default]
- **TIMER\_B\_CAPTURE\_SYNCHRONOUS**

Referenced by `Timer_B_initCaptureMode()`.

The documentation for this struct was generated from the following file:

- `timer_b.h`

## 29.23 Timer\_B\_initCompareModeParam Struct Reference

Used in the [Timer\\_B\\_initCompareMode\(\)](#) function as the param parameter.

```
#include <timer_b.h>
```

### Data Fields

- [uint16\\_t compareRegister](#)
- [uint16\\_t compareInterruptEnable](#)
- [uint16\\_t compareOutputMode](#)
- [uint16\\_t compareValue](#)

*Is the count to be compared with in compare mode.*

#### 29.23.1 Detailed Description

Used in the [Timer\\_B\\_initCompareMode\(\)](#) function as the param parameter.

## 29.23.2 Field Documentation

uint16\_t Timer\_B\_initCompareModeParam::compareInterruptEnable

Is to enable or disable Timer\_B capture compare interrupt.  
Valid values are:

- **TIMER\_B\_CAPTURECOMPARE\_INTERRUPT\_DISABLE** [Default]
- **TIMER\_B\_CAPTURECOMPARE\_INTERRUPT\_ENABLE**

Referenced by Timer\_B\_initCompareMode().

uint16\_t Timer\_B\_initCompareModeParam::compareOutputMode

Specifies the output mode.  
Valid values are:

- **TIMER\_B\_OUTPUTMODE\_OUTBITVALUE** [Default]
- **TIMER\_B\_OUTPUTMODE\_SET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE\_RESET**
- **TIMER\_B\_OUTPUTMODE\_SET\_RESET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE**
- **TIMER\_B\_OUTPUTMODE\_RESET**
- **TIMER\_B\_OUTPUTMODE\_TOGGLE\_SET**
- **TIMER\_B\_OUTPUTMODE\_RESET\_SET**

Referenced by Timer\_B\_initCompareMode().

uint16\_t Timer\_B\_initCompareModeParam::compareRegister

Selects the compare register being used. Refer to datasheet to ensure the device has the compare register being used.  
Valid values are:

- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_0**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_1**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_2**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_3**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_4**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_5**
- **TIMER\_B\_CAPTURECOMPARE\_REGISTER\_6**

Referenced by Timer\_B\_initCompareMode().

The documentation for this struct was generated from the following file:

- timer\_b.h

## 29.24 Timer\_B\_initContinuousModeParam Struct Reference

Used in the [Timer\\_B\\_initContinuousMode\(\)](#) function as the param parameter.

```
#include <timer_b.h>
```

### Data Fields

- uint16\_t [clockSource](#)
- uint16\_t [clockSourceDivider](#)
- uint16\_t [timerInterruptEnable\\_TBIE](#)
- uint16\_t [timerClear](#)
- bool [startTimer](#)

*Whether to start the timer immediately.*

### 29.24.1 Detailed Description

Used in the [Timer\\_B\\_initContinuousMode\(\)](#) function as the param parameter.

### 29.24.2 Field Documentation

uint16\_t [Timer\\_B\\_initContinuousModeParam::clockSource](#)

Selects the clock source  
Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_B\_CLOCKSOURCE\_ACLK**
- **TIMER\_B\_CLOCKSOURCE\_SMCLK**
- **TIMER\_B\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by [Timer\\_B\\_initContinuousMode\(\)](#).

uint16\_t [Timer\\_B\\_initContinuousModeParam::clockSourceDivider](#)

Is the divider for Clock source.  
Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_6**

- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_B_initContinuousMode()`.

`uint16_t Timer_B_initContinuousModeParam::timerClear`

Decides if `Timer_B` clock divider, count direction, count need to be reset.  
Valid values are:

- **TIMER\_B\_DO\_CLEAR**
- **TIMER\_B\_SKIP\_CLEAR** [Default]

Referenced by `Timer_B_initContinuousMode()`.

`uint16_t Timer_B_initContinuousModeParam::timerInterruptEnable_TBIE`

Is to enable or disable `Timer_B` interrupt  
Valid values are:

- **TIMER\_B\_TBIE\_INTERRUPT\_ENABLE**
- **TIMER\_B\_TBIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_B_initContinuousMode()`.

The documentation for this struct was generated from the following file:

- `timer_b.h`

## 29.25 `Timer_B_initUpDownModeParam` Struct Reference

Used in the `Timer_B_initUpDownMode()` function as the `param` parameter.

```
#include <timer_b.h>
```

## Data Fields

- uint16\_t [clockSource](#)
- uint16\_t [clockSourceDivider](#)
- uint16\_t [timerPeriod](#)
  - Is the specified Timer\_B period.*
- uint16\_t [timerInterruptEnable\\_TBIE](#)
- uint16\_t [captureCompareInterruptEnable\\_CCR0\\_CCIE](#)
- uint16\_t [timerClear](#)
- bool [startTimer](#)
  - Whether to start the timer immediately.*

### 29.25.1 Detailed Description

Used in the [Timer\\_B\\_initUpDownMode\(\)](#) function as the param parameter.

### 29.25.2 Field Documentation

#### uint16\_t Timer\_B\_initUpDownModeParam::captureCompareInterruptEnable\_CCR0\_CCIE

Is to enable or disable Timer\_B CCR0 capture compare interrupt.  
Valid values are:

- **TIMER\_B\_CCIE\_CCR0\_INTERRUPT\_ENABLE**
- **TIMER\_B\_CCIE\_CCR0\_INTERRUPT\_DISABLE** [Default]

Referenced by [Timer\\_B\\_initUpDownMode\(\)](#).

#### uint16\_t Timer\_B\_initUpDownModeParam::clockSource

Selects the clock source  
Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_B\_CLOCKSOURCE\_ACLK**
- **TIMER\_B\_CLOCKSOURCE\_SMCLK**
- **TIMER\_B\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by [Timer\\_B\\_initUpDownMode\(\)](#).

#### uint16\_t Timer\_B\_initUpDownModeParam::clockSourceDivider

Is the divider for Clock source.  
Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_1** [Default]

- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_B_initUpDownMode()`.

#### `uint16_t Timer_B_initUpDownModeParam::timerClear`

Decides if `Timer_B` clock divider, count direction, count need to be reset.  
Valid values are:

- **TIMER\_B\_DO\_CLEAR**
- **TIMER\_B\_SKIP\_CLEAR** [Default]

Referenced by `Timer_B_initUpDownMode()`.

#### `uint16_t Timer_B_initUpDownModeParam::timerInterruptEnable_TBIE`

Is to enable or disable `Timer_B` interrupt  
Valid values are:

- **TIMER\_B\_TBIE\_INTERRUPT\_ENABLE**
- **TIMER\_B\_TBIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_B_initUpDownMode()`.

The documentation for this struct was generated from the following file:

- `timer_b.h`

## 29.26 Timer\_B\_initUpModeParam Struct Reference

Used in the [Timer\\_B\\_initUpMode\(\)](#) function as the param parameter.

```
#include <timer_b.h>
```

### Data Fields

- uint16\_t [clockSource](#)
- uint16\_t [clockSourceDivider](#)
- uint16\_t [timerPeriod](#)
- uint16\_t [timerInterruptEnable\\_TBIE](#)
- uint16\_t [captureCompareInterruptEnable\\_CCR0\\_CCIE](#)
- uint16\_t [timerClear](#)
- bool [startTimer](#)

*Whether to start the timer immediately.*

### 29.26.1 Detailed Description

Used in the [Timer\\_B\\_initUpMode\(\)](#) function as the param parameter.

### 29.26.2 Field Documentation

uint16\_t [Timer\\_B\\_initUpModeParam::captureCompareInterruptEnable\\_CCR0\\_CCIE](#)

Is to enable or disable Timer\_B CCR0 capture compare interrupt.

Valid values are:

- **TIMER\_B\_CCIE\_CCR0\_INTERRUPT\_ENABLE**
- **TIMER\_B\_CCIE\_CCR0\_INTERRUPT\_DISABLE** [Default]

Referenced by [Timer\\_B\\_initUpMode\(\)](#).

uint16\_t [Timer\\_B\\_initUpModeParam::clockSource](#)

Selects the clock source

Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_B\_CLOCKSOURCE\_ACLK**
- **TIMER\_B\_CLOCKSOURCE\_SMCLK**
- **TIMER\_B\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by [Timer\\_B\\_initUpMode\(\)](#).

`uint16_t Timer_B_initUpModeParam::clockSourceDivider`

Is the divider for Clock source.

Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_1** [Default]
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_2**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_3**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_4**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_5**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_6**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_7**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_8**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_10**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_12**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_14**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_16**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_20**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_24**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_28**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_32**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_40**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_48**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_56**
- **TIMER\_B\_CLOCKSOURCE\_DIVIDER\_64**

Referenced by `Timer_B_initUpMode()`.

`uint16_t Timer_B_initUpModeParam::timerClear`

Decides if `Timer_B` clock divider, count direction, count need to be reset.

Valid values are:

- **TIMER\_B\_DO\_CLEAR**
- **TIMER\_B\_SKIP\_CLEAR** [Default]

Referenced by `Timer_B_initUpMode()`.

`uint16_t Timer_B_initUpModeParam::timerInterruptEnable_TBIE`

Is to enable or disable `Timer_B` interrupt

Valid values are:

- **TIMER\_B\_TBIE\_INTERRUPT\_ENABLE**
- **TIMER\_B\_TBIE\_INTERRUPT\_DISABLE** [Default]

Referenced by `Timer_B_initUpMode()`.



**uint16\_t Timer\_B\_initUpModeParam::timerPeriod**

Is the specified Timer\_B period. This is the value that gets written into the CCR0. Limited to 16 bits[uint16\_t]

Referenced by Timer\_B\_initUpMode().

The documentation for this struct was generated from the following file:

- timer\_b.h

## 29.27 Timer\_B\_outputPWMParam Struct Reference

Used in the [Timer\\_B\\_outputPWM\(\)](#) function as the param parameter.

```
#include <timer_b.h>
```

### Data Fields

- uint16\_t [clockSource](#)
- uint16\_t [clockSourceDivider](#)
- uint16\_t [timerPeriod](#)  
*Selects the desired Timer\_B period.*
- uint16\_t [compareRegister](#)
- uint16\_t [compareOutputMode](#)
- uint16\_t [dutyCycle](#)  
*Specifies the dutycycle for the generated waveform.*

### 29.27.1 Detailed Description

Used in the [Timer\\_B\\_outputPWM\(\)](#) function as the param parameter.

### 29.27.2 Field Documentation

**uint16\_t Timer\_B\_outputPWMParam::clockSource**

Selects the clock source

Valid values are:

- **TIMER\_B\_CLOCKSOURCE\_EXTERNAL\_TXCLK** [Default]
- **TIMER\_B\_CLOCKSOURCE\_ACLK**
- **TIMER\_B\_CLOCKSOURCE\_SMCLK**
- **TIMER\_B\_CLOCKSOURCE\_INVERTED\_EXTERNAL\_TXCLK**

Referenced by [Timer\\_B\\_outputPWM\(\)](#).

`uint16_t Timer_B_outputPWMParam::clockSourceDivider`

Is the divider for Clock source.

Valid values are:

- `TIMER_B_CLOCKSOURCE_DIVIDER_1` [Default]
- `TIMER_B_CLOCKSOURCE_DIVIDER_2`
- `TIMER_B_CLOCKSOURCE_DIVIDER_3`
- `TIMER_B_CLOCKSOURCE_DIVIDER_4`
- `TIMER_B_CLOCKSOURCE_DIVIDER_5`
- `TIMER_B_CLOCKSOURCE_DIVIDER_6`
- `TIMER_B_CLOCKSOURCE_DIVIDER_7`
- `TIMER_B_CLOCKSOURCE_DIVIDER_8`
- `TIMER_B_CLOCKSOURCE_DIVIDER_10`
- `TIMER_B_CLOCKSOURCE_DIVIDER_12`
- `TIMER_B_CLOCKSOURCE_DIVIDER_14`
- `TIMER_B_CLOCKSOURCE_DIVIDER_16`
- `TIMER_B_CLOCKSOURCE_DIVIDER_20`
- `TIMER_B_CLOCKSOURCE_DIVIDER_24`
- `TIMER_B_CLOCKSOURCE_DIVIDER_28`
- `TIMER_B_CLOCKSOURCE_DIVIDER_32`
- `TIMER_B_CLOCKSOURCE_DIVIDER_40`
- `TIMER_B_CLOCKSOURCE_DIVIDER_48`
- `TIMER_B_CLOCKSOURCE_DIVIDER_56`
- `TIMER_B_CLOCKSOURCE_DIVIDER_64`

Referenced by `Timer_B_outputPWM()`.

`uint16_t Timer_B_outputPWMParam::compareOutputMode`

Specifies the output mode.

Valid values are:

- `TIMER_B_OUTPUTMODE_OUTBITVALUE` [Default]
- `TIMER_B_OUTPUTMODE_SET`
- `TIMER_B_OUTPUTMODE_TOGGLE_RESET`
- `TIMER_B_OUTPUTMODE_SET_RESET`
- `TIMER_B_OUTPUTMODE_TOGGLE`
- `TIMER_B_OUTPUTMODE_RESET`
- `TIMER_B_OUTPUTMODE_TOGGLE_SET`
- `TIMER_B_OUTPUTMODE_RESET_SET`

Referenced by `Timer_B_outputPWM()`.

`uint16_t Timer_B_outputPWMParm::compareRegister`

Selects the compare register being used. Refer to datasheet to ensure the device has the compare register being used.

Valid values are:

- `TIMER_B_CAPTURECOMPARE_REGISTER_0`
- `TIMER_B_CAPTURECOMPARE_REGISTER_1`
- `TIMER_B_CAPTURECOMPARE_REGISTER_2`
- `TIMER_B_CAPTURECOMPARE_REGISTER_3`
- `TIMER_B_CAPTURECOMPARE_REGISTER_4`
- `TIMER_B_CAPTURECOMPARE_REGISTER_5`
- `TIMER_B_CAPTURECOMPARE_REGISTER_6`

Referenced by `Timer_B_outputPWM()`.

The documentation for this struct was generated from the following file:

- `timer_b.h`

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