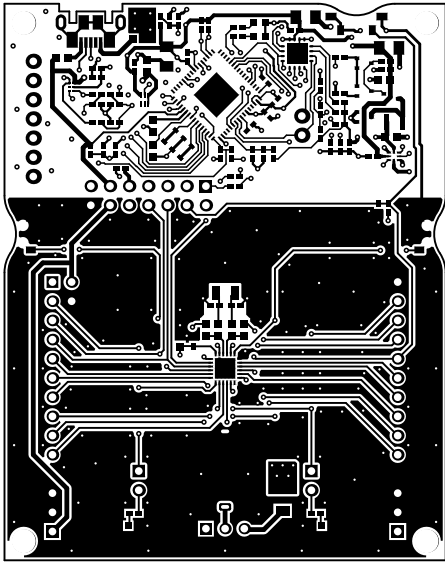
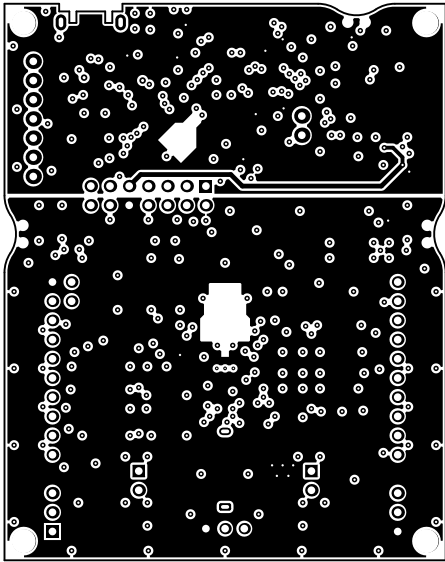


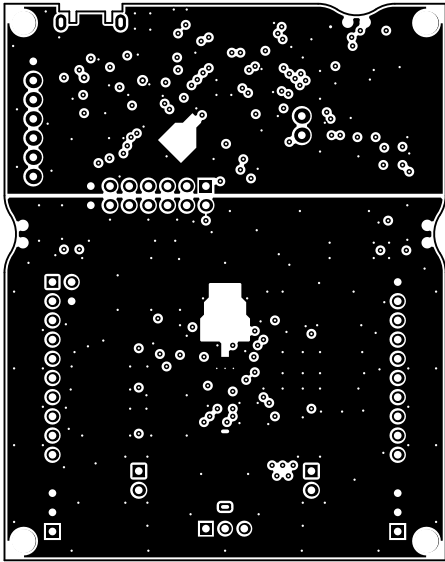
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU024	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: N/A		
PLOT NAME = Top Overlay	GENERATED : 7/11/2017 10:46:06 AM	TEXAS INSTRUMENTS	



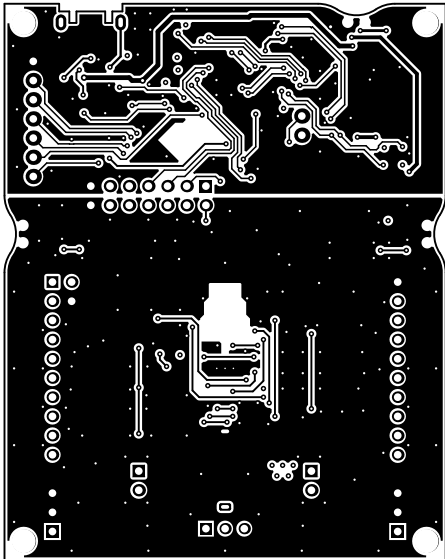
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU024	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Layer	TID #: N/A		
PLOT NAME = Top Layer	GENERATED : 7/11/2017 10:46:09 AM	TEXAS INSTRUMENTS	



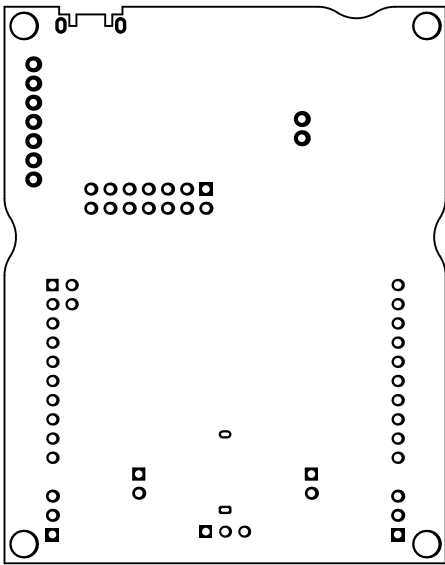
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU024	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal Layer 1	GENERATED : 7/11/2017 10:46:11 AM	TEXAS INSTRUMENTS	



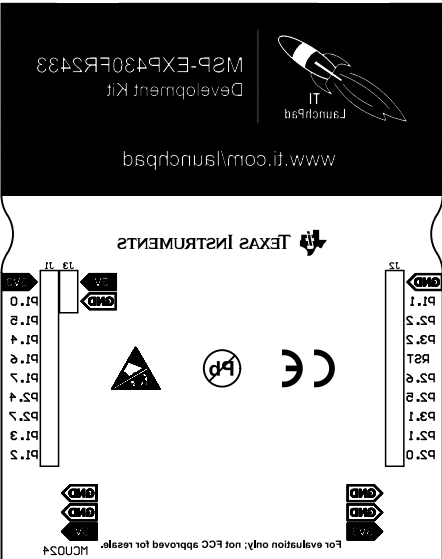
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU024	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal Layer 2	GENERATED : 7/11/2017 10:46:12 AM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU024	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bottom Layer	TID #: N/A		
PLOT NAME = Bottom Layer	GENERATED : 7/11/2017 10:46:13 AM	TEXAS INSTRUMENTS	



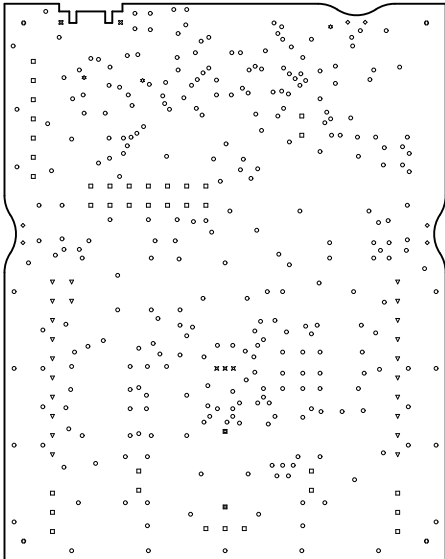
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU024	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bottom Solder	TID #: N/A		
PLOT NAME = Bottom Solder Mask	GENERATED : 7/11/2017 10:46:15 AM		TEXAS INSTRUMENTS



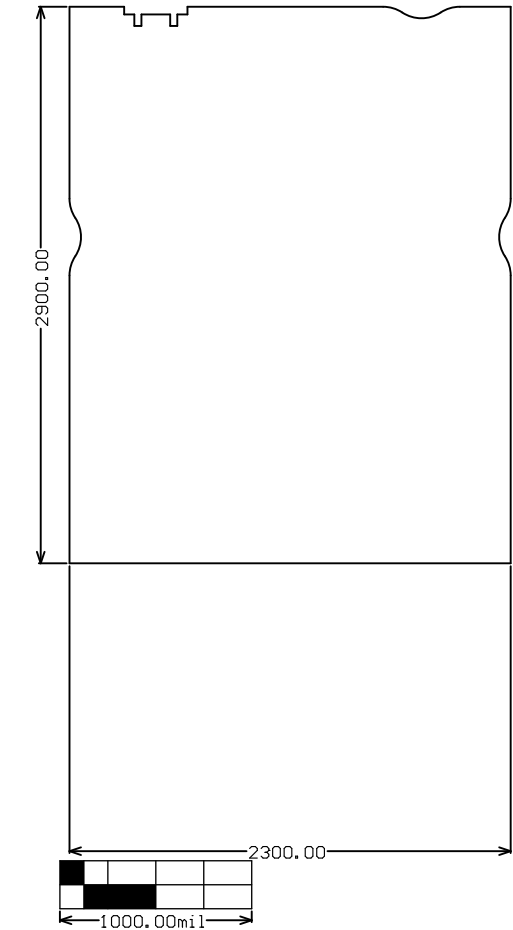
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU024	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED : 7/11/2017 10:46:16 AM	TEXAS INSTRUMENTS	

Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance	Hole Length
◆	6	35,43mil (0,900mm)					
⊗	4	125,00mil (3,175mm)	NPTH	Round	Top Layer - Bottom Layer		-
⊗	3	7,87mil (0,200mm)	PTH	Round	Top Layer - Bottom Layer		-
○	304	12,00mil (0,305mm)	PTH	Round	Top Layer - Bottom Layer		-
⊗	3	16,00mil (0,406mm)	PTH	Round	Top Layer - Bottom Layer		-
□	36	40,00mil (1,016mm)	PTH	Round	Top Layer - Bottom Layer		-
▽	22	40,16mil (1,020mm)	PTH	Round	Top Layer - Bottom Layer		-
■	2	19,69mil (0,500mm)	PTH	Slot	Top Layer - Bottom Layer		43,31mil (1,100mm)
⊗	2	23,62mil (0,600mm)	PTH	Slot	Top Layer - Bottom Layer		51,18mil (1,300mm)
	382 Total						

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU024	REV: 1.0	SUN REV: Not In VersionControl
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Drill Drawing	GENERATED : 7/11/2017	10:46:18 AM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU024	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: N/A		
PLOT NAME = Board Dimensions	GENERATED : 7/11/2017 10:46:19 AM	TEXAS INSTRUMENTS	