

CC2650 Size Benchmark

May 4, 2015

A benchmark application was created for CC26xx to allow a quick relative comparison of sizes between different kernel and driverlib releases. This application includes a representative set of SYS/BIOS kernel modules and API calls as understood to be used in a typical BLE application. This includes the Power module and its APIs. The application isn't intended to be functional - it includes several API calls for no purpose other than to have them linked into the application.

The total Flash and RAM sizes for recent releases are listed in tables below. In each case, the application is built to use SYS/BIOS in ROM, and uses the default tool optimizations of that release.

bios_6_42_00_07 (Build date: April 30, 2015)

xdctools_3_31_03_33

cc26xxware_2_21_01_15600

	RCOSC Calibration	Flash	RAM
IAR	Disabled	13520	8035
	Enabled	14418	8091
TI	Disabled	18098	7583
	Enabled	19474	7635

Changes in this release known to affect size:

- Removed PG1, PG2.0, and PG2.1 workarounds
- Removed backdoor recovery mechanism
- Eliminate all RSOSC calibration code when the feature is disabled

bios_6_41_02_41 (Build date: February 9, 2015)

xdctools_3_30_06_67

cc26xxware_2_20_06_14829

	RCOSC Calibration	Flash	RAM
IAR	Disabled	14173	8063
	Enabled	15201	8087
TI	Disabled	18310	7611
	Enabled	19386	7635